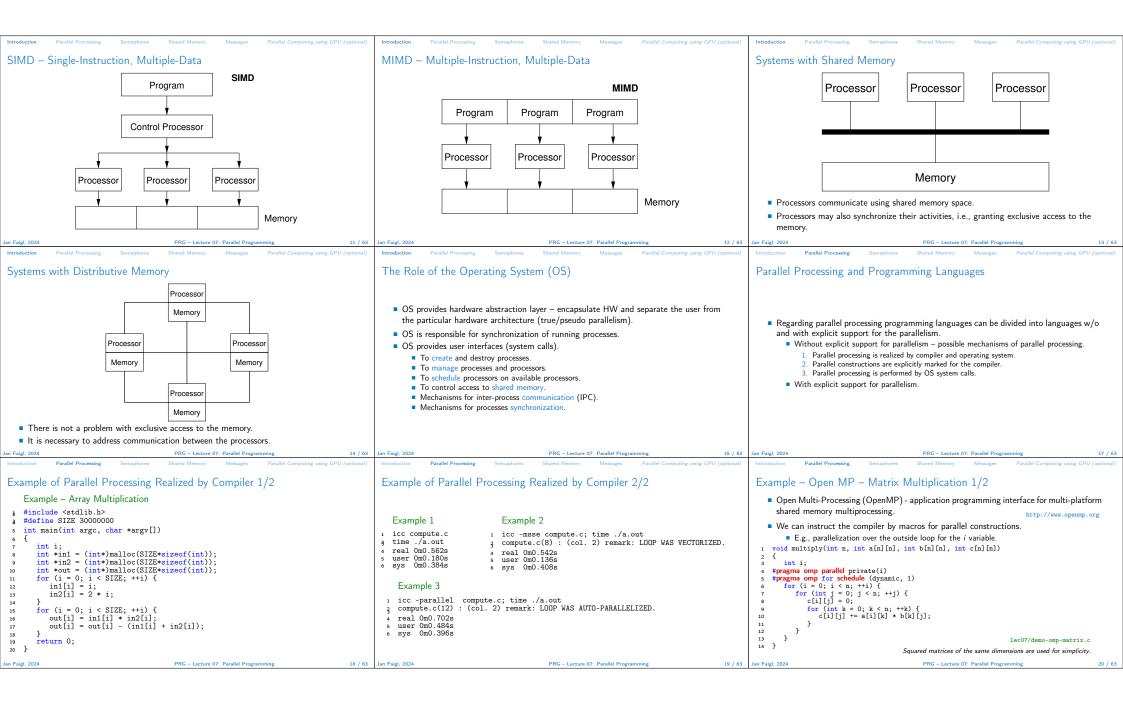
Parallel Programming Jan Faigl Department of Computer Science Faculty of Electrical Engineering Czech Technical University in Prague Lecture 07 PRG – Programming in C	Overview of the Lecture Part 1 - Introduction to Parallel Programming Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional)	Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional) Part I Part 1 – Introduction to Parallel Programming
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Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional) Parallel Programming	Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional) Motivation Why to Deal with Parallel Programming	Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional) Process – Executed Program
 The idea of parallel programming comes from the 60s with the first multi-program and pseudo-parallel systems. Parallelism can be hardware or software based. Hardware based - true hardware parallelism of multiprocessor systems. Software based - pseudo-parallelism. Pseudo-parallelism - A program with parallel constructions may run in pseudo-parallel environment on single or multi-processor systems. 	 Increase computational power. Having multi-processor system we can solve the computational problem faster. Efficient usage of the computational power. Even a running program may wait for data. E.g., a usual program with user-interaction typically waits for the user input. Simultaneous processing of many requests. Handling requests from individual clients in client/server architecture. 	 Process is executed program running in a dedicated memory space. Process is an entity of the Operating System (OS) that is schedule for independent execution. Process is usually in one of three basic states: Executing - currently running on the processor (CPU); Blocked - waiting for the periphery; Waiting - waiting for the processor . A process is identified in the OS by its identifier, e.g., Process IDentificator PID. Scheduler of the OS manage running processes to be allocated to the available processors.
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Process States External event to run the process Transition to the head queue with ready processes Running Process asked for termination System call that can be performed immediatelly	 Multi-processor Systems Multi-processor systems allow true parallelism. It is necessary to synchronize processors and support data communication. Resources for activity synchronization. Resources for communication between processors (processes). 	Particle Control of individual instructions. • Control of individual instructions. • SIMD – Single-Instruction, Multiple-Data – same instructions are simultaneously performed on different data. • Processors' are identical and run synchronously. • E.g., "Vectorization" such as MMX, SSE, 3Dnowl, and AVX, AVX2, etc. • MIMD – Multiple-Instruction, Multiple-Data – processors run independently and asynchronously. • Memory Control Access. • Systems with shared memory – central shared memory. • Systems with distributed memory – each processor has its memory. • Berg, rouptational grids.



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 Example - Open MP - Matrix Multiplication 2/2 Comparison of matrix multiplication with 1000× 1000 matrices using OpenMP on iCore5 (2 cores with HT). gcc -std=c99 -02 -o demo-omp demo-omp-matrix.classes 200 cores 200 c	 Languages with Explicit Support for Parallelism It has support for creation of new processes. Running process create a copy of itself. Both processes execute the identical code (copied). 	Parallelism – Statement Level Example – parbegin-parend block parbegin S ₂ : S ₂ : S _n
 2/demo-omp 1000 3. Size of matrices 1000 x 1000 naive multiplication single core 9.33 sec Multiplication multi-core 4.73 sec export OMP_NUM_THREADS=2 ./demo-omp 1000 Size of matrices 1000 x 1000 naive multiplication single core 9.48 sec Multiplication multi-core 6.23 sec Multiplication multi-core 6.23 sec Multiplication multi-core 6.24 sec Multiplication multi-core 6.23 sec Multiplication multi-core 6.24 sec 	 The parent process and child process are distinguished by the process identifier (PID). The code segment is explicitly linked with the new process. Regardless how a new process is created, the most important is the relation to the parent process execution and memory access. Does the parent process stops its execution till the end of the child process? Is the memory shared by the child and parent processes? Granularity of the processes – parallelism ranging from the level of the instructions to the parallelism of programs. 	<pre>parend Statement S₁ are S_n executed in parallel. Execution of the main program is interrupted until all statements S₁ to S_n are terminated. Statement S₁ are S_n executed in parallel. Example - doparallel for i = 1 to n doparalel { for j = 1 to n do { c[i,j] = 0; for k = 1 to n do { c[i,j] = c[i,j] + a[i,k]*b[k,j]; } } Parallel execution of the outer loop over all i. Jan Faigl 2024 PRG - Lecture 07. Parallel Programming 23 / 63 23 / 63 23 / 63</pre>
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Parallelism – Procedure Level	Parallelism – Program (Process) Level	Example - fork()
 A procedure is coupled with the execution process. procedure P; 	 A new process can be only a whole program. A new program is created by a system call, which creates a complete copy of itself 	<pre>1 #define NUMPROCS 4 2 for (int i = 0; i < NUMPROCS; +++i) { 3 pid_t t pid = fork(); 4 if (pid == 0) { 5 compute(i, n); 6 ext(t0); </pre>
$\begin{array}{l} \dot{PID} \ x_{pid} = newprocess(P); \\ \dots \\ killprocess(x_{nid}); \end{array}$	including all variable and data at the moment of the call. Example - Creating a copy of the process by fork system call	7 }else { 8 printf("Child %d created\n", pid); Clang demo-fork.c &k ./s.out 9 } 10 Child 2045 created Process prid 0 start computing
 P is a procedure and x_{pid} is a process identifier. Assignment of the procedure/function to the process at the declaration PID x_{pid} process (P). The process is created at the creation of the variable x. The process is terminated at the end of x or sooner. 	<pre>1 if (fork() == 0) { 2 /* code executed by the child process */ 3 } else { 4 /* code executed by the parent process */ 5 } E.g., fork() in C</pre>	11 printf("All processes created\n"); Process myid 1 start computing 12 for (int i = 0; i < NUMPROCS; ++i) {
Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 24 / 63	Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 25 / 63	21 J lec07/demo-fork.c wait for pid 2052 return: 0 Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 26 / 63
Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using CPU (optional) Semaphore E. W. Dijkstra – semaphore is a mechanism to synchronize parallel processes with shared memory. Semaphore is an integer variable with the following operations. InitSem - initialization. Wait If S > 0 then S ← S − 1 (resources are available, in this case, acquire one). Otherwise suspend execution of the calling process (wait for S become S > 0). Signal If there is a waiting process, awake it (let the process acquire one resource). Otherwise increase value of S by one, i.e., S ← S + 1 (release one resource). Semaphores can be used to control access to shared resource. S < 0 - shared resource is in use. The process areleases the resource. S > 0 - shared resource is available. The process releases the resource. The value of the semaphore can represent the number of available resources. Then, we can acquire (or wait for) k resources – wait(k): S ← S - k for S > k, and also releases k resources – signal(k): S ← S + k.	Introduction Parallel Processing Semaphores Shared Memory Message Parallel Computing using GPU (optional) Semaphores Implementation • <td>Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using CPU (optional) Usage of Semaphores Semaphores can be utilized for defining a critical sections. Critical sections is a part of the program where exclusive access to the shared memory (resources) must be guaranteed. Example of critical section protected by a semaphore InitSem(S,1); Wait(S); /* code of the critical section */ Signal(S); Synchronization of the processes using semaphores. Example of synchronization of processes. /* process p */ / signal(S); exit(); Process p waits for termination of the process q. Jan Faigl, 2024 </td>	Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using CPU (optional) Usage of Semaphores Semaphores can be utilized for defining a critical sections. Critical sections is a part of the program where exclusive access to the shared memory (resources) must be guaranteed. Example of critical section protected by a semaphore InitSem(S,1); Wait(S); /* code of the critical section */ Signal(S); Synchronization of the processes using semaphores. Example of synchronization of processes. /* process p */ / signal(S); exit(); Process p waits for termination of the process q. Jan Faigl, 2024

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Example – Semaphore 1/4 (System Calls)	Example – Semaphore 2/4 (Synchronization Protocol)	Example – Semaphore 3/4 (Primary Process)
Semaphore is an entity of the Operating System (OS).	 Example when the main (primary) process waits for two other processes (secondary) become ready. 	<pre>1 int main(int argc, char* argv[]) 2 { 3 struct sembuf sem[2]; // structure for semaphore atomic operations 4 int id = semget(1000, 1, IPC_CREAT 0666); // create semaphore</pre>
<pre>1 #include <sys types.h=""></sys></pre>	 Primary process suspend the execution and waits for two other secondary processes become ready. 	5 if (id != -1) {
2 #include <sys ipc.h=""></sys>	 Secondary processes then wait to be released by the primary process. 	<pre> int r = semctl(id, 0, SETVAL, 0) == 0; sem[0].sem_num = 0; // operation to acquire semaphore </pre>
<pre>3 #include <sys sem.h=""></sys></pre>	 Proposed synchronization "protocol". 	<pre>9 sem[0].sem_op = -2; // once its value will be >= 2</pre>
<pre>* * * * * * * * * * * * * * * * * * *</pre>	 Define our way to synchronize the processes using the system semaphores. 	in sem[0].sem_flg = 0; // representing two secondary processes are ready sem[1].sem_num = 0; // the next operation in the atomic set
<pre>int semget(key_t key, int nsems, int flag);</pre>	 Secondary process increments semaphore by 1. 	<pre>13 sem[1].sem_op = 2; // of operations increases the value of</pre>
<pre>8 /* atomic array of operations on a set of semphores */</pre>	 Secondary process waits the semaphore become 0 and then it is terminated. 	<pre>sem[1].sem_flg = 0; // the semaphore about 2 fig printf("Wait for semvalue >= 2\n");</pre>
⁹ int semop(int semid, struct sembuf *array, size_t nops);	Primary process waits for two secondary processes and decrements the semaphore about 2	<pre>17 r = semop(id, sem, 2); // perform all operations atomically</pre>
<pre>10</pre>	 It must also ensure the semaphore value is not 0; otherwise secondary processes would be 	<pre>18 printf("Press ENTER to set semaphore to 0\n"); 19 getchar();</pre>
<pre>in /* control operations on a st of semaphores */ i2 int semctl(int semid, int semnum, int cmd,);</pre>	terminated prematurely.	20 r = semctl(id, 0, SETVAL, 0) == 0; // set the value of semaphore 21 r = semctl(id, 0, IPC_RMID, 0) == 0; // remove the semaphore
in bomoti (in bomia, in bomiam, in oma,),	We need to use the atomic operations with the semaphore.	22 }
	<pre>lec07/sem-primary.c lec07/sem-secondary.c</pre>	23 return 0; 24 } lec07/sem-primary.c
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Example – Semaphore 4/4 (Secondary Process)	Issues with Semaphores	Shared Memory
<pre>1 int main(int argc, char* argv[]) 2 {</pre>		Labeled part of the memory accessible from different processes.
<pre>3 struct sembuf sem; 4 int id = semget(1000, 1, 0);</pre>	The main issues are arising from a wrong usage.	OS service provided by system calls.
5 int r; 6 if (id != -1) {	 Typical mistakes are as follows. 	Example of System Calls
7 sem.sem_num = 0; // add the secondary process	 Wrongly identified a critical section. 	
<pre>8 sem.sem_op = 1; // to the "pool" of resources 9 sem.sem_flg = 0;</pre>	 Process may block by multiple calls of Wait(S). E.g., the deadlock issues may arise from situations like. 	<pre>1 /* obtain a shared memory identifier */ 2 int shmget(key_t key, size_t size, int flag);</pre>
<pre>10 printf("Increase semafore value (add resource)\n"); 11 r = semop(id, &sem, 1);</pre>		4 /* attach shared memory */
12 sem.sem_op = 0;	Example – Deadlock	<pre>g void* shmat(int shmid, const void *addr, int flag);</pre>
<pre>13 printf("Semaphore value is %d\n", semctl(id, 0, GETVAL, 0)); 14 printf("Wait for semaphore value 0\n");</pre>	/* process 1*/ /* process 2*/	<pre>7 /* detach shared memory */ 8 int shmdt(const void *addr);</pre>
<pre>15 r = semop(id, &sem, 1); 16 printf("Done\n");</pre>	Wait(S1); Wait(S2); Wait(S2); Wait(S1);	10 /* shared memory control */
17 }	Signal (S2); Signal (S1);	<pre>int shmctl(int shmid, int cmd, struct shmid_ds *buf);</pre>
<pre>18 return 0; 19 } lec07/sem-secondary.c</pre>	Signal(S1); Signal(S2);	OS manages information about usage of shared memory.
The IPC entities can be listed by ipcs.		 OS also manages permissions and access rights.
clang sem-primary.c -o sem-primary clang sem-secondary.c -o sem-secondary		
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Example – Shared Memory 1/4 (Write)	Example – Shared Memory 2/4 (Read)	Example – Shared Memory 3/4 (Demo)
Write a line read from stdin to the shared memory.	Read a line from the shared memory and put it to the stdout.	1. Use shm-write to write a text string to the shared memory.
1 #include <sys types.h=""></sys>	<pre>1 #include <sys types.h=""></sys></pre>	c ,
2 #include <sys ipc.h=""> 3 #include <sys shm.h=""></sys></sys>	2 #include <sys shm.h=""> 2 #include <stdio.h></stdio.h></sys>	2. Use shm-read to read data (string) from the shared memory.
g #include <stdio.h></stdio.h>	#define SIZE 512	3. Remove shared memory segment.
<pre>9 #define SIZE 512 8 int main(int argc, char *argv[])</pre>	<pre>7 int main(int argc, char *argv[]) 8 {</pre>	ipcrm -M 1000 4. Try to read data from the shared memory.
9 {	9 int id;	 If y clang -o shm-write shm-write.c % clang -o shm-read shm-read.c
11 int id;	<pre>10 char *buf; 11 if ((id = shmget(1000, 512, 0)) != -1) {</pre>	2 % ./shm-write sim-write 2 % ./shm-read 3 Hello! I like programming in C! 3 mem:Hello! I like programming in C!
<pre>12 if ((id = shmget(1000, SIZE, IPC_CREAT 0666)) != -1) { 13 if ((buf = (char*)shmat(id, 0, 0))) { </pre>	<pre>12 if ((buf = (char*)shmat(id, 0, 0))) { 13 printf("mem:%s\n", buf);</pre>	5 % ./shm-read
14 fgets(buf, SIZE, stdin);	14 }	<pre>9 mem:Hello! I like programming in C!</pre>
15 shmdt(buf); 16 }	<pre>15 shmdt(buf); 16 } else {</pre>	8 % ipcrm -M 1000 9 % ./shm-read
17 }	<pre>17 fprintf(stderr, "Cannot access to shared memory!\n");</pre>	10 Cannot access to shared memory!
<pre>18 return 0; 19 } lec07/shm-write.c</pre>	18 } 19 return 0; lec07/shm-read.c	lec07/shm-write.c lec07/shm-read.c
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Example – Shared Memory 4/4 (Status)	Sensing Messages and Queues of Messages	Example – Messages Passing 1/4 (Synchronization, Primary)
	Processes can communicate via messages send/received to/from system messages queues.	Two processes are synchronized using messages.
	 Queues are entities of the OS with defined system calls. 	1. The primary process waits for the message from the secondary process
A list of accesses to the shared memory using ipcs command.	Example of System Calls	 The primary process informs secondary to solve the task. The secondary process informs primary about the solution.
1 after creating shared memory segment and before writing the text	1 #include <sys types.h=""></sys>	 The secondary process miorns primary about the solution. The primary process sends message about termination.
2 m 65539 1000rw-rw jf jf jf jf 1 512 1239 1239 22:18:48 no-entry 22:18:48	2 #include <sys ipc.h=""> 3 #include <sys msg.h=""></sys></sys>	
4 after writing the text to the shared memory	<pre>5 /* Create a new message queue */ 6 int msgget(key_t key, int msgflg);</pre>	Example of Master Process 1/2
5 m 65539 1000rw-rw- jf jf jf jf 0 6 512 1239 1239 22:18:48 22:19:37 22:18:48	8 /* Send a message to the queue block/non-block (IPC_NOWAIT) */	1 struct msgbuf { 2 long mtype;
7 after reading the text 8 m 65539 1000rw-rw- jf jf jf jf 0	<pre>9 int msgsnd(int msqid, const void *msgp, size_t msgsz, int msgflg); 10</pre>	<pre>3 char mtext[SIZE]; 4 };</pre>
512 1239 1260 22:20:07 22:2 ⁰ :07 2 ² :18:4 ⁸	11 /* Receive message from the queue block/non-block (IPC_NOWAIT) */ 12 int msgrcv(int msqid, void *msgp, size_t msgsz, long msgtyp, int msgflg);	<pre>6 int main(int argc, char *argv[]) 7 {</pre>
	13 14 /* Control operations (e.g., destroy) the message queue */	<pre>s struct msgbuf msg; int id = msgget(KEY, IPC_CREAT 0666);</pre>
	<pre>15 int msgctl(int msqid, int cmd, struct msqid_ds *buf);</pre>	10 int r; 11 if (id != -1) {
	Another message passing system can be implemented by a user library, e.g., using network communication.	
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Example – Messages Passing 2/4 (Primary)	Example – Messages Passing 3/4 (Secondary)	Example – Messages Passing 4/4 (Demo)
Example of Primary Process 2/2	1 int main(int argc, char *argv[])	1. Execute the primary process.
<pre>1 msg.mtype = 3; //type must be > 0</pre>	2 { 3 4 msg.mtype = 3;	2. Execute the secondary process.
<pre>2 printf("Wait for other process \n"); 3 r = msgrcv(id, &msg, SIZE, 3, 0);</pre>	<pre>5 printf("Inform main process\n");</pre>	3. Perform the computation.
<pre>4 printf("Press ENTER to send work\n"); 5 getchar();</pre>	<pre>6 strcpy(msg.mtext, "I'm here, ready to work"); 7 r = msgsnd(id, &msg, sizeof(msg.mtext), 0);</pre>	4. Remove the created message queue identified by the msgid. #define KEY 1000 ipcrm -Q 1000
6 strcpy(msg.mtext, "Do work"):	<pre>8 printf("Wait for work\n"); 9 r = msgrcv(id, &msg, sizeof(msg.mtext), 4, 0);</pre>	1 % clang msg-primary.c -o primary 1 % clang msg-secondary.c -o secondary
<pre>msg.mtype = 4; //work msg is type 4 r = msgsnd(id, &msg, sizeof(msg.mtext), 0);</pre>	<pre>10 printf("Received message:%s\n", msg.mtext); 11 for (i = 0; i < 4; i++) {</pre>	2 % ./primary 2 % ./secondary 3 Wait for other process 3 Inform main process
<pre>9 fprintf(stderr, "msgsnd r:%d\n",r); 10 printf("Wait for receive work results\n",r);</pre>	12 sleep(1); 13 printf(".");	4 Worker msg received, press ENTER to send 5 work msg 5 Received message:Do work
<pre>11 msg.mtype = 5; 12 r = msgrcv(id, &msg, sizeof(msg.mtext), 5, 0);</pre>	<pre>14 fflush(stdout); 15 } //do something useful</pre>	6 msgsnd r:0 7 Wait for receive work results 8 Received message: I'm going to wait for 8 Wait for exit msg
<pre>printf("Received message:%s\n", msg.mtext); printf("Press ENTER to send exit msg\n");</pre>	<pre>16 printf("Work done, send wait for exit\n");</pre>	exit msg exit msg a Brook EWIP to cond evit msg 10 %iDCS -0
15 getchar();	<pre>17 strcpy(msg.mtext, "Work done, wait for exit"); 18 msg.mtype = 5;</pre>	11 Message Queues: 11 %ipcrm -Q 1000 12 T ID KEY MODE OWNER GROUP
<pre>msg.mtype = EXIT_MSG; //I choose type 10 as exit msg r = msgsnd(id, &msg, 0, 0);</pre>	<pre>19 r = msgsnd(id, &msg, sizeof(msg.mtext), 0); 20 msg.mtype = 10;</pre>	12 % 11 1
18 } 19 return 0;	<pre>21 printf("Wait for exit msg\n"); 22 r = msgrcv(id, &msg, SIZE, EXIT_MSG, 0);</pre>	directory 14 % lec07/msg-primary.c lec07/msg-secondary.c
20 } lec07/msg-primary.c an Faigl, 2024 PRG - Lecture 07: Parallel Programming 45 / 63	23 printf("Exit message has been received\n"); Jan Faigl, 2024 PRG - Lecture 07: Parallel Programming 46 / 63	Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 47 / 63
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Massive parallelism using graphics cards	Computational Power (2008)	CUDA
massive parallelisht ashig Braphies cards	What is the reported processor computational power?	
	 Graphics (stream) processors. 	 NVIDIA Compute Unified Device Architecture.
Image rendering performed pixel-by-pixel can be easily parallelized.	CSX700 96 GigaFLOPs	 Extension of the C to access to the parallel computational units of the GPU.
 Graphics Processing Units (GPU) has similar (or even higher) degree of integration with the main processors (CPU). 	GeForce 8800 GTX 518 GigaFLOPs Radeon HD 4670 480 GigaFLOPs	 Computation (kernel) is executed by the GPU.
 They have huge number of parallel processors. 	Peak catalogue values.	 Kernel is performed in parallel using available computational units.
E.g., GeForce GTX 1060 \sim 1280 cores.	 Main processors : Phenom X4 9950 (@2.6 GHz) 21 GigaFLOPs 	 Host - Main processor (process).
The computational power can also be used in another applications.	Core 2 Duo E8600 (@3.3 GHz) 22 GigaFLOPs Cure 2 Quad QX9650 (@3.3 GHz) 35 GigaFLOPs	Device - GPU.
 Processing stream of data (SIMD instructions - processors). CDCDLL Consult Durates expectation on CDLL 	Cure 2 Quad QX9650 (@3.3 GHz) 35 GigaFLOPs Core i7 970 (@3.2 GHz) 42 GigaFLOPs	Data must be in the memory accessible by the GPU.
 GPGPU - General Purpose computation on GPU. http://www.gpgpu.org OpenCL (Open Computing Language) - GPGPU abstract interface. 	Test linpack 32-bit.	Host memory \rightarrow Device memory
 CUDA - Parallel programming interface for NVIDIA graphics cards. 	Is the reported power really achievable? (float vs double)	The result (of the computation) is stored in the GPU memory.
http://www.nvidia.com/object/cuda_home.html	How about other indicators?	Host memory \leftarrow Device memory
	E.g., computational power / power consumption.	
an Faigl, 2024 PRG – Lecture 07: Parallel Programming 49 / 63	CSX700 has typical power consumption around 9W. Jan Faigl. 2024 PRG – Lecture 07: Parallel Programming 50 / 63	Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 51 / 63

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 CUDA – Computational Model Kernel (computation) is divided into blocks. Each block represent a parallel computation of the part of the result. <i>E.g., a part of the matrix multiplication.</i> Each block consists of computational threads. Parallel computations are synchronization within the block. Blocks are organized into the grid. Scalability is realized by dividing the computation into blocks. Blocks may not be necessarily computed in parallel. Based on the available number of parallel units, particular blocks can be computed sequentially. 	CUDA – Grid, Blocks, Threads, and Memory Access	 CUDA – Example – Matrix Multiplication 1/8 NVIDIA CUDA SDK - Version 2.0, matrixMul. Simple matrix multiplication. C = A · B, Matrices have identical dimensions n × n, where n is the multiple of the block size. Comparison naive implementation in C (3× for loop), naive implementation. Hardware CPU - Intel Core 2 Duo @ 3 GHz, 4 GB RAM, GPU - NVIDIA G84 (GeForce 8600 GT), 512 MB RAM.
Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 5 Introduction Parallel Processing Shared Memory Messages Parallel Computing using GPU (op	/ 63 Jan Faigl, 2024 PRG - Lecture 07: Parallel Programming 53 / 63 onal) Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional)	Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 54 / 63 Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional)
CUDA – Example – Matrix Multiplication 2/8	CUDA – Example – Matrix Multiplication 3/8	CUDA – Example – Matrix Multiplication 4/8
<pre>Naive implementation void simple_multiply(const int n, const float *A, const float *B, float *C) { for (int i = 0; i < n; ++i) { for (int j = 0; j < n; ++j) { for (int k = 0; k < n; ++k) {</pre>	<pre>Naive implementation with transpose 1 void simple_multiply_trans(const int n, 2 const float *a, const float *b, float *c) 3 { 4 float * bT = create_matrix(n); 5 for (int i = 0; i < n; ++i) { 6 bT[i*n + i] = b[i*n + i]; 7 for (int j = i + 1; j < n; ++j) { 8 bT[i*n + j] = b[j*n + i]; 9 bT[j*n + i] = b[i*n + j]; 10 } 11 } 12 for (int i = 0; i < n; ++i) { 13 for (int j = 0; j < n; ++i) { 14 float tmp = 0; 15 for (int k = 0; k < n; ++k) { 16 tmm = a[i*n + k] * bT[j*n + k]; 17 } 18 c[i*n + j] = tmp; 19 } 10 } 10 } 11 free(bT); 12 } 12 } 13 / 14 float tmp = di / float /</pre>	 CUDA - computation strategy Divide matrices into blocks. Each block computes a single sub-matrix C_{sub}. Each thread of the individual blocks computes a single element of C_{sub}.
Jan Faigl, 2024 PRG - Lecture 07: Parallel Programming 5 Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (op	/ 63 Jan Faigl, 2024 PRG – Lecture 07: Parallel Programming 56 / 63	Jan Faigl. 2024 PRG - Lecture 07: Parallel Programming 57 / 63 Introduction Parallel Processing Semaphores Shared Memory Messages Parallel Computing using GPU (optional)
<pre>CUDA - Example - Matrix Multiplication 5/8 CUDA - Implementation - main function void cuda_multiply(const int n,</pre>	<pre>(d) // Minimum //</pre>	<pre>CUDA - Example - Matrix Multiplication 7/8 • CUDA source codes. Example - Dedicated source file cuda_func.cu 1. Declaration of the external function. extern "C" { // declaration of the external function (cuda kernel) void cuda_multiply(const int n, const float *A, const float *B, float *C); } 2. Compile the CUDA code to the C++ code. 1 nvcccuda cuda_func.cu -o cuda_func.cu.cc 3. Compilation of the cuda_func.cu.cc file using standard compiler.</pre>
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