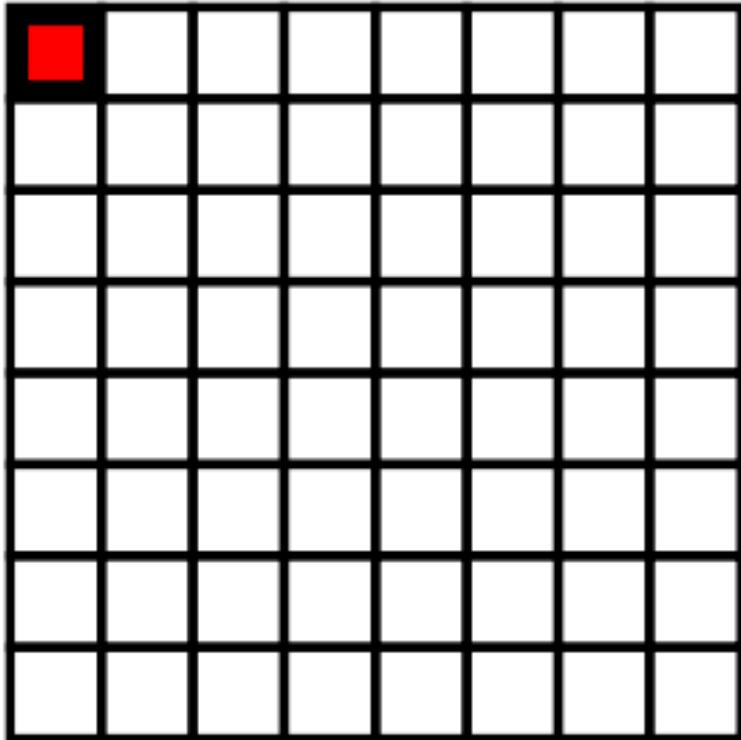
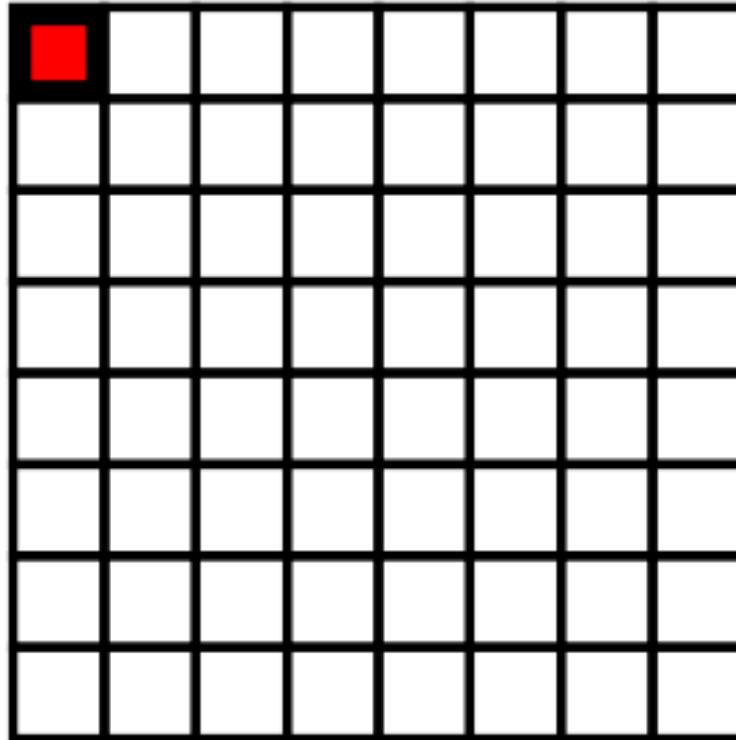


A mem:1 cache hit:0



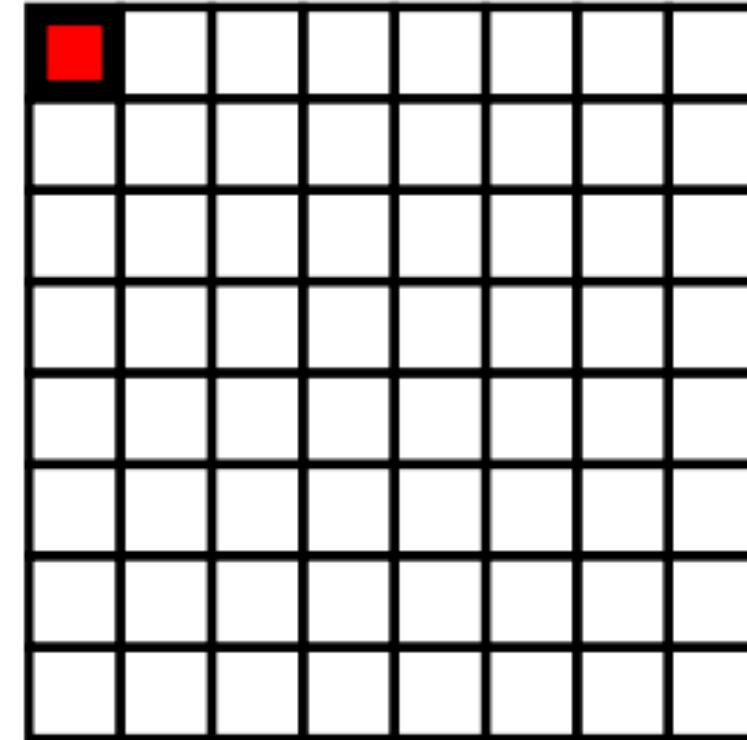
mem:3

B mem:1 cache hit:0



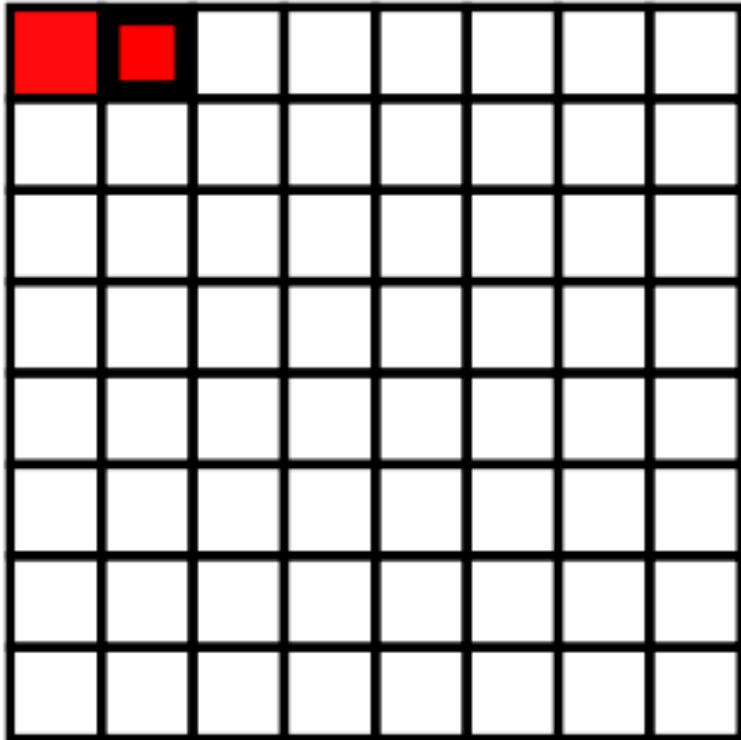
cache hits:0 $\cong 0\%$

C mem:1 cache hit:0



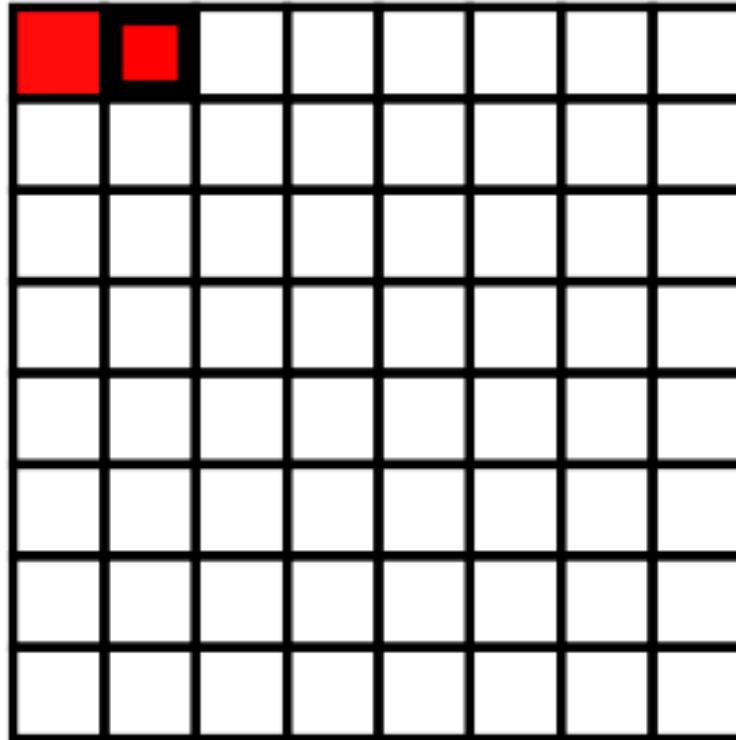
=

A mem:2 cache hit:0



mem:6

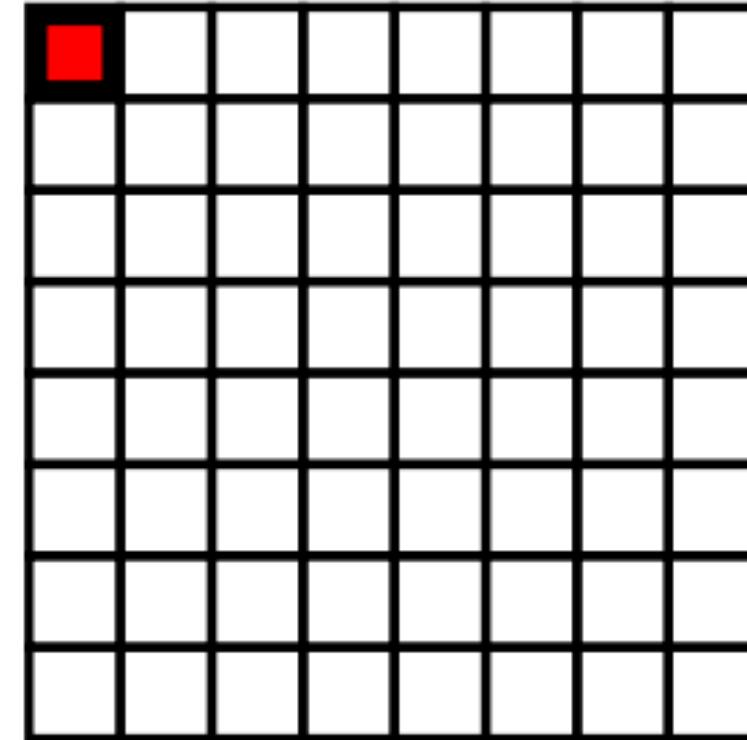
B mem:2 cache hit:0



x

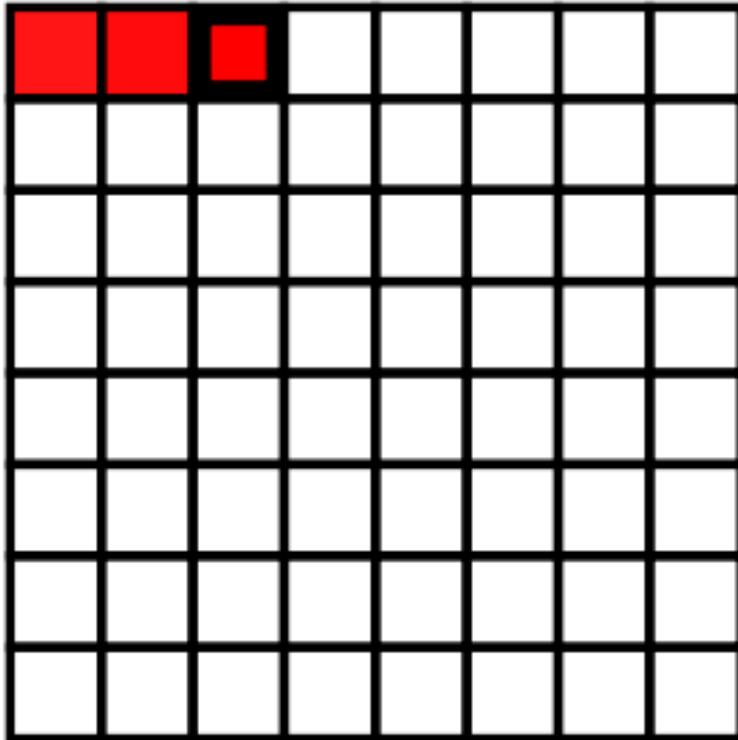
cache hits:1 $\cong 16\%$

C mem:2 cache hit:1



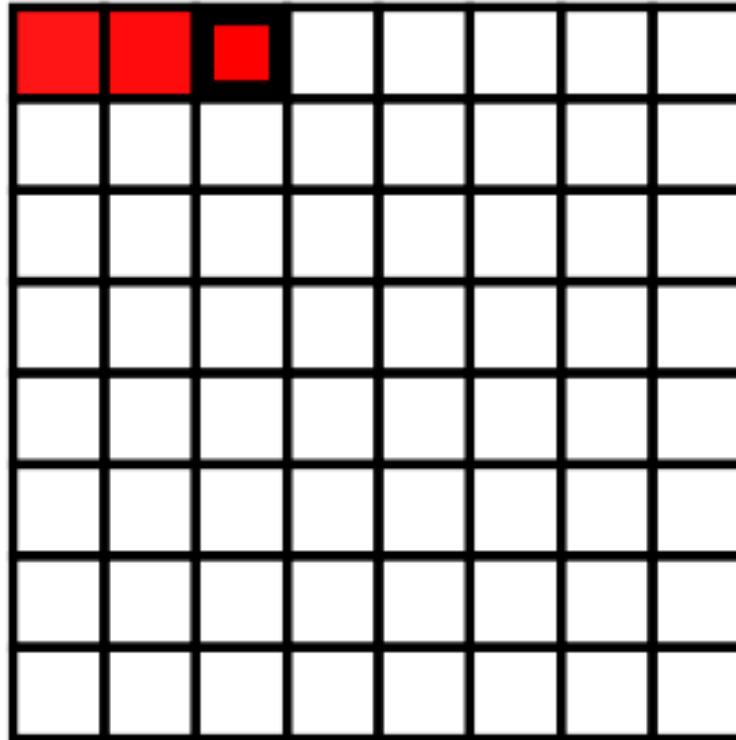
=

A mem:3 cache hit:0



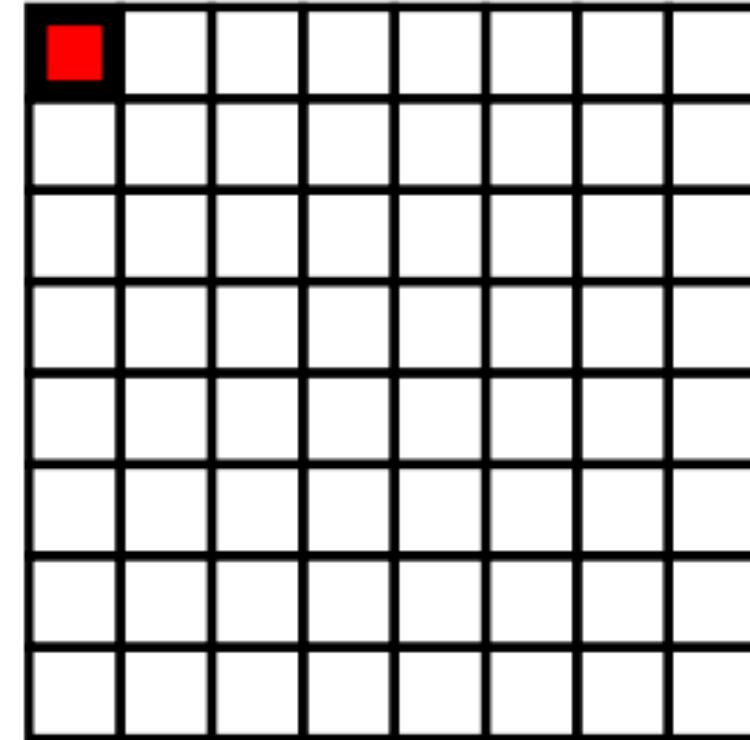
mem:9

B mem:3 cache hit:0



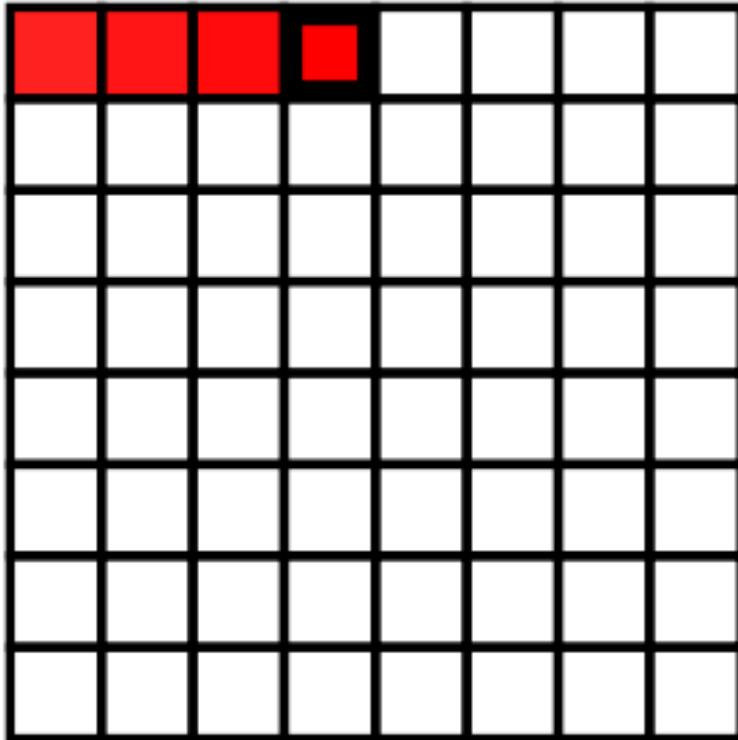
cache hits:2 $\approx 22\%$

C mem:3 cache hit:2



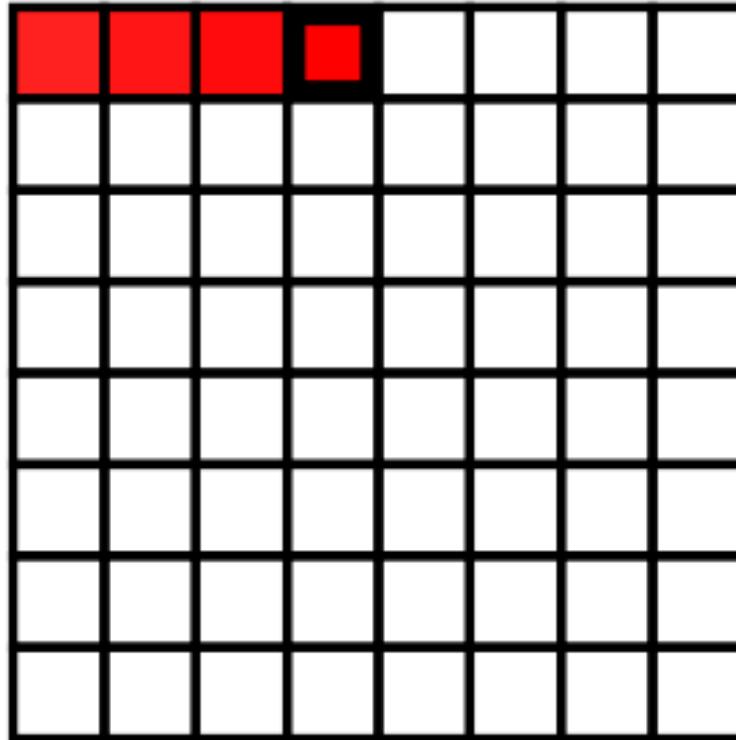
=

A mem:4 cache hit:0



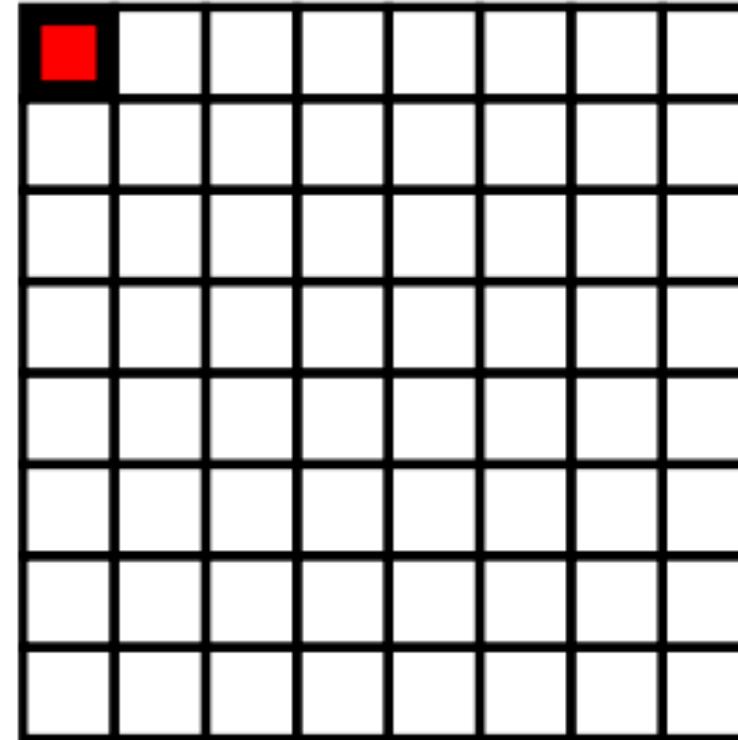
mem:12

B mem:4 cache hit:0

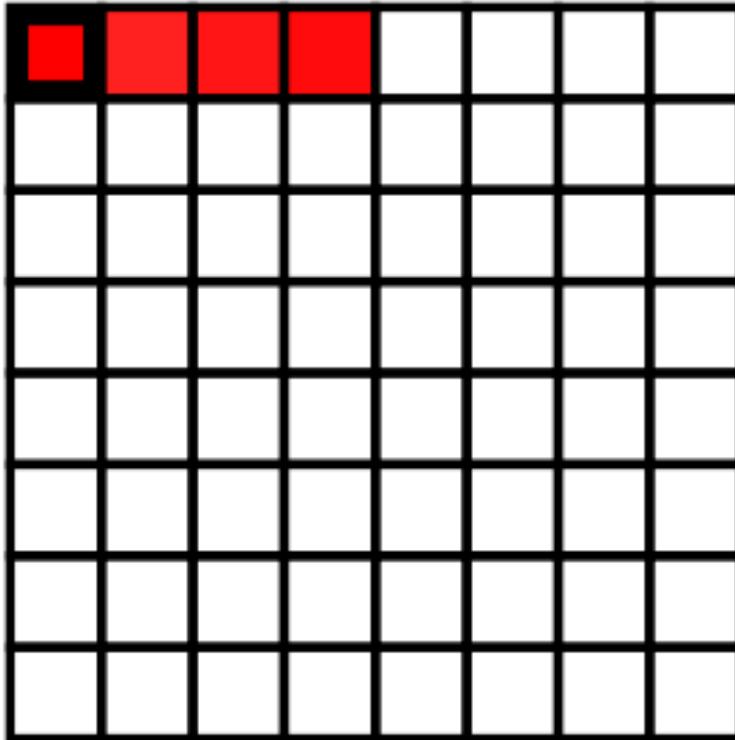


cache hits:3 $\cong 25\%$

C mem:4 cache hit:3

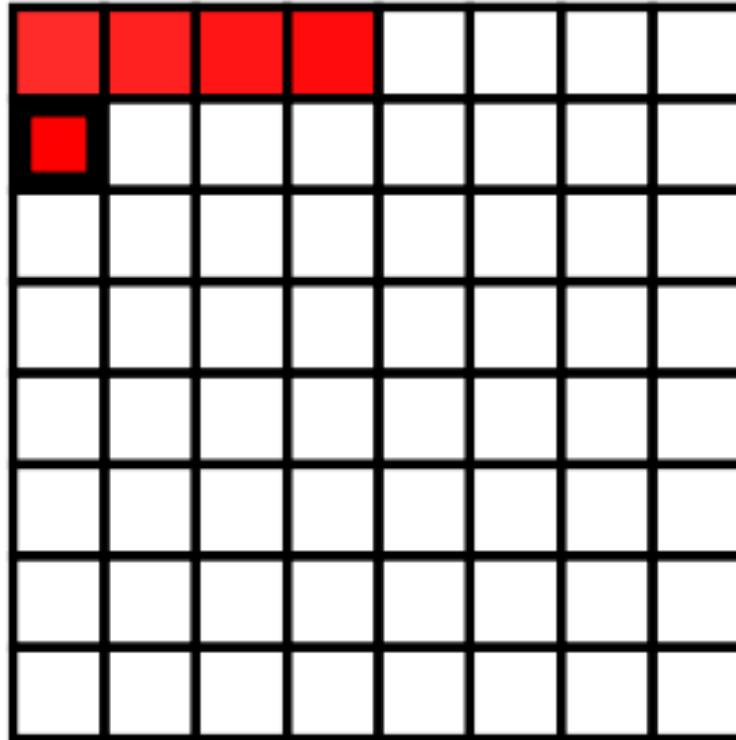


A mem:5 cache hit:1



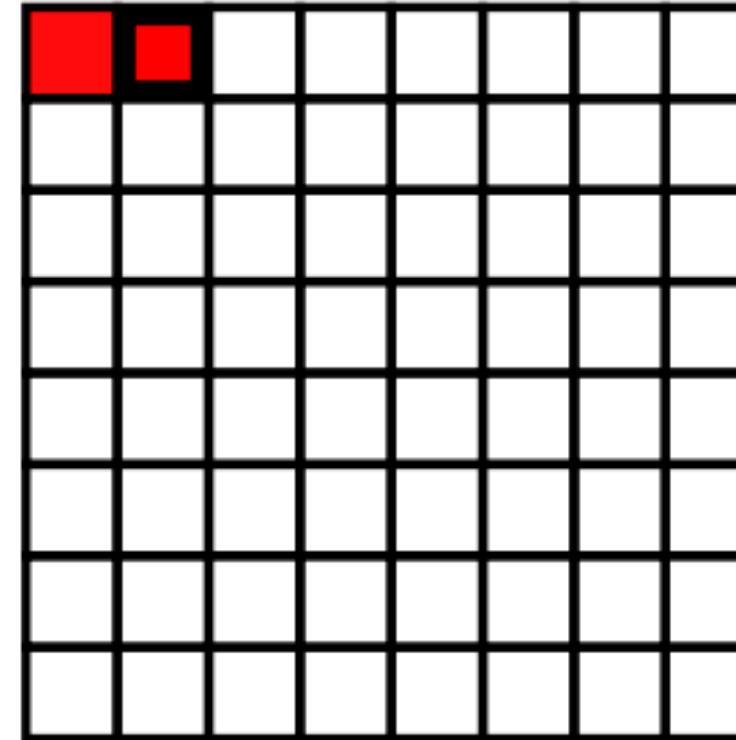
mem:15

B mem:5 cache hit:0



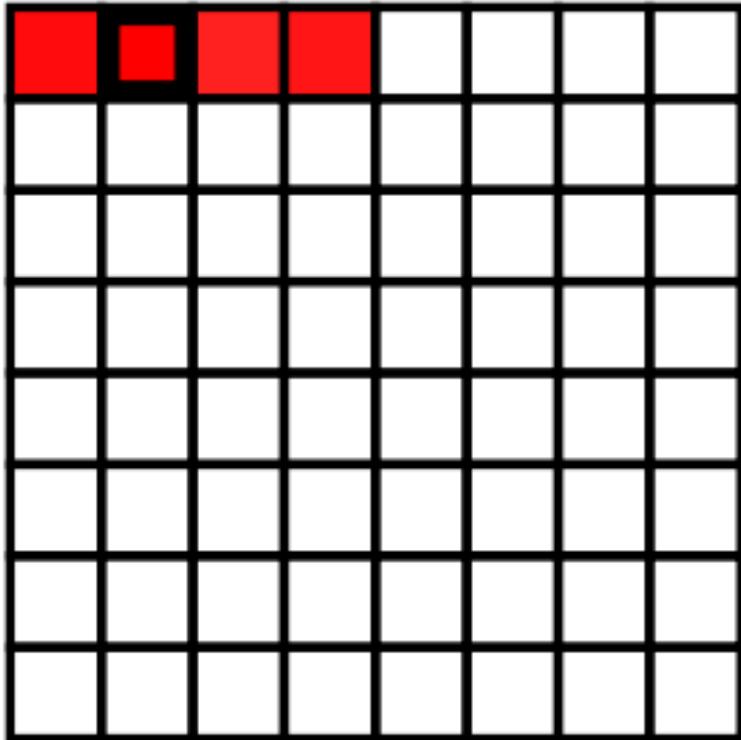
cache hits:4 ≈26%

C mem:5 cache hit:3



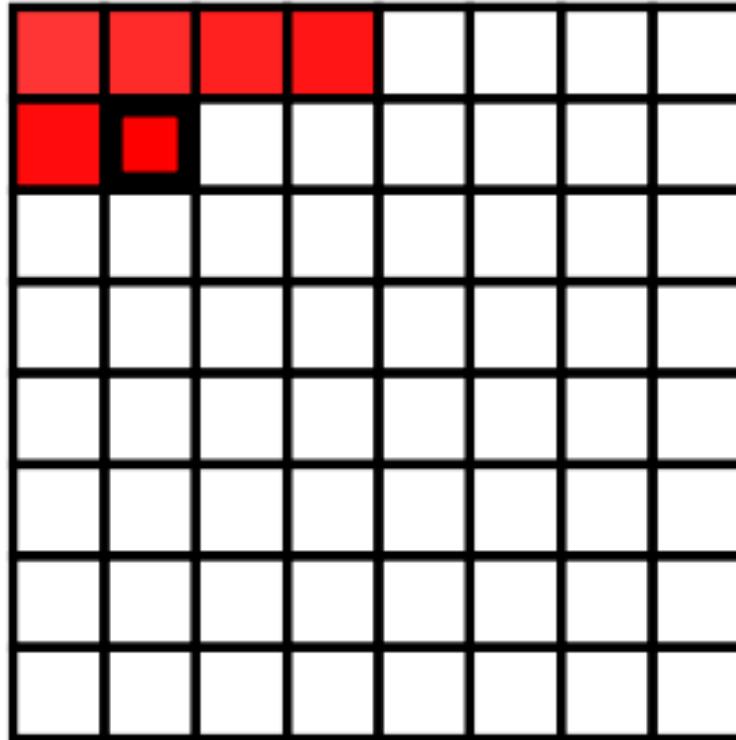
=

A mem:6 cache hit:2



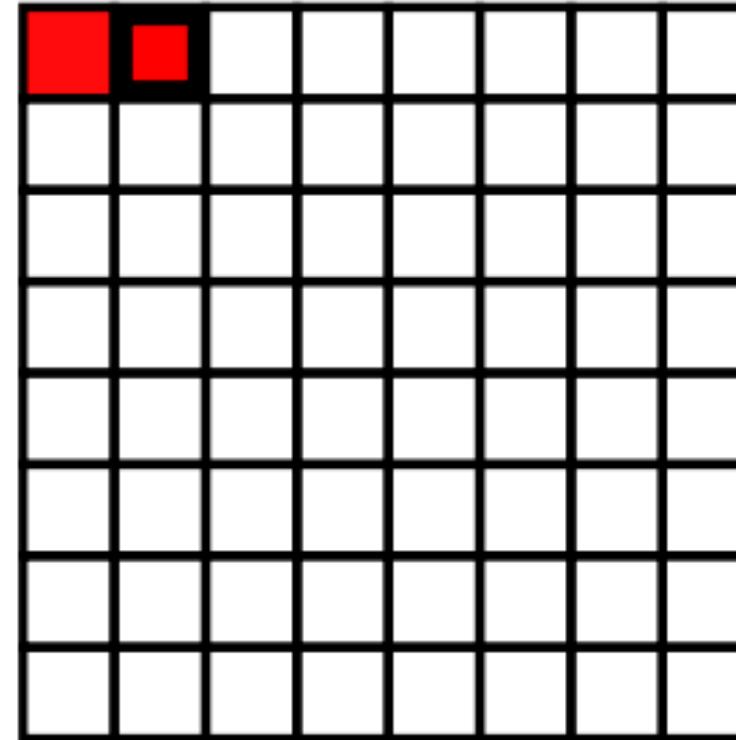
mem:18

B mem:6 cache hit:0

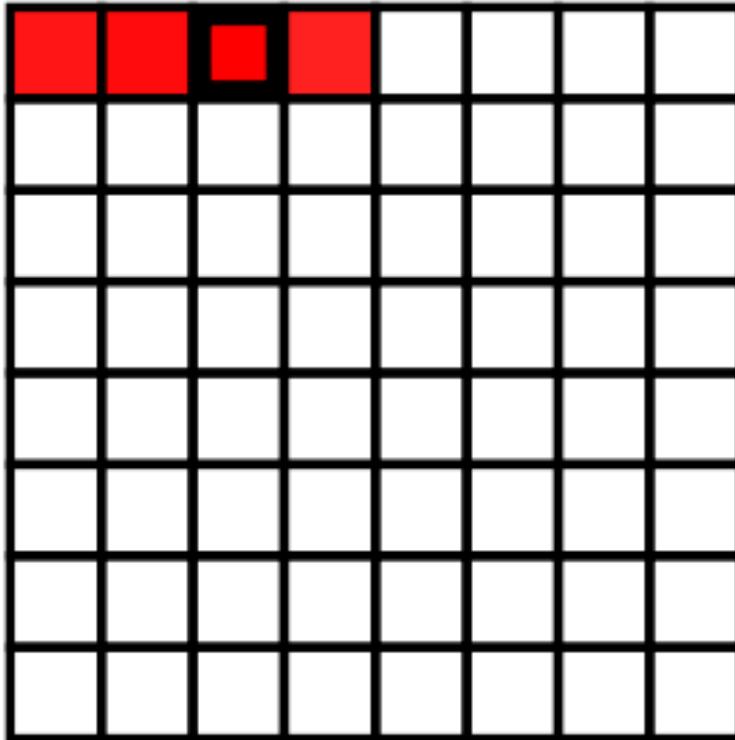


cache hits:6 $\cong 33\%$

C mem:6 cache hit:4

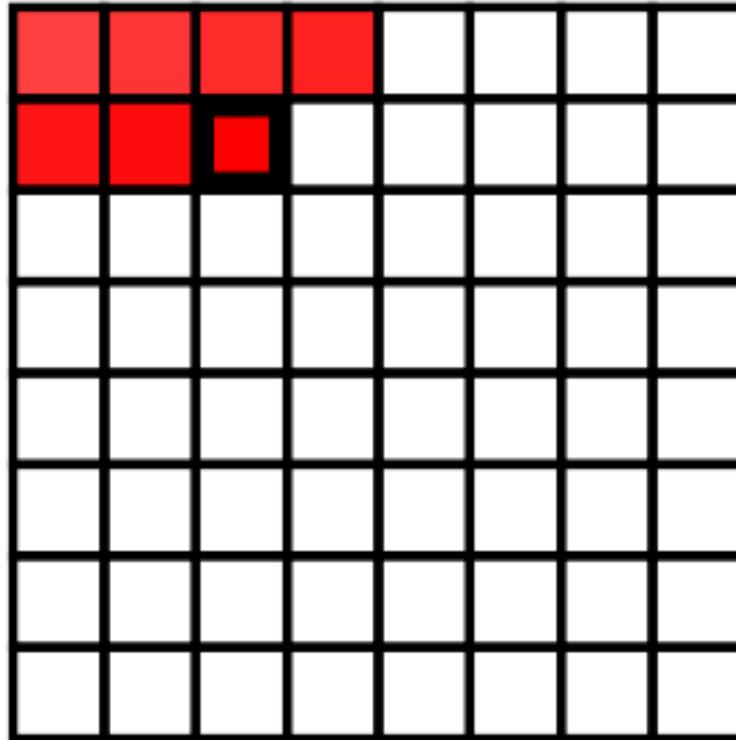


A mem:7 cache hit:3



mem:21

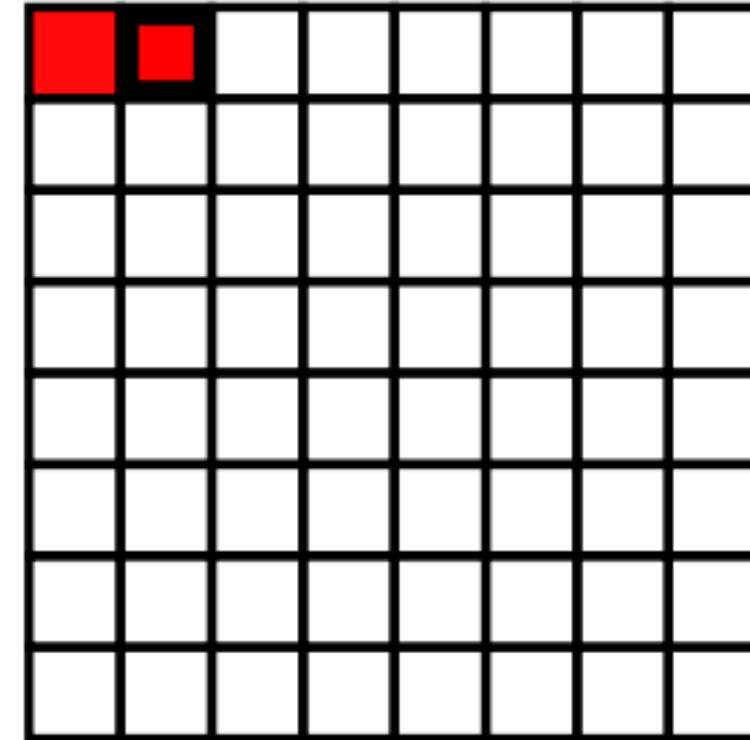
B mem:7 cache hit:0



×

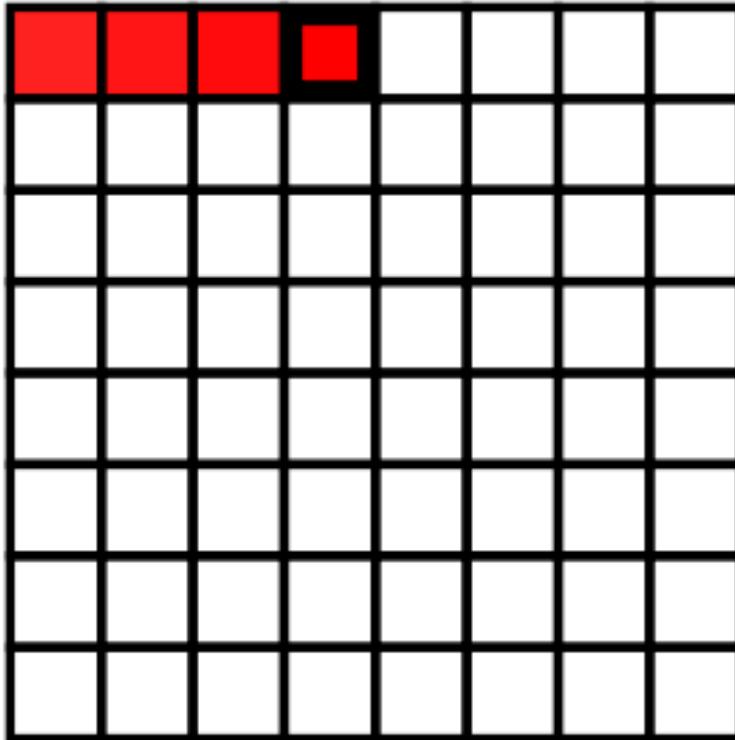
cache hits:8 $\cong 38\%$

C mem:7 cache hit:5



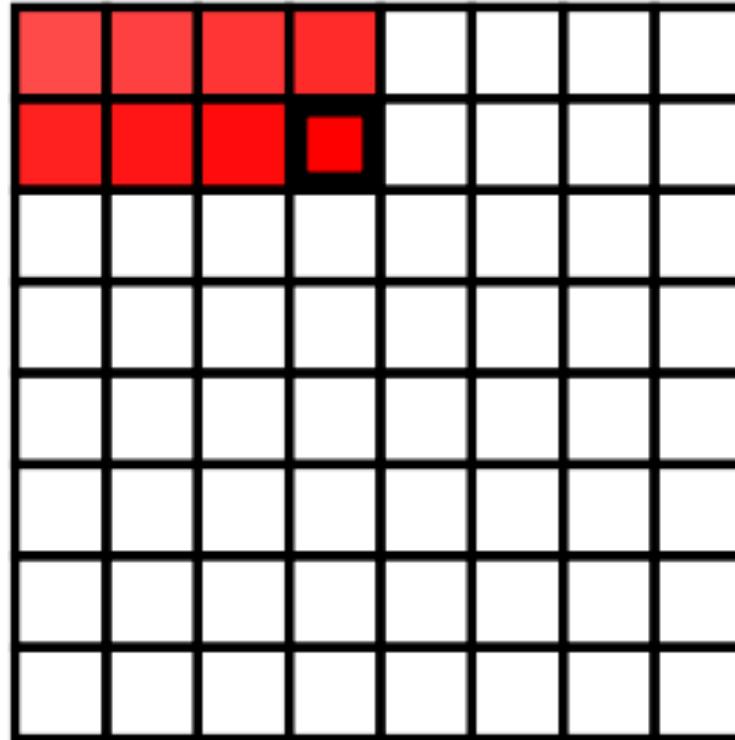
=

A mem:8 cache hit:4



mem:24

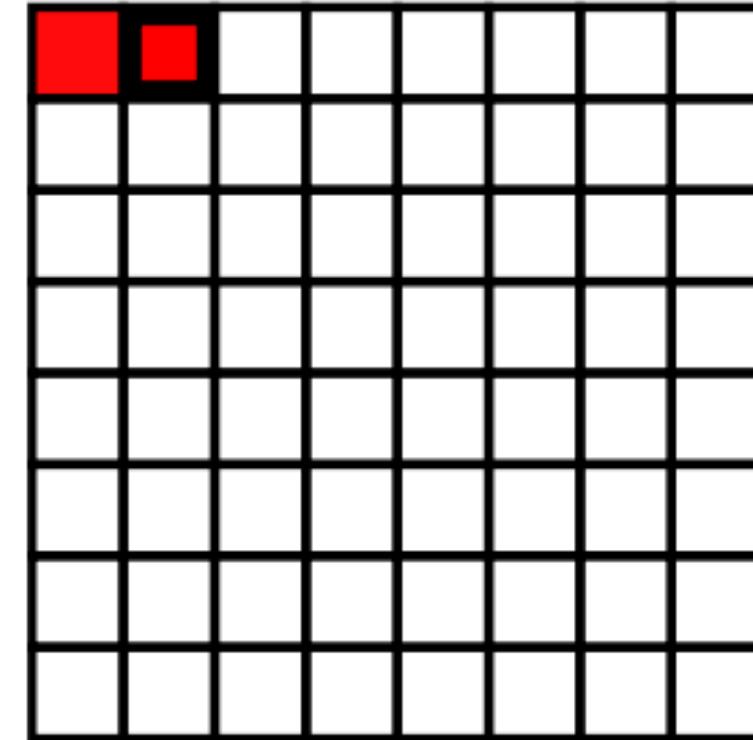
B mem:8 cache hit:0



×

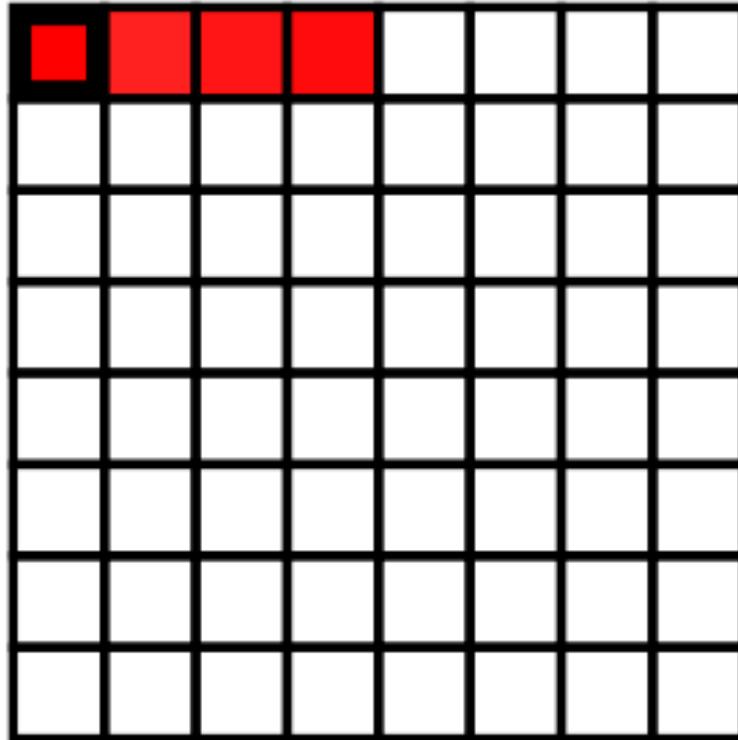
cache hits:10 ≈41%

C mem:8 cache hit:6



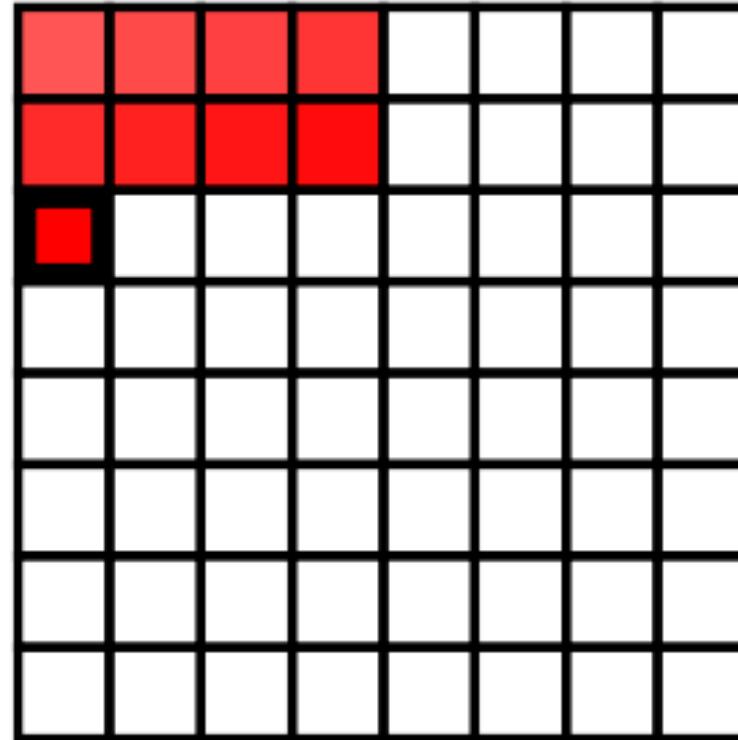
=

A mem:9 cache hit:5



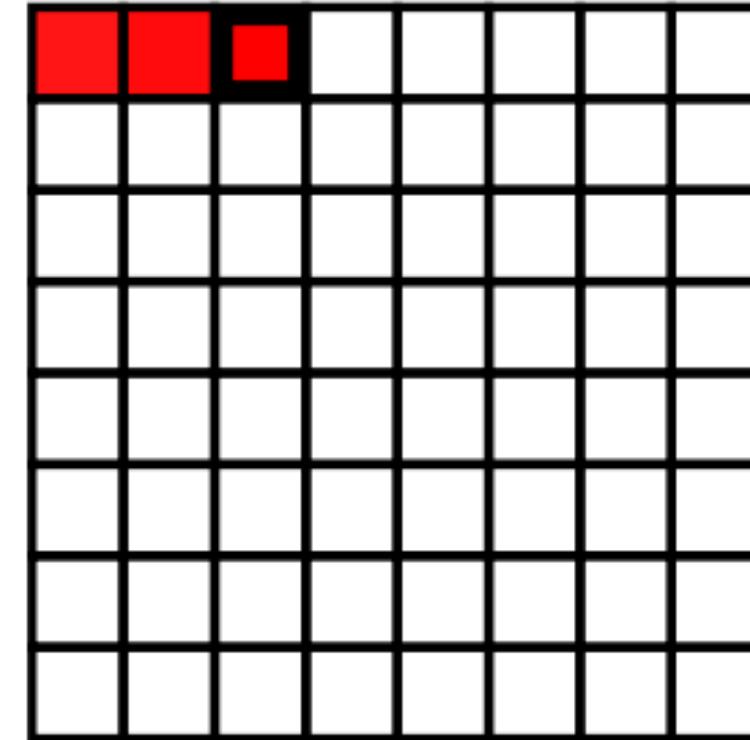
mem:27

B mem:9 cache hit:0

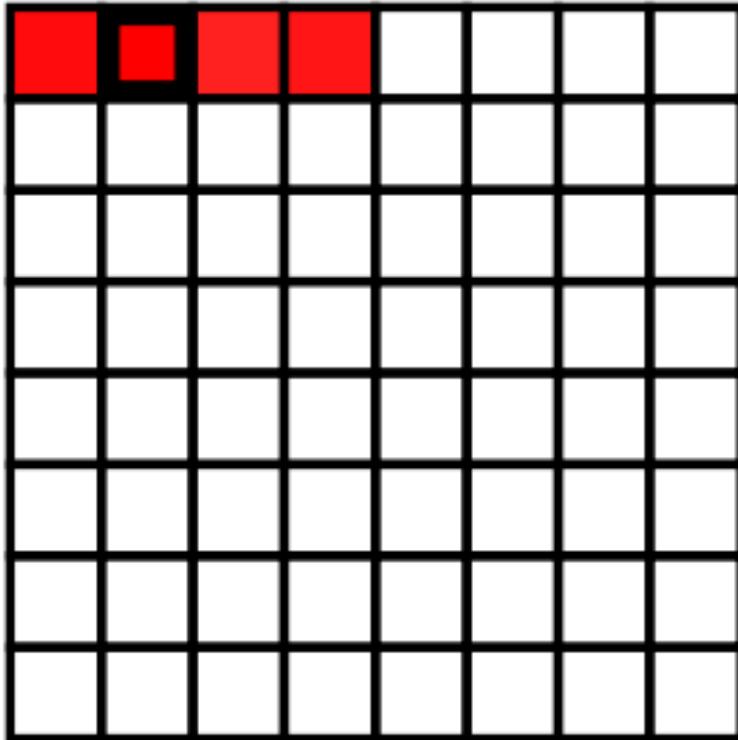


cache hits:11 ≈40%

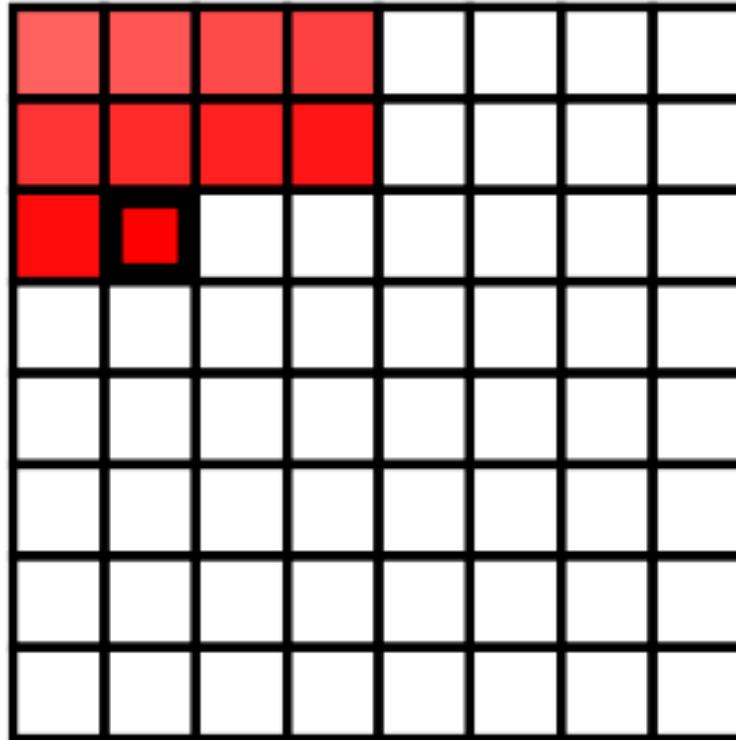
C mem:9 cache hit:6



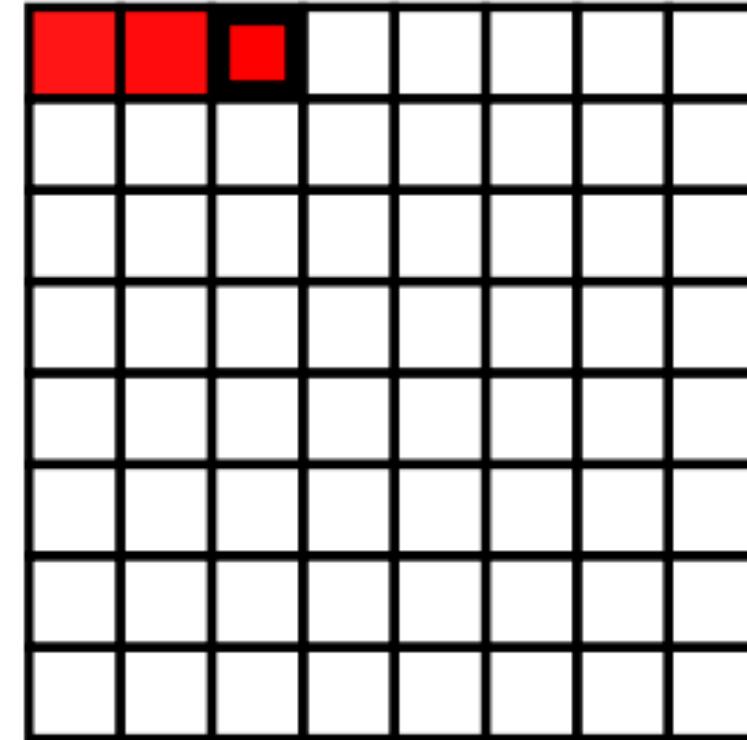
A mem:10 cache hit:6



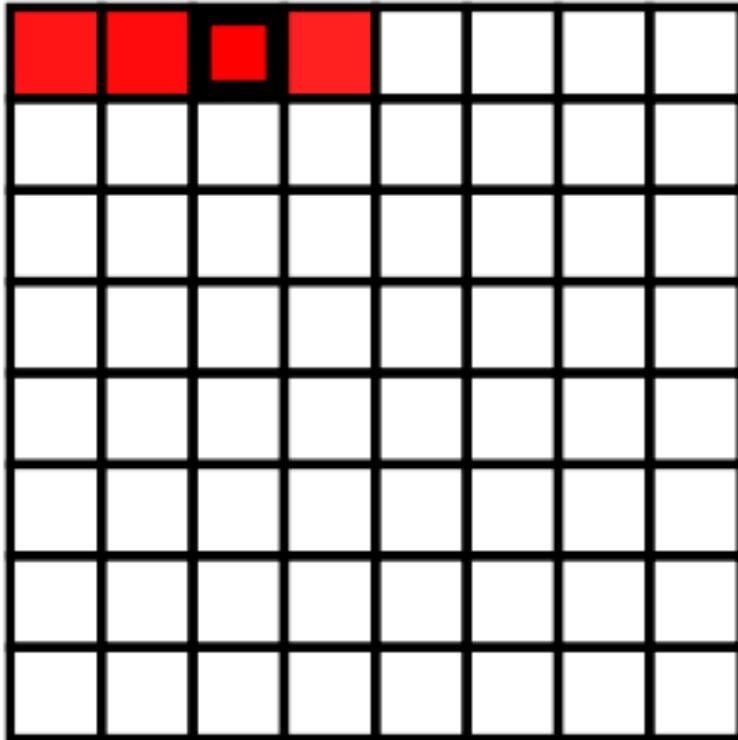
B mem:10 cache hit:0



C mem:10 cache hit:7

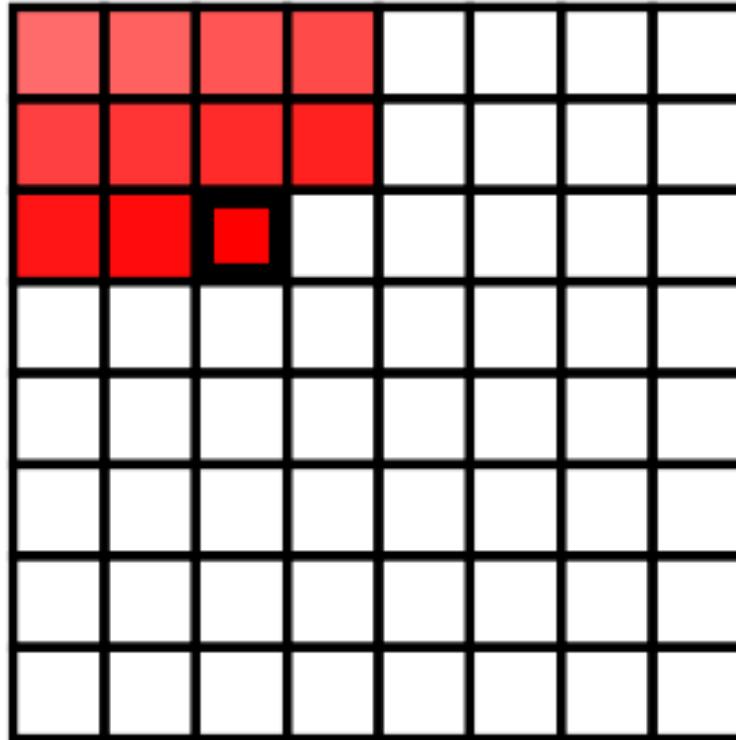


A mem:11 cache hit:7



mem:33

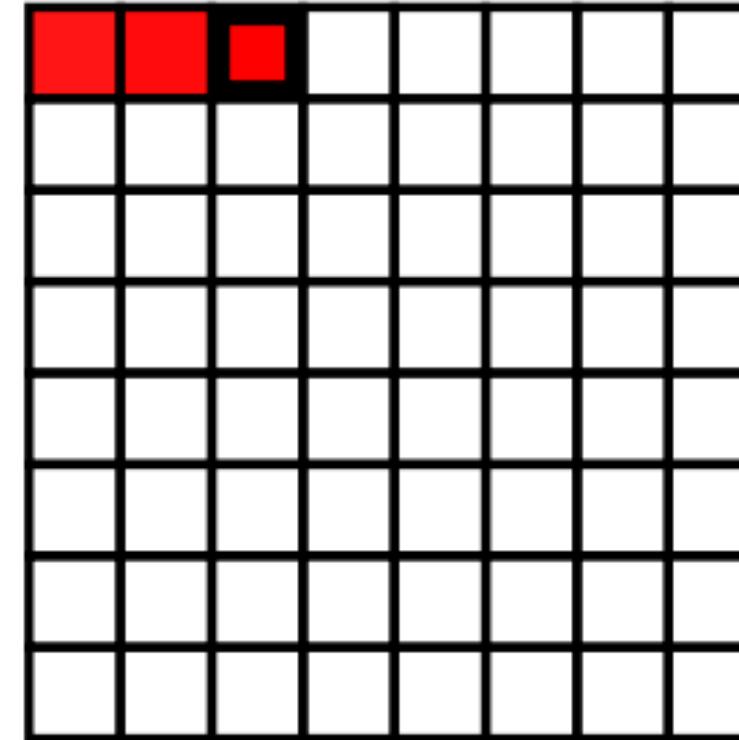
B mem:11 cache hit:0



×

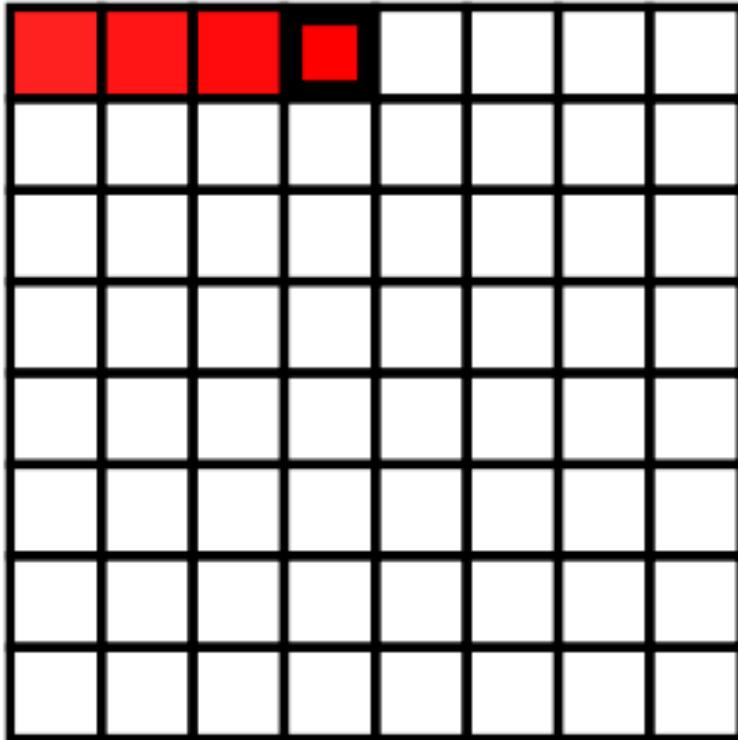
cache hits:15 ≈45%

C mem:11 cache hit:8



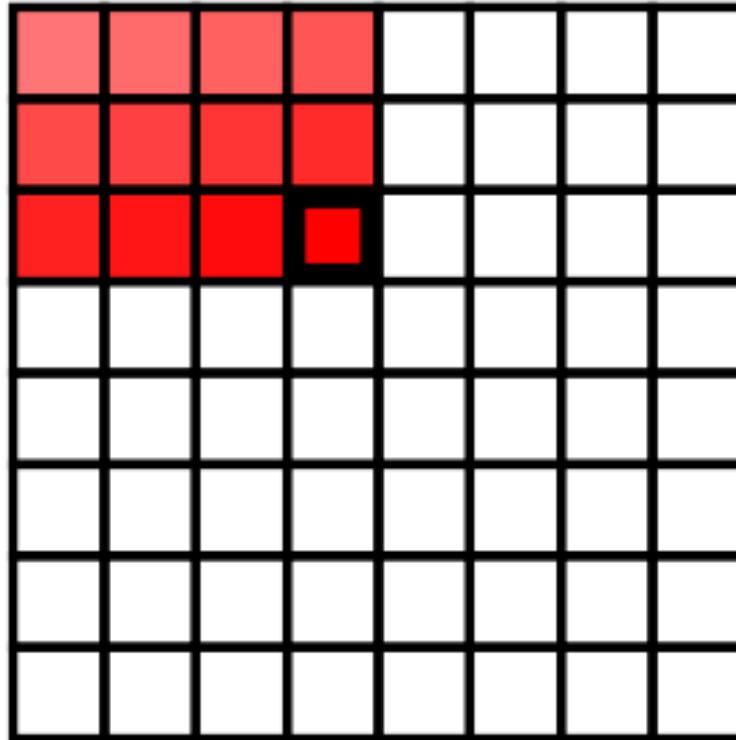
=

A mem:12 cache hit:8



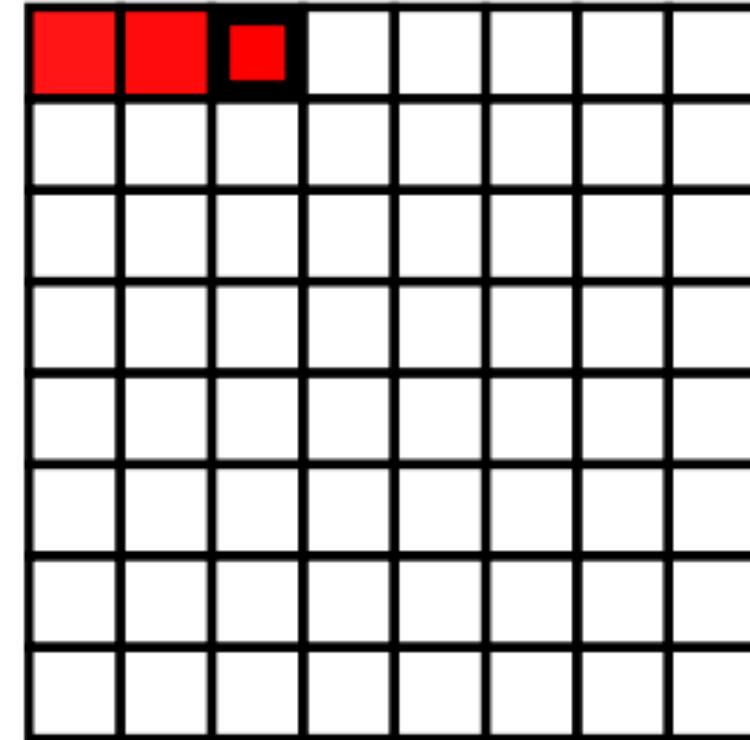
mem:36

B mem:12 cache hit:0

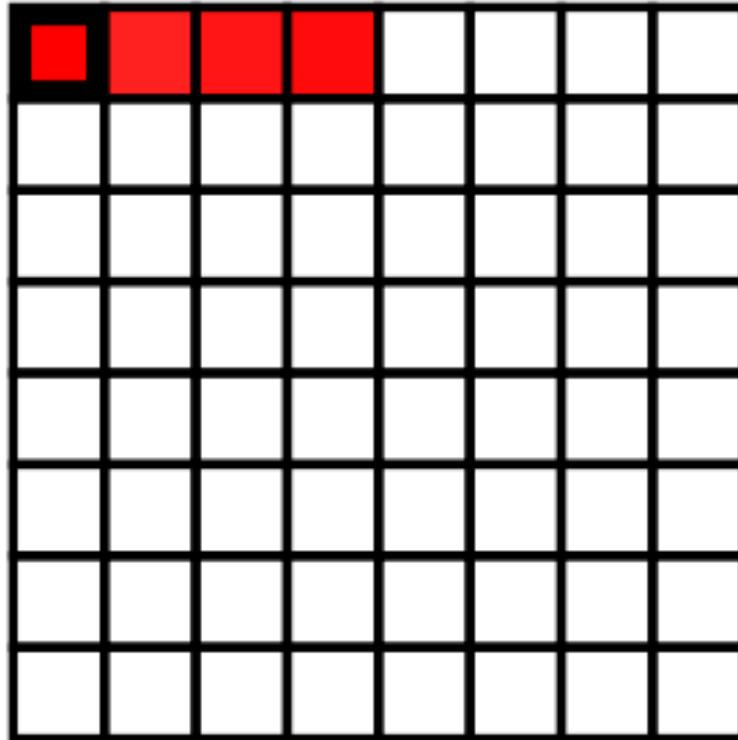


cache hits:17 ≈47%

C mem:12 cache hit:9

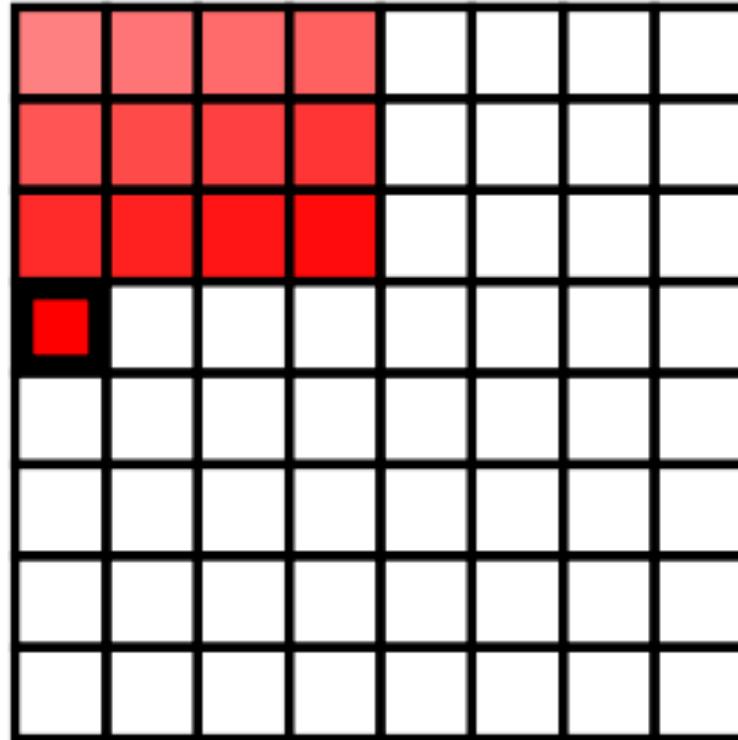


A mem:13 cache hit:9



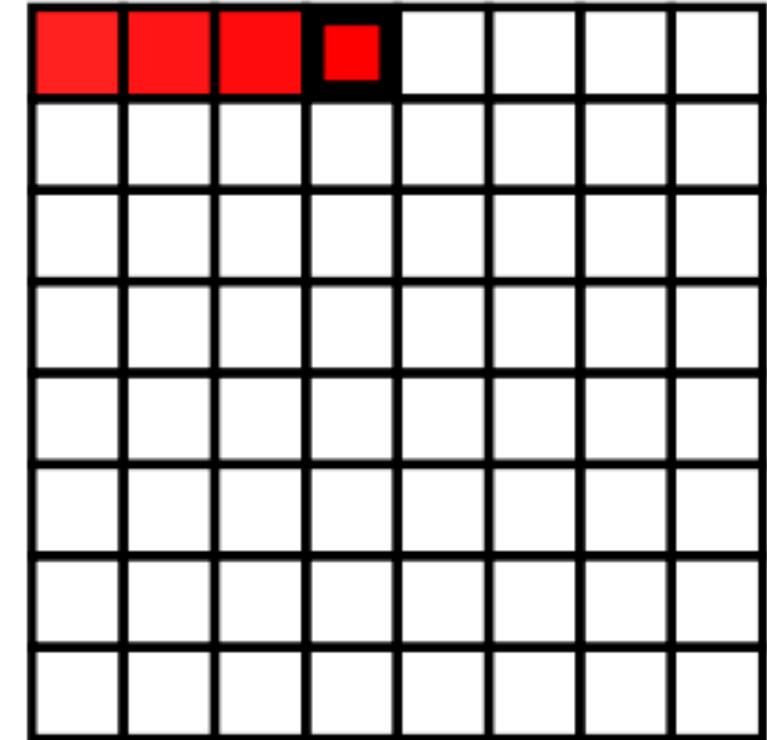
mem:39

B mem:13 cache hit:0

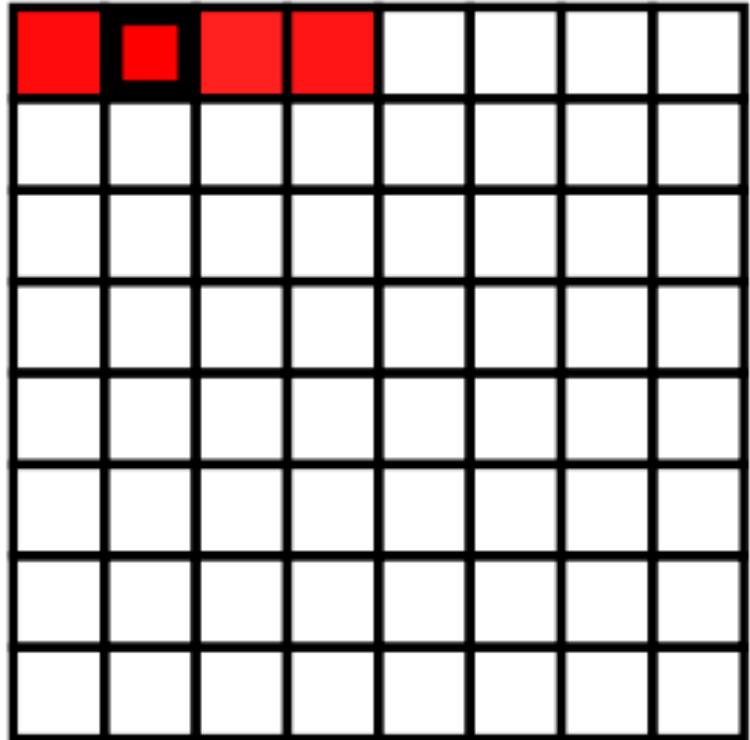


cache hits:18 ≈46%

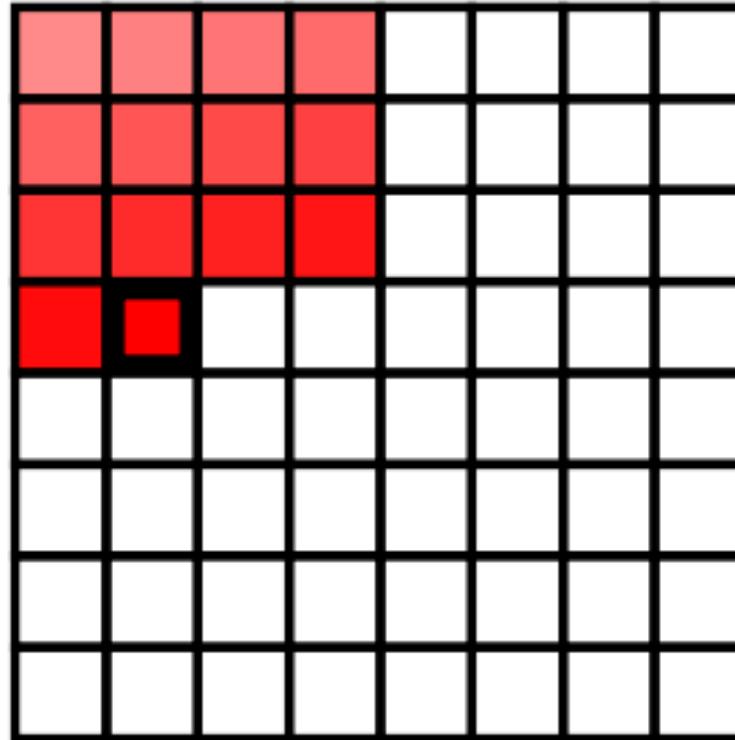
C mem:13 cache hit:9



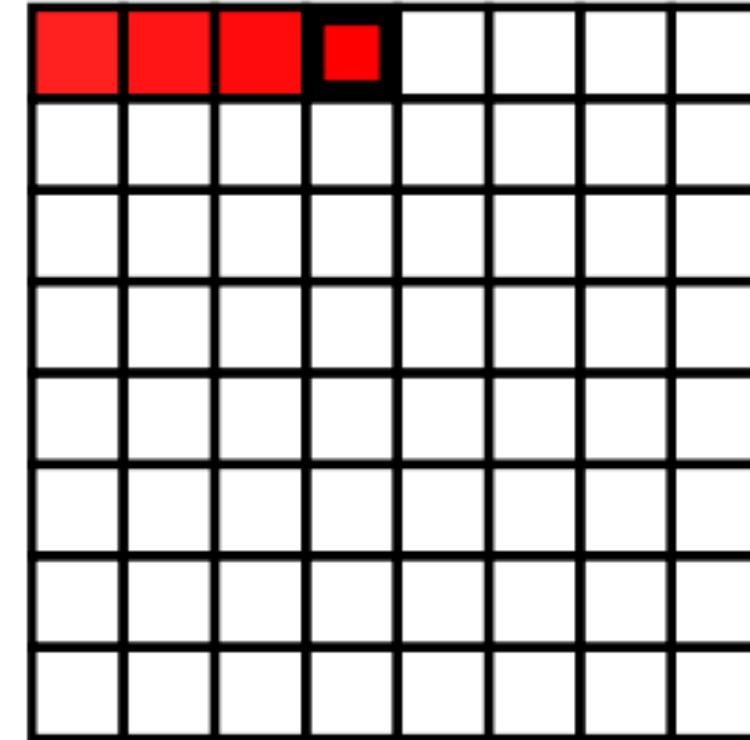
A mem:14 cache hit:10



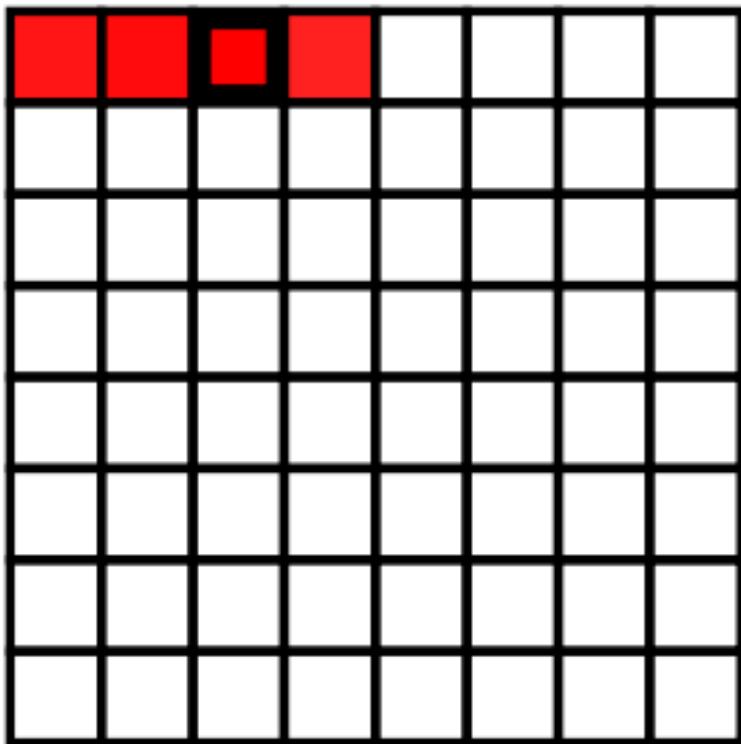
B mem:14 cache hit:0



C mem:14 cache hit:10

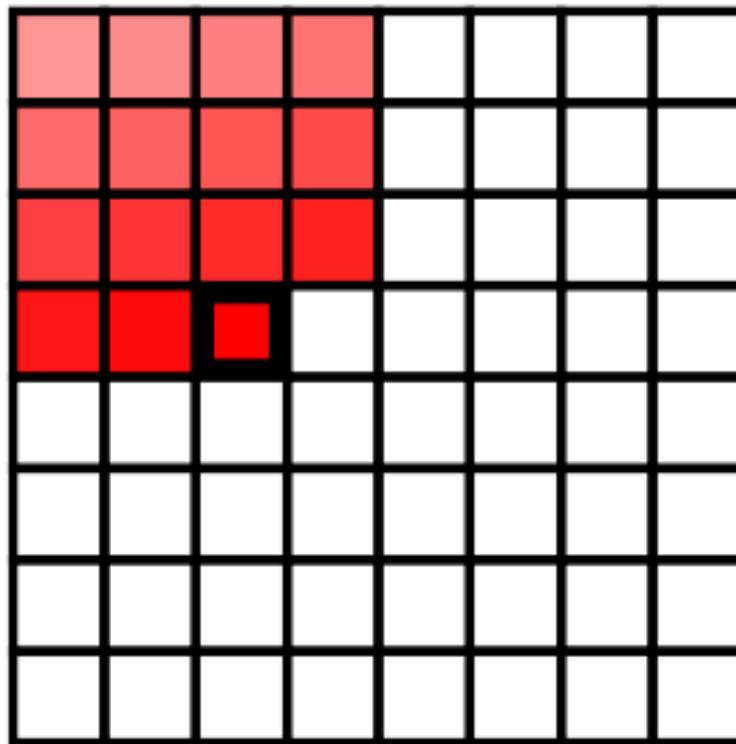


A mem:15 cache hit:11



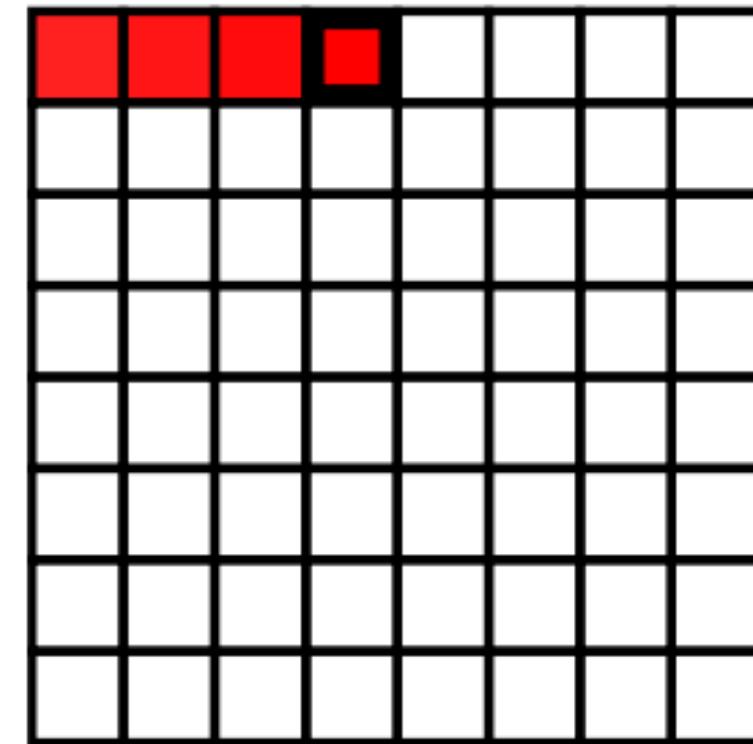
mem:45

B mem:15 cache hit:0

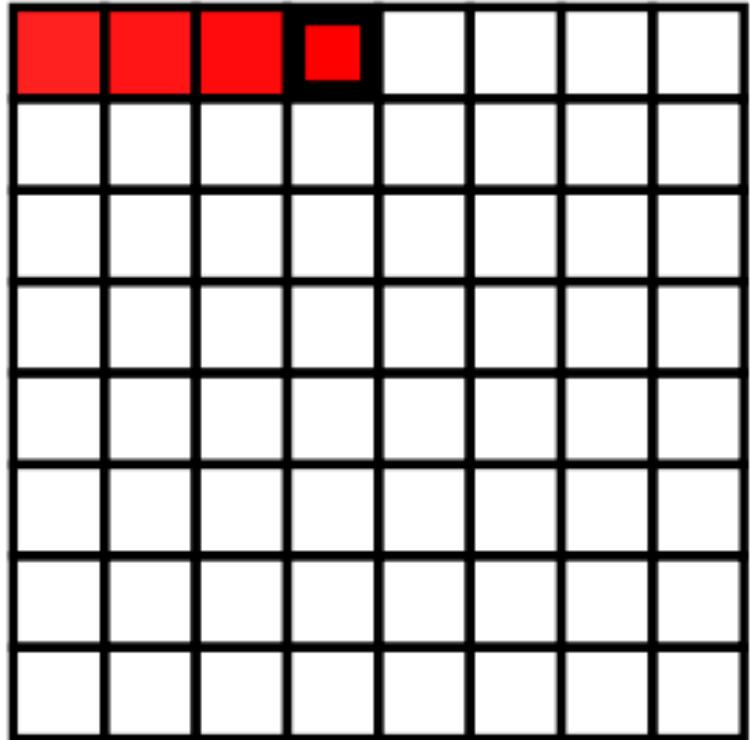


cache hits:22 ≈48%

C mem:15 cache hit:11

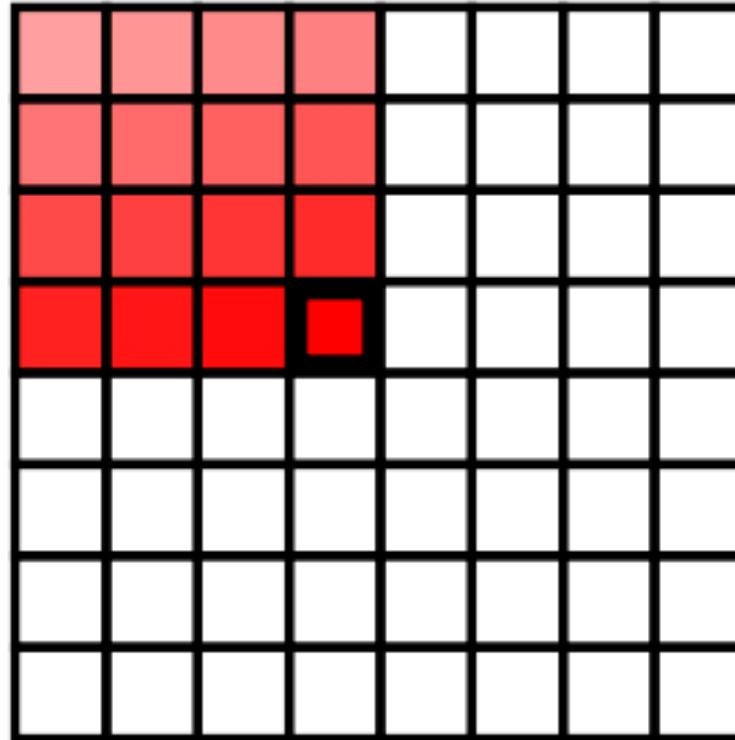


A mem:16 cache hit:12



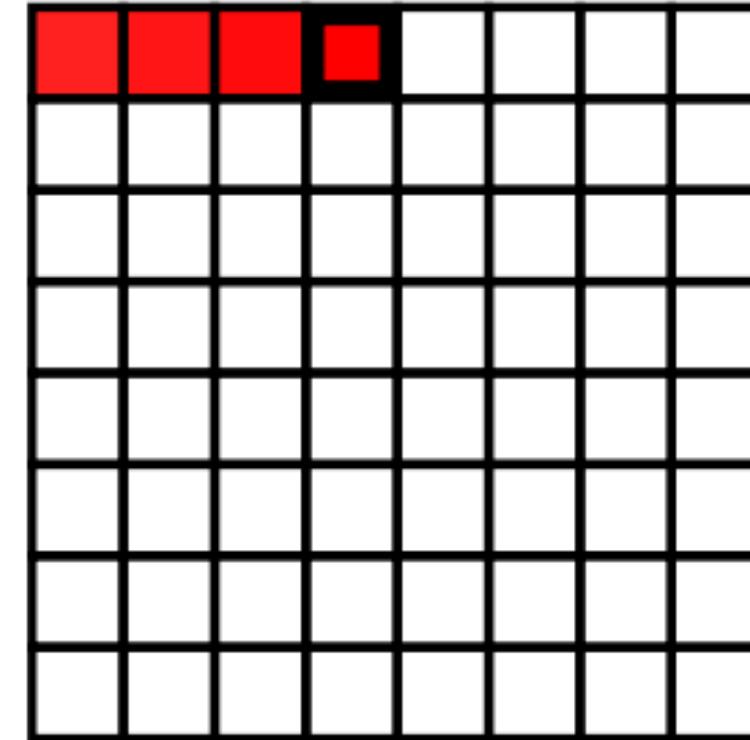
mem:48

B mem:16 cache hit:0

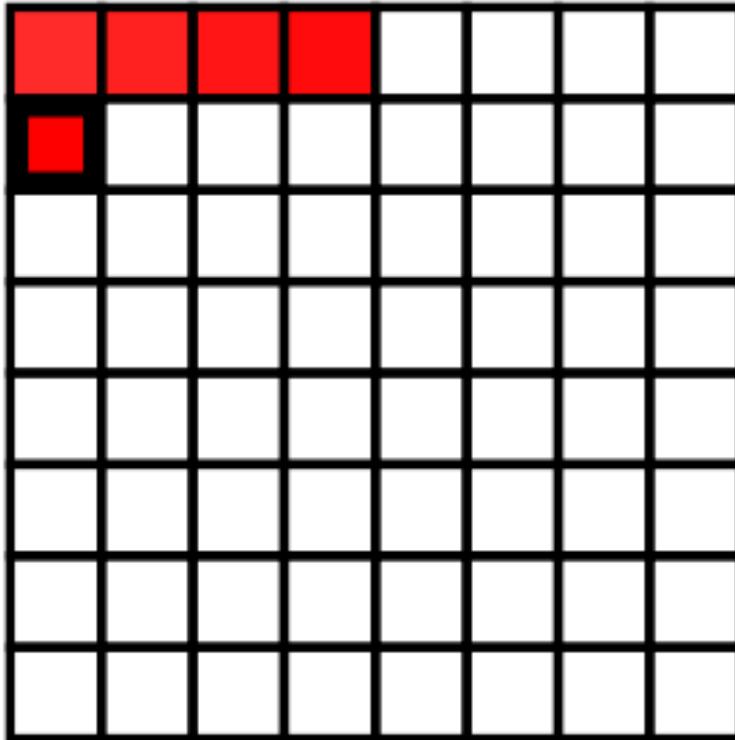


cache hits:24 ≈50%

C mem:16 cache hit:12

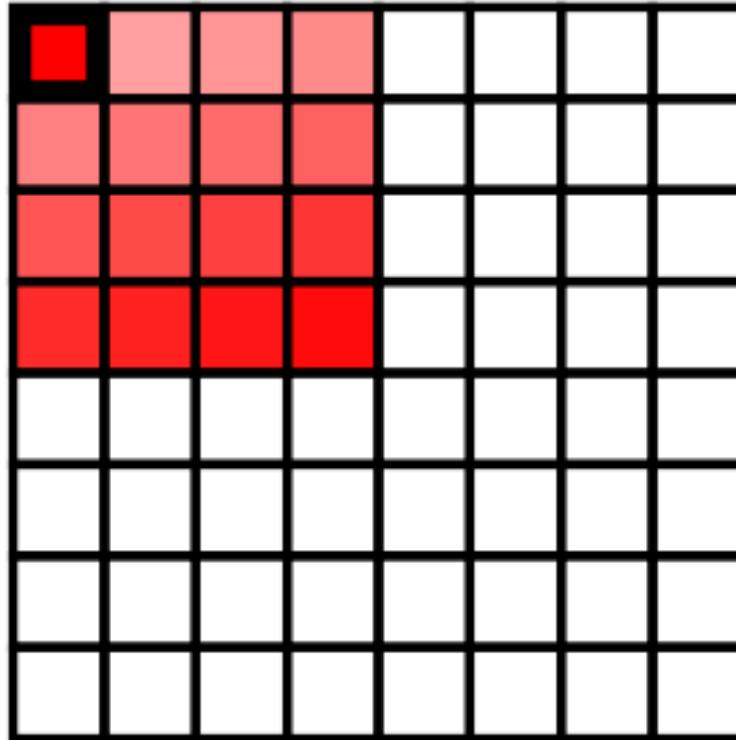


A mem:17 cache hit:12



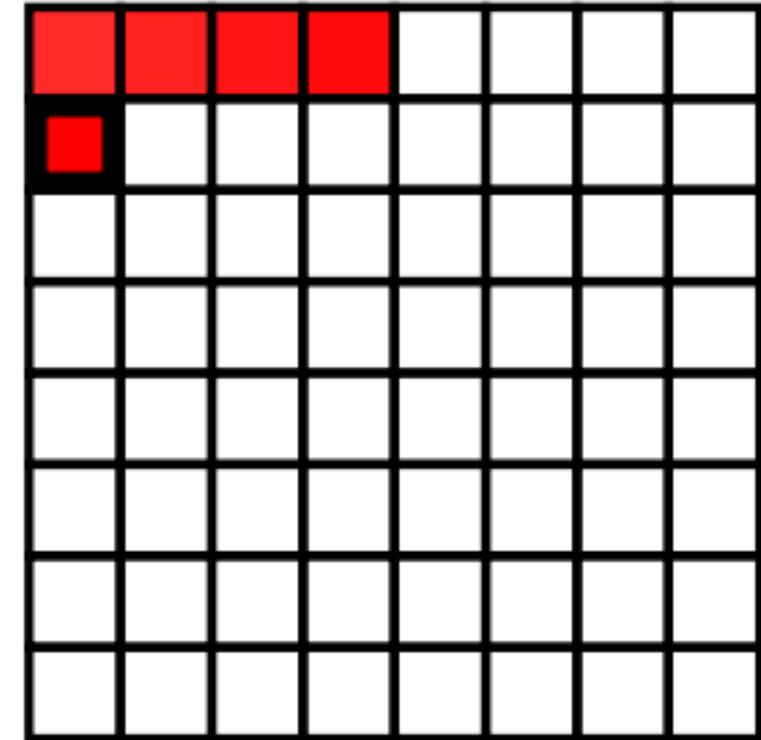
mem:51

B mem:17 cache hit:1

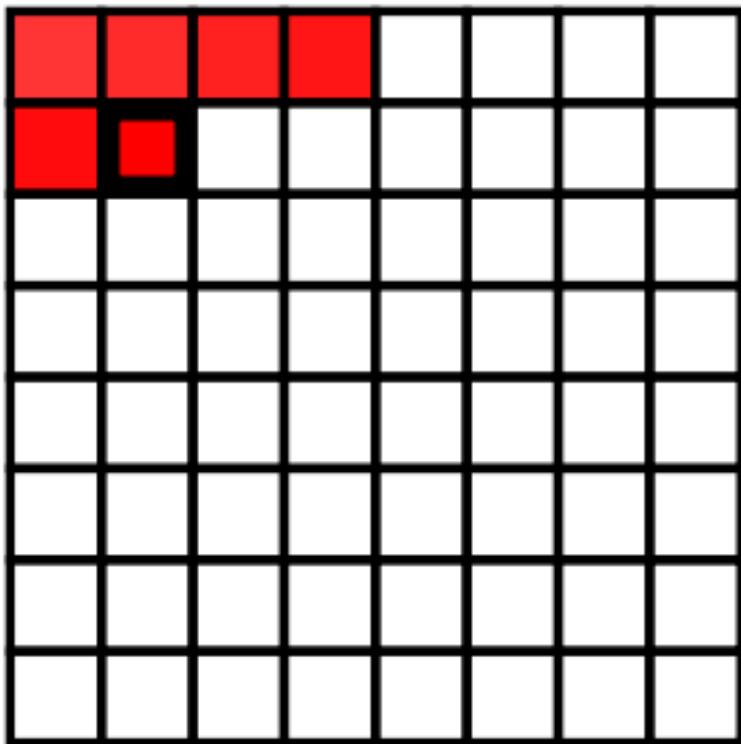


cache hits:25 ≈49%

C mem:17 cache hit:12

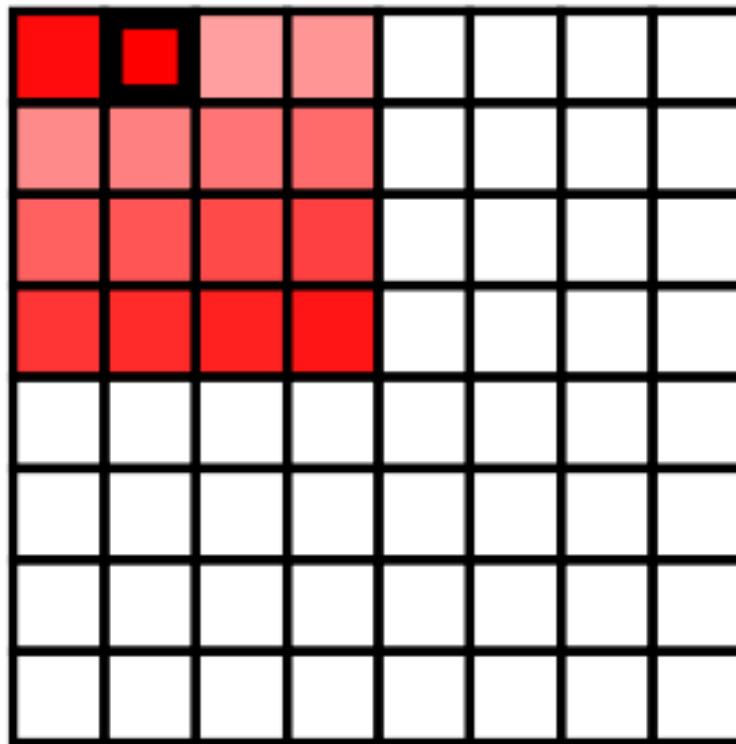


A mem:18 cache hit:12



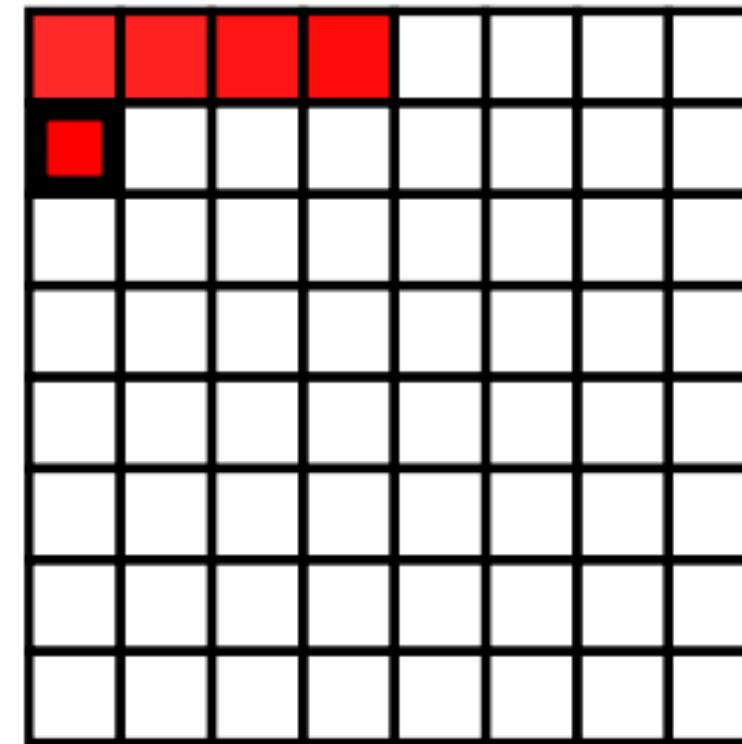
mem:54

B mem:18 cache hit:2

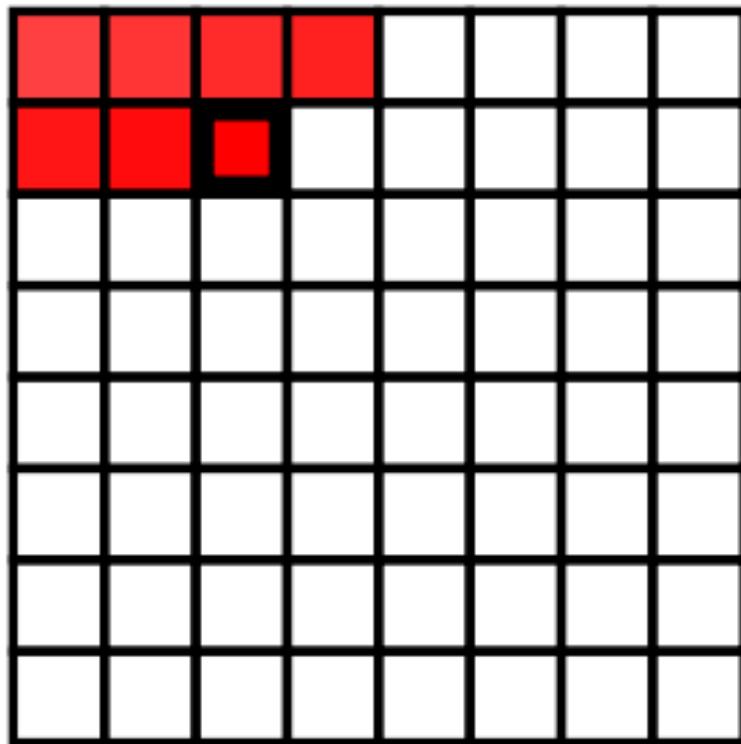


cache hits:27 ≈50%

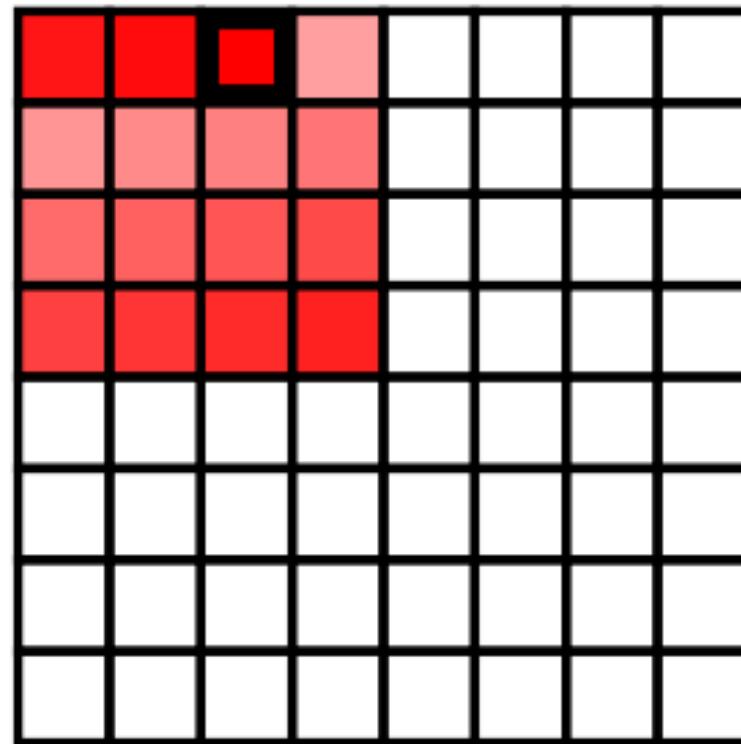
C mem:18 cache hit:13



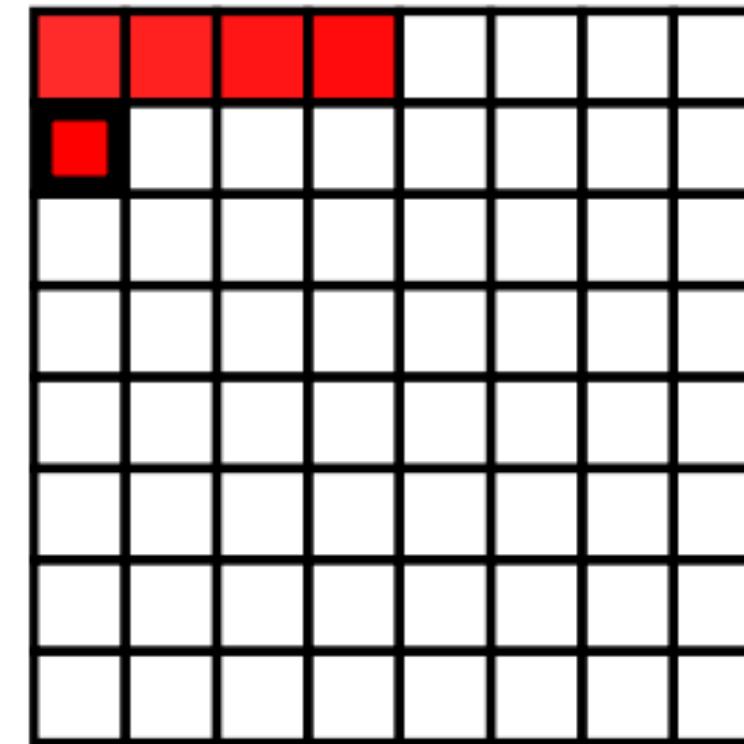
A mem:19 cache hit:12



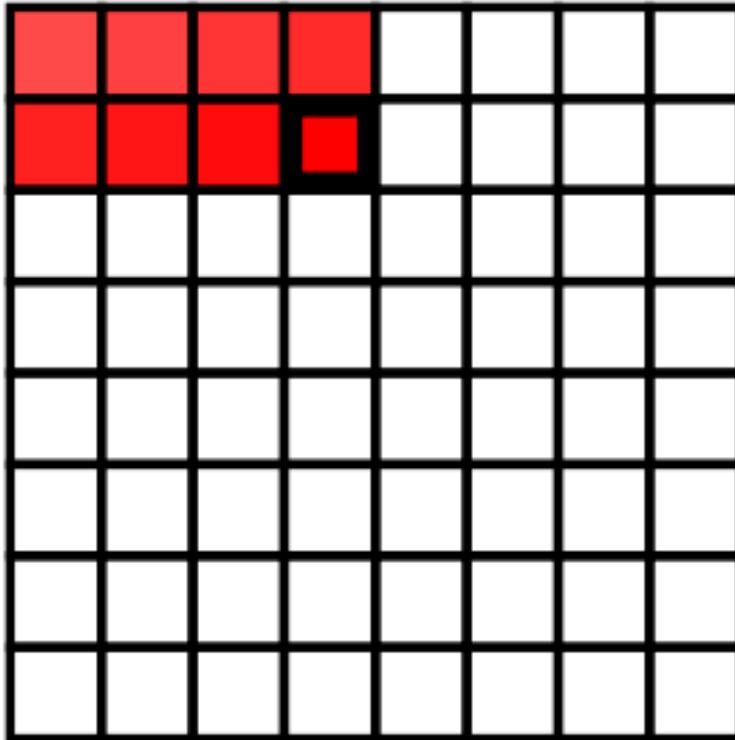
B mem:19 cache hit:3



C mem:19 cache hit:14

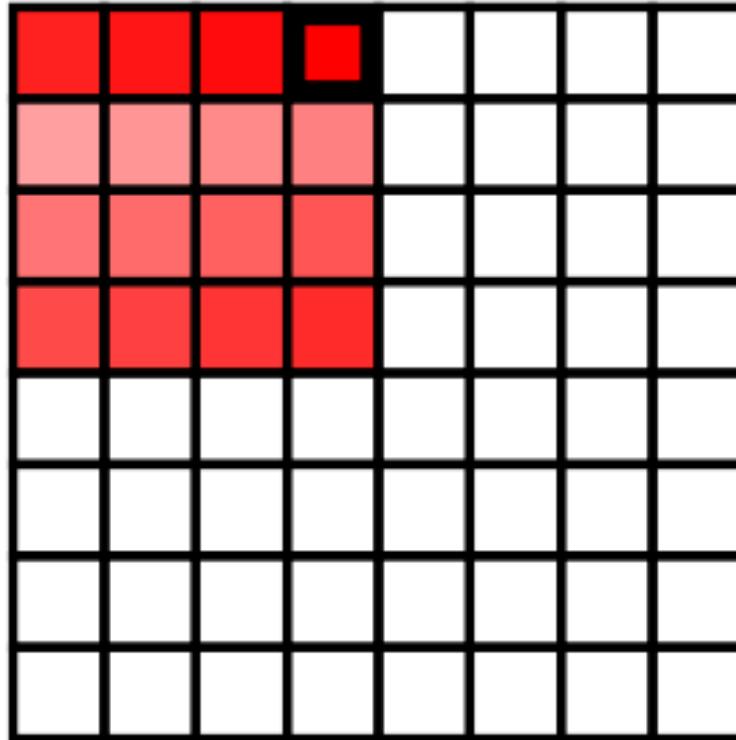


A mem:20 cache hit:12



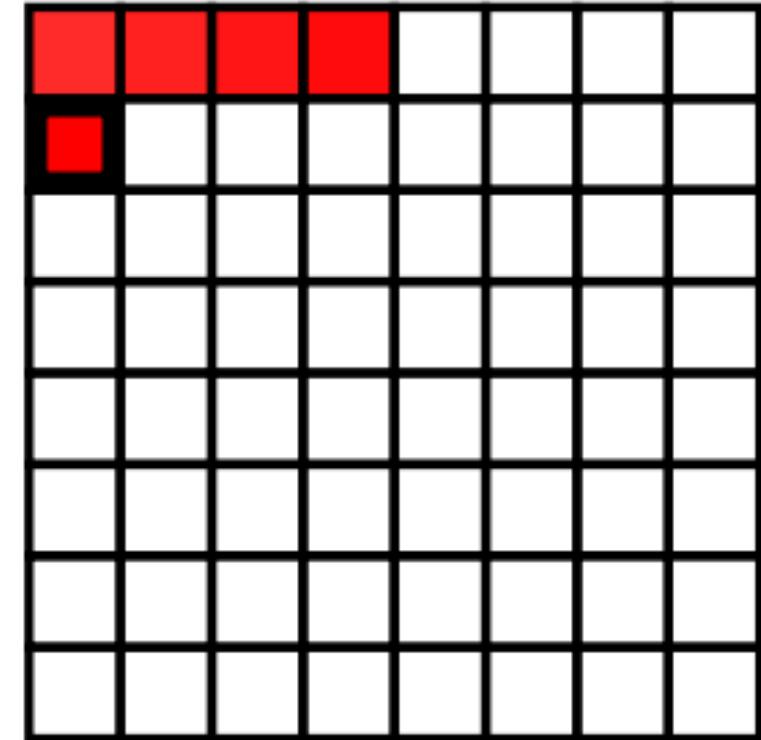
mem:60

B mem:20 cache hit:4

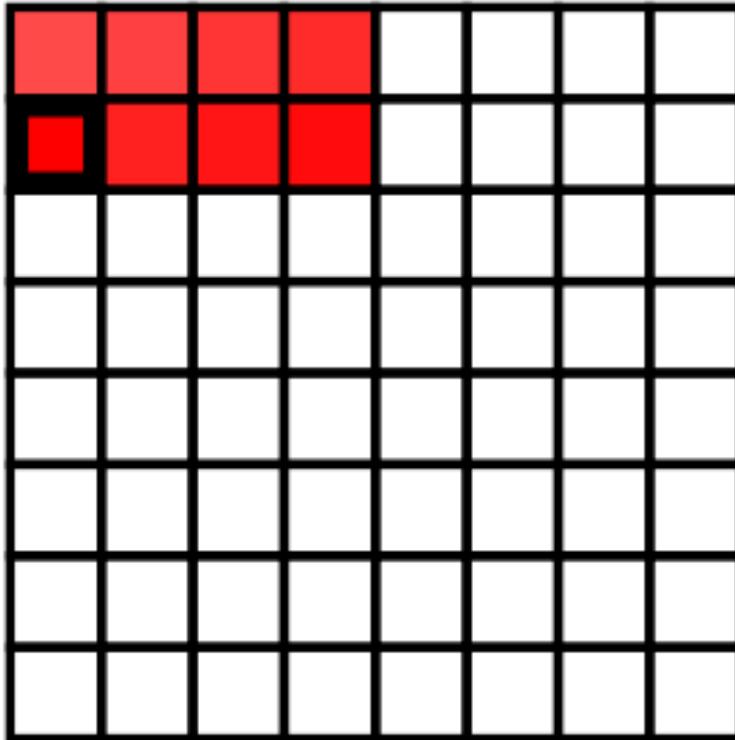


cache hits:31 ≈51%

C mem:20 cache hit:15

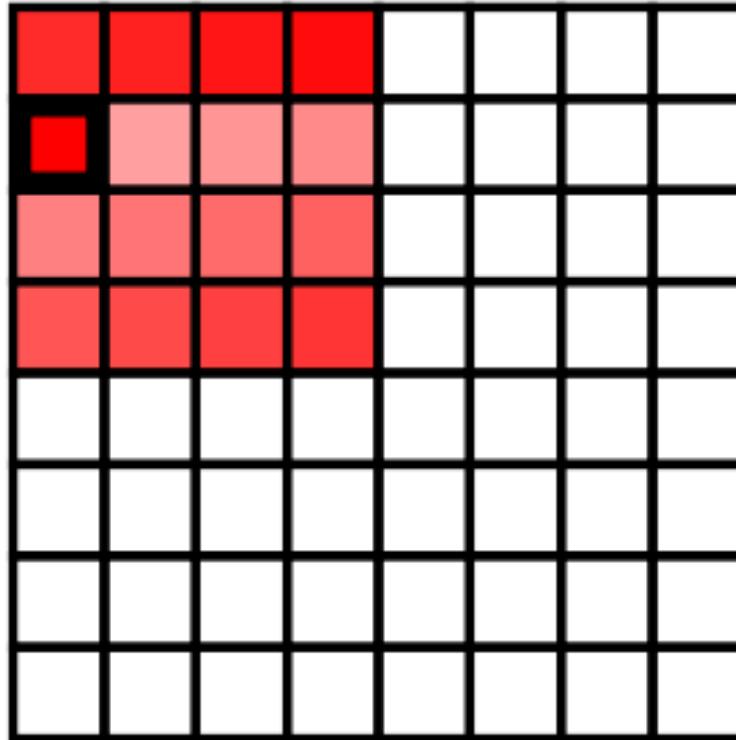


A mem:21 cache hit:13



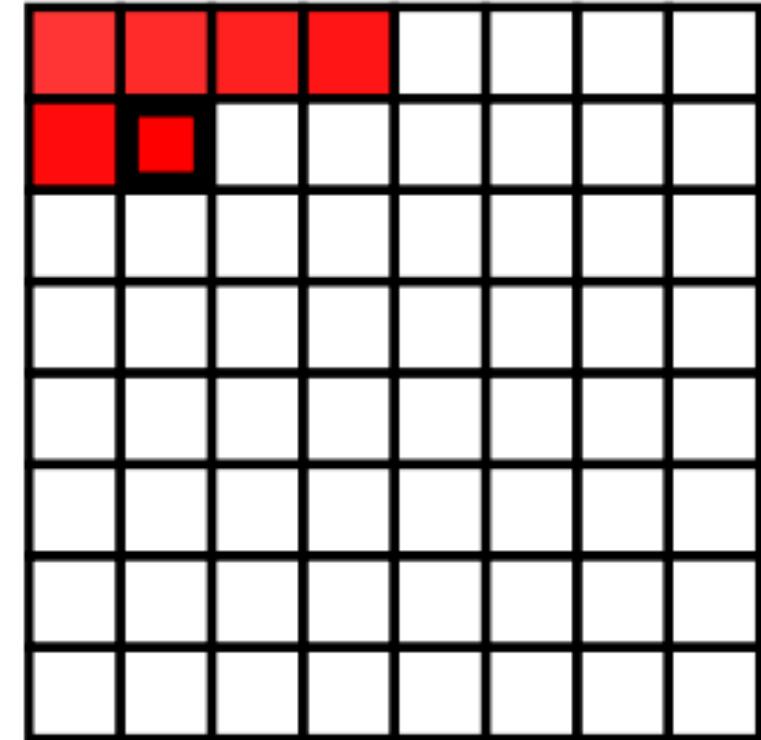
mem:63

B mem:21 cache hit:5

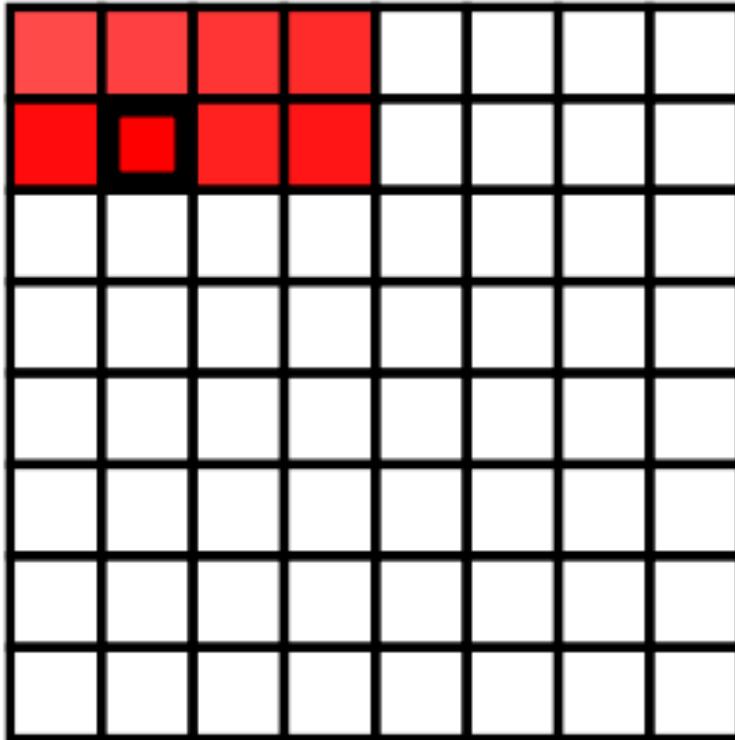


cache hits:33 ≈52%

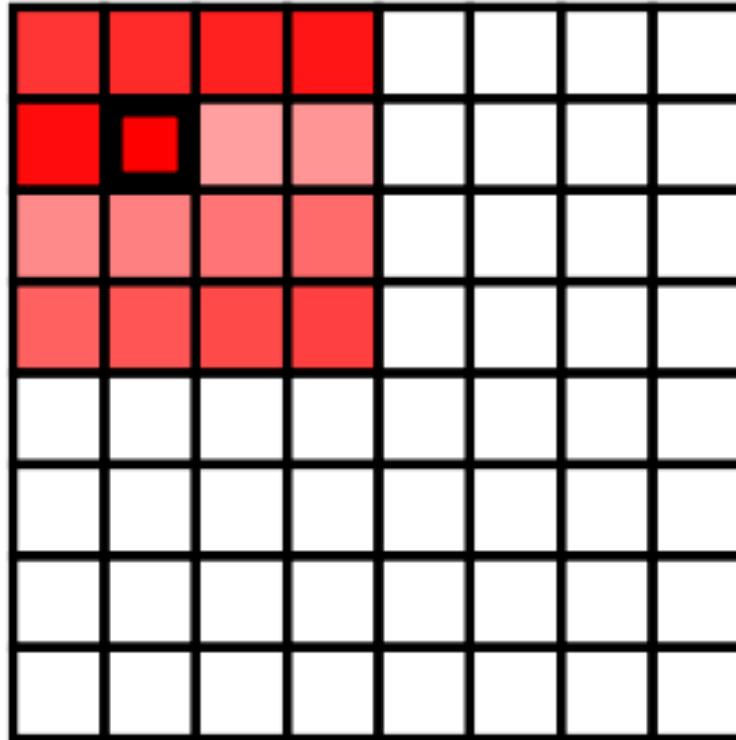
C mem:21 cache hit:15



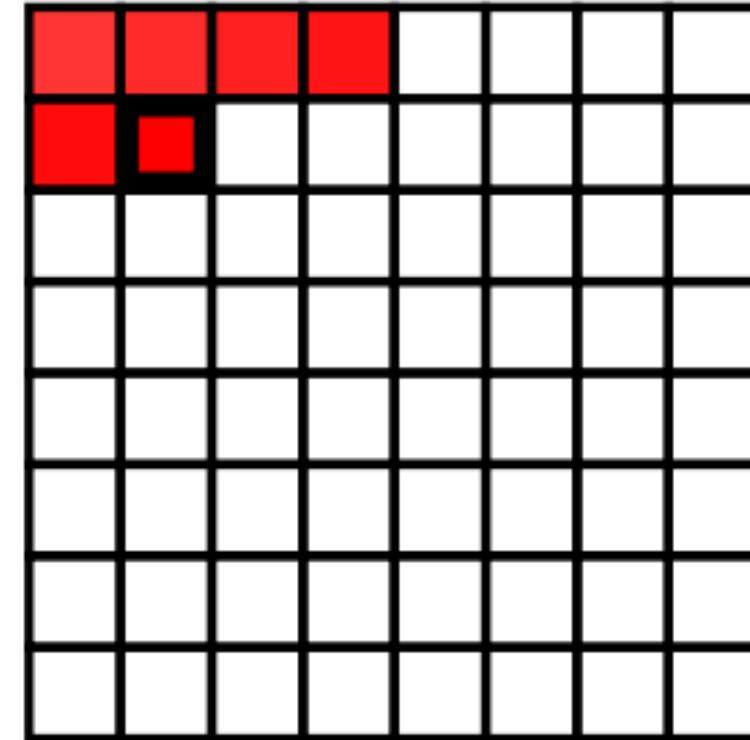
A mem:22 cache hit:14



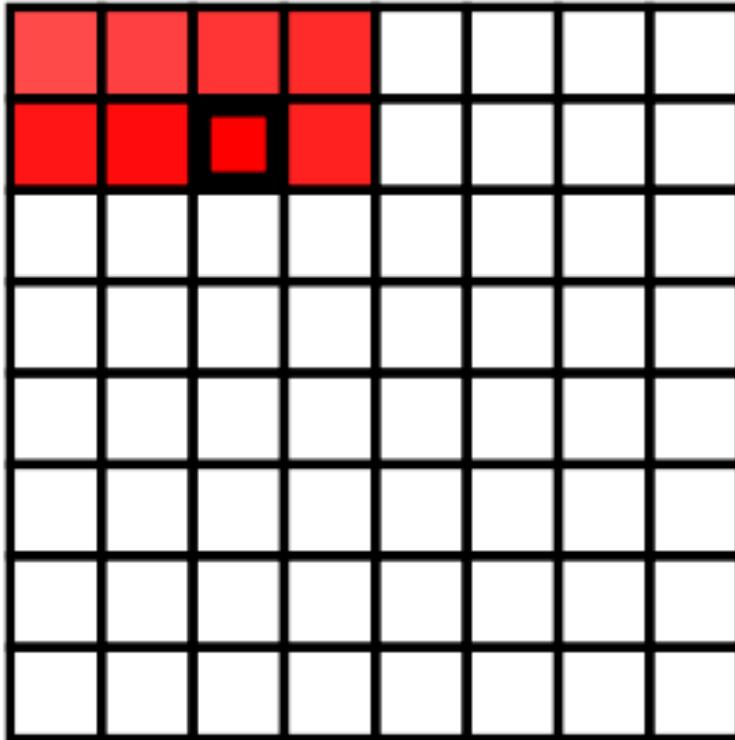
B mem:22 cache hit:6



C mem:22 cache hit:16

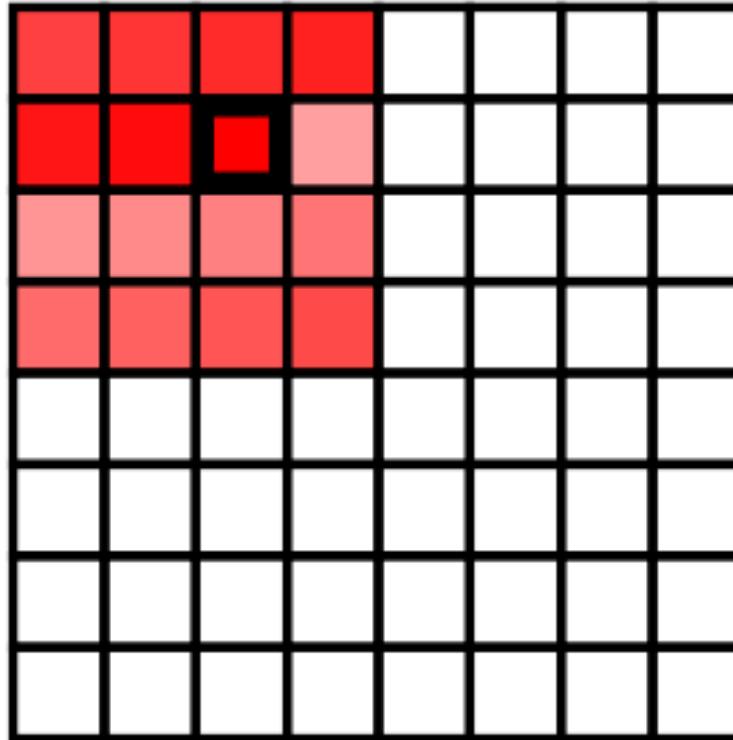


A mem:23 cache hit:15



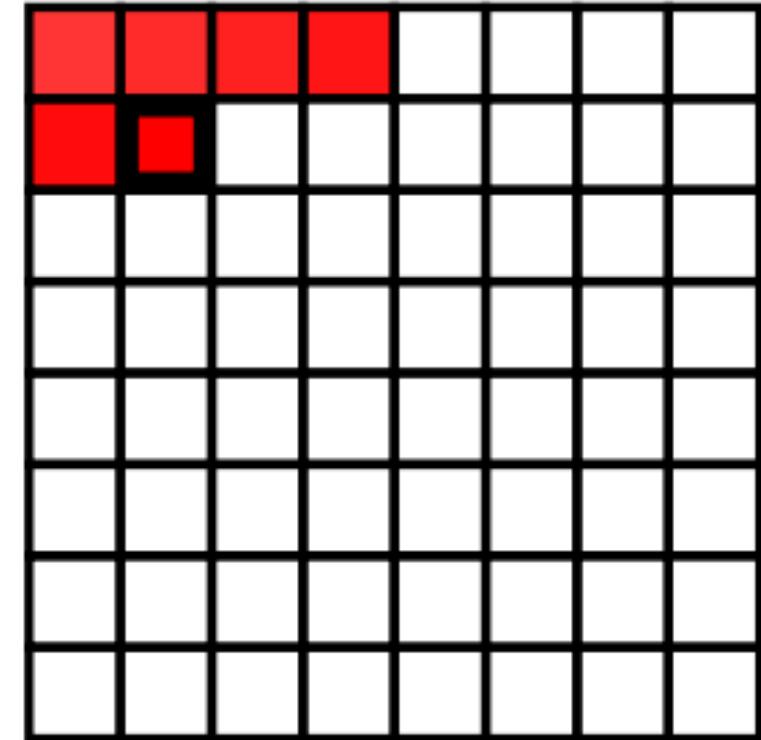
mem:69

B mem:23 cache hit:7

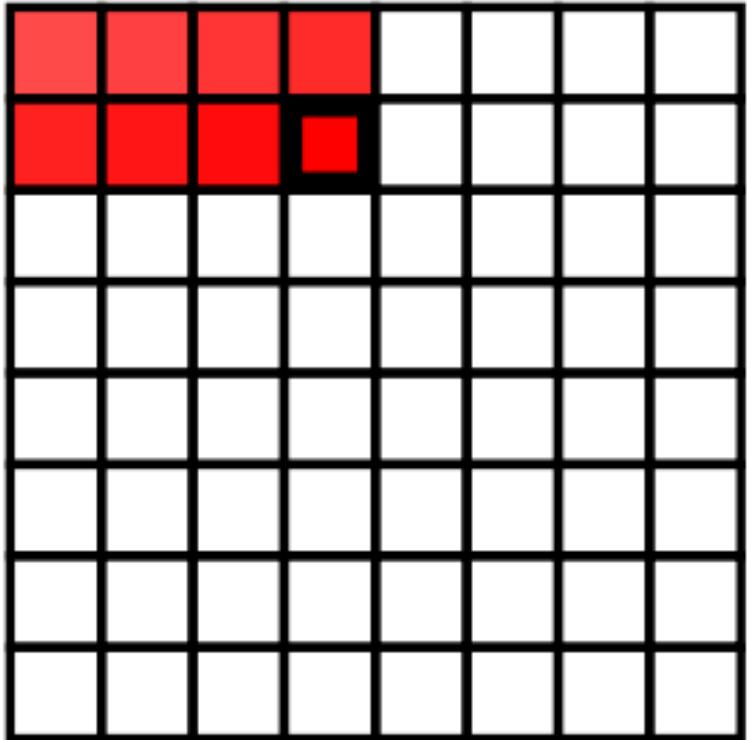


cache hits:39 ≈56%

C mem:23 cache hit:17

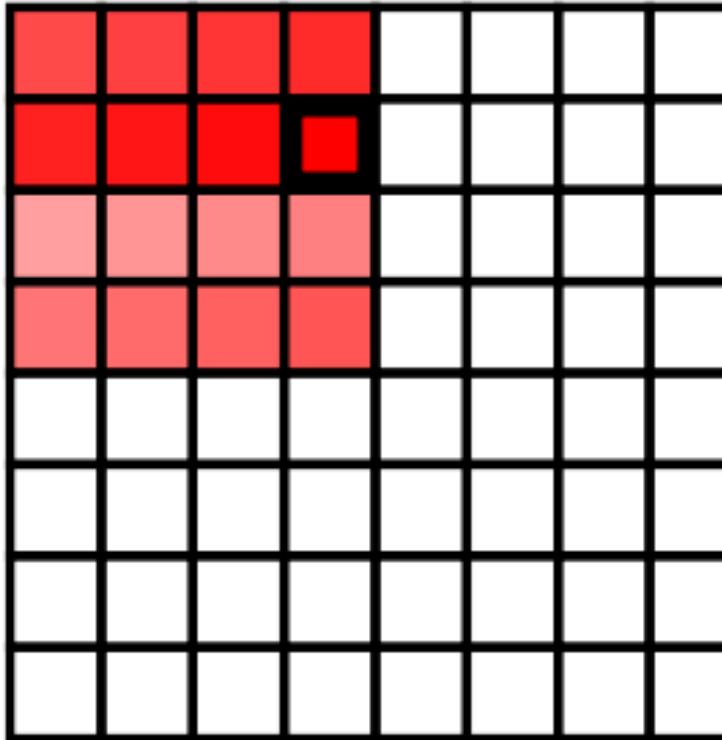


A mem:24 cache hit:16



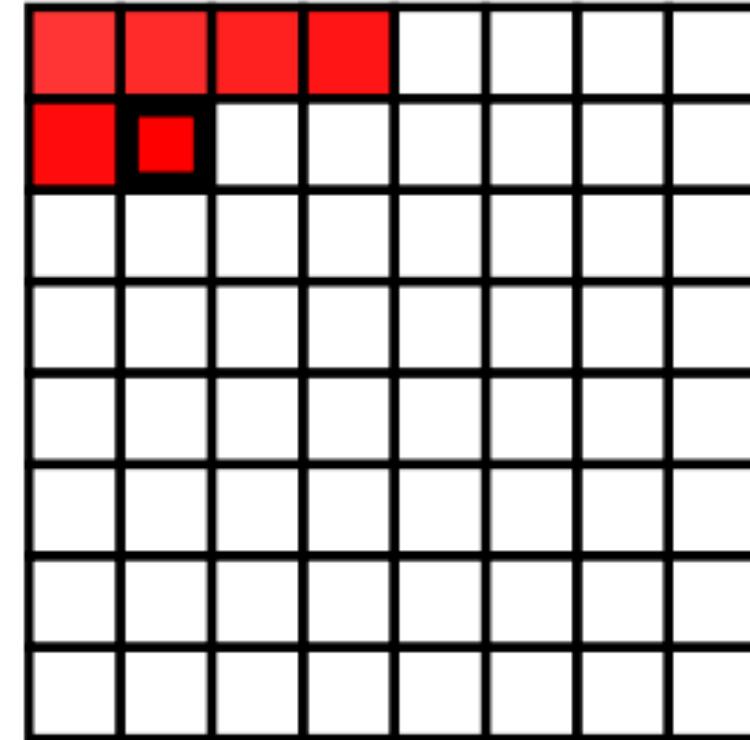
mem:72

B mem:24 cache hit:8

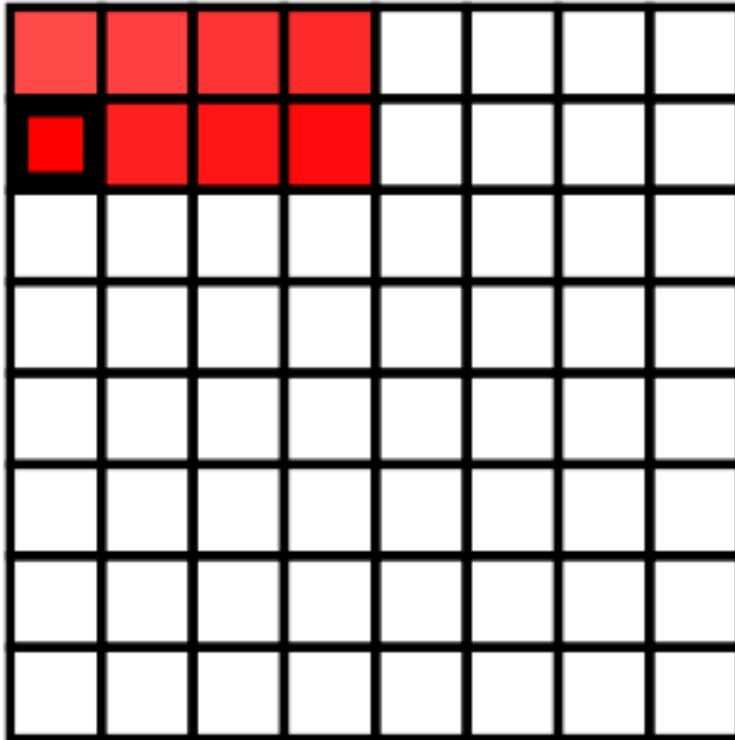


cache hits:42 ≈58%

C mem:24 cache hit:18

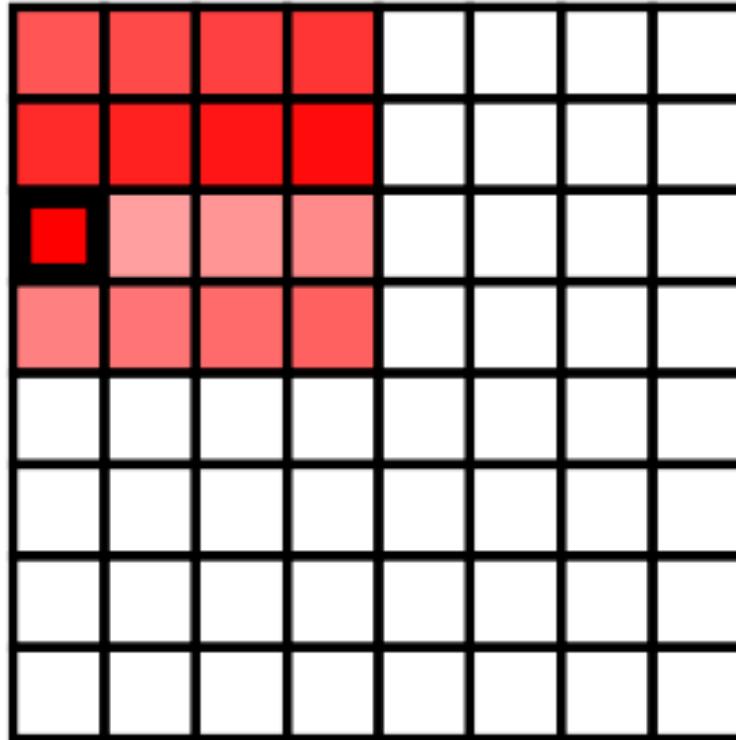


A mem:25 cache hit:17



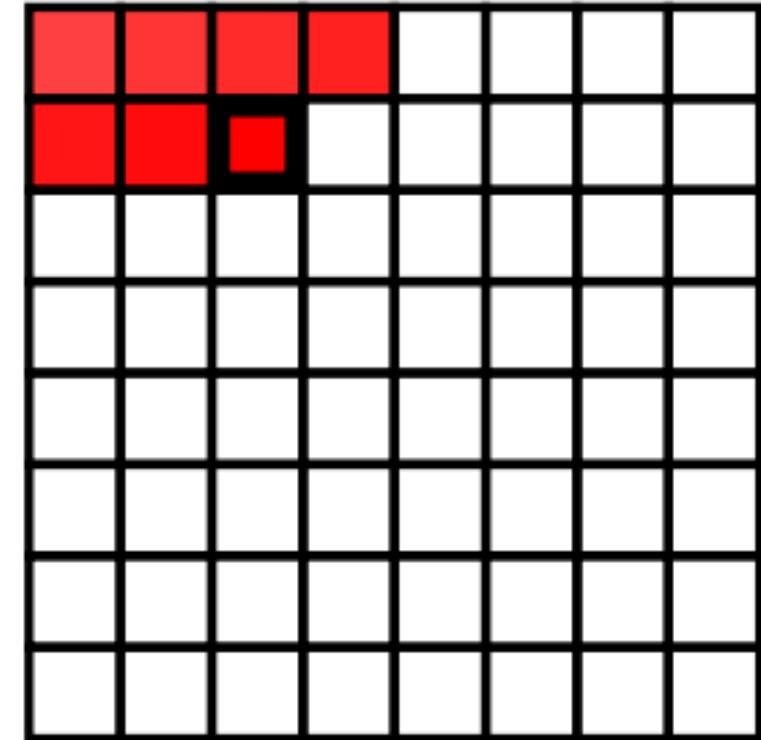
mem:75

B mem:25 cache hit:9

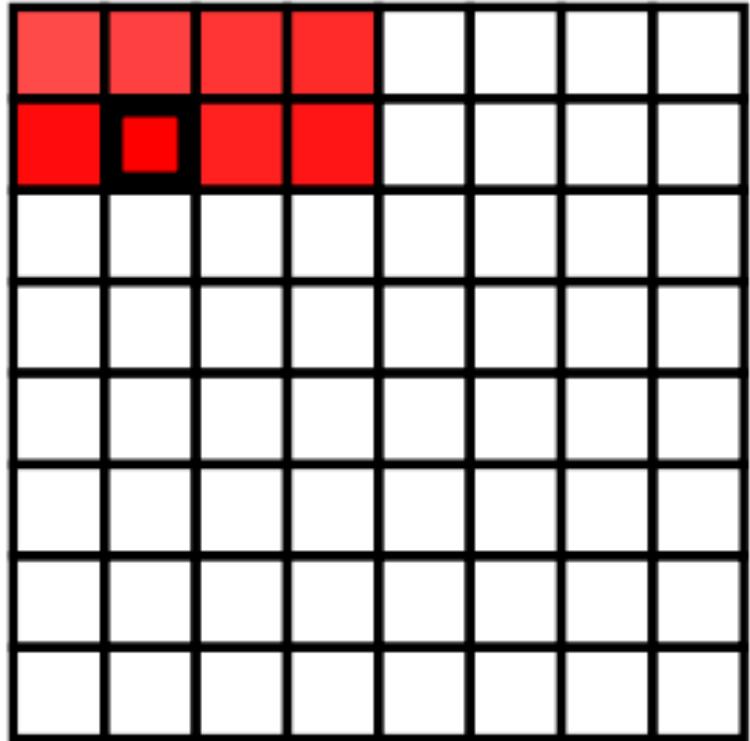


cache hits:44 ≈58%

C mem:25 cache hit:18

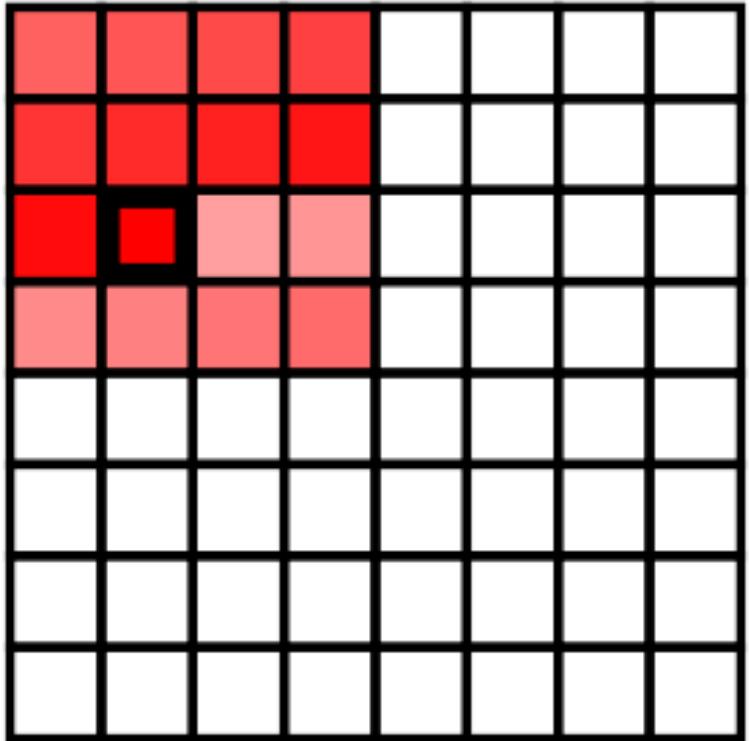


A mem:26 cache hit:18



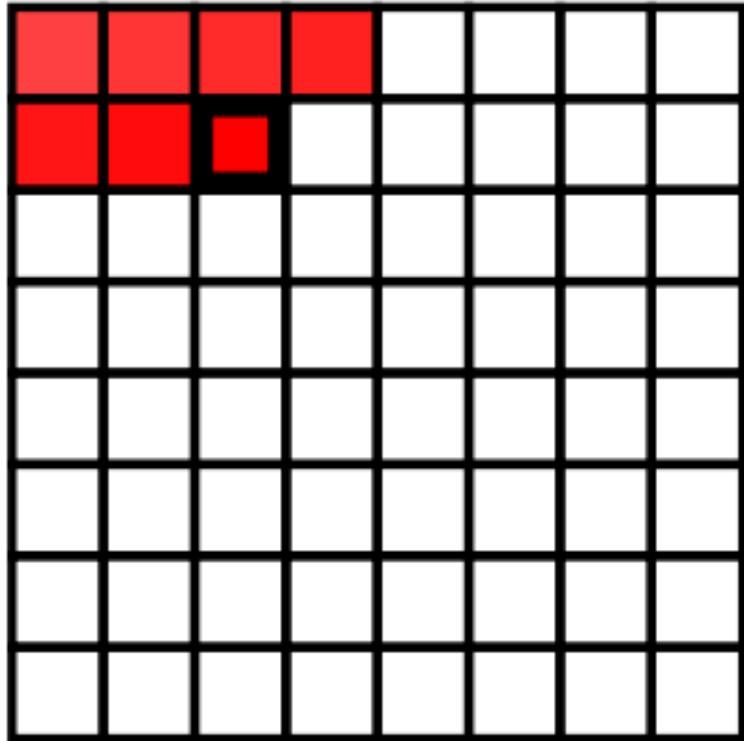
mem:78

B mem:26 cache hit:10

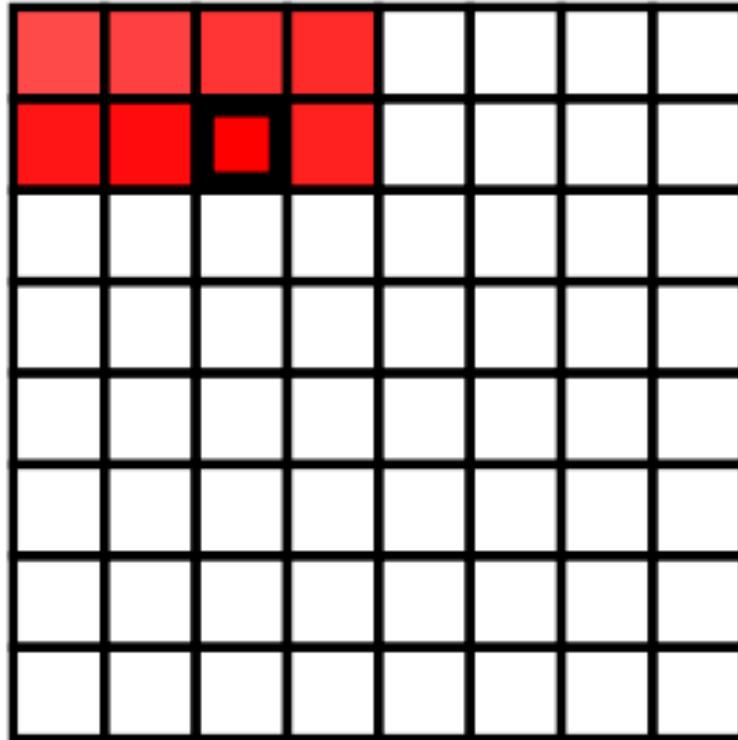


cache hits:47 ≈60%

C mem:26 cache hit:19

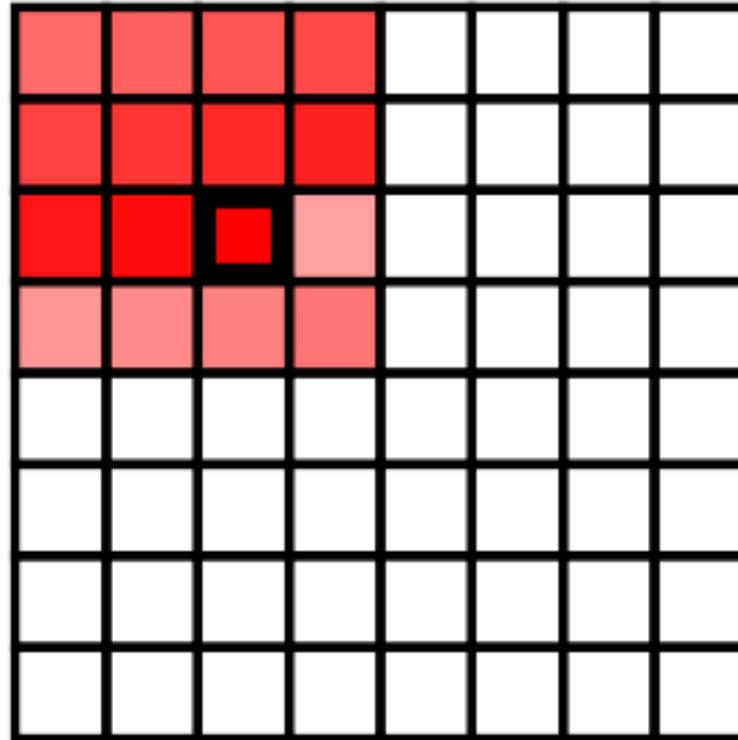


A mem:27 cache hit:19



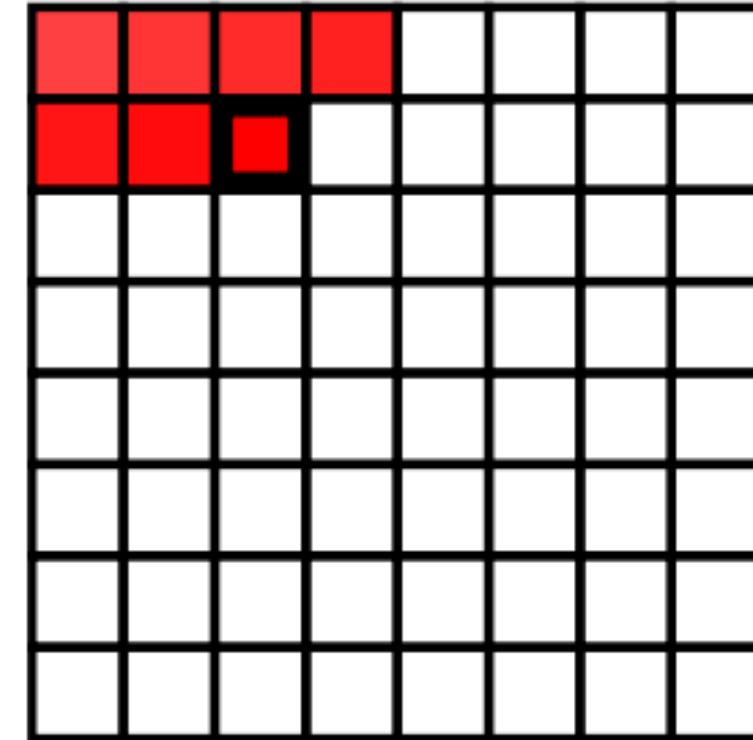
mem:81

B mem:27 cache hit:11

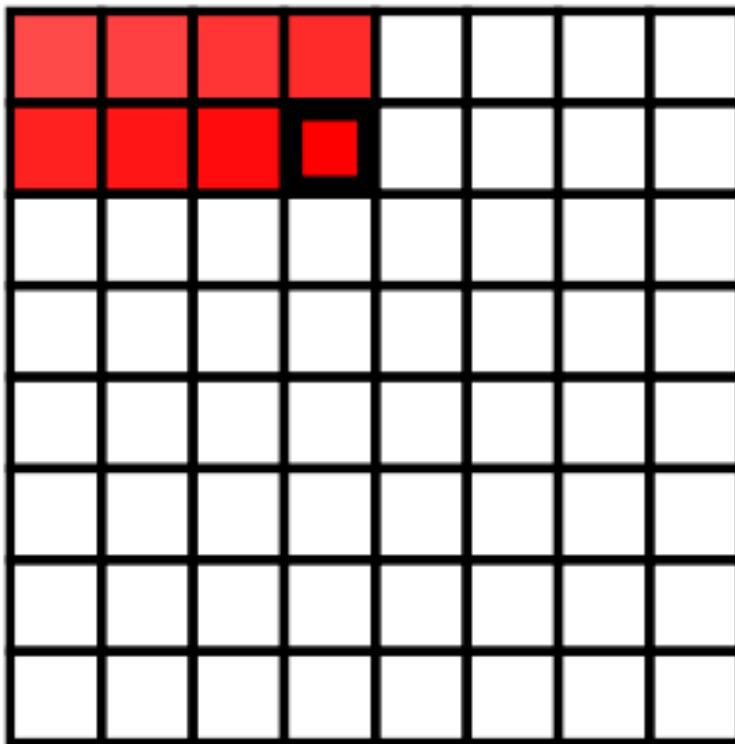


cache hits:50 ≈61%

C mem:27 cache hit:20

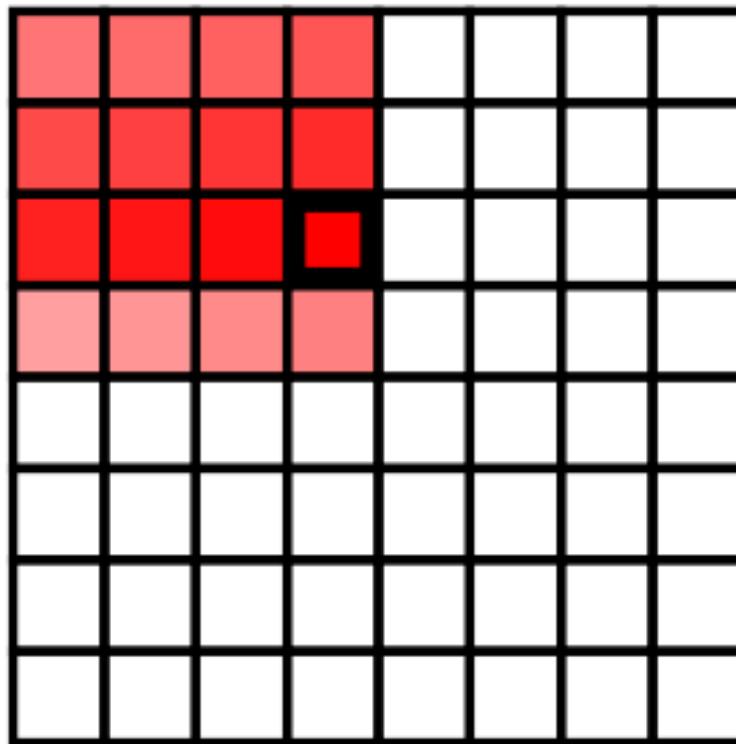


A mem:28 cache hit:20



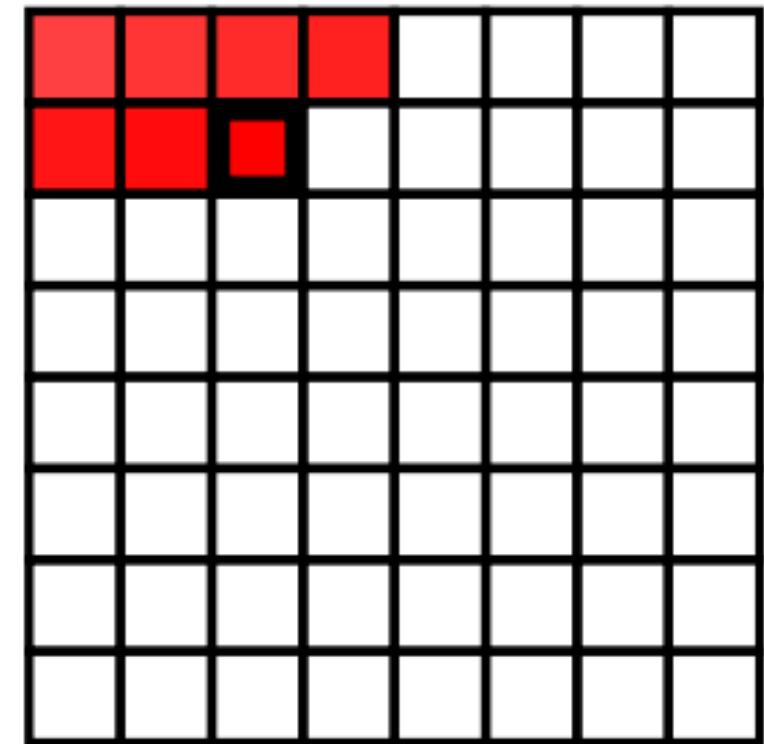
mem:84

B mem:28 cache hit:12



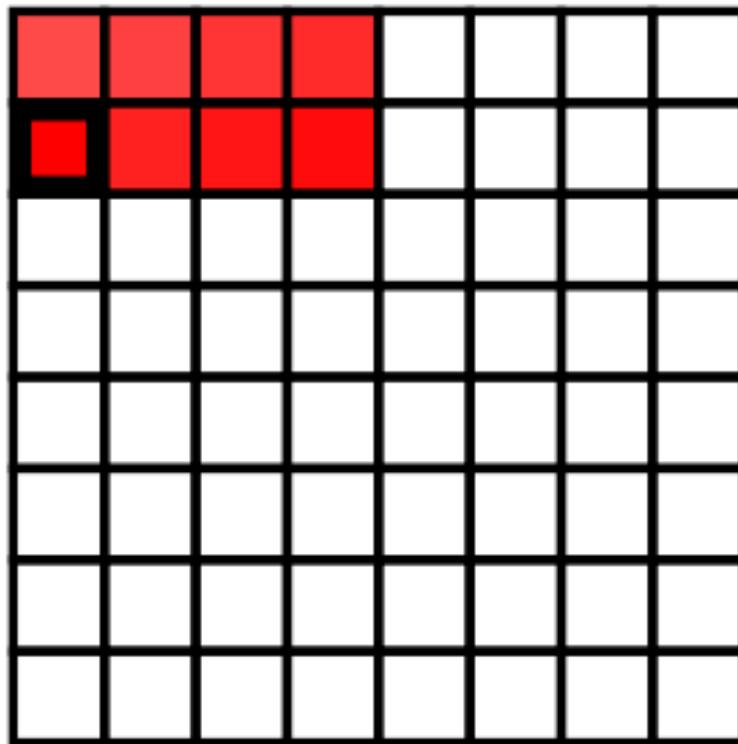
cache hits:53 ≈63%

C mem:28 cache hit:21



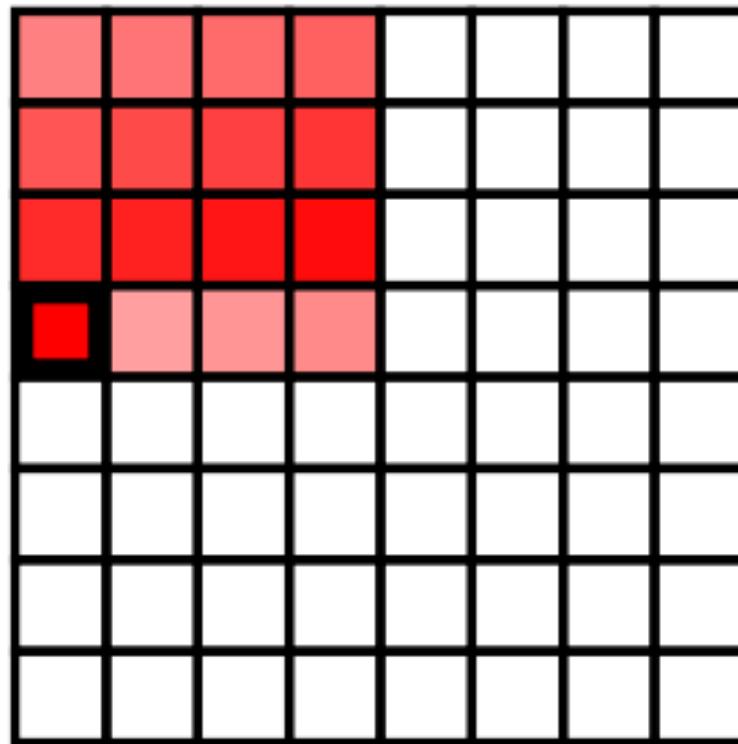
=

A mem:29 cache hit:21



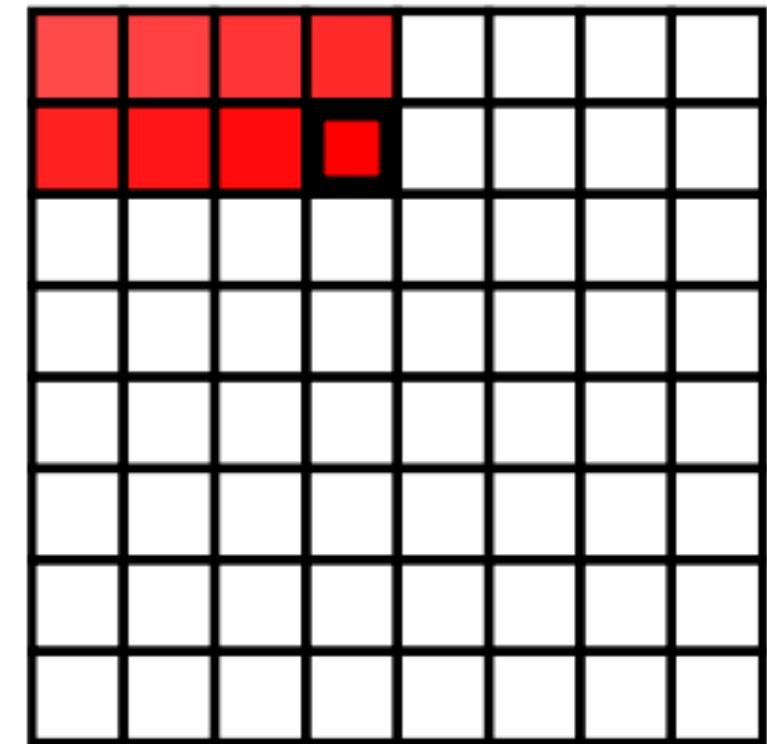
mem:87

B mem:29 cache hit:13

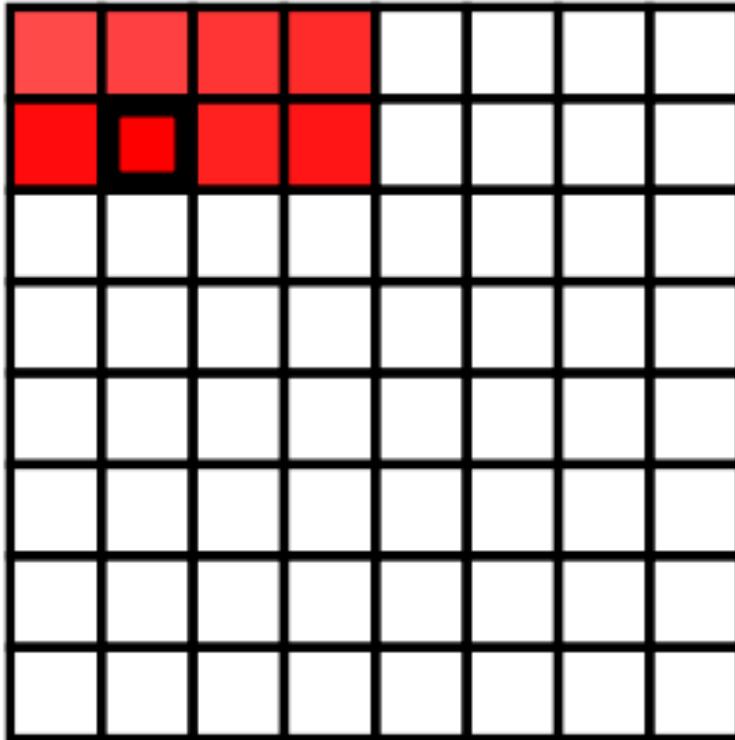


cache hits:55 ≈63%

C mem:29 cache hit:21

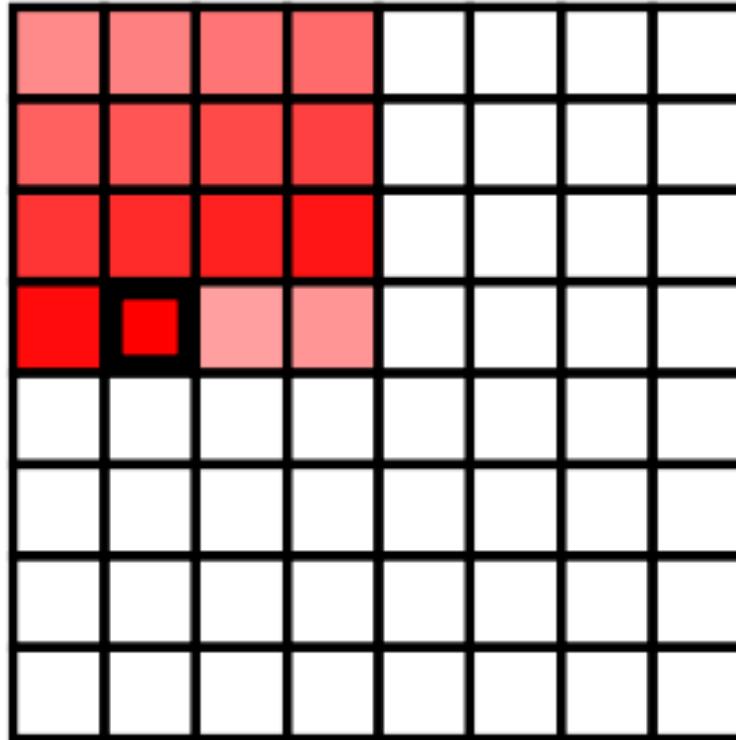


A mem:30 cache hit:22



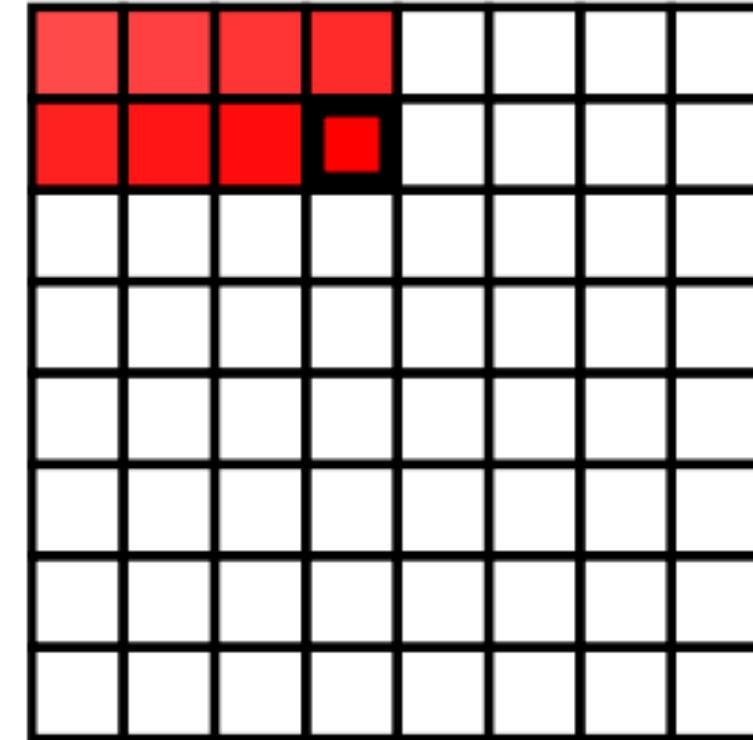
mem:90

B mem:30 cache hit:14

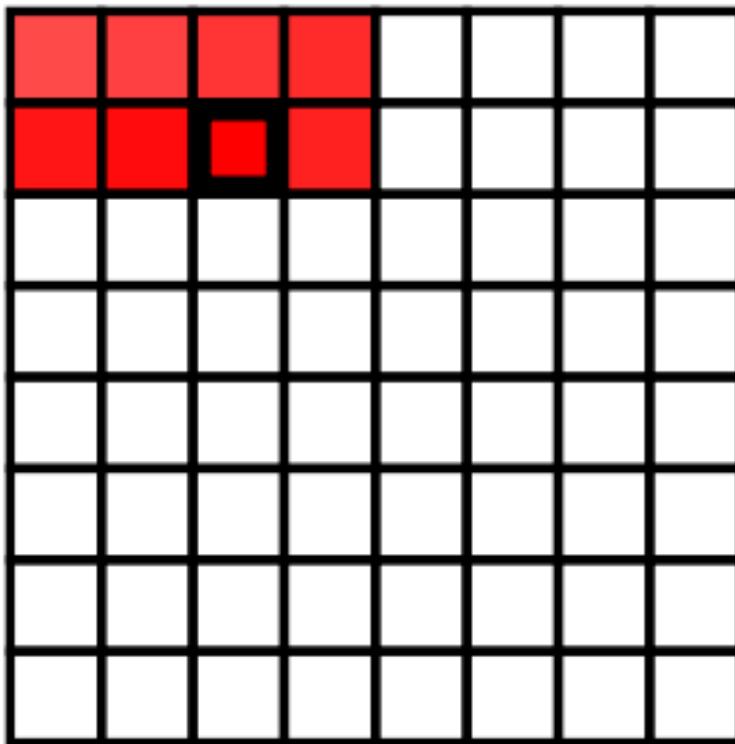


cache hits:58 ≈64%

C mem:30 cache hit:22

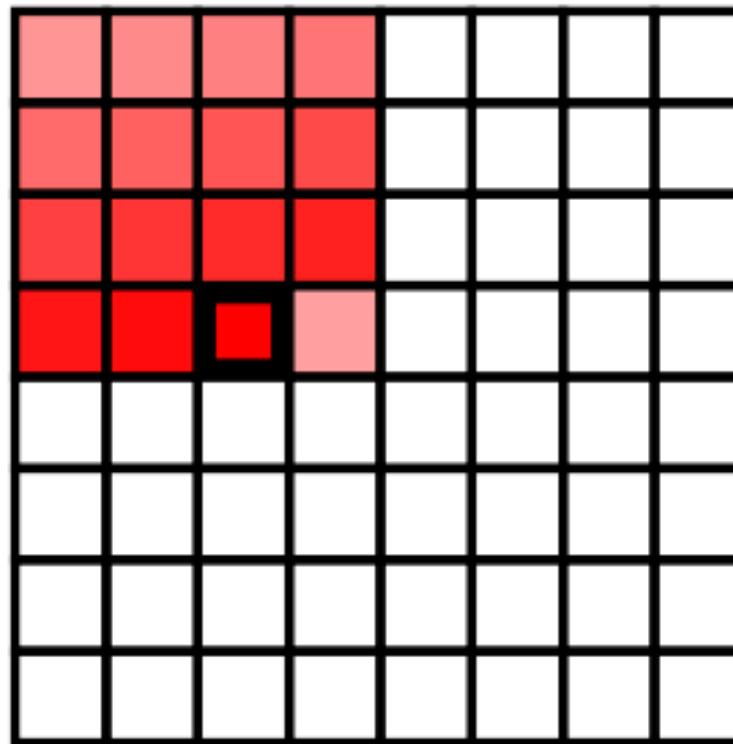


A mem:31 cache hit:23



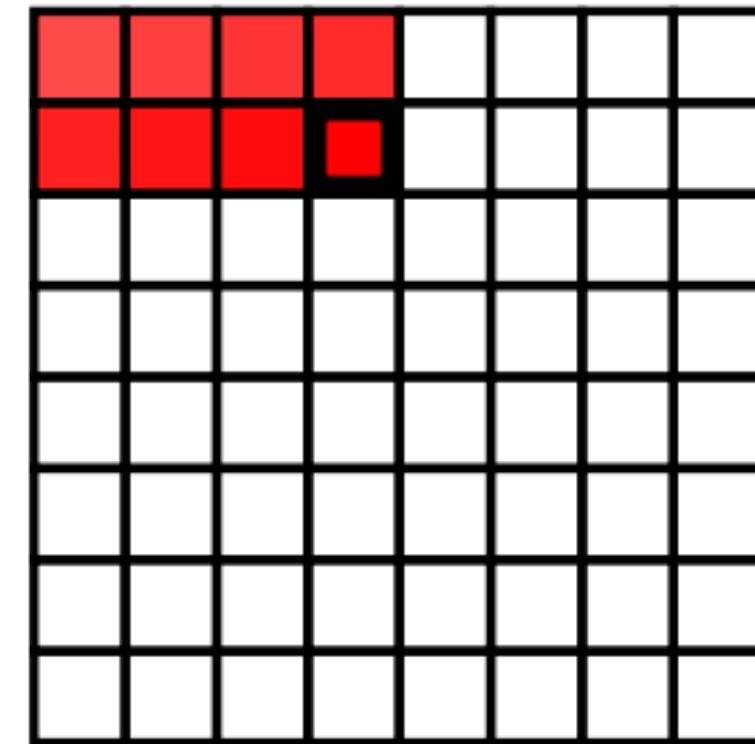
mem:93

B mem:31 cache hit:15

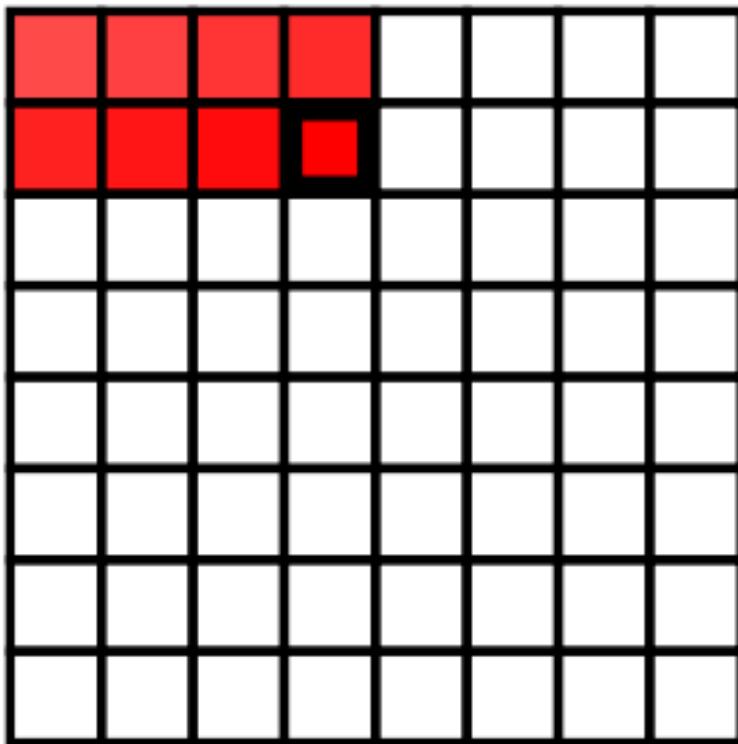


cache hits:61 ≈65%

C mem:31 cache hit:23

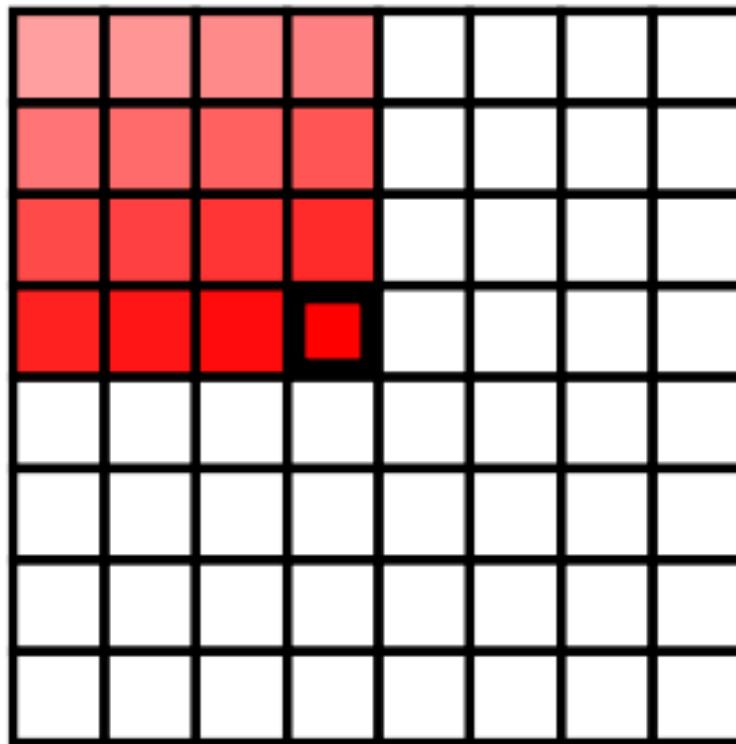


A mem:32 cache hit:24



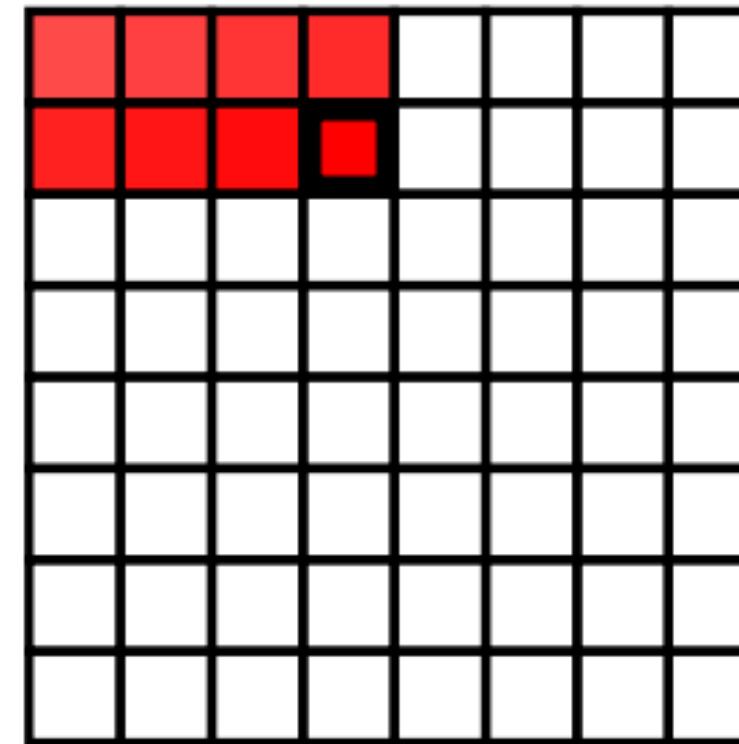
mem:96

B mem:32 cache hit:16

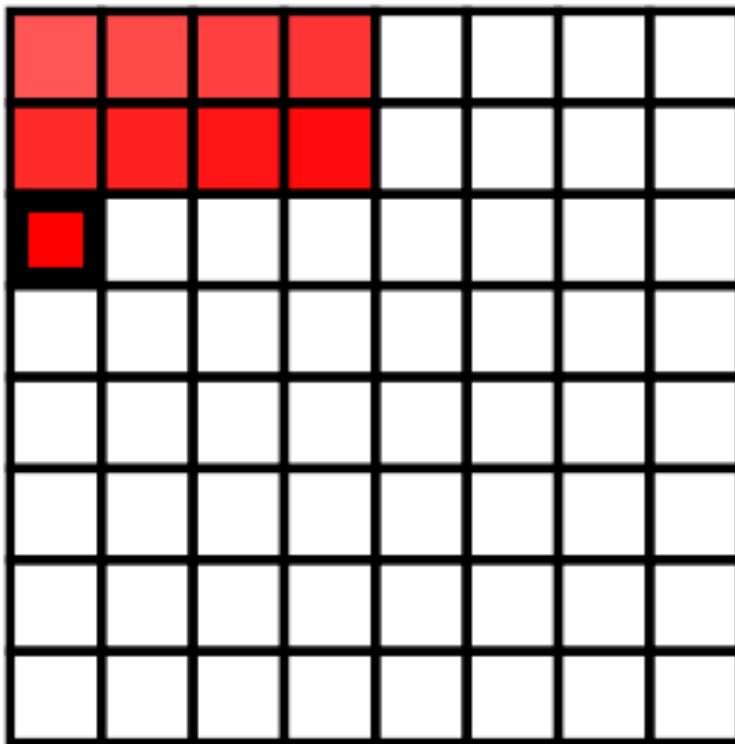


cache hits:64 ≈66%

C mem:32 cache hit:24

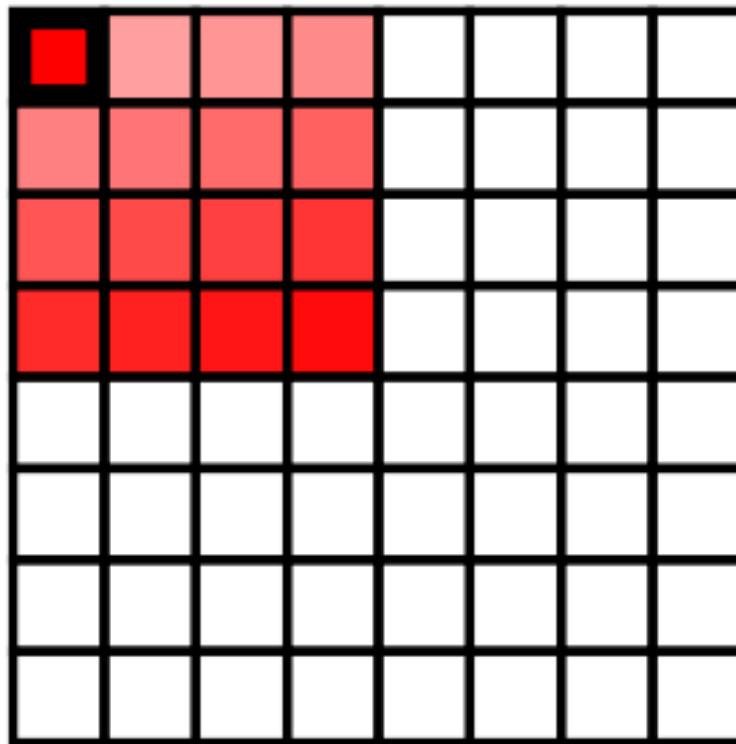


A mem:33 cache hit:24



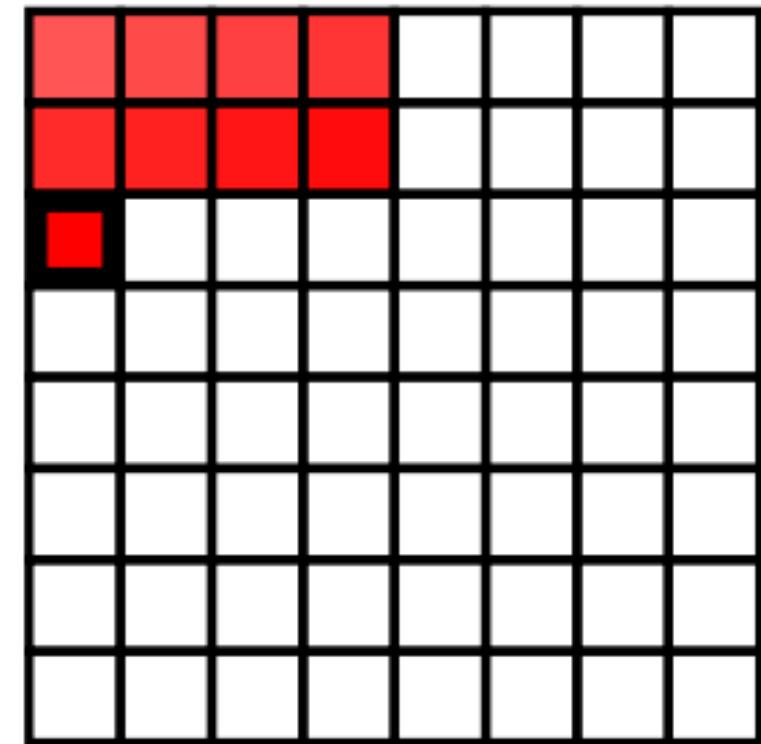
mem:99

B mem:33 cache hit:17

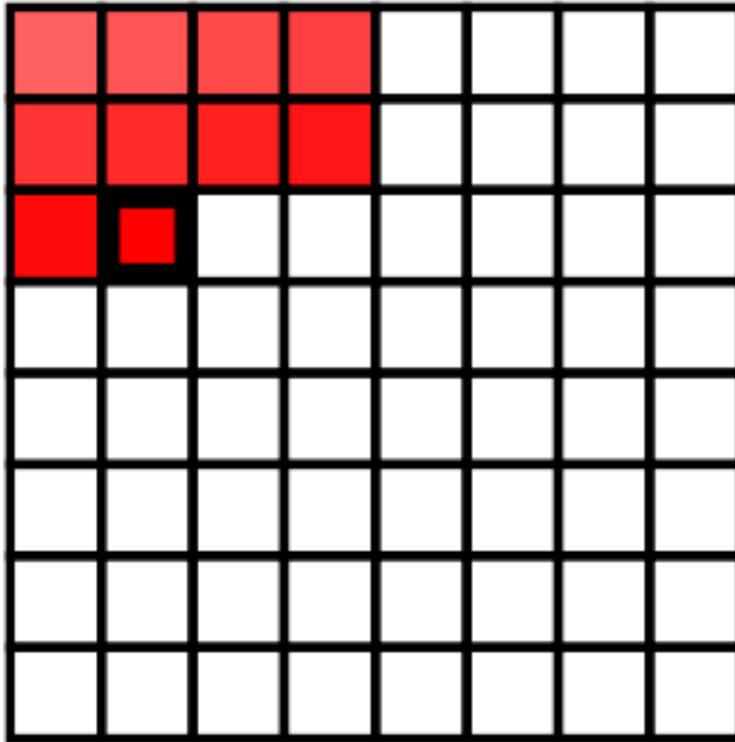


cache hits:65 $\approx 65\%$

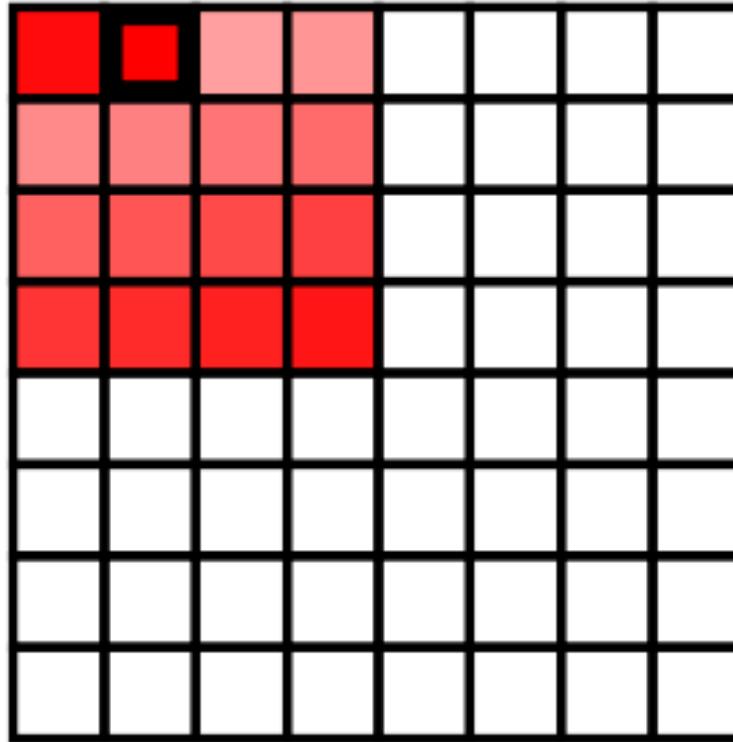
C mem:33 cache hit:24



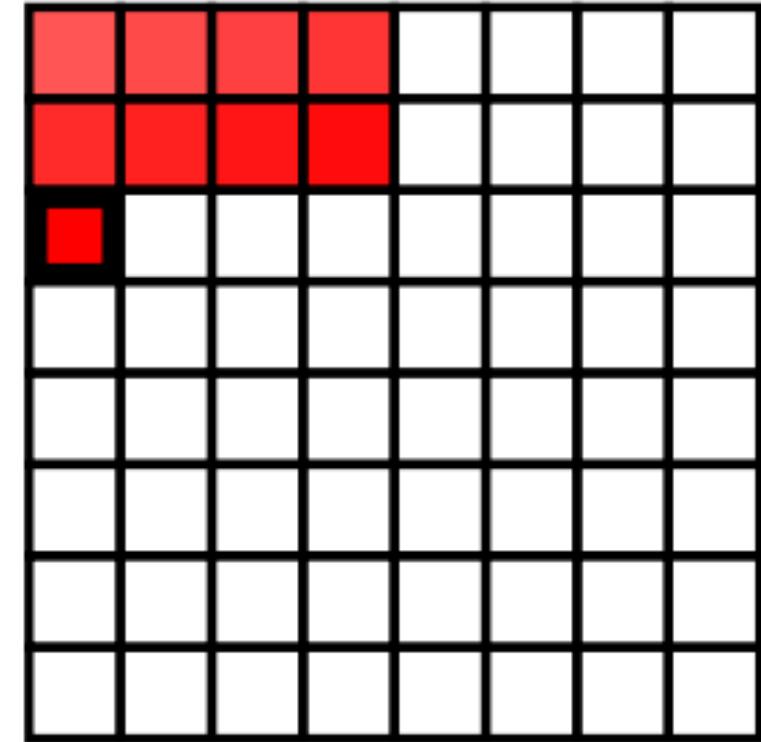
A mem:34 cache hit:24



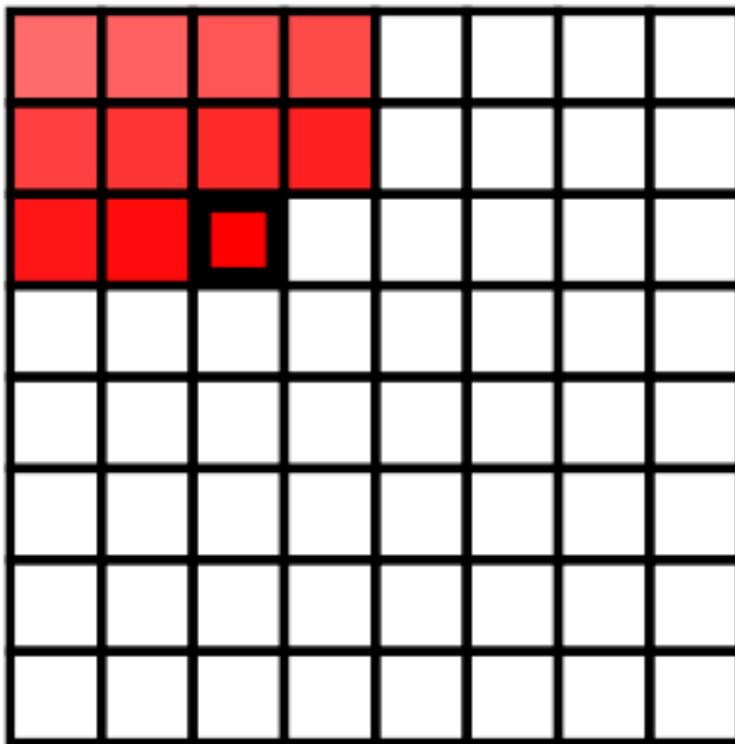
B mem:34 cache hit:18



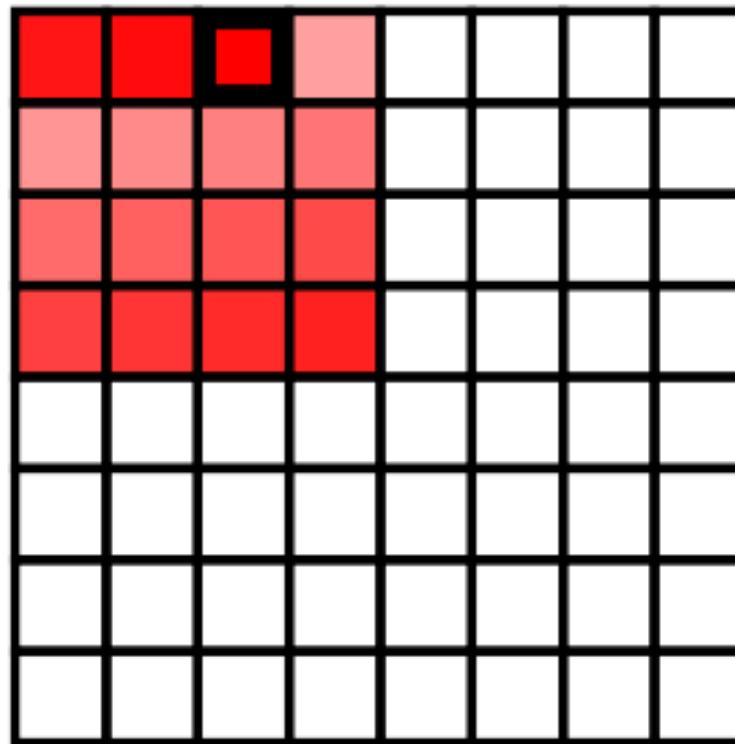
C mem:34 cache hit:25



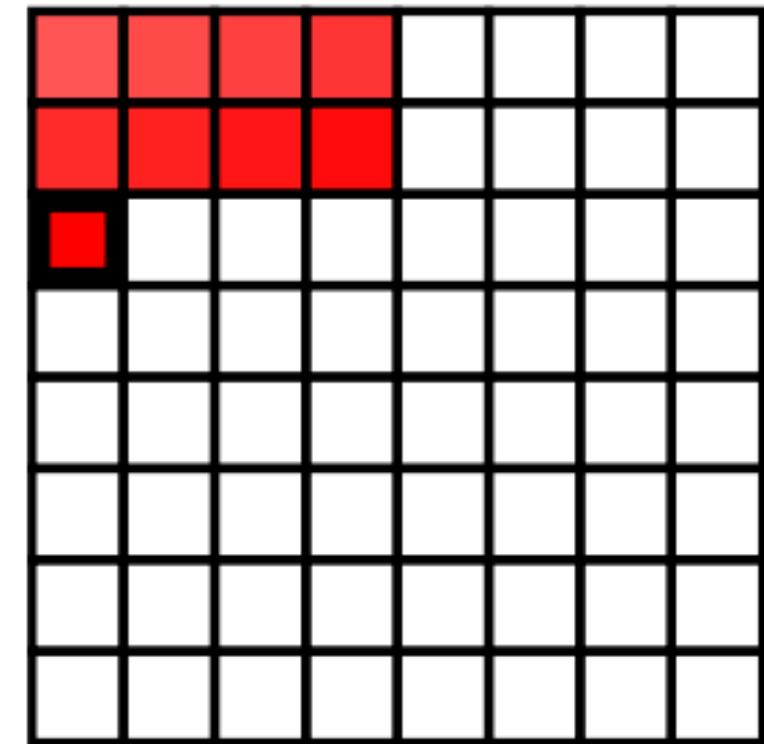
A mem:35 cache hit:24



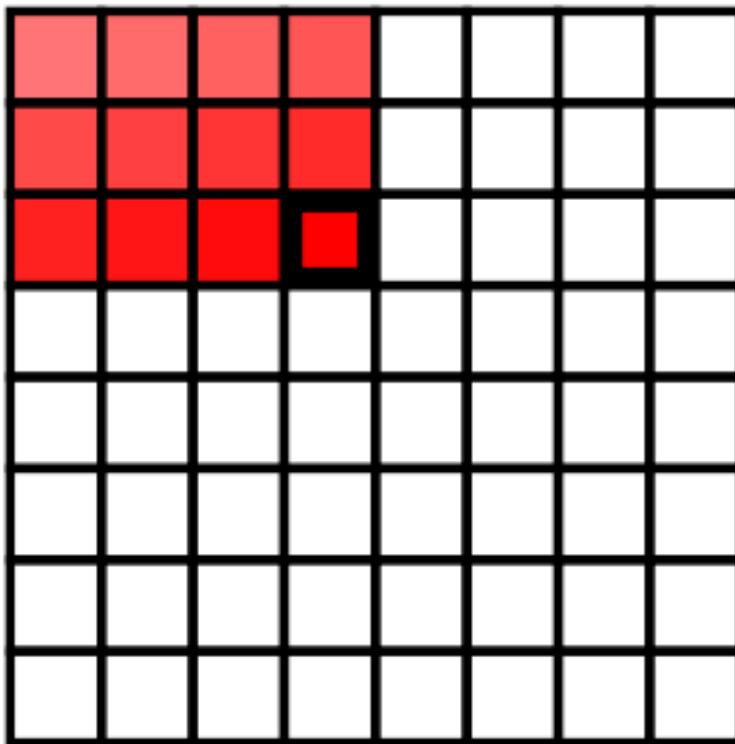
B mem:35 cache hit:19



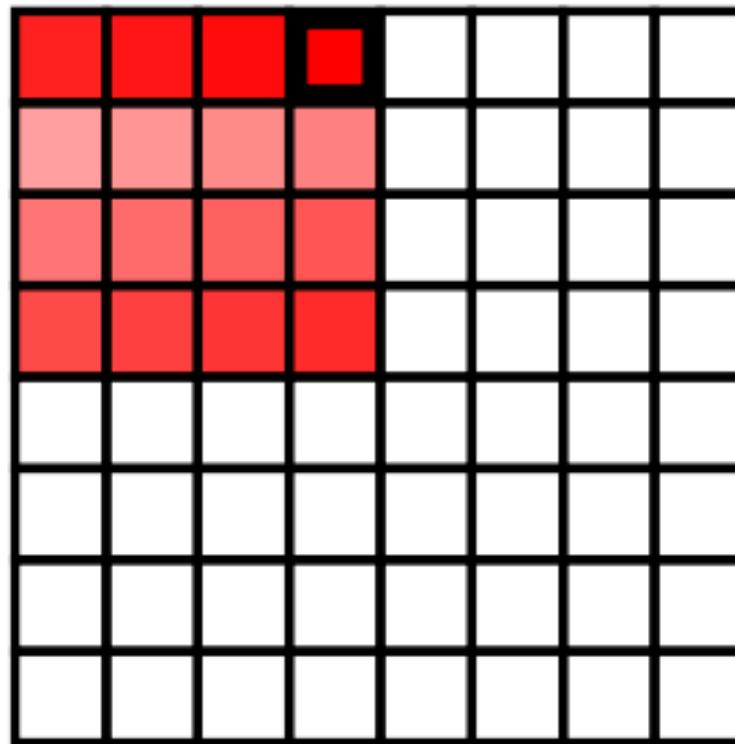
C mem:35 cache hit:26



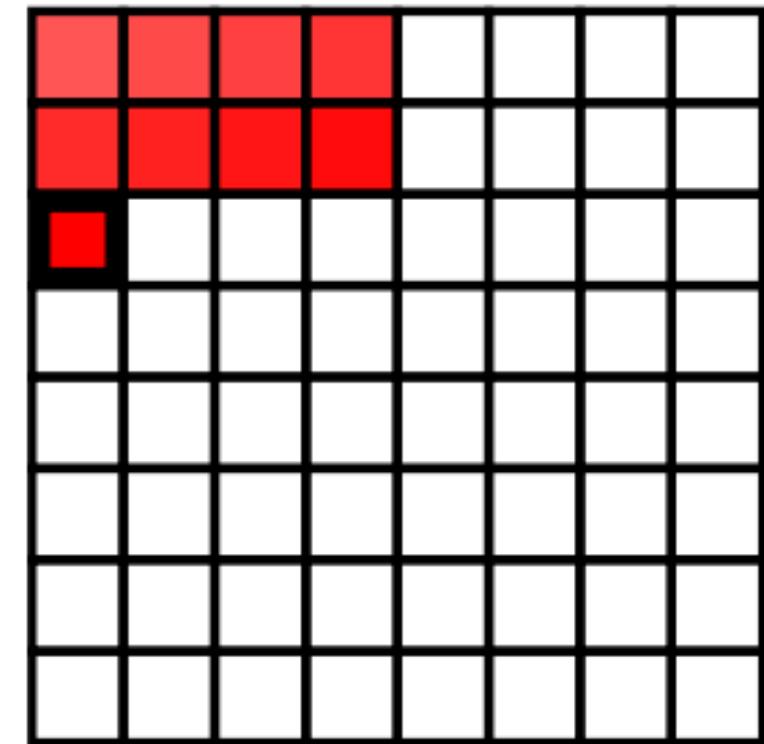
A mem:36 cache hit:24



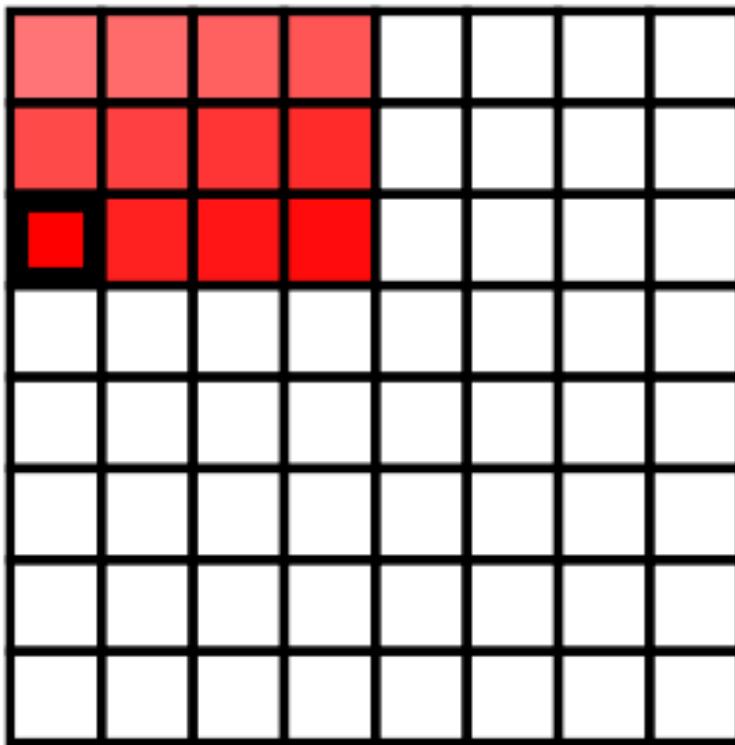
B mem:36 cache hit:20



C mem:36 cache hit:27

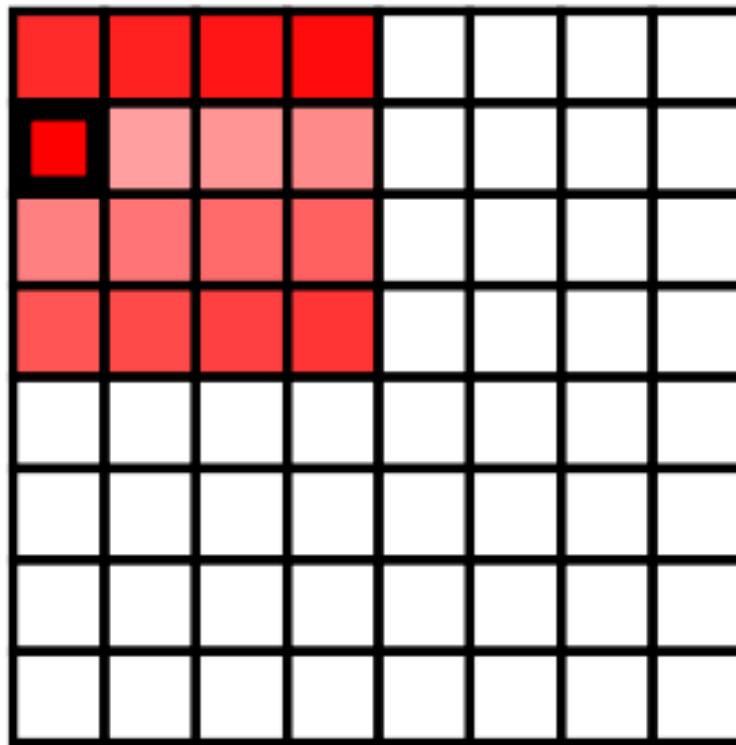


A mem:37 cache hit:25



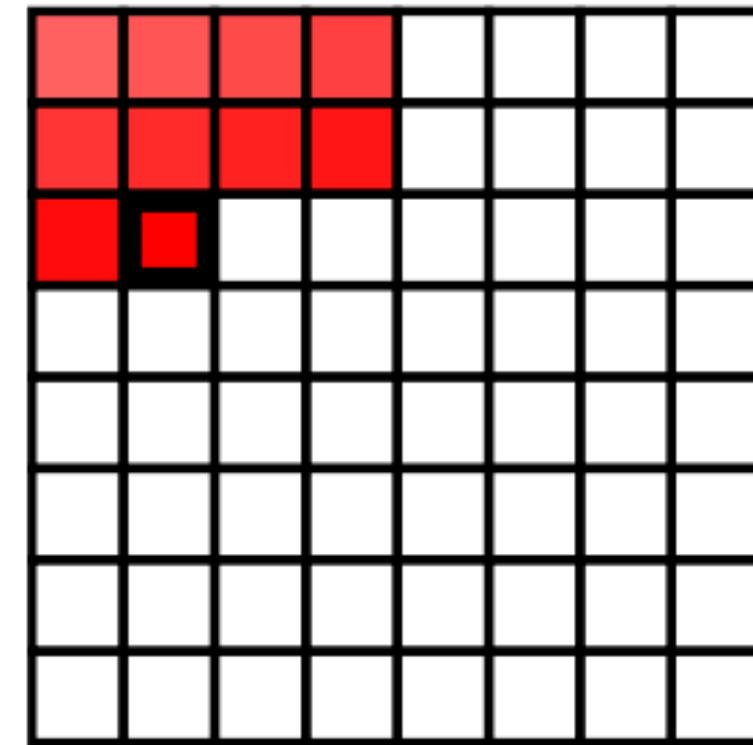
mem:111

B mem:37 cache hit:21

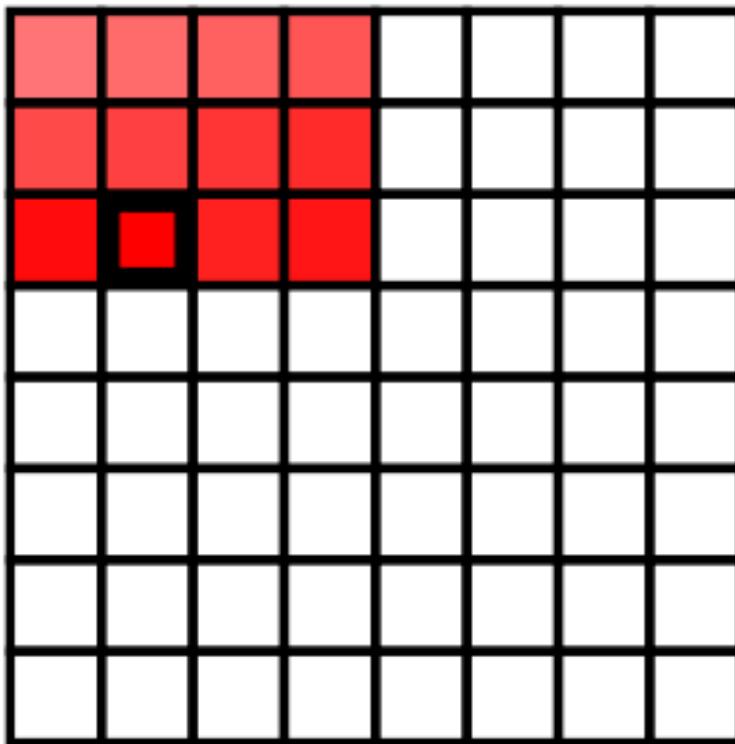


cache hits:73 $\cong 65\%$

C mem:37 cache hit:27

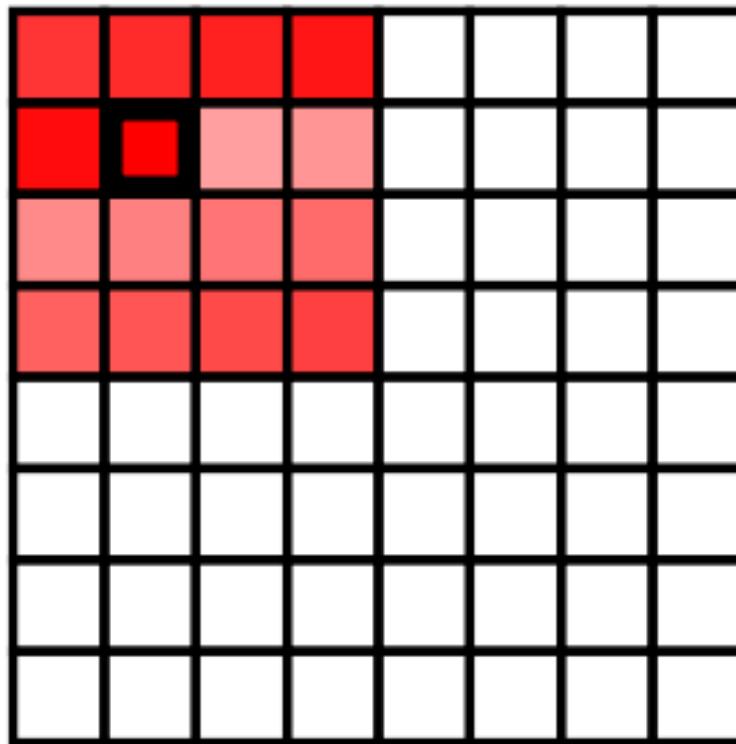


A mem:38 cache hit:26



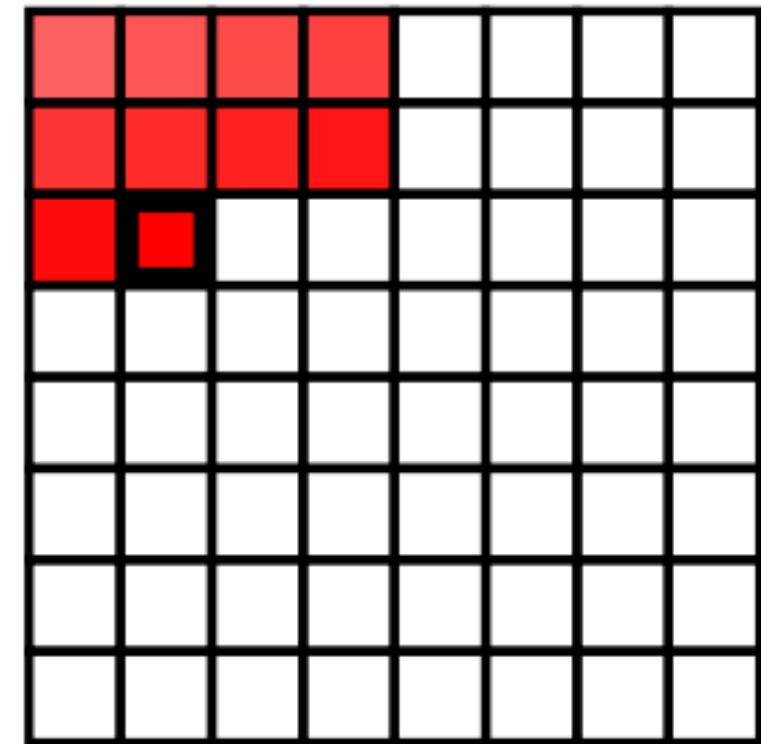
mem:114

B mem:38 cache hit:22

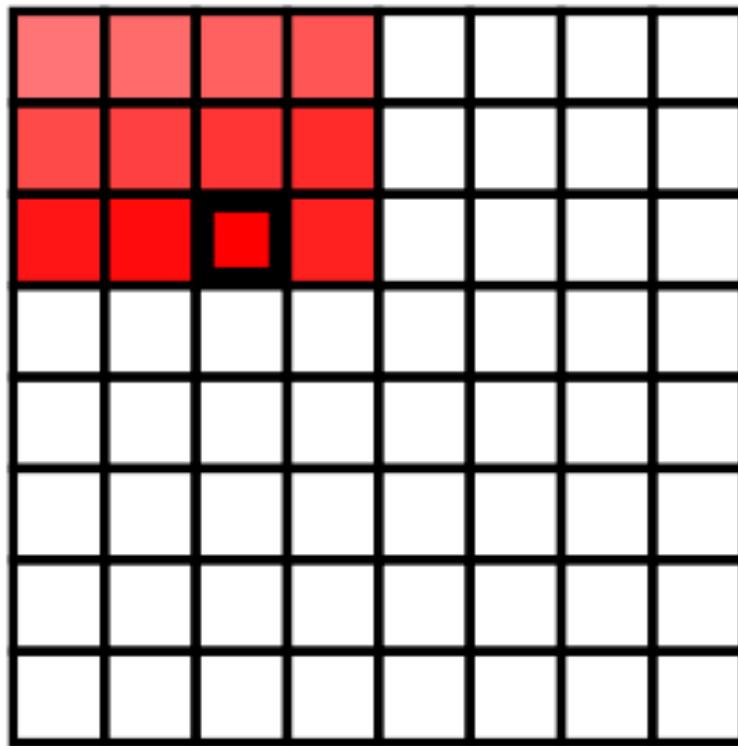


cache hits:76 $\approx 66\%$

C mem:38 cache hit:28

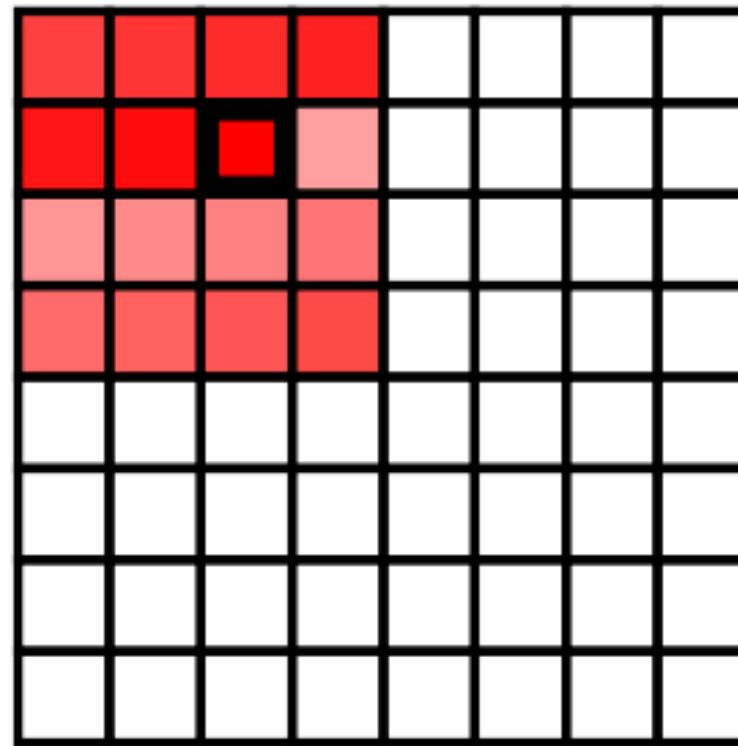


A mem:39 cache hit:27



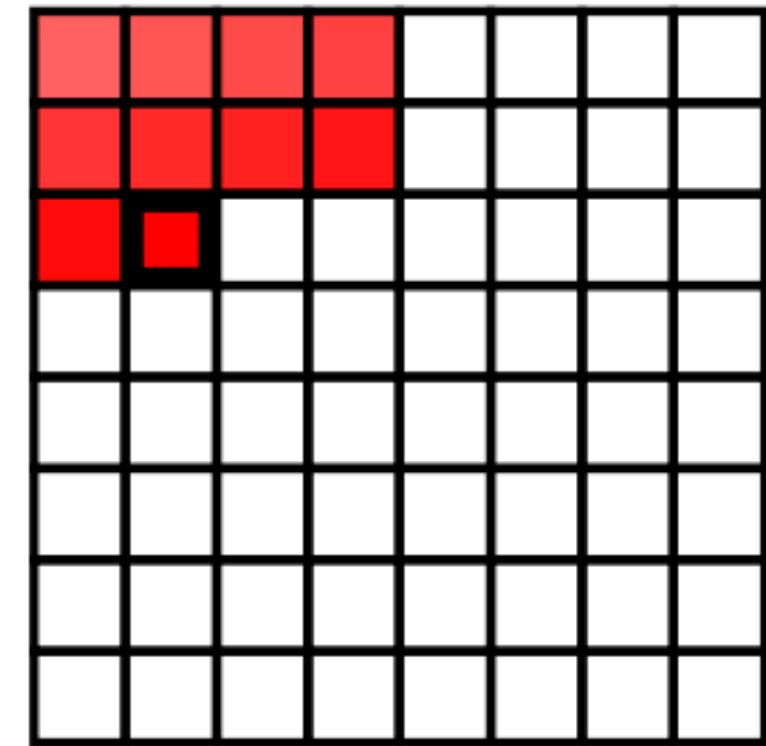
mem:117

B mem:39 cache hit:23

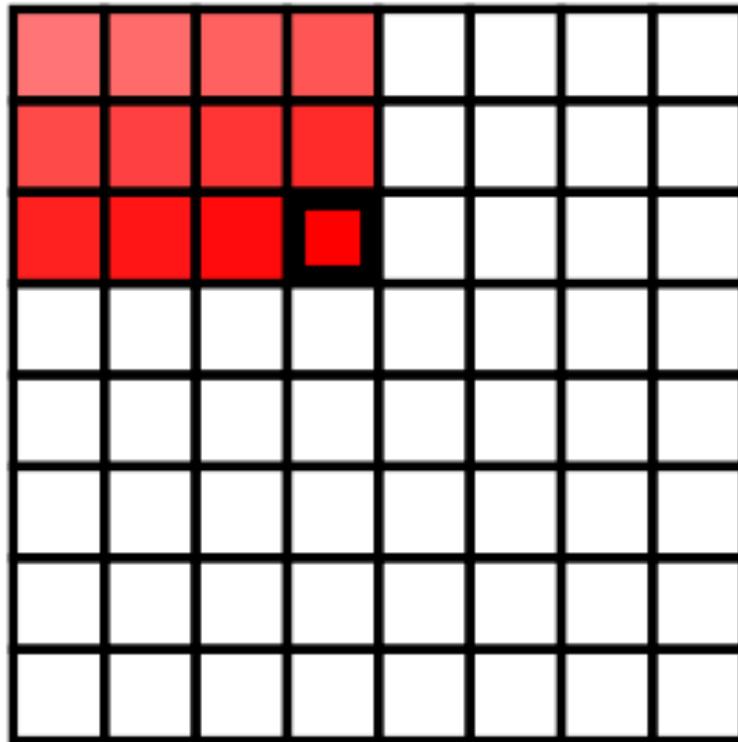


cache hits:79 $\cong 67\%$

C mem:39 cache hit:29

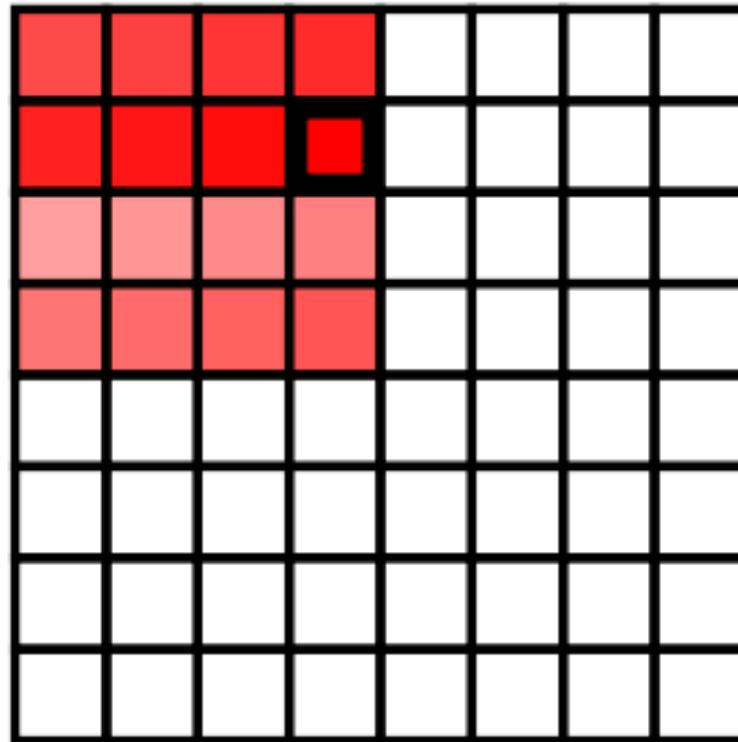


A mem:40 cache hit:28



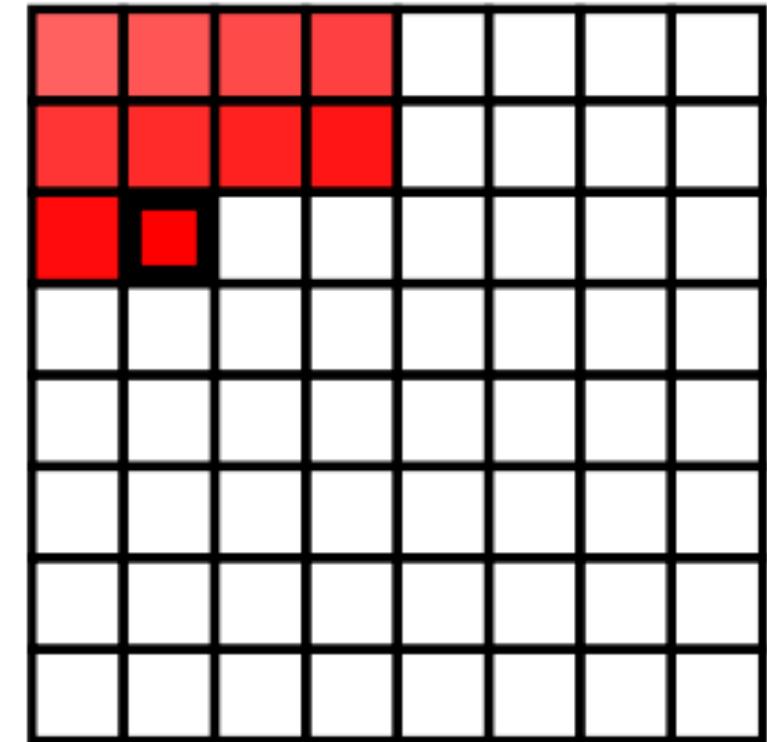
mem:120

B mem:40 cache hit:24

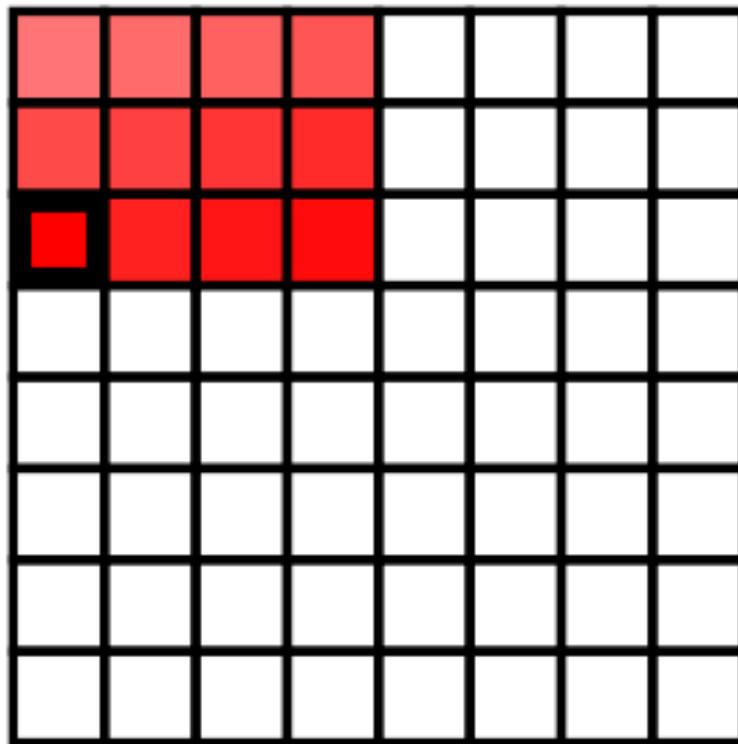


cache hits:82 $\cong 68\%$

C mem:40 cache hit:30

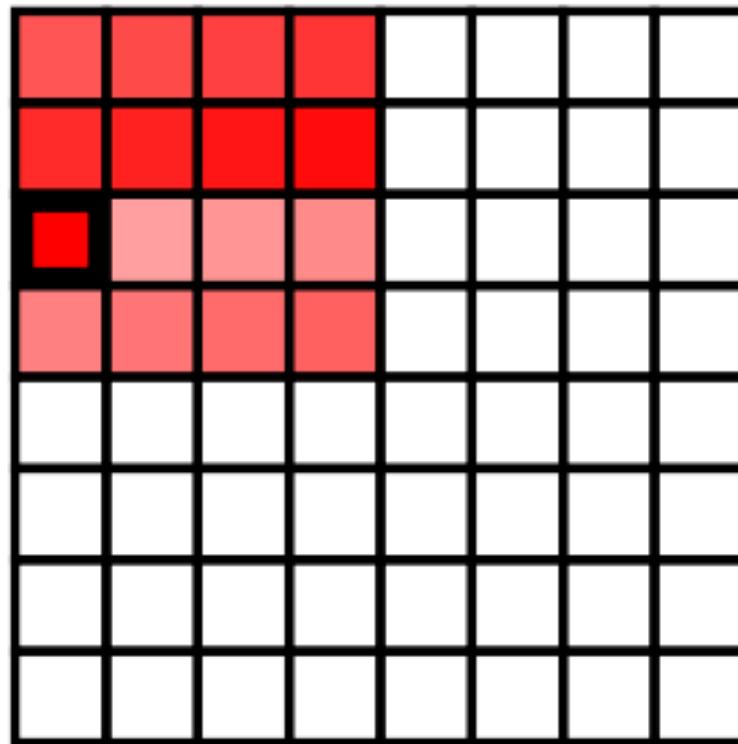


A mem:41 cache hit:29



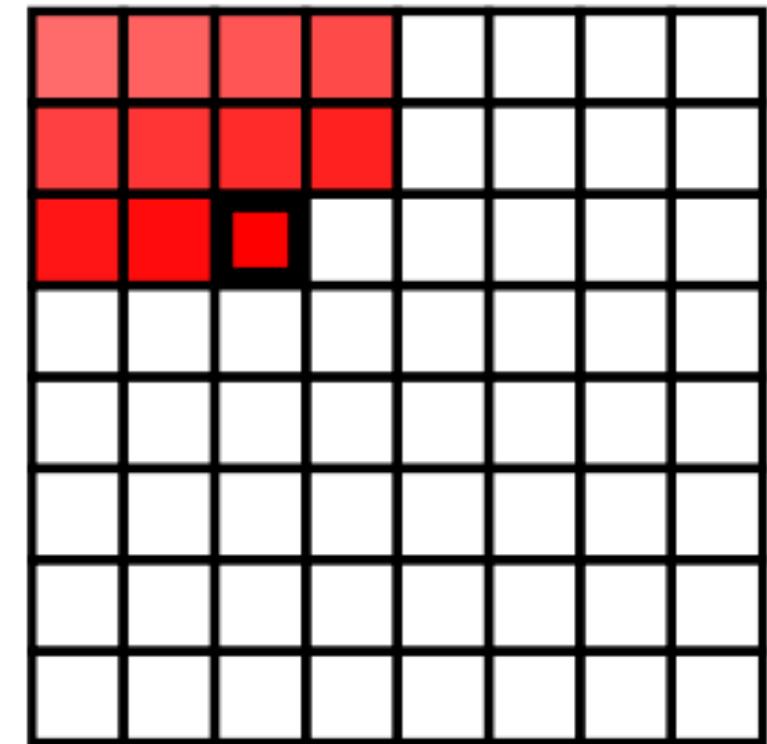
mem:123

B mem:41 cache hit:25

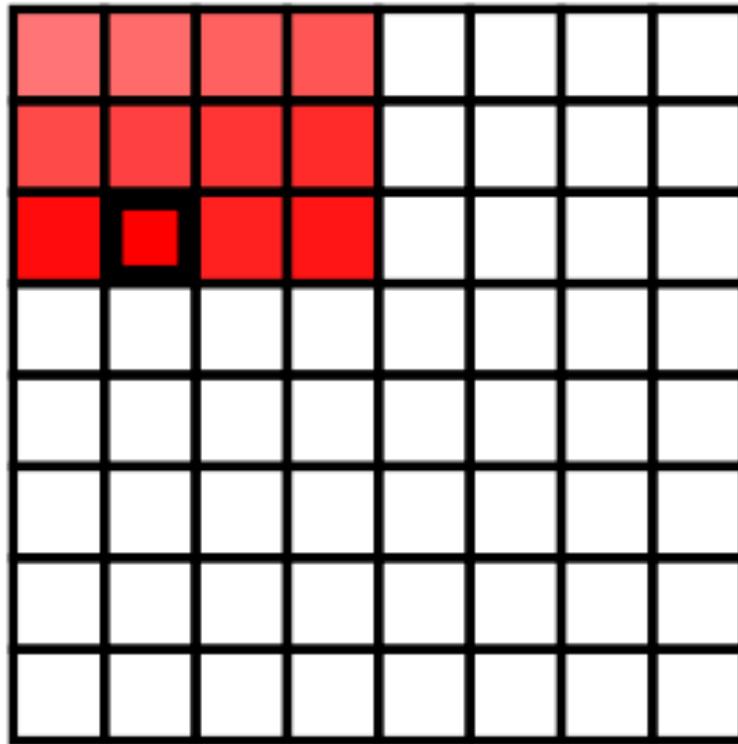


cache hits:84 $\cong 68\%$

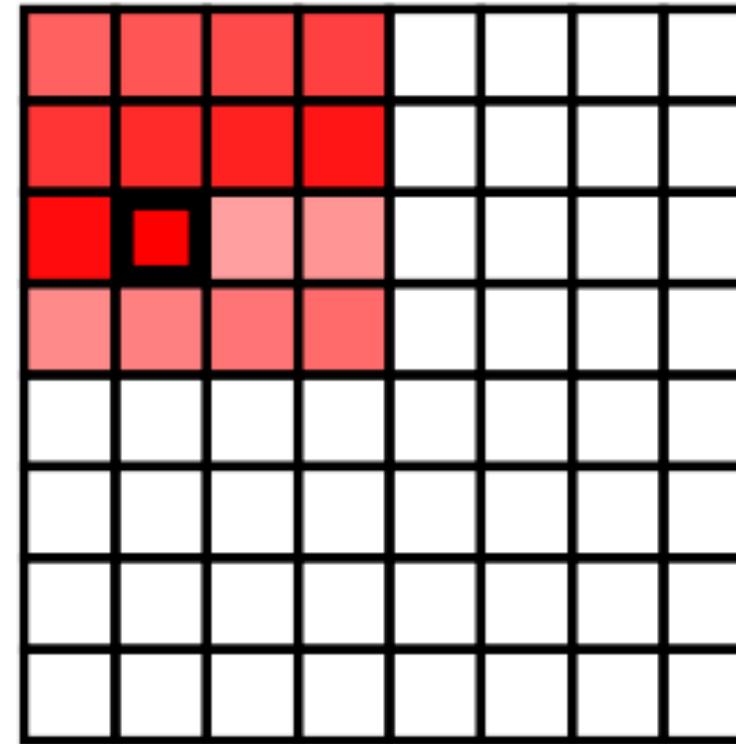
C mem:41 cache hit:30



A mem:42 cache hit:30



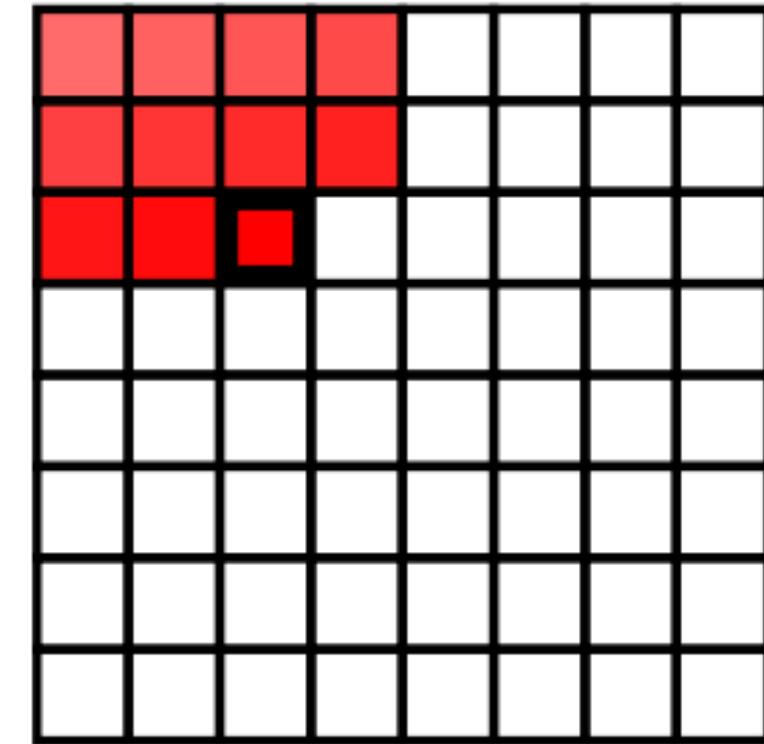
B mem:42 cache hit:26



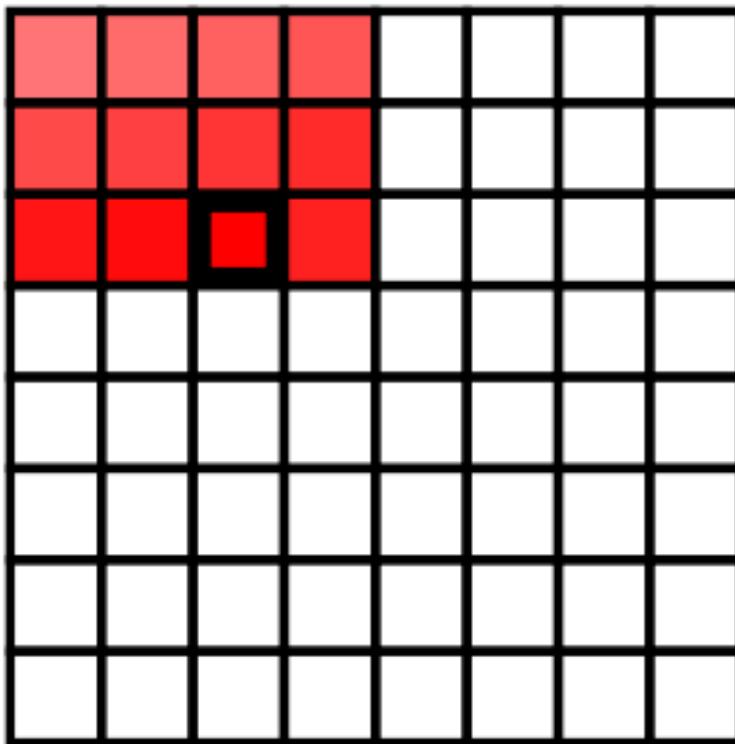
mem:126

cache hits:87 $\approx 69\%$

C mem:42 cache hit:31

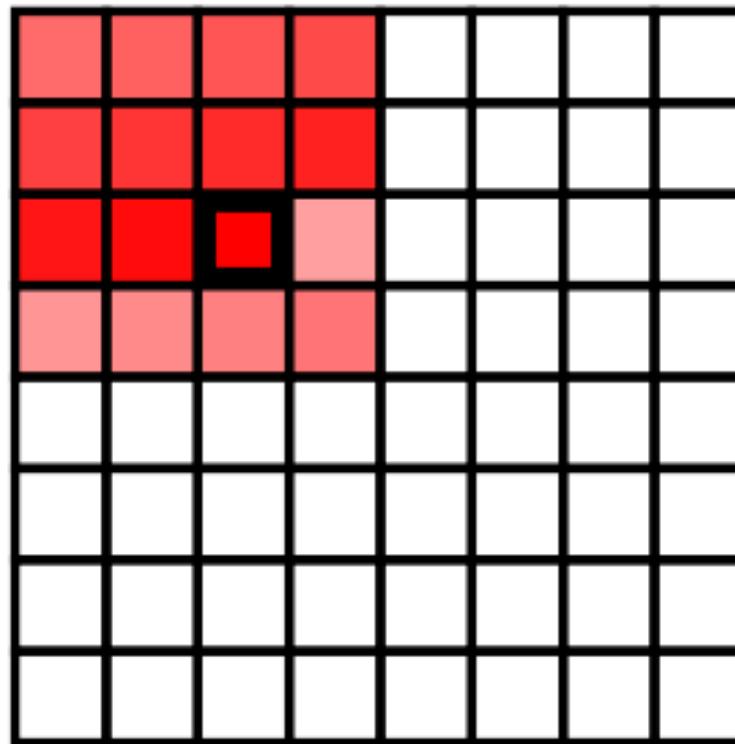


A mem:43 cache hit:31



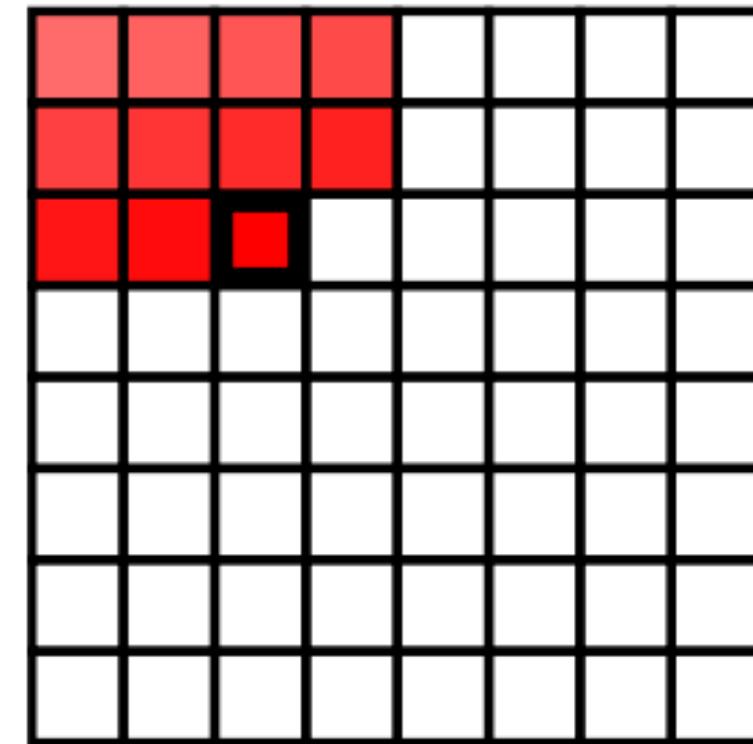
mem:129

B mem:43 cache hit:27

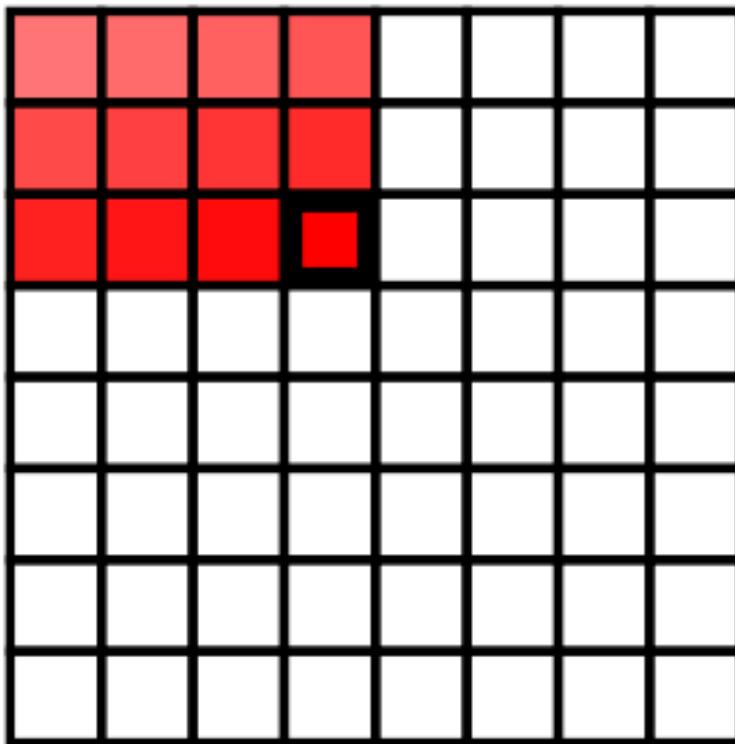


cache hits:90 $\cong 69\%$

C mem:43 cache hit:32

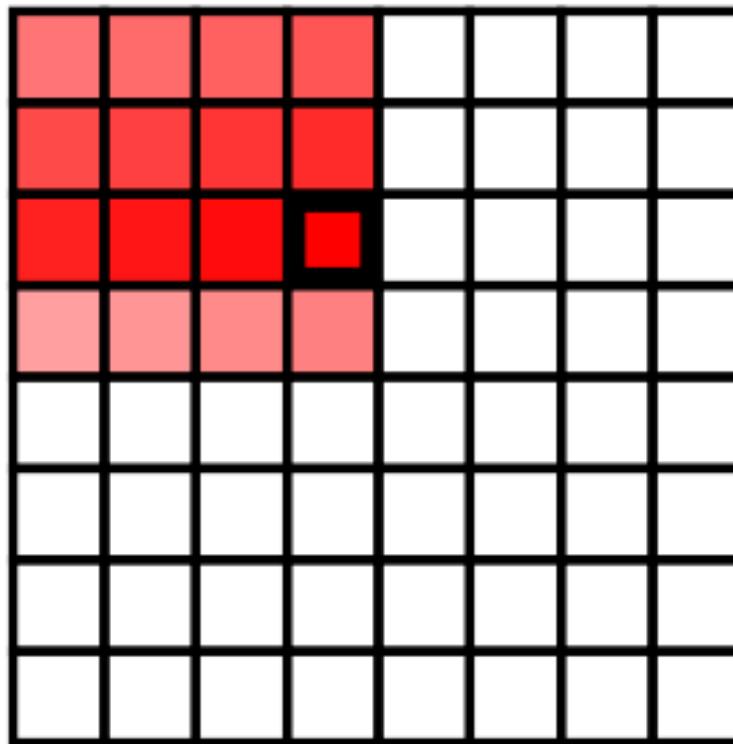


A mem:44 cache hit:32



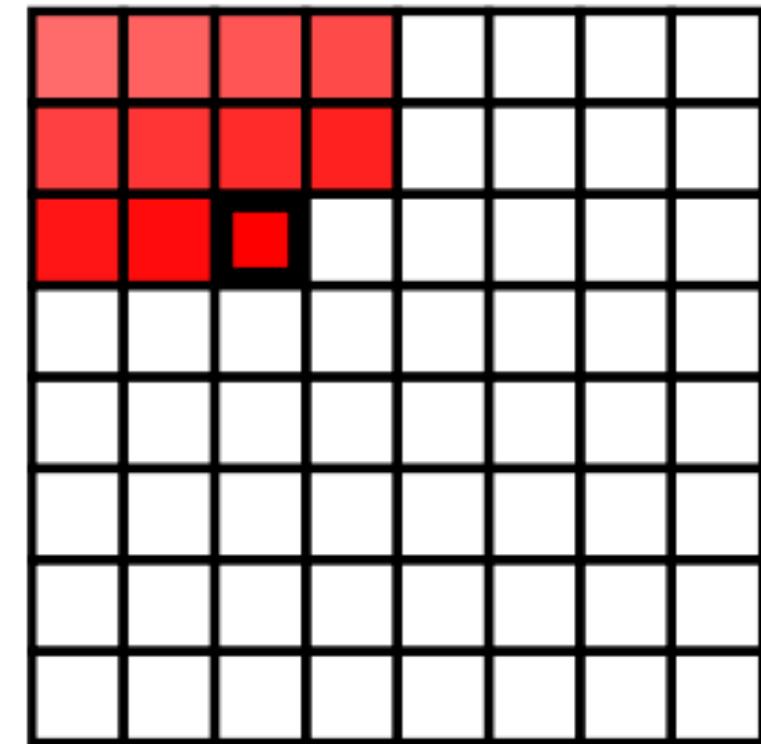
mem:132

B mem:44 cache hit:28

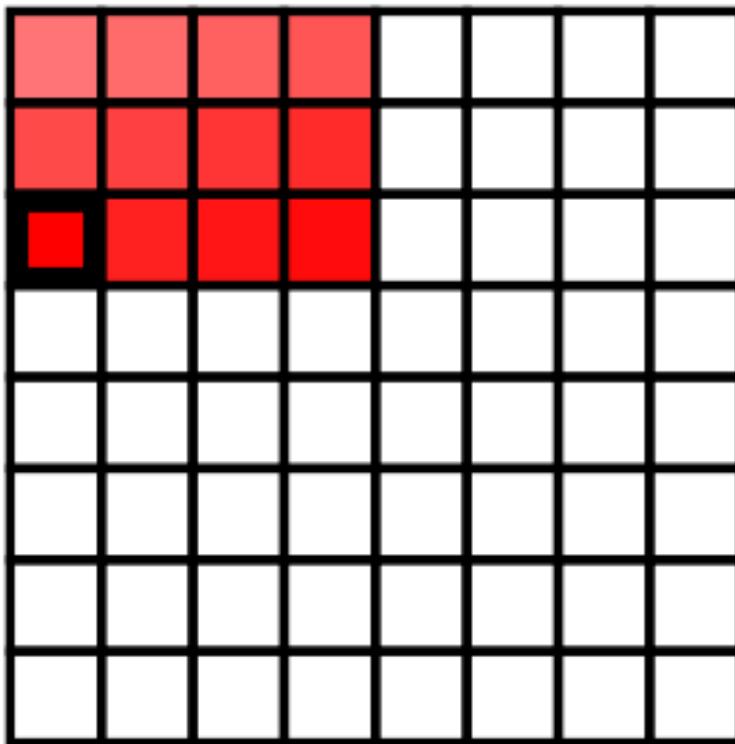


cache hits:93 $\cong 70\%$

C mem:44 cache hit:33

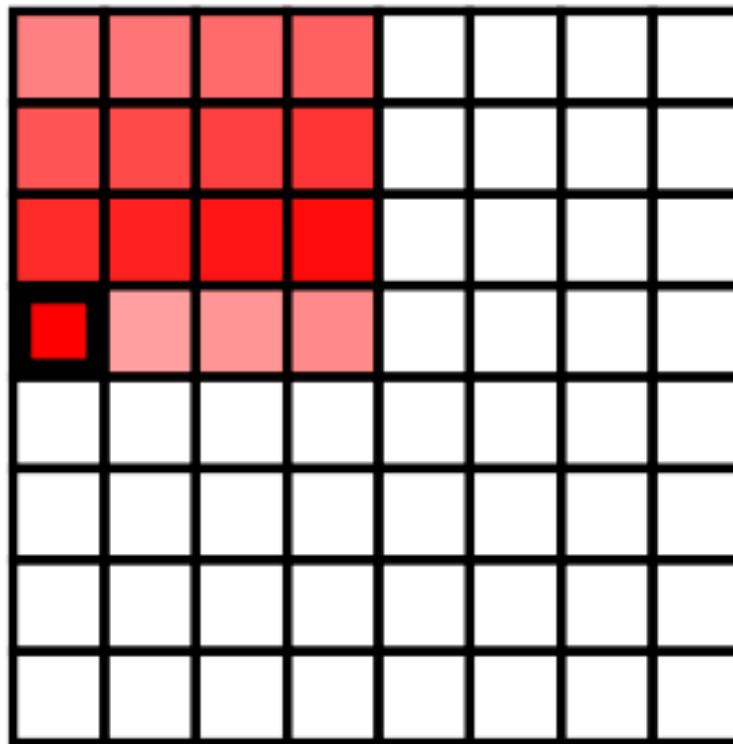


A mem:45 cache hit:33



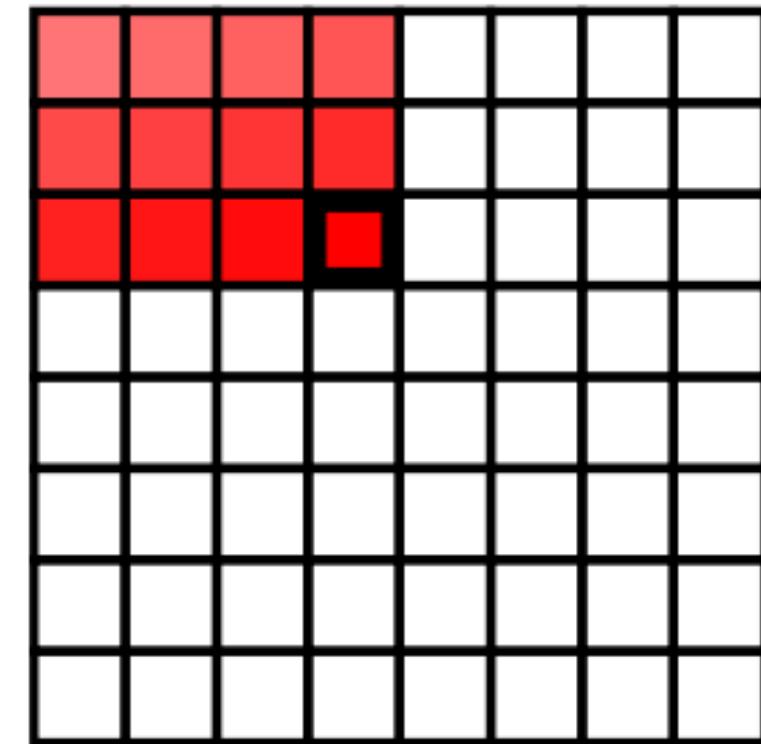
mem:135

B mem:45 cache hit:29

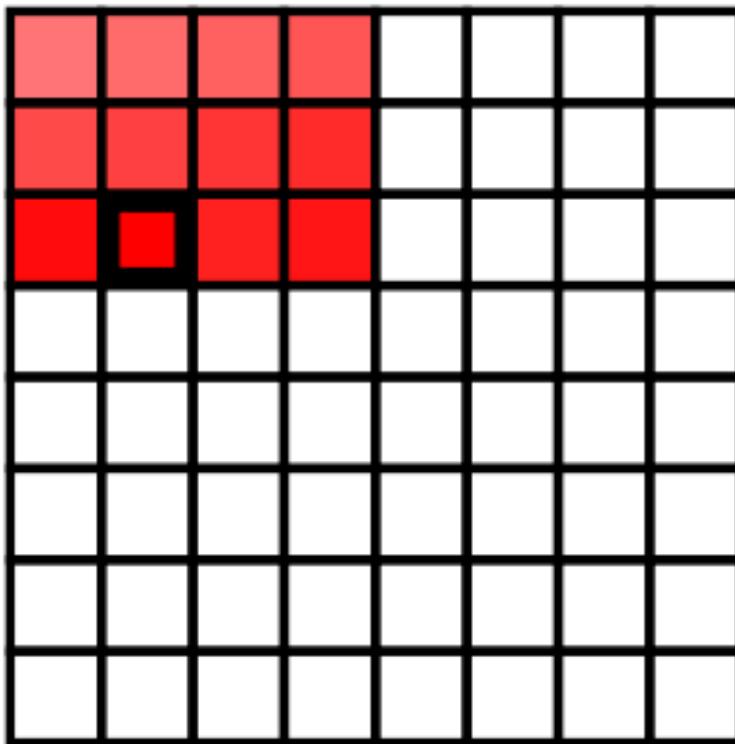


cache hits:95 $\cong 70\%$

C mem:45 cache hit:33

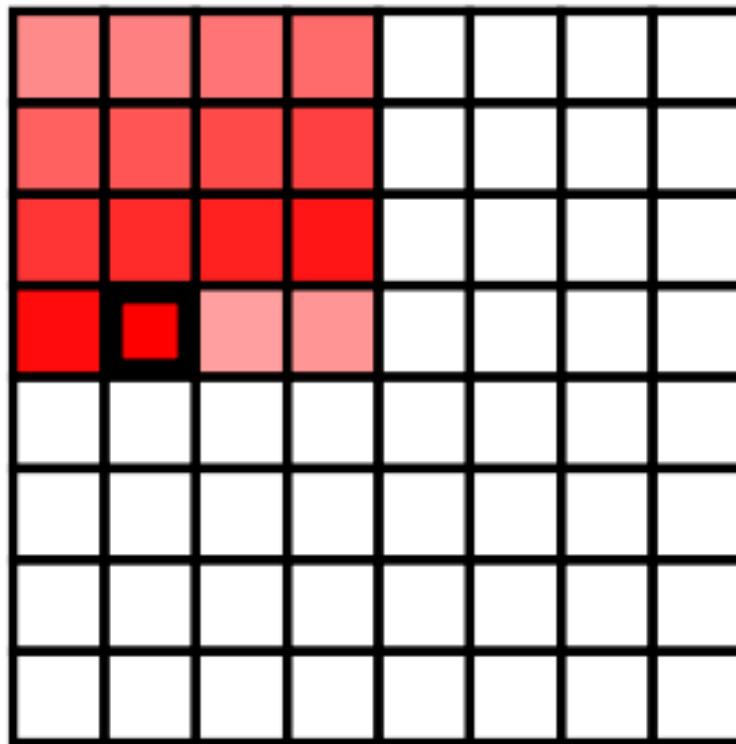


A mem:46 cache hit:34



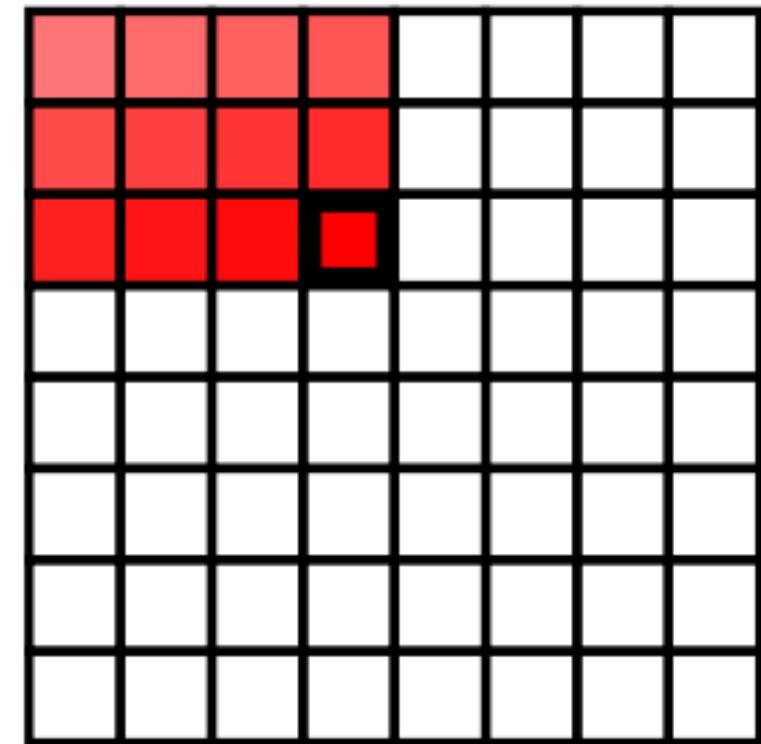
mem:138

B mem:46 cache hit:30

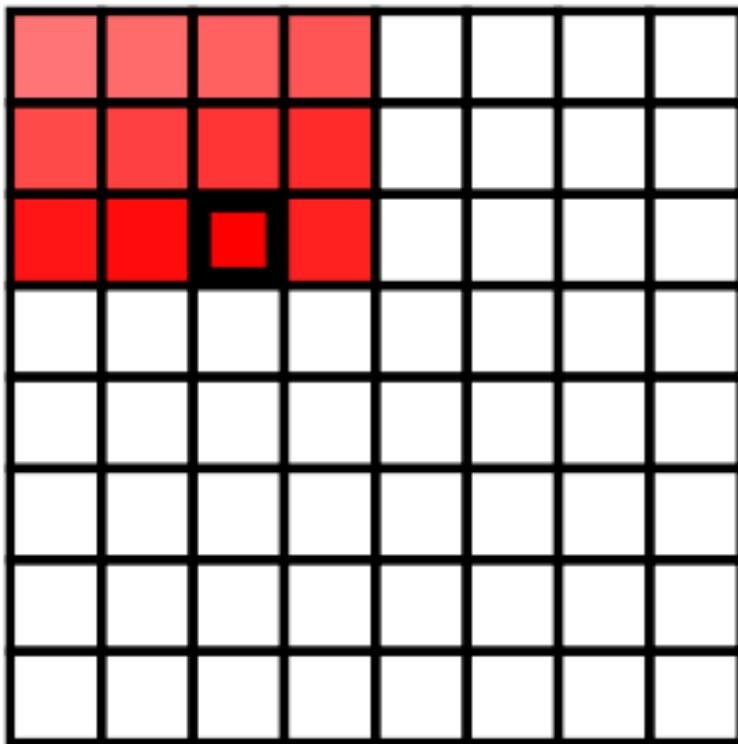


cache hits:98 $\cong 71\%$

C mem:46 cache hit:34

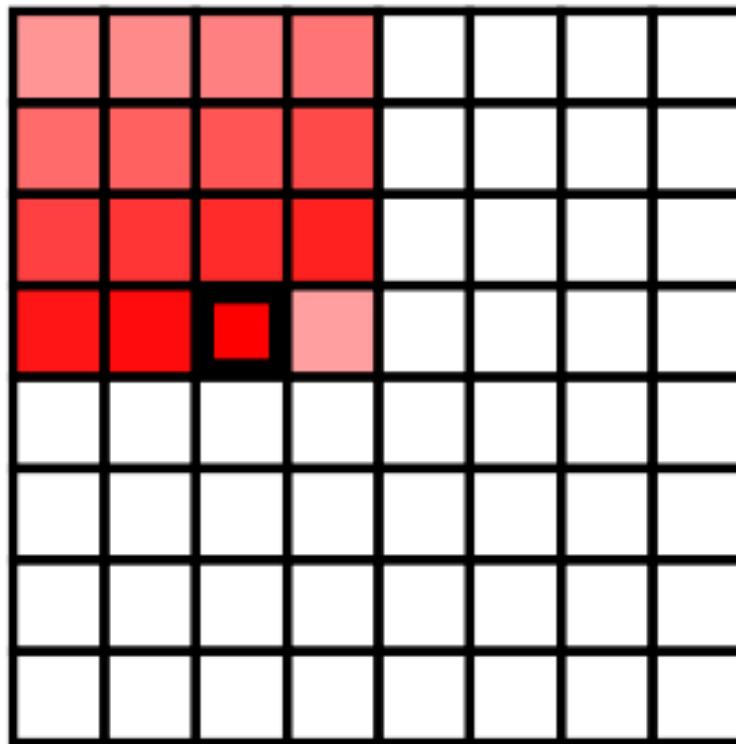


A mem:47 cache hit:35



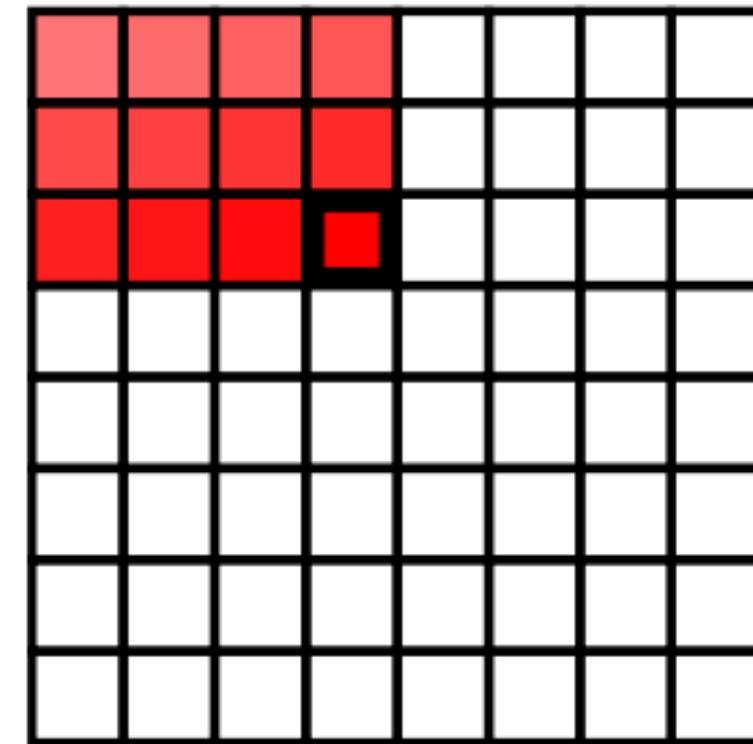
mem:141

B mem:47 cache hit:31

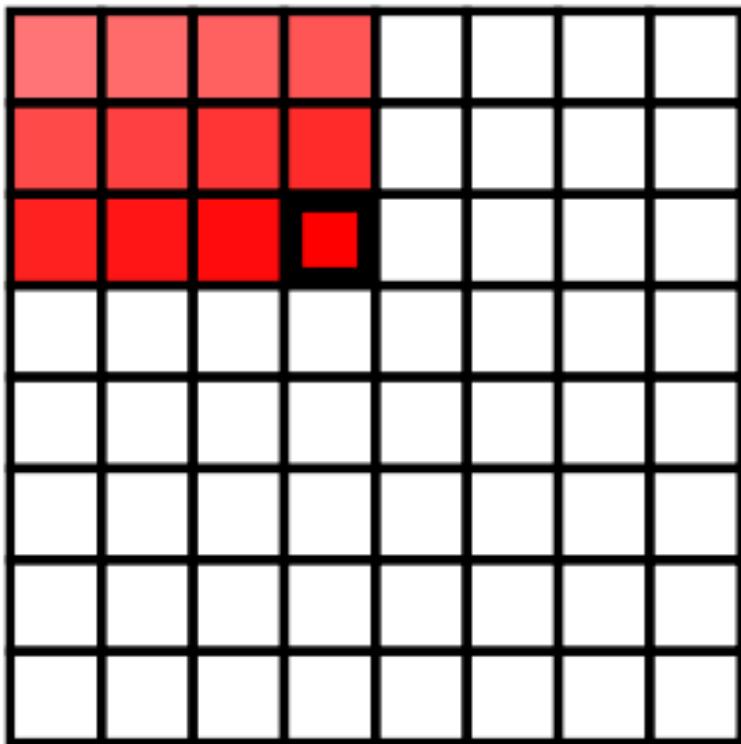


cache hits:101 ≈71%

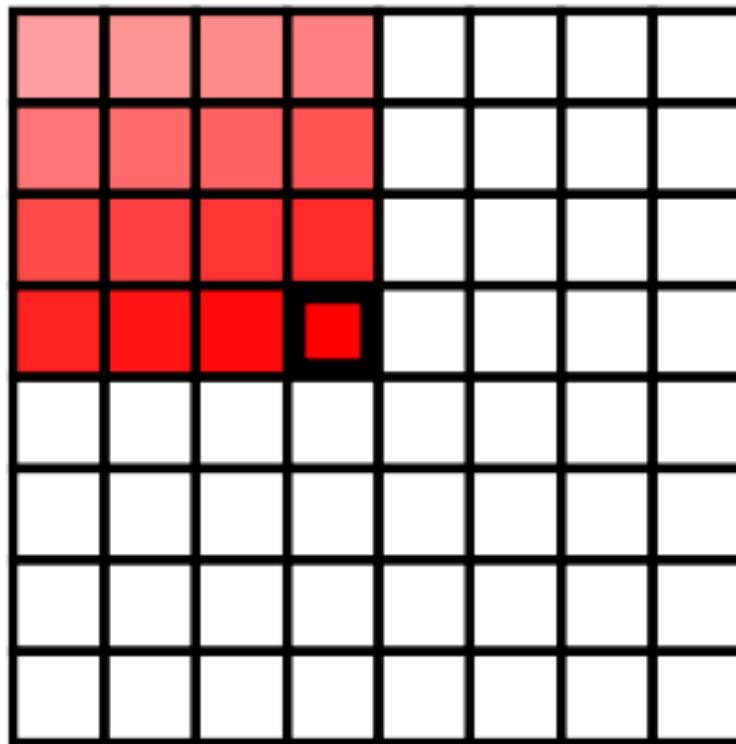
C mem:47 cache hit:35



A mem:48 cache hit:36



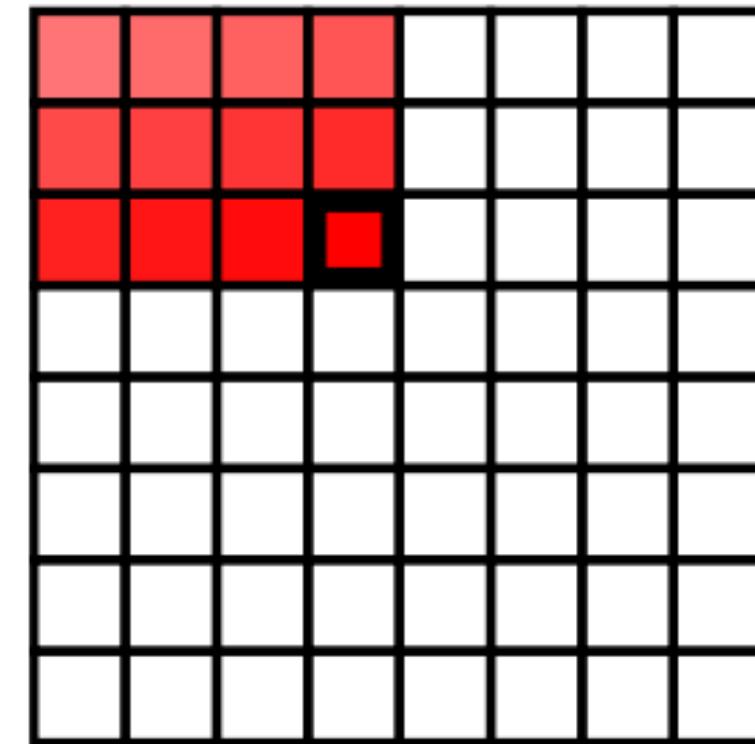
B mem:48 cache hit:32



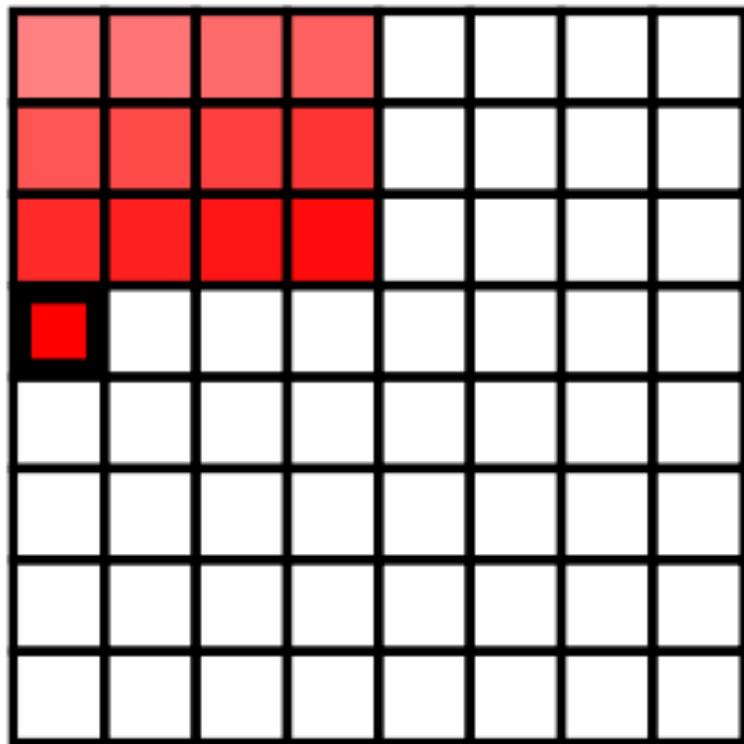
mem:144

cache hits:104 ≈72%

C mem:48 cache hit:36

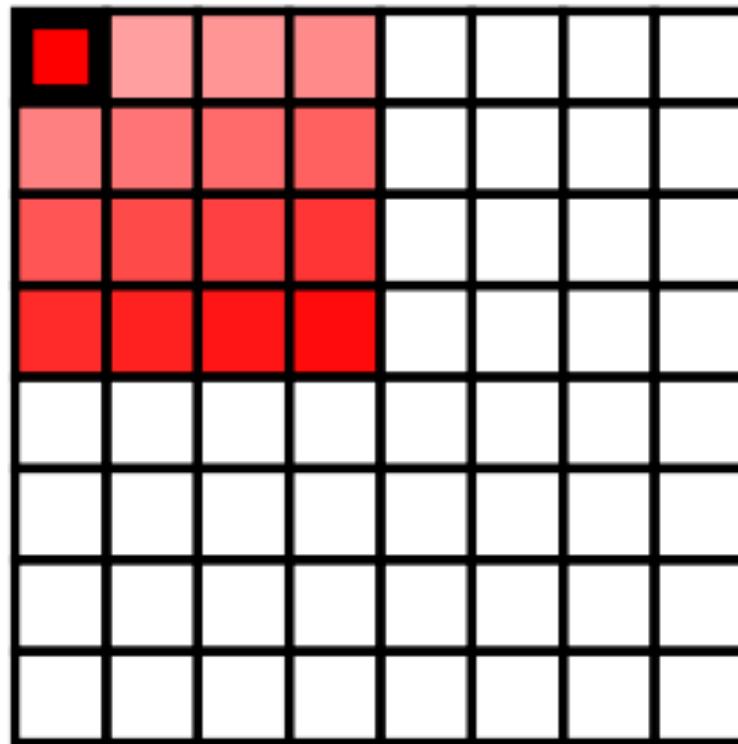


A mem:49 cache hit:36



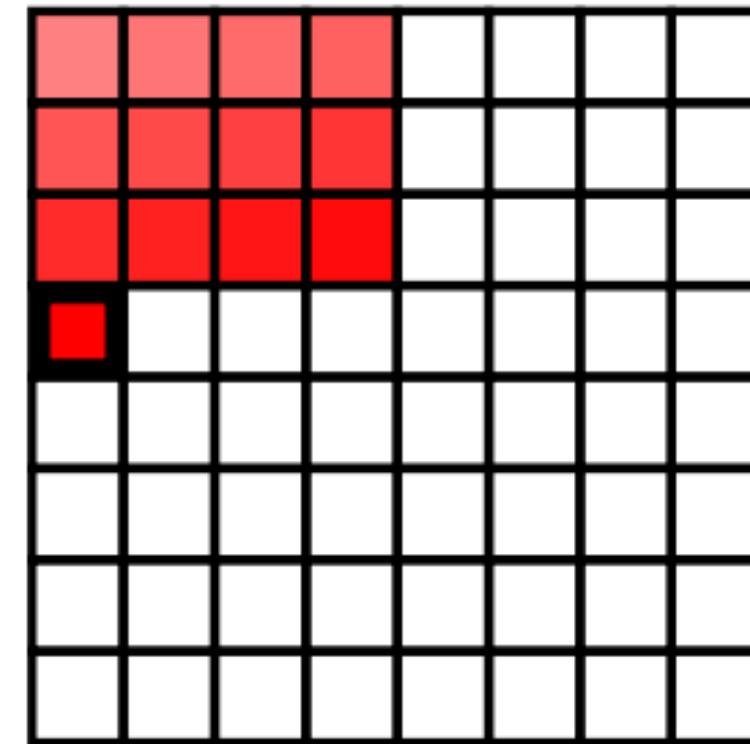
mem:147

B mem:49 cache hit:33

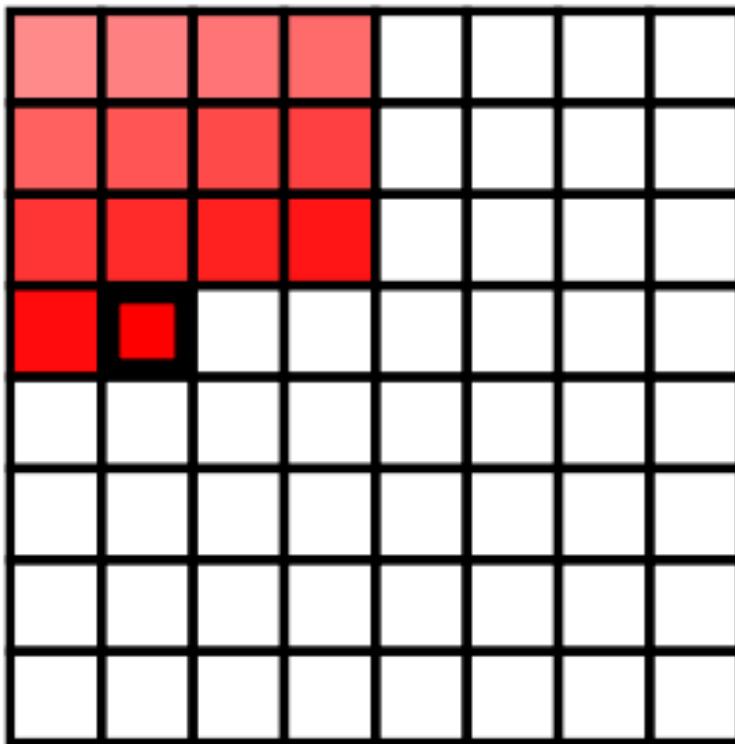


cache hits:105 ≈71%

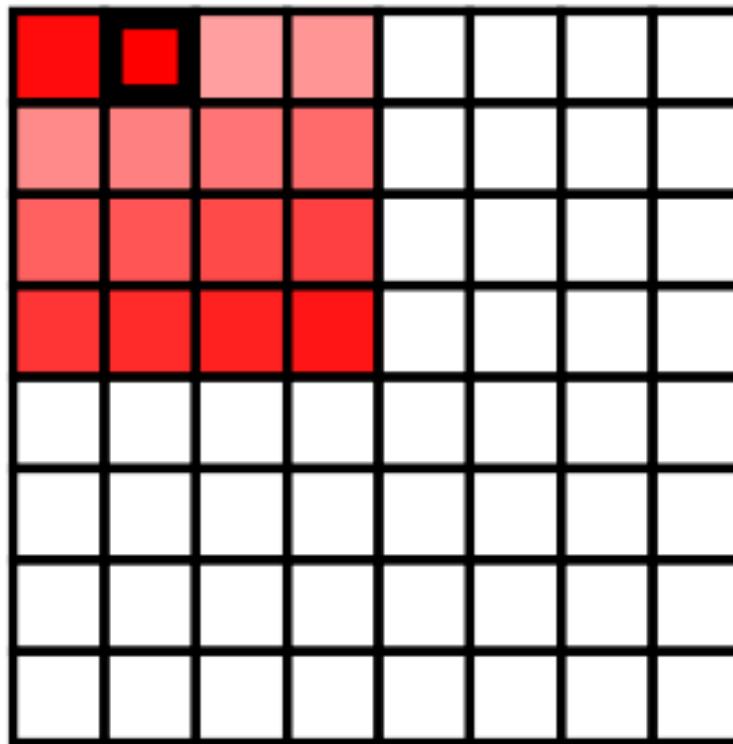
C mem:49 cache hit:36



A mem:50 cache hit:36



B mem:50 cache hit:34

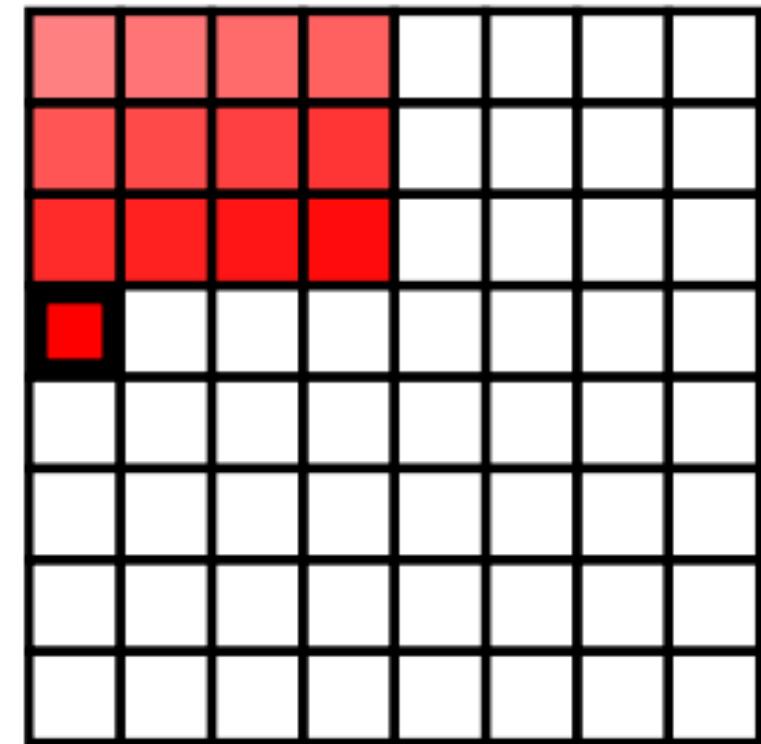


mem:150

cache hits:107 ≈71%

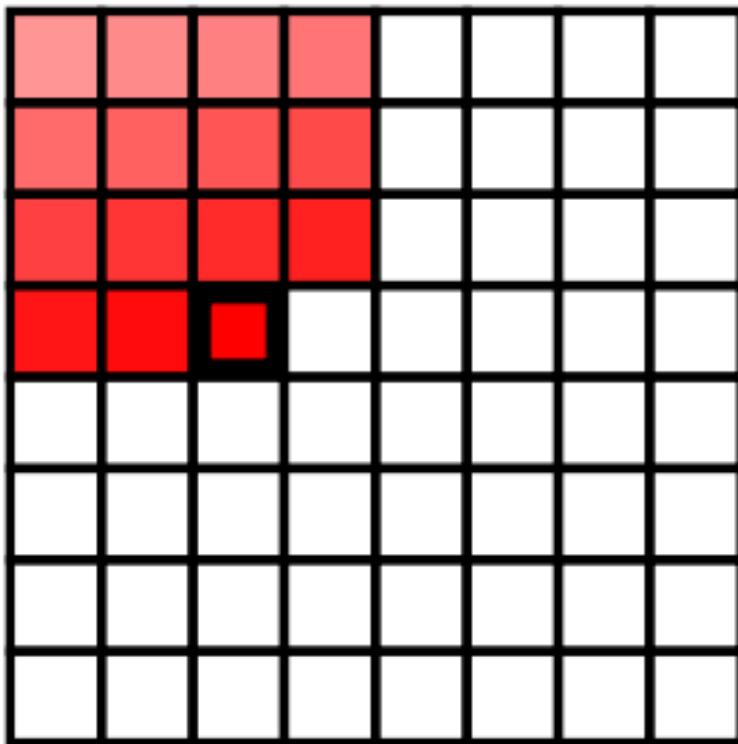
×

C mem:50 cache hit:37

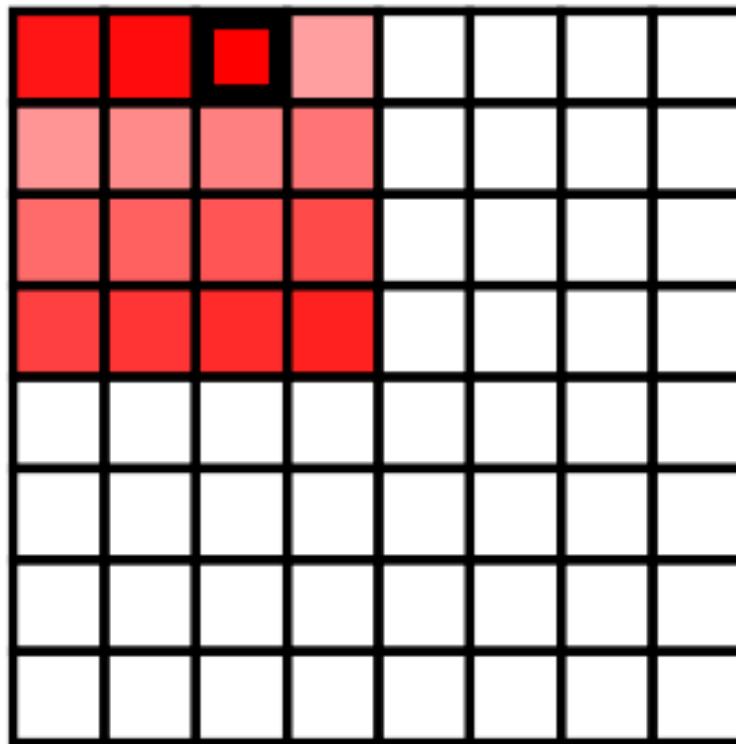


=

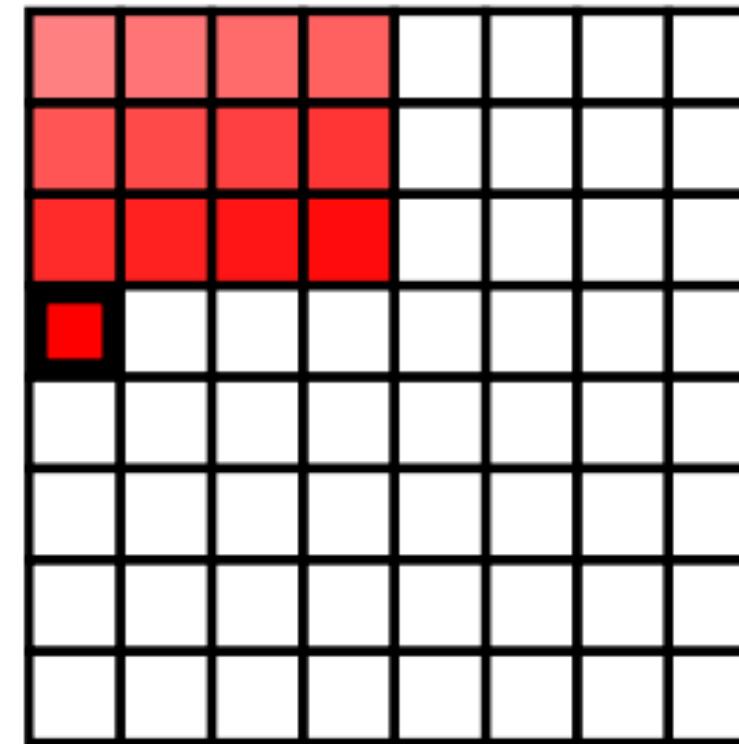
A mem:51 cache hit:36



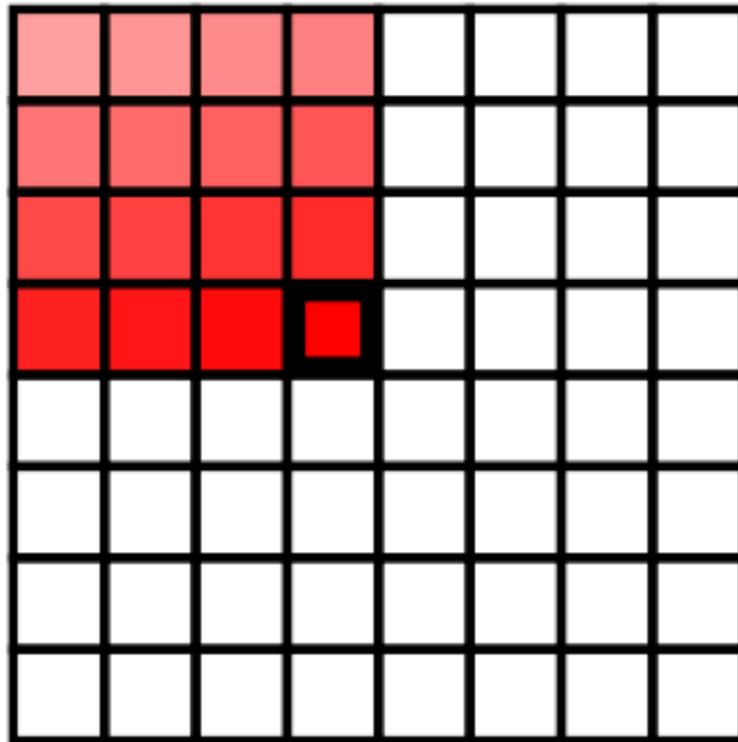
B mem:51 cache hit:35



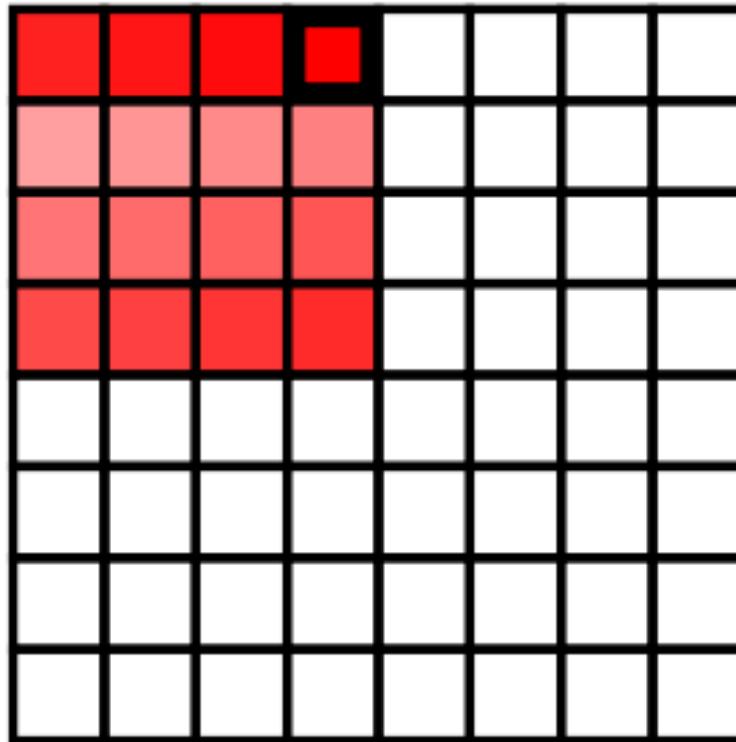
C mem:51 cache hit:38



A mem:52 cache hit:36



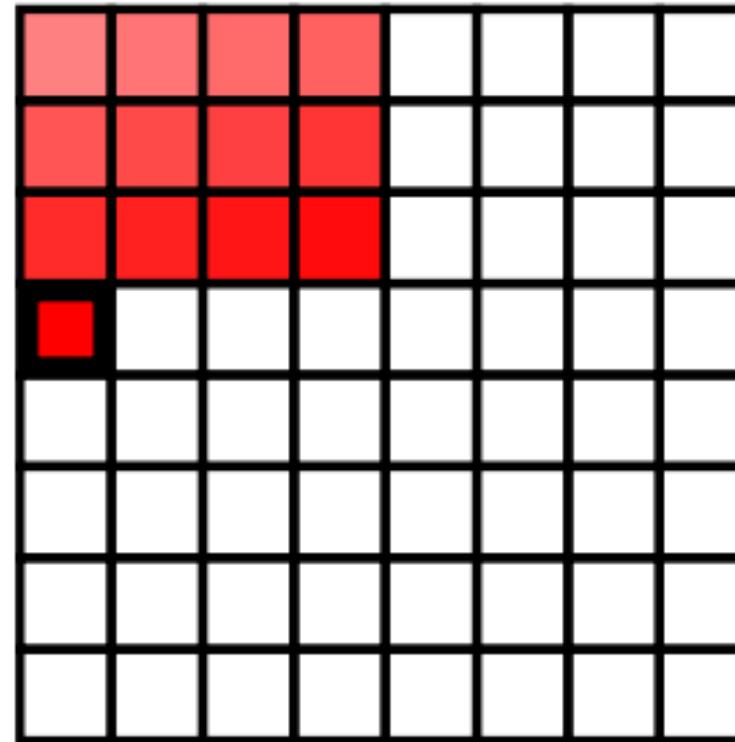
B mem:52 cache hit:36



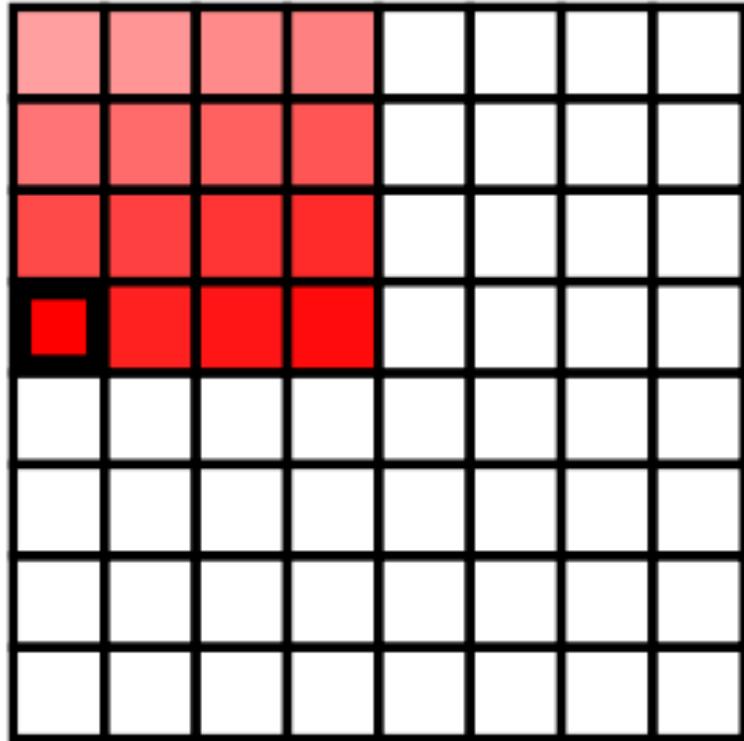
mem:156

cache hits:111 ≈71%

C mem:52 cache hit:39

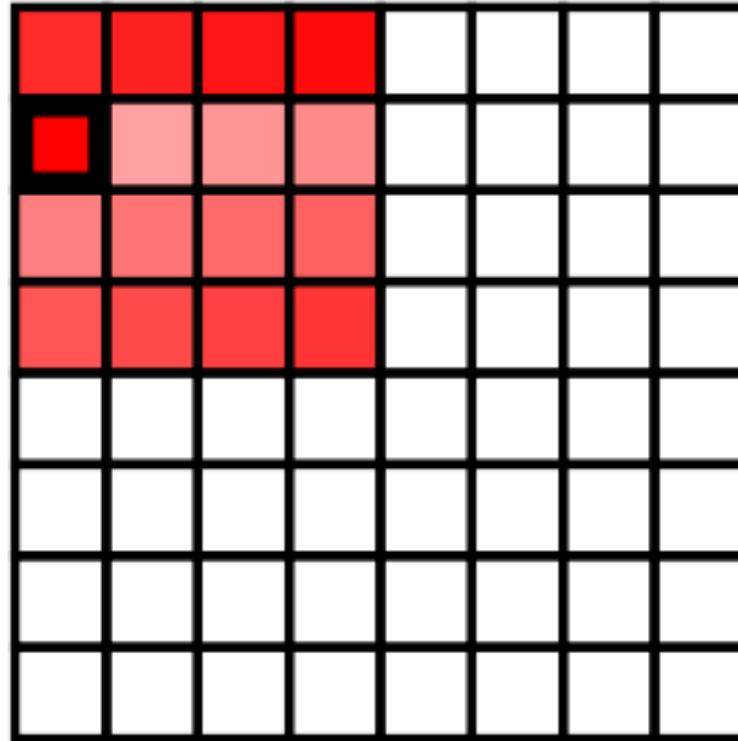


A mem:53 cache hit:37



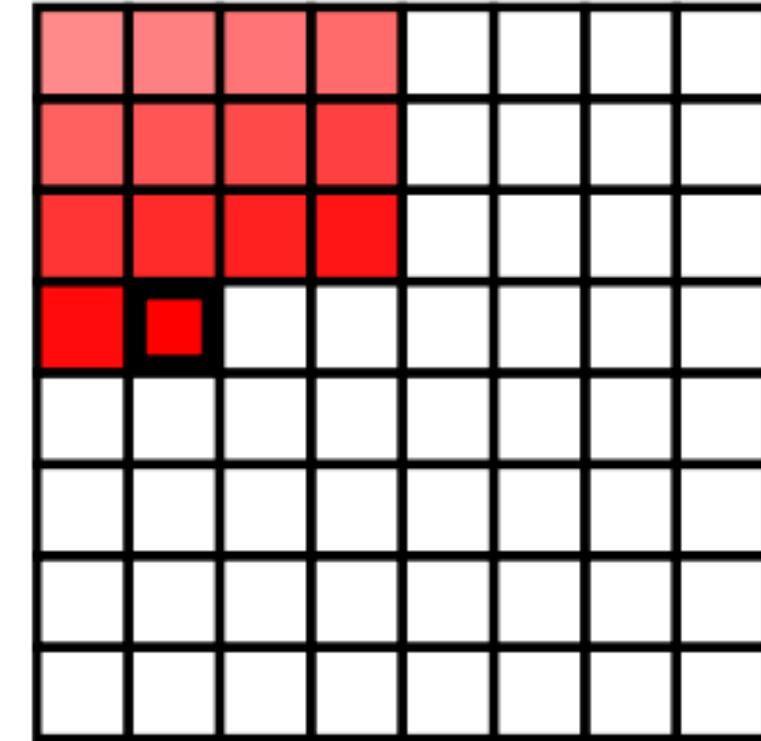
mem:159

B mem:53 cache hit:37

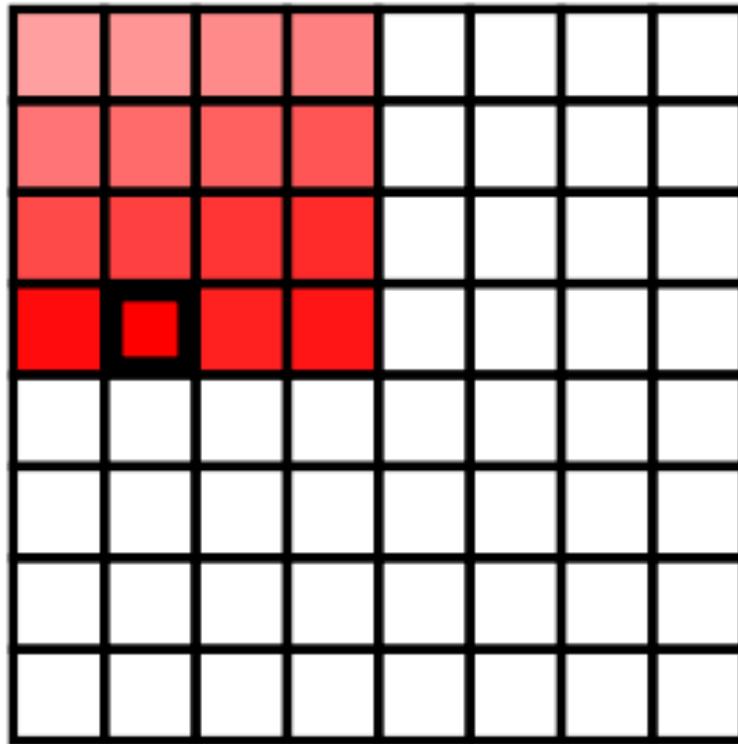


cache hits:113 ≈71%

C mem:53 cache hit:39

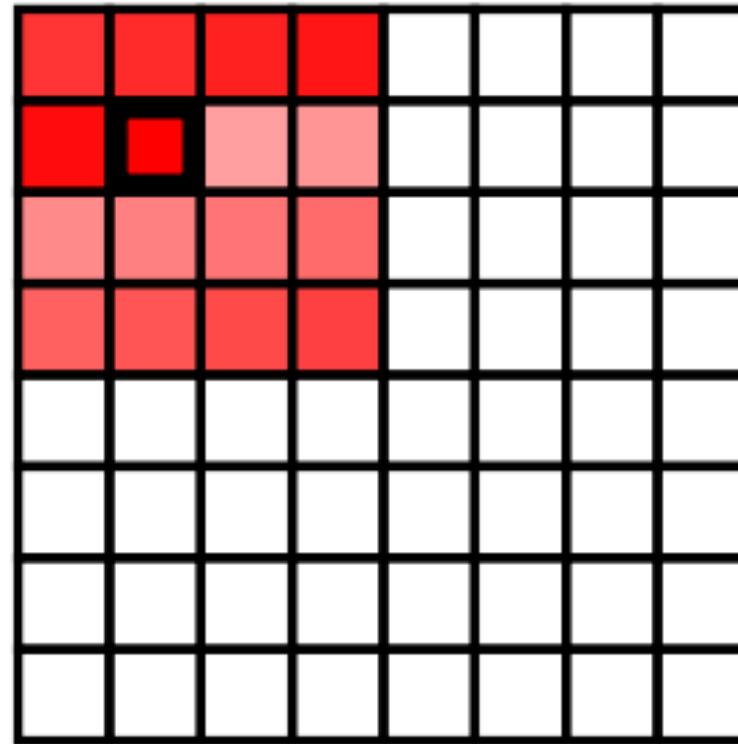


A mem:54 cache hit:38



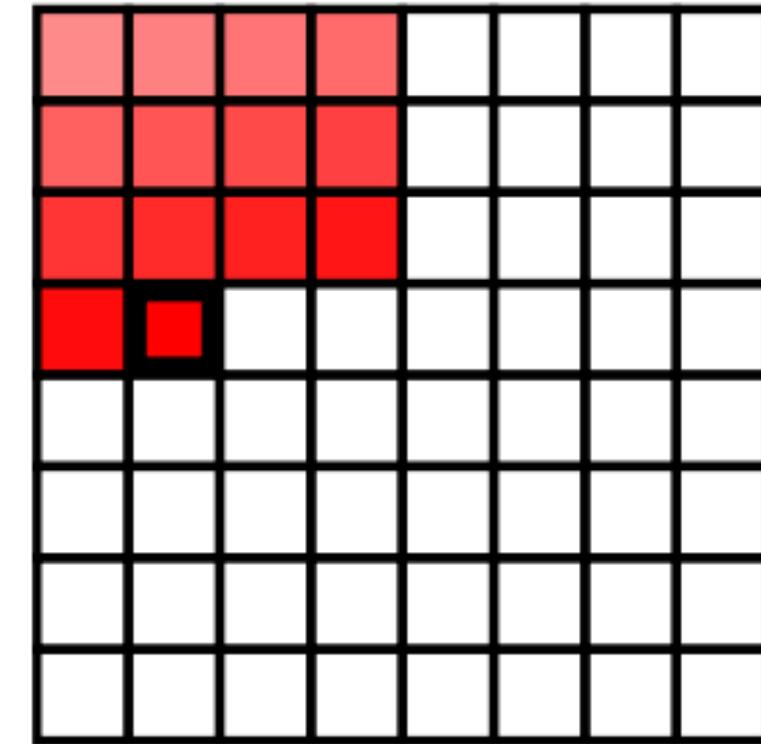
mem:162

B mem:54 cache hit:38

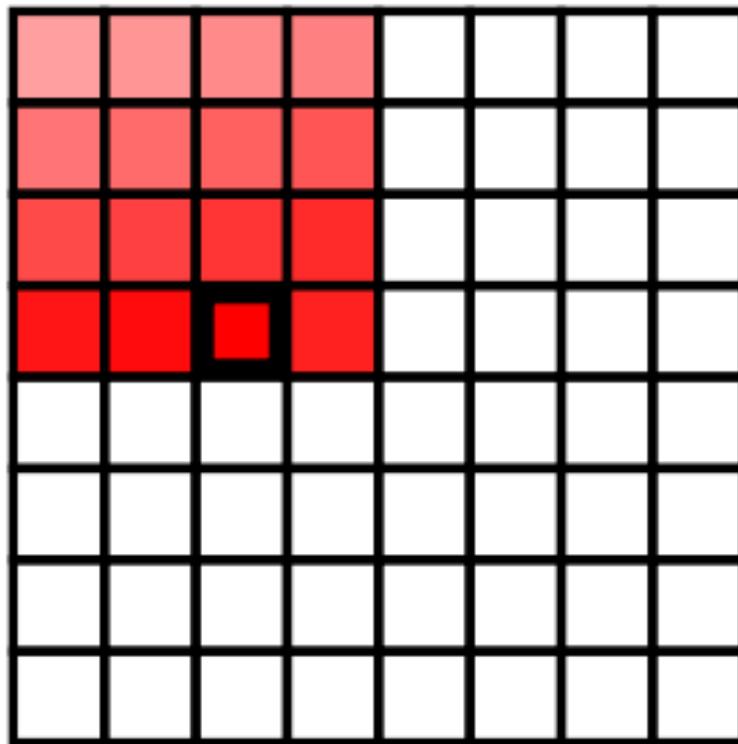


cache hits:116 ≈71%

C mem:54 cache hit:40

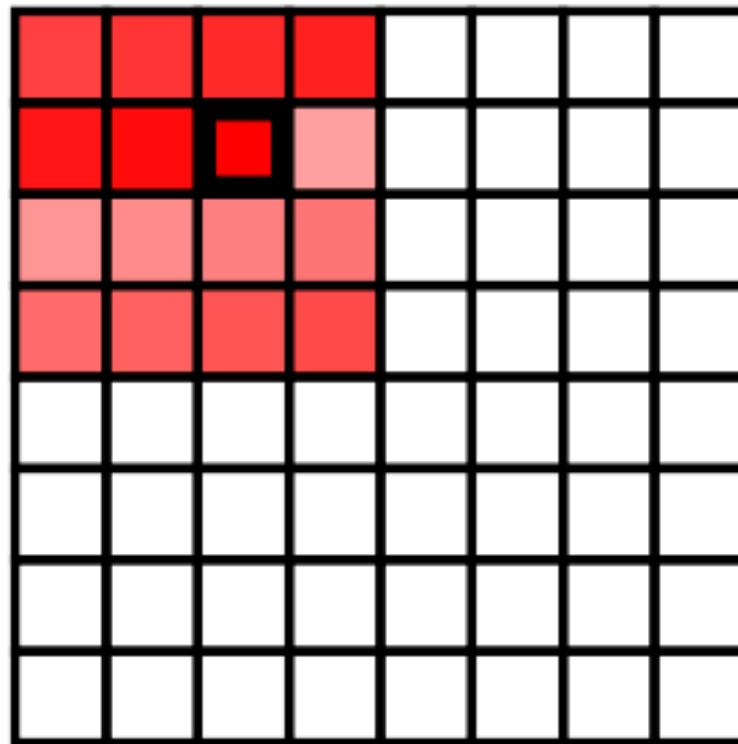


A mem:55 cache hit:39



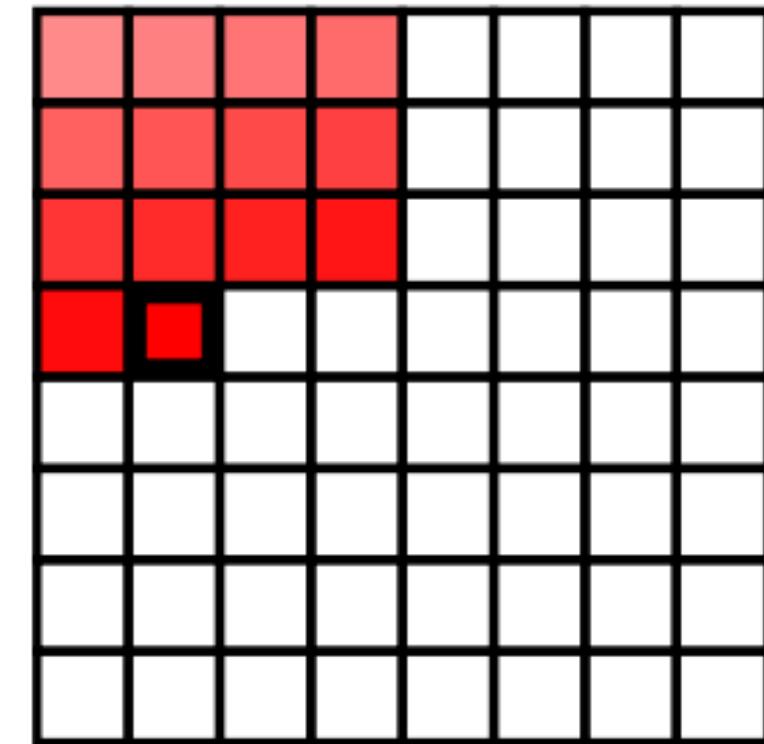
mem:165

B mem:55 cache hit:39

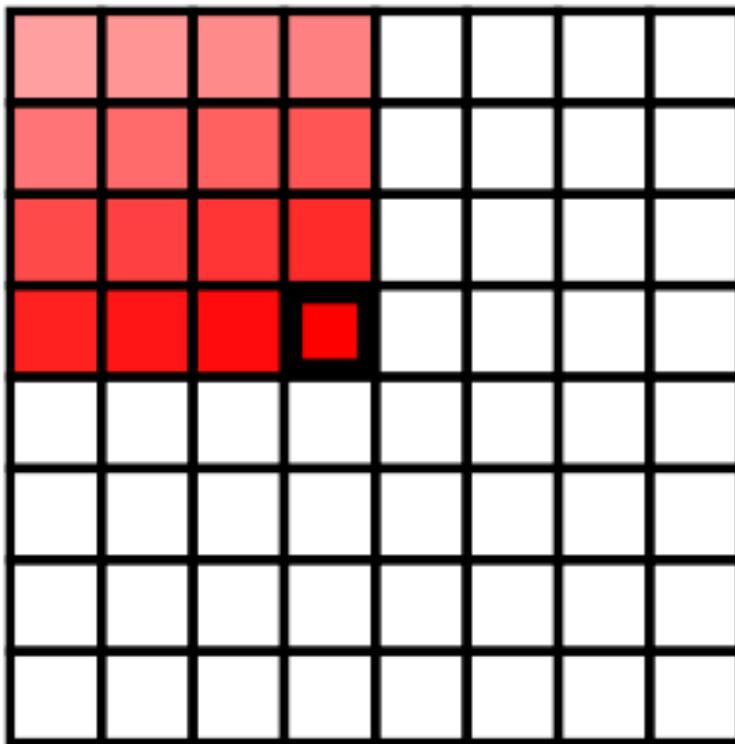


cache hits:119 ≈72%

C mem:55 cache hit:41

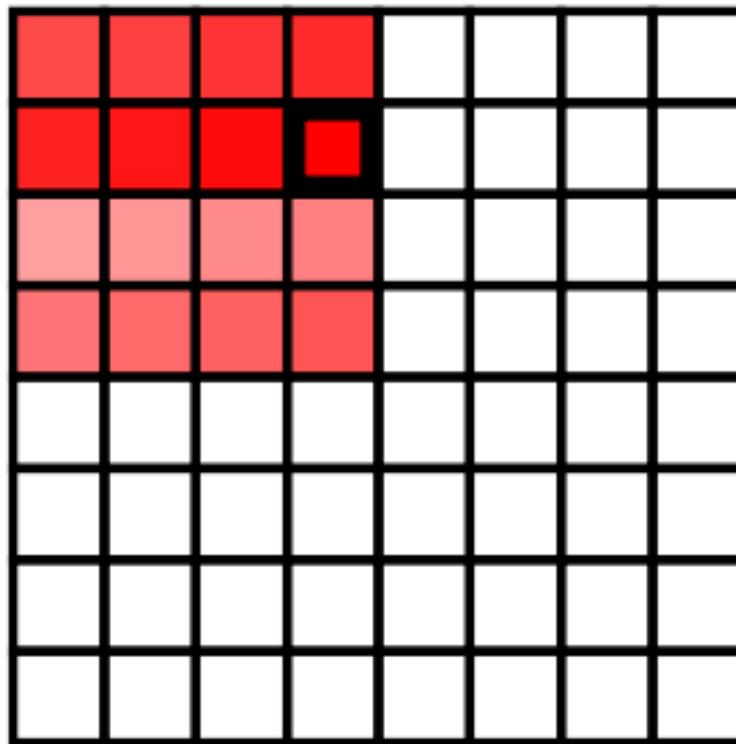


A mem:56 cache hit:40



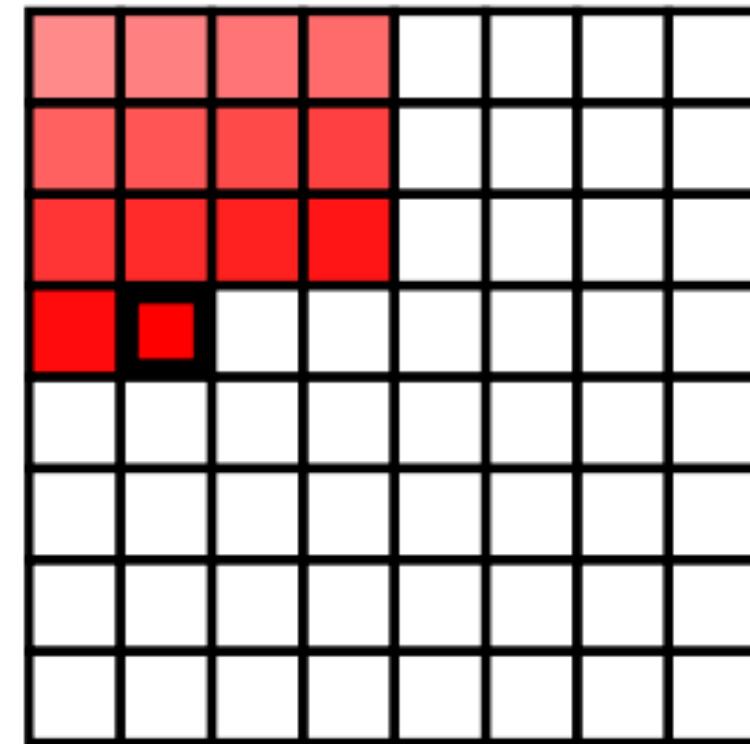
mem:168

B mem:56 cache hit:40

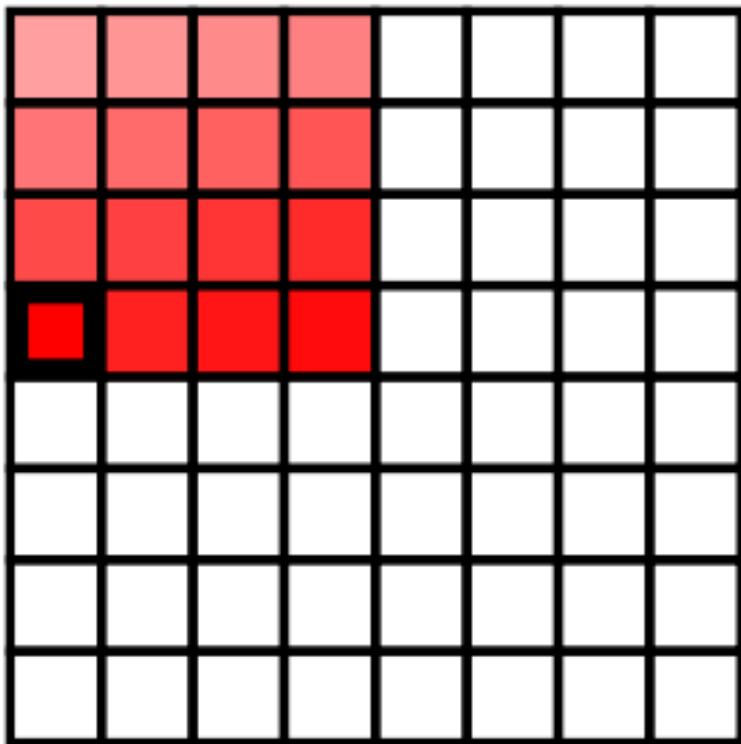


cache hits:122 ≈72%

C mem:56 cache hit:42

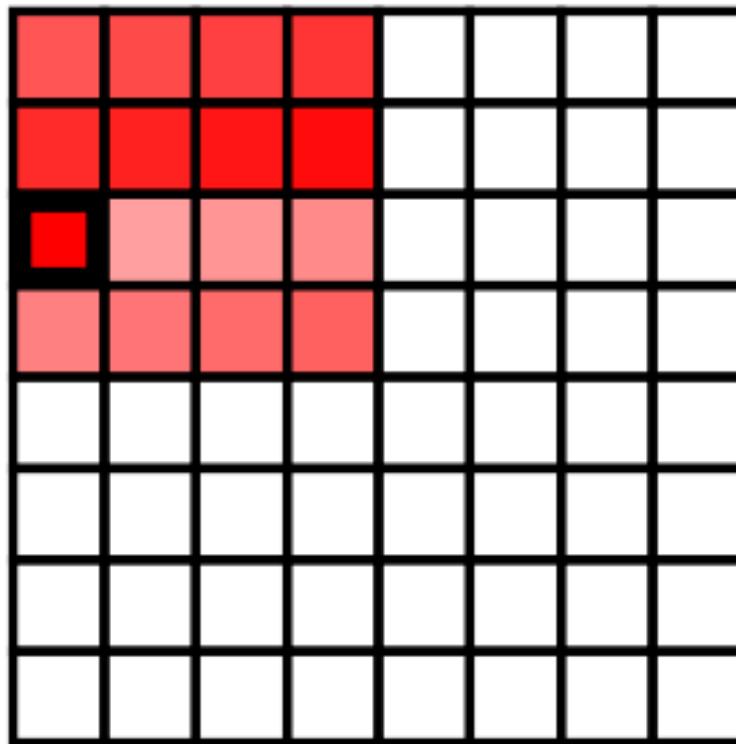


A mem:57 cache hit:41



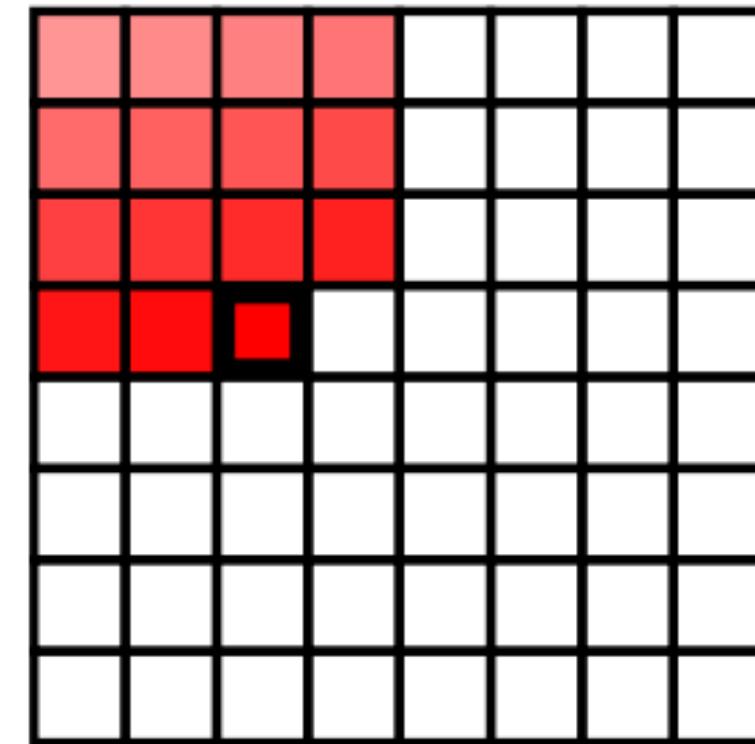
mem:171

B mem:57 cache hit:41

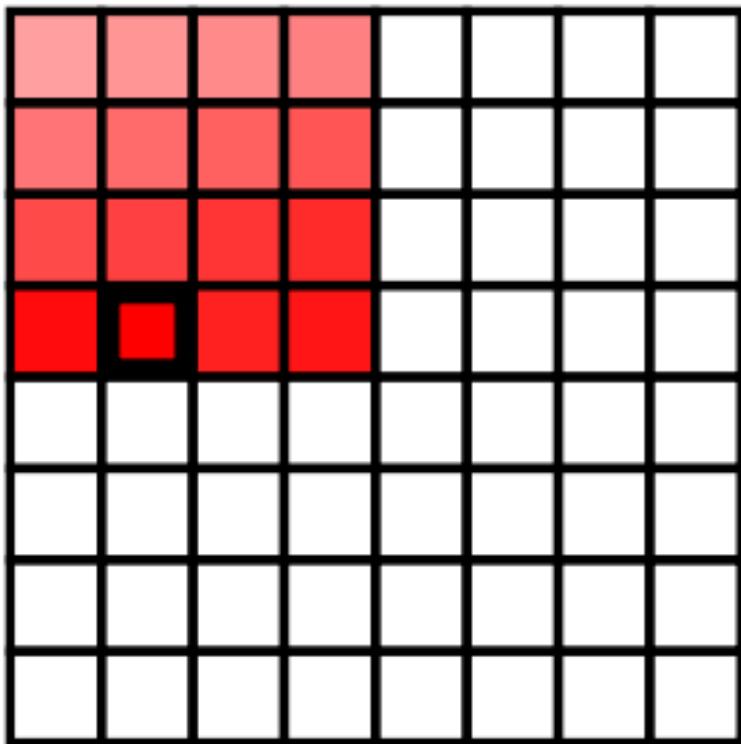


cache hits:124 ≈72%

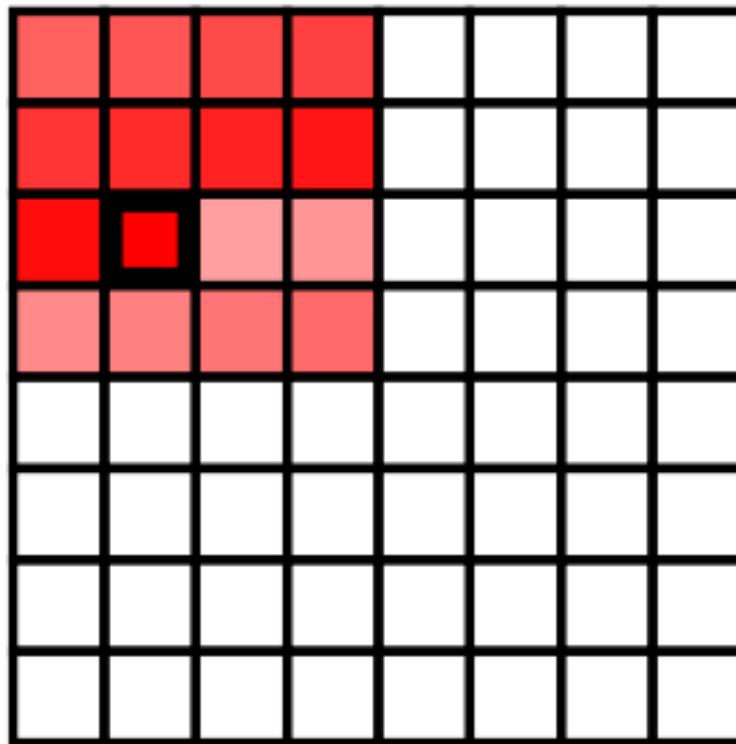
C mem:57 cache hit:42



A mem:58 cache hit:42



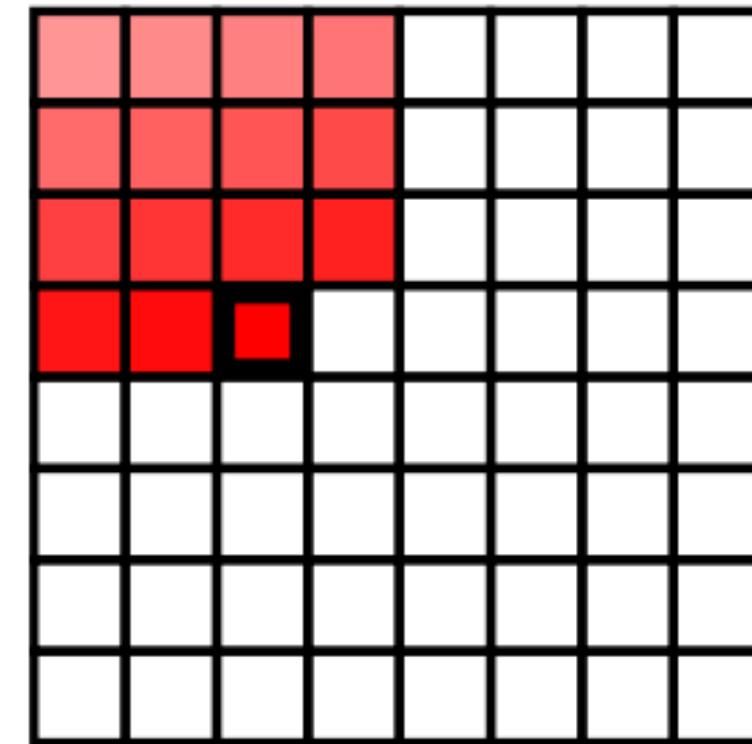
B mem:58 cache hit:42



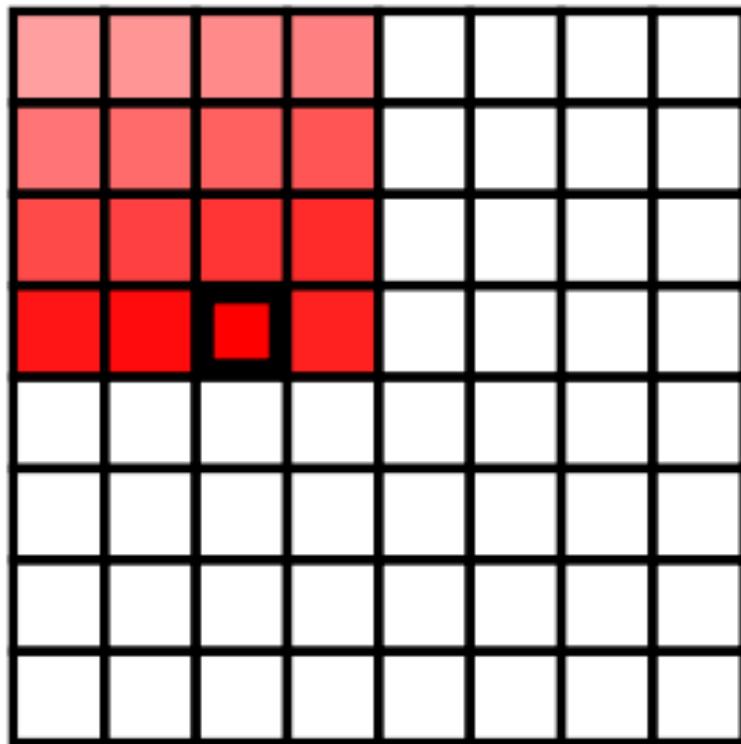
mem:174

cache hits:127 $\approx 72\%$

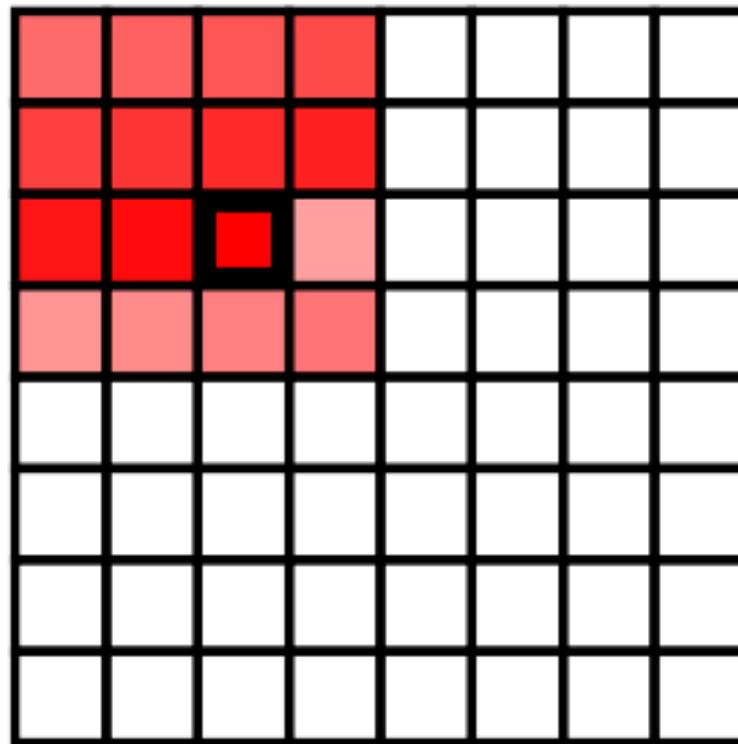
C mem:58 cache hit:43



A mem:59 cache hit:43



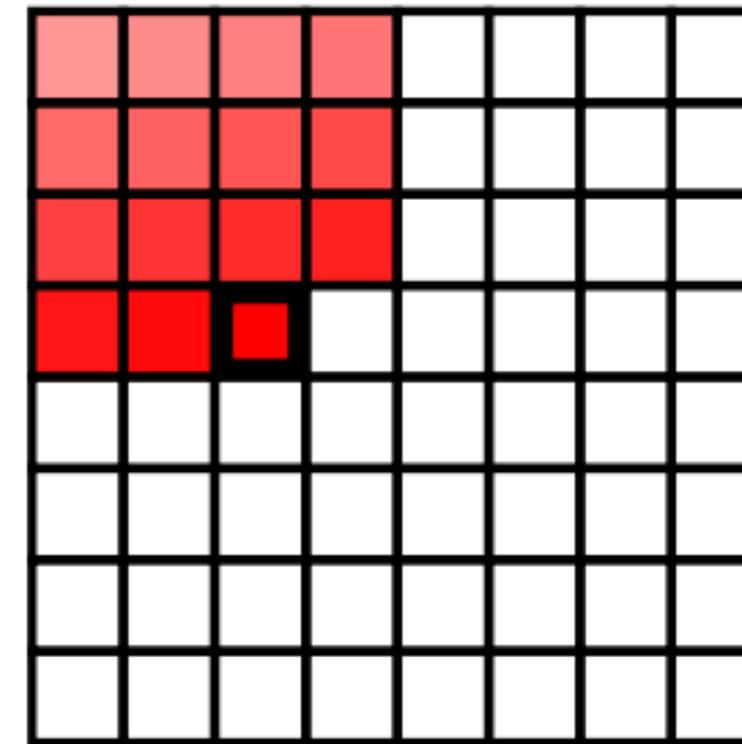
B mem:59 cache hit:43



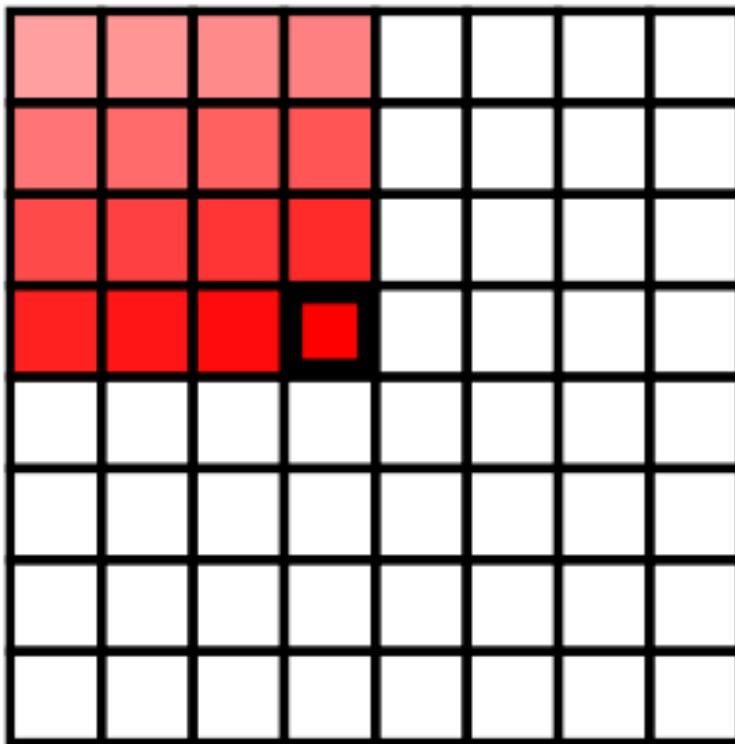
mem:177

cache hits:130 $\approx 73\%$

C mem:59 cache hit:44

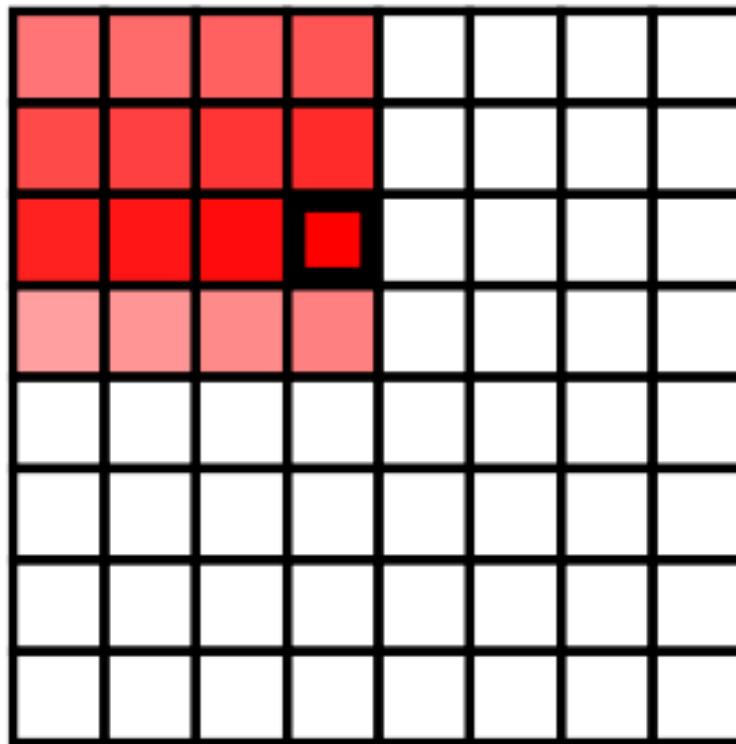


A mem:60 cache hit:44



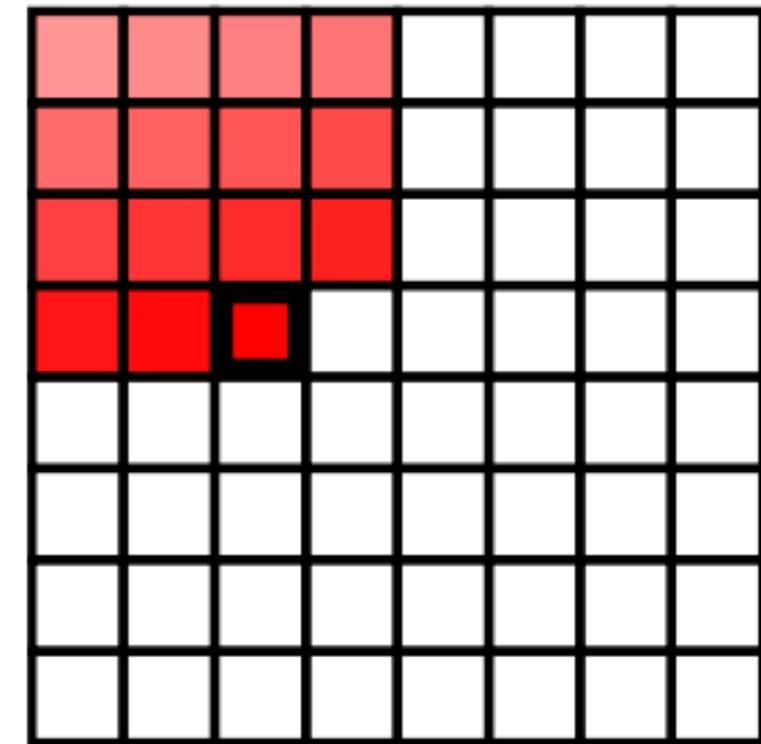
mem:180

B mem:60 cache hit:44

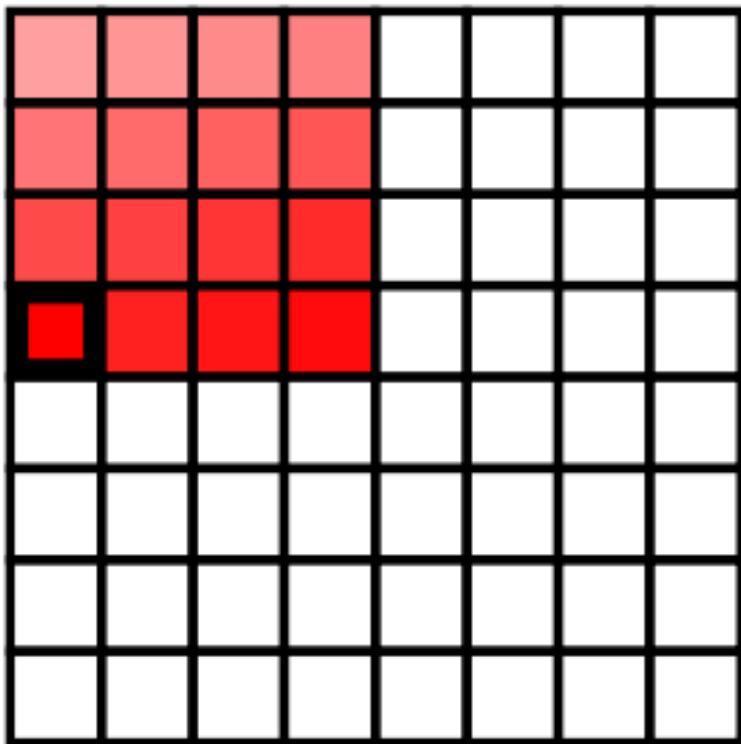


cache hits:133 ≈73%

C mem:60 cache hit:45

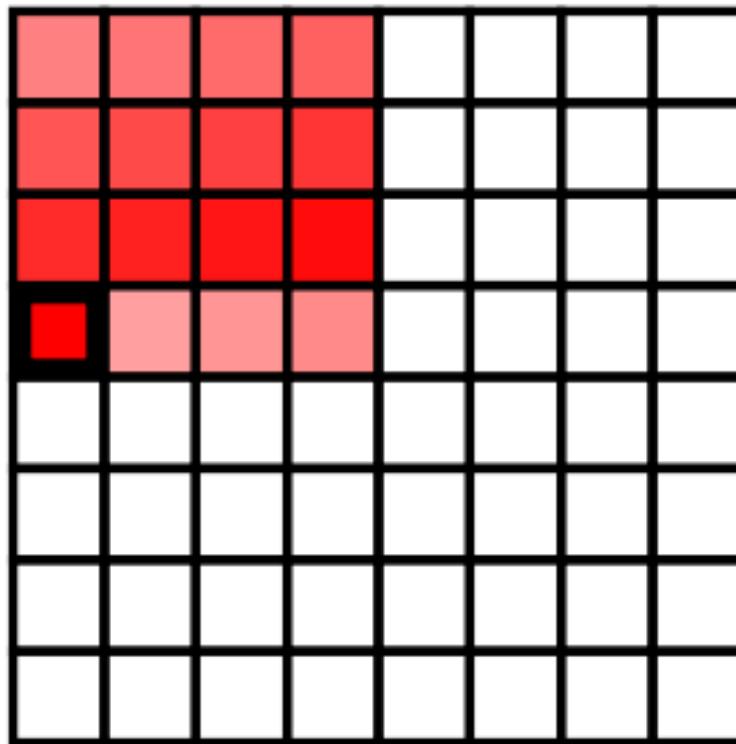


A mem:61 cache hit:45



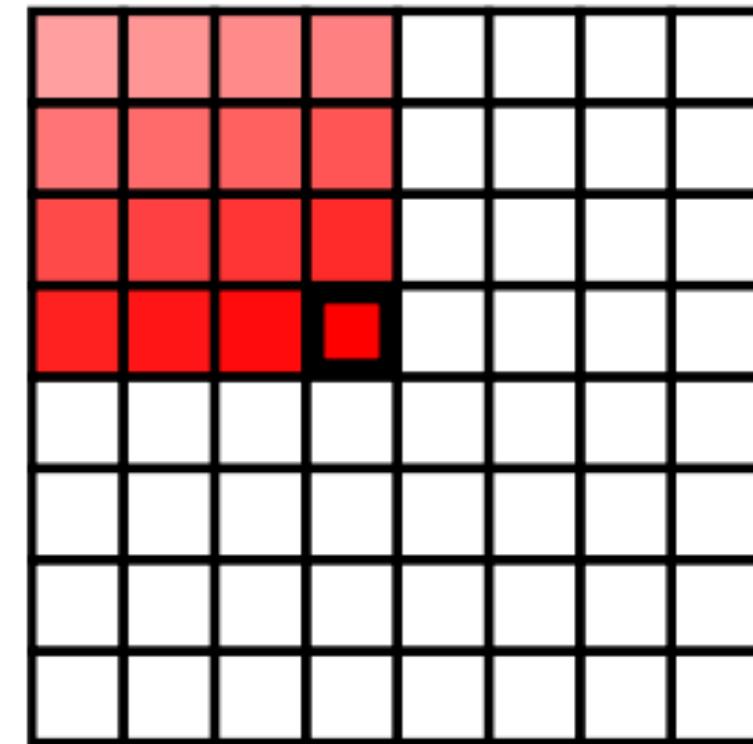
mem:183

B mem:61 cache hit:45

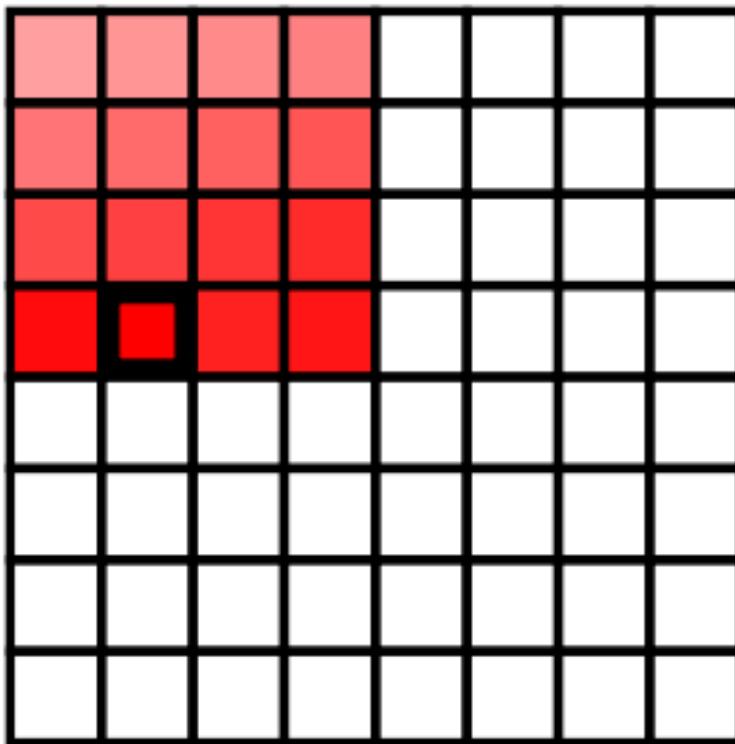


cache hits:135 ≈73%

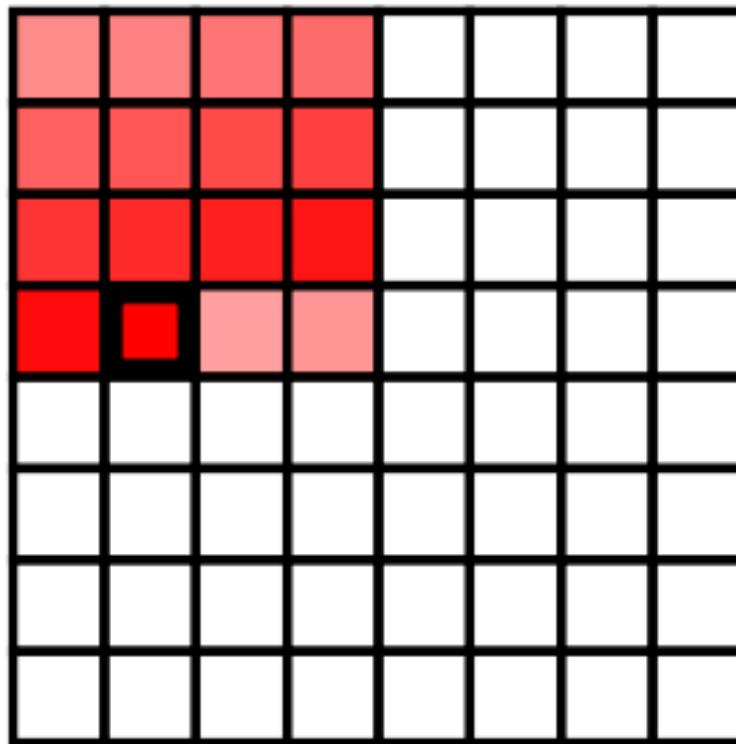
C mem:61 cache hit:45



A mem:62 cache hit:46



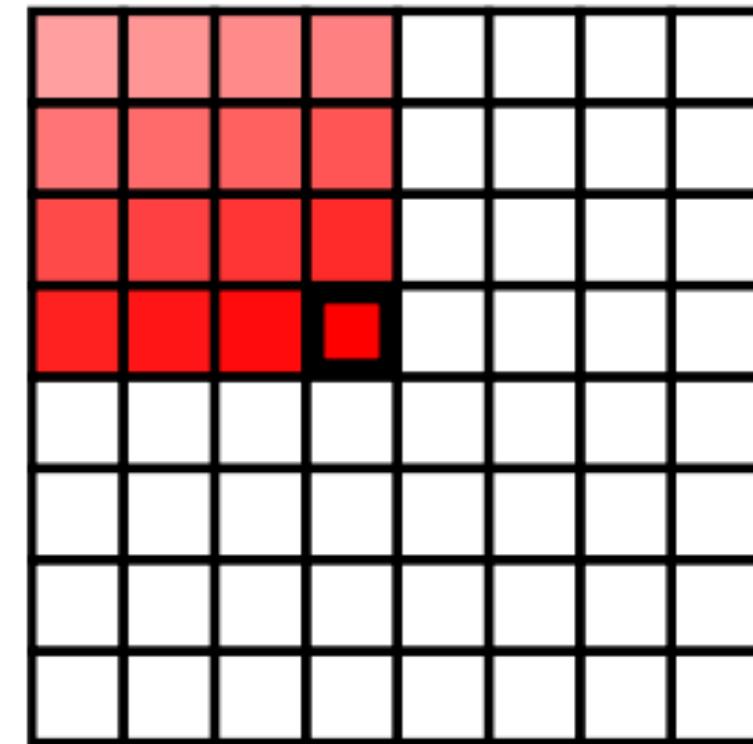
B mem:62 cache hit:46



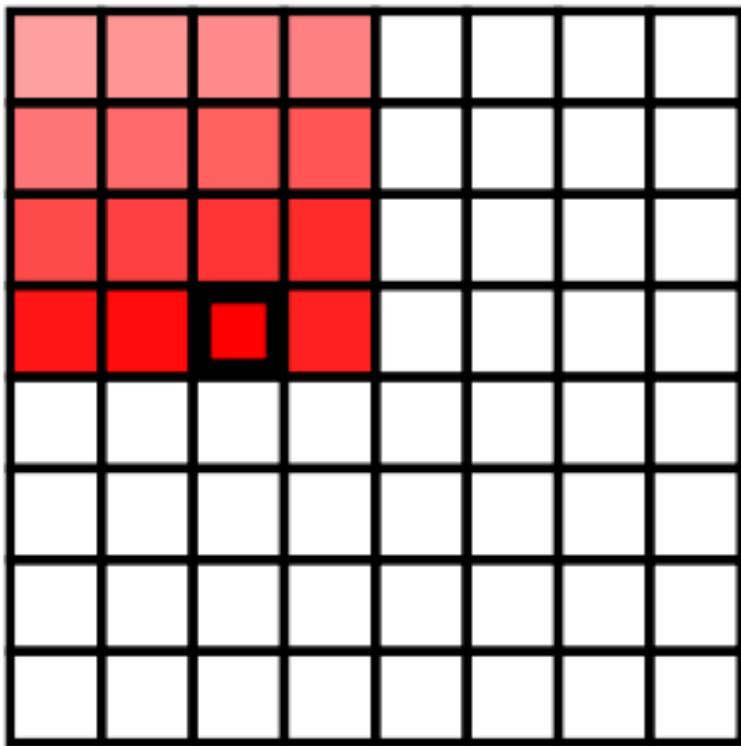
mem:186

cache hits:138 $\approx 74\%$

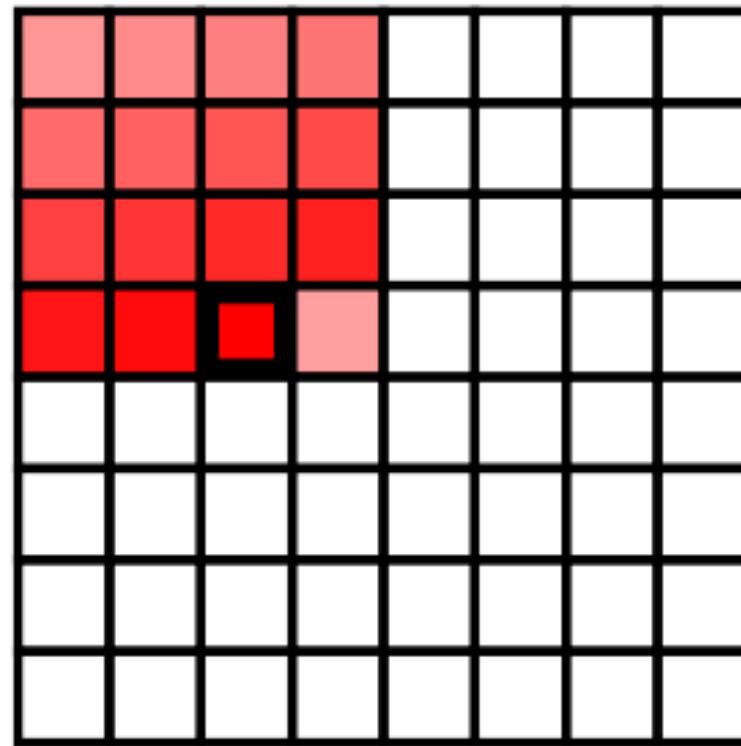
C mem:62 cache hit:46



A mem:63 cache hit:47

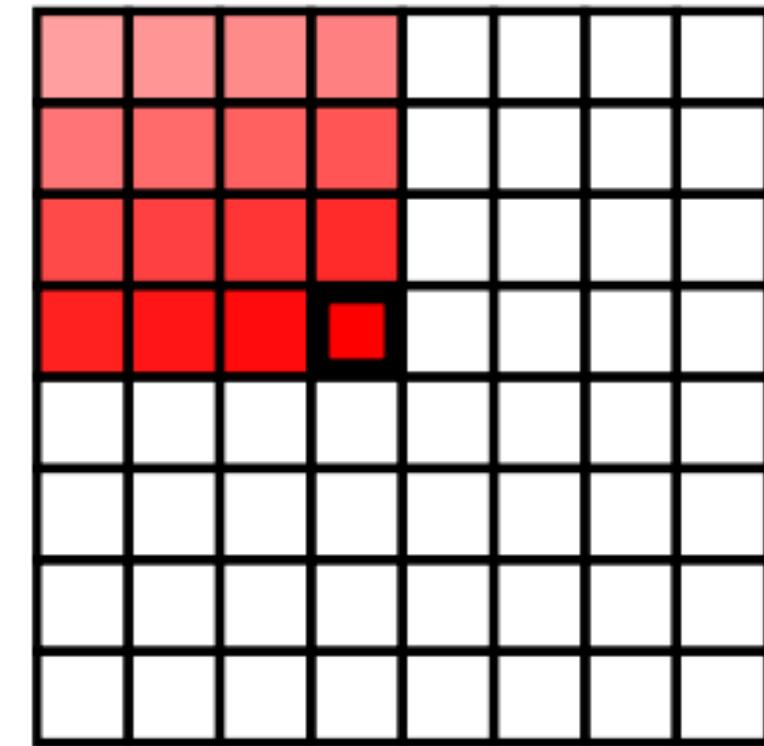


B mem:63 cache hit:47



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C mem:63 cache hit:47

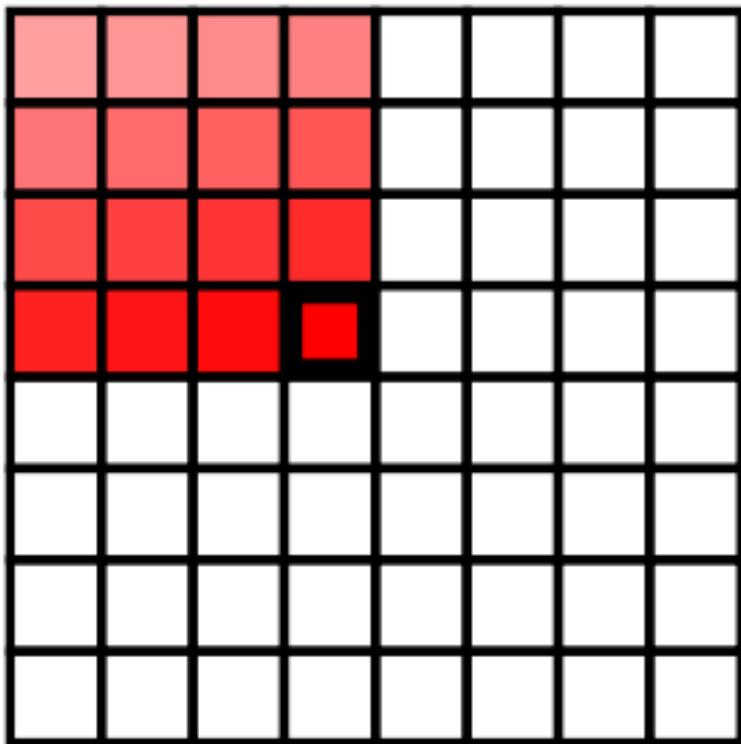


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mem:189

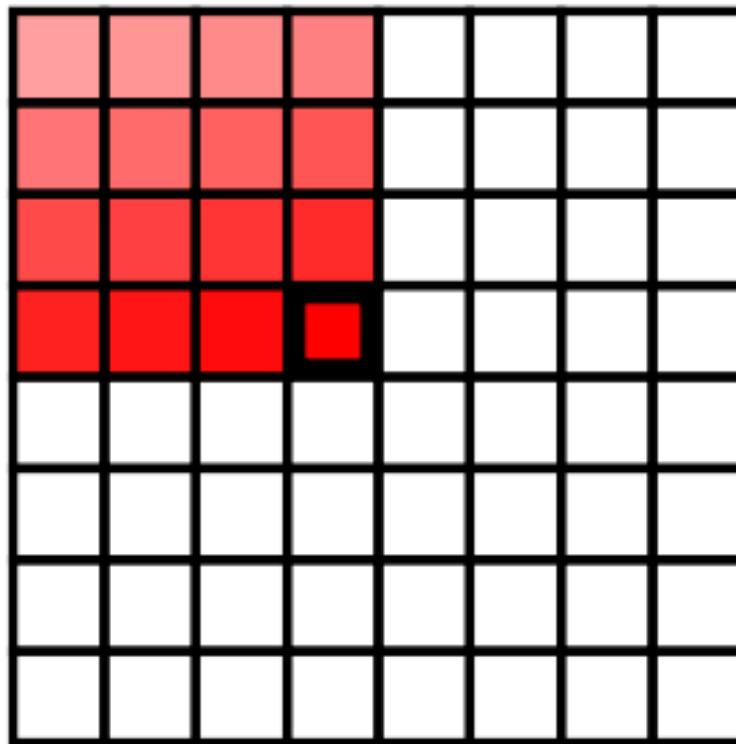
cache hits:141 $\approx 74\%$

A mem:64 cache hit:48



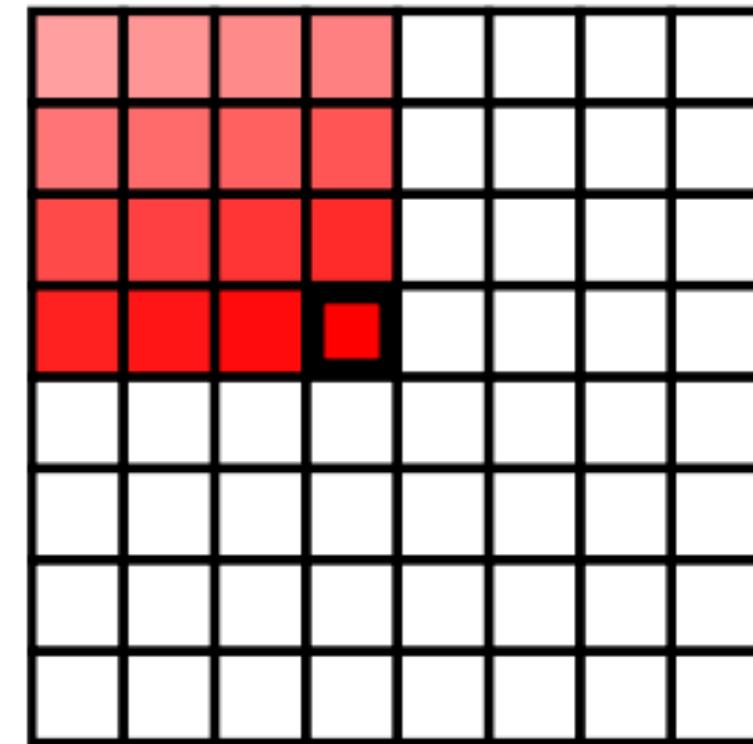
mem:192

B mem:64 cache hit:48

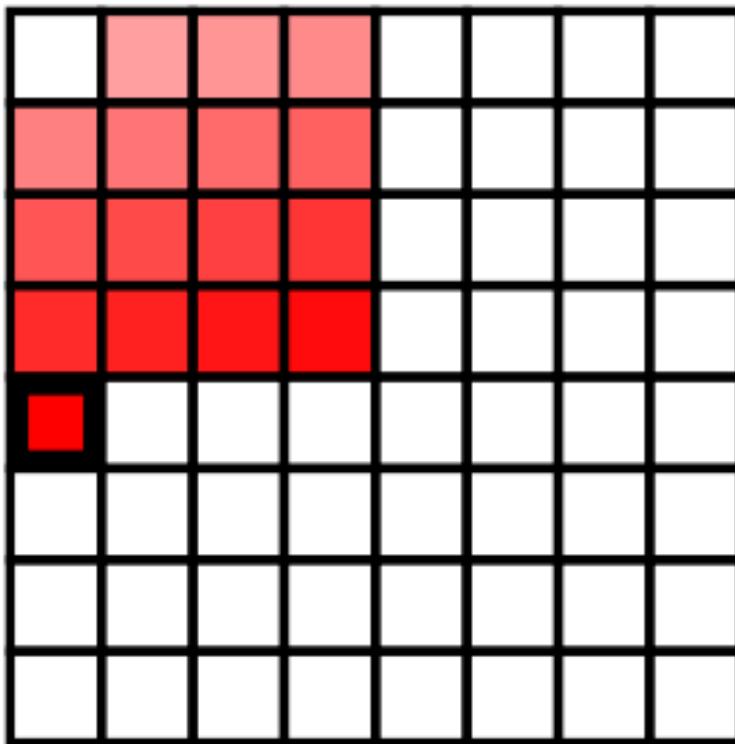


cache hits:144 $\approx 75\%$

C mem:64 cache hit:48

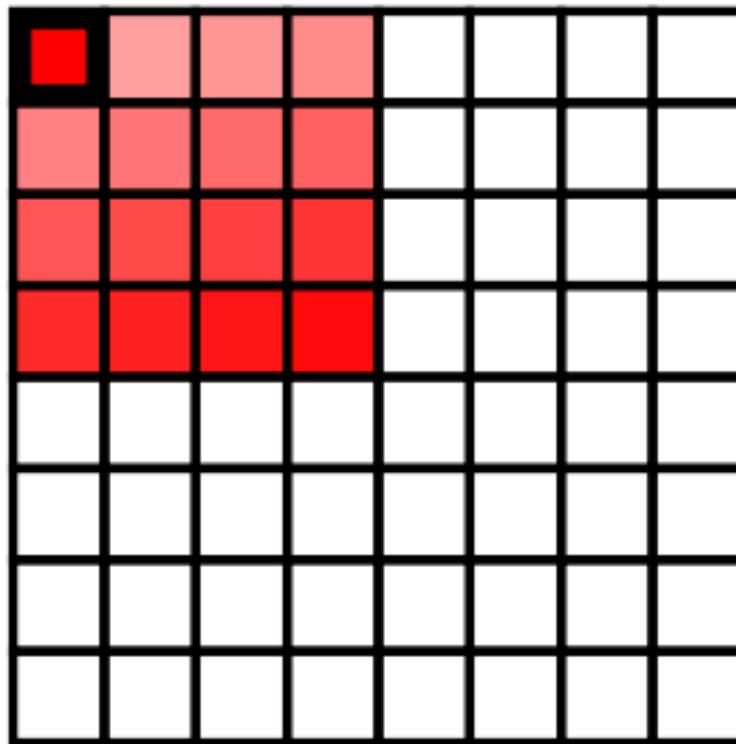


A mem:65 cache hit:48



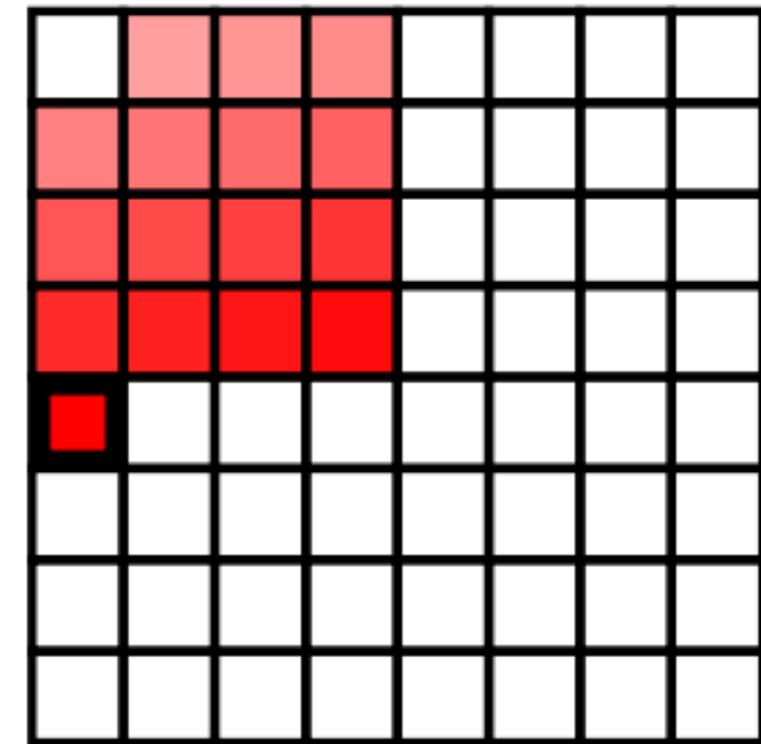
mem:195

B mem:65 cache hit:49

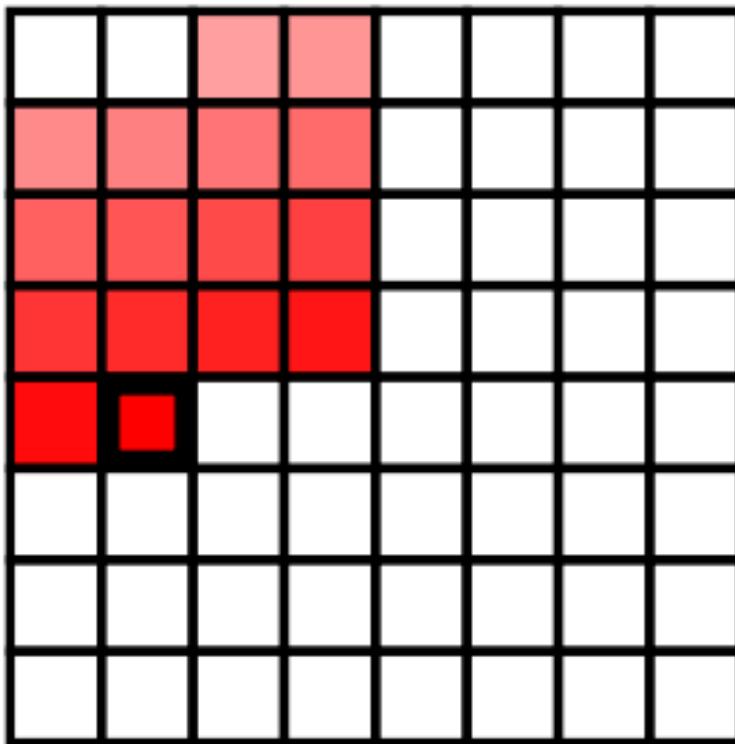


cache hits:145 ≈74%

C mem:65 cache hit:48

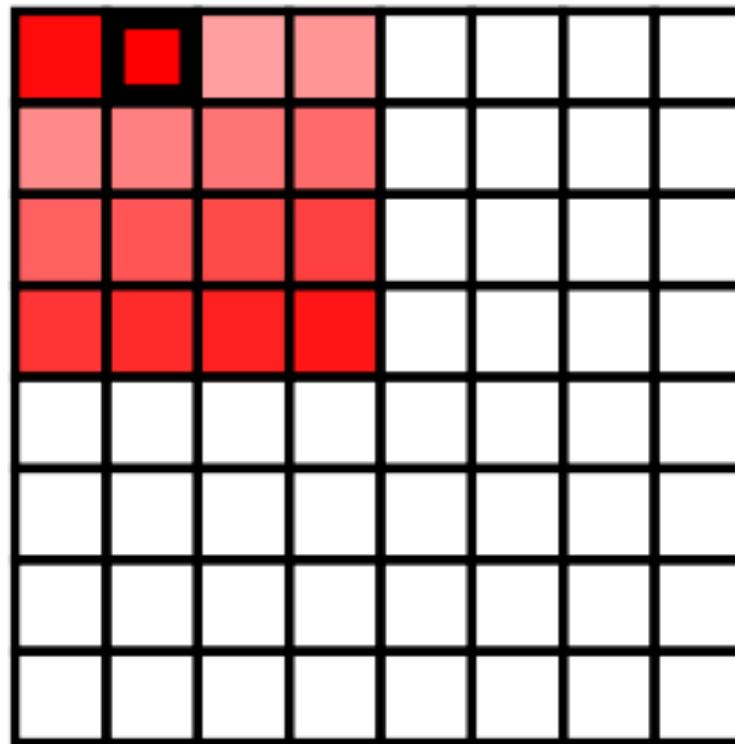


A mem:66 cache hit:48



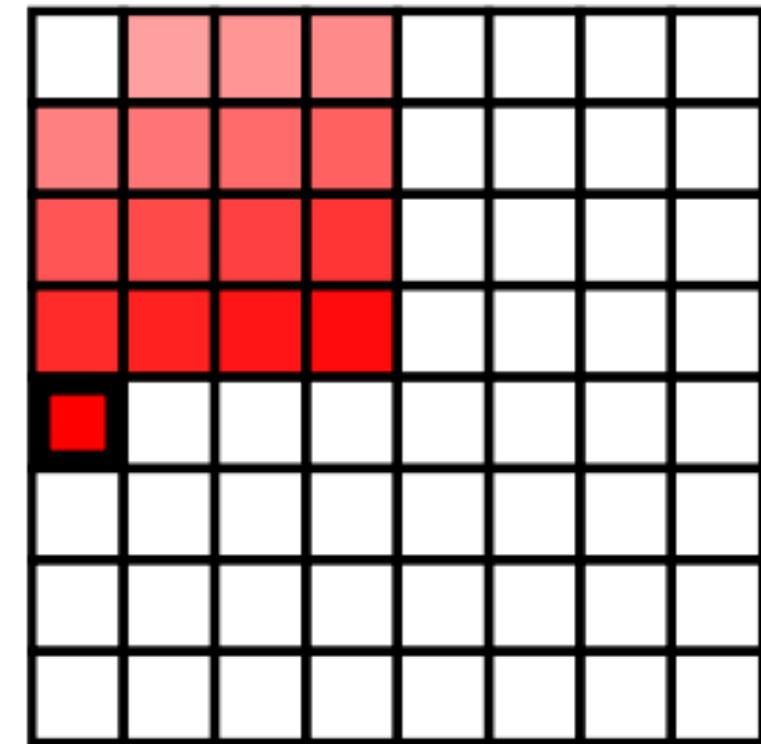
mem:198

B mem:66 cache hit:50

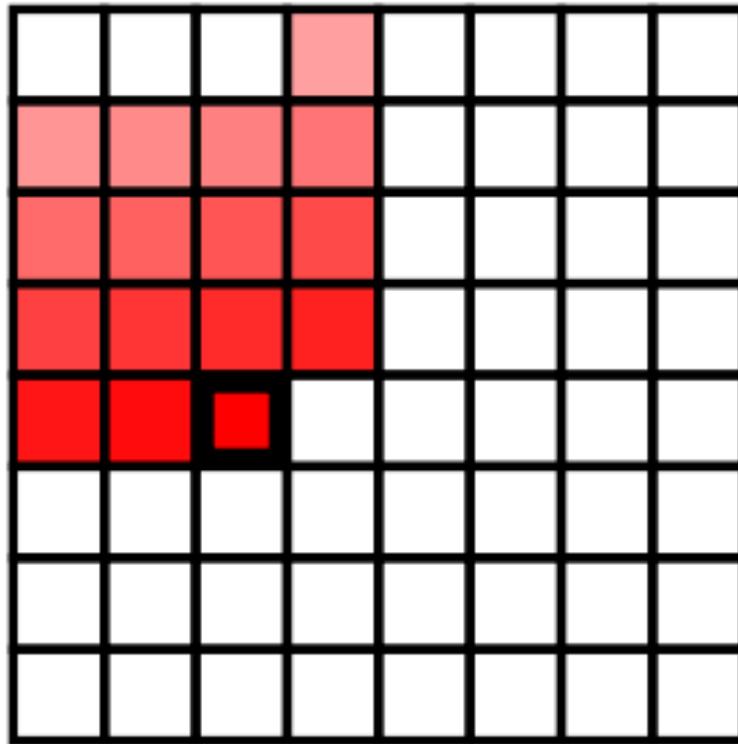


cache hits:147 ≈74%

C mem:66 cache hit:49

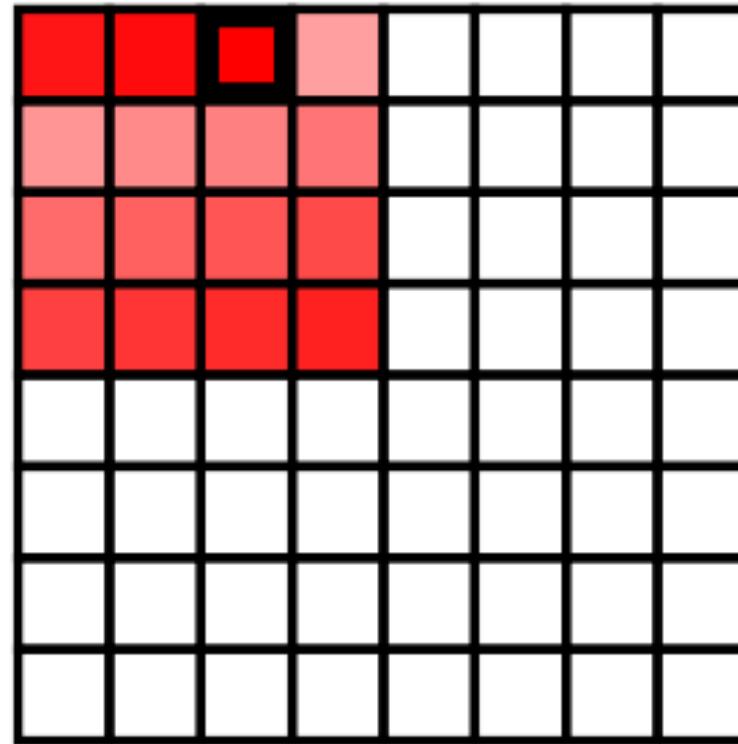


A mem:67 cache hit:48



mem:201

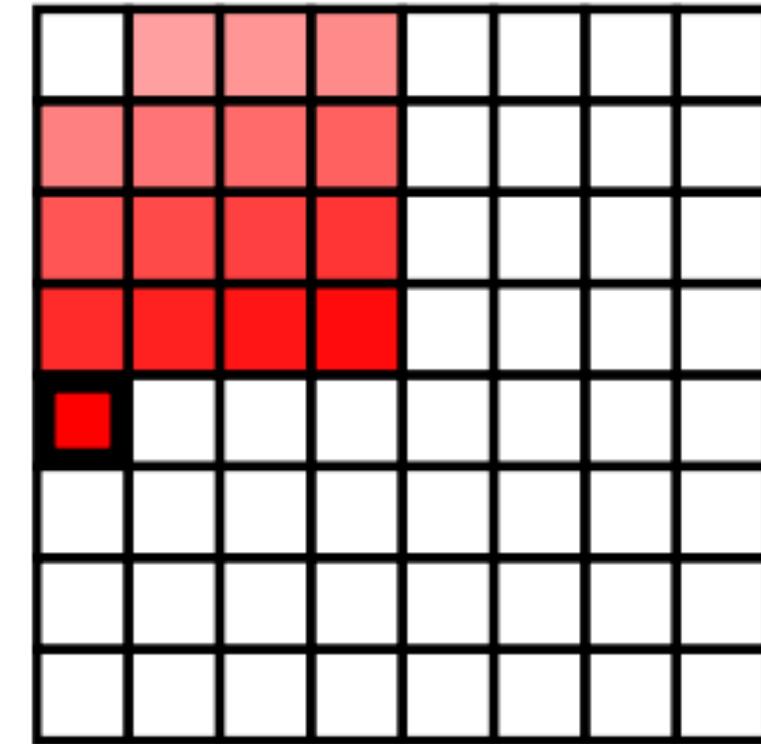
B mem:67 cache hit:51



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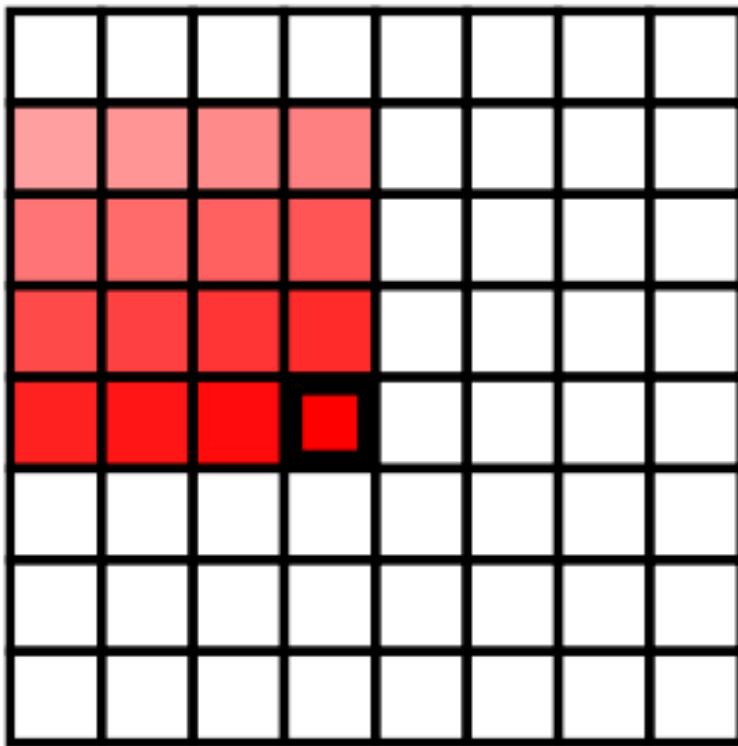
cache hits:149 ≈74%

C mem:67 cache hit:50



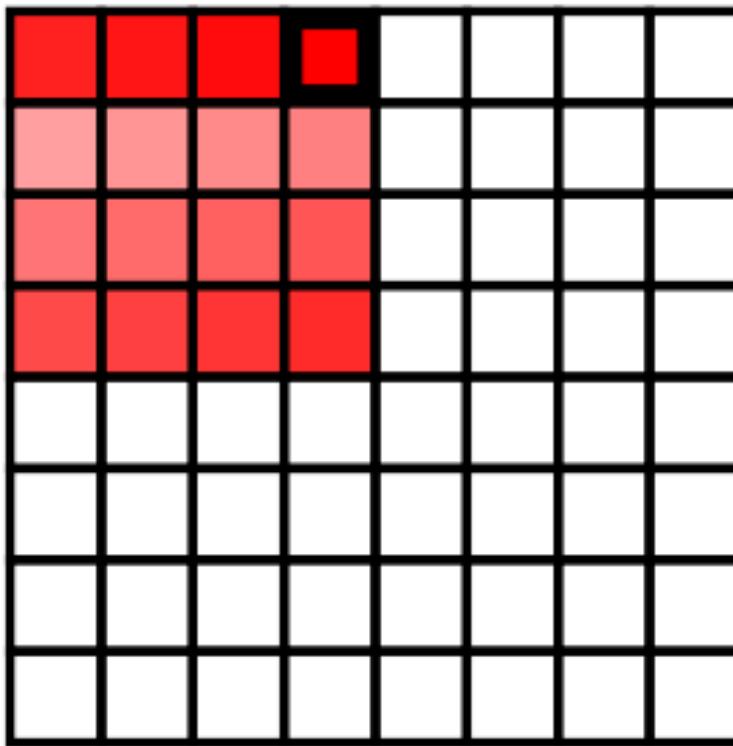
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A mem:68 cache hit:48



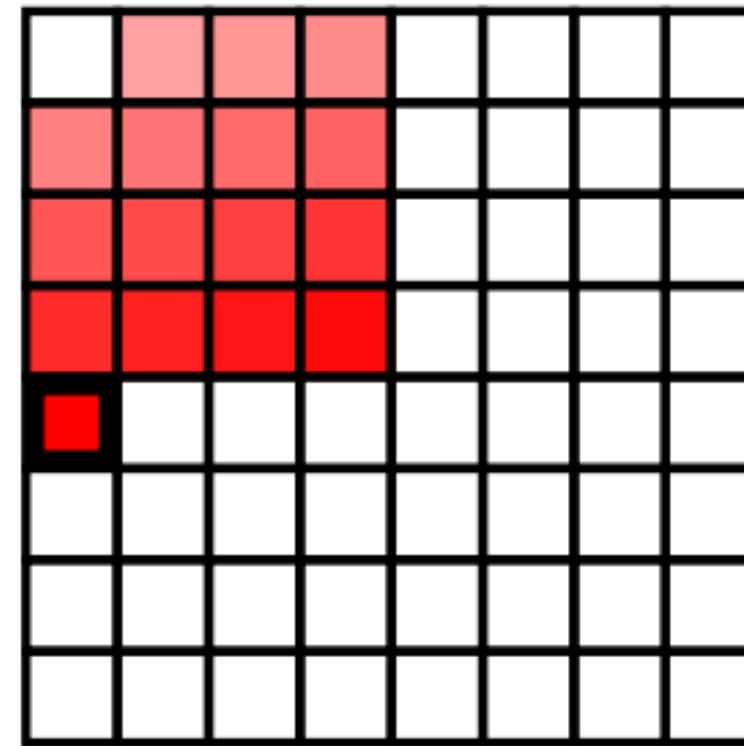
mem:204

B mem:68 cache hit:52

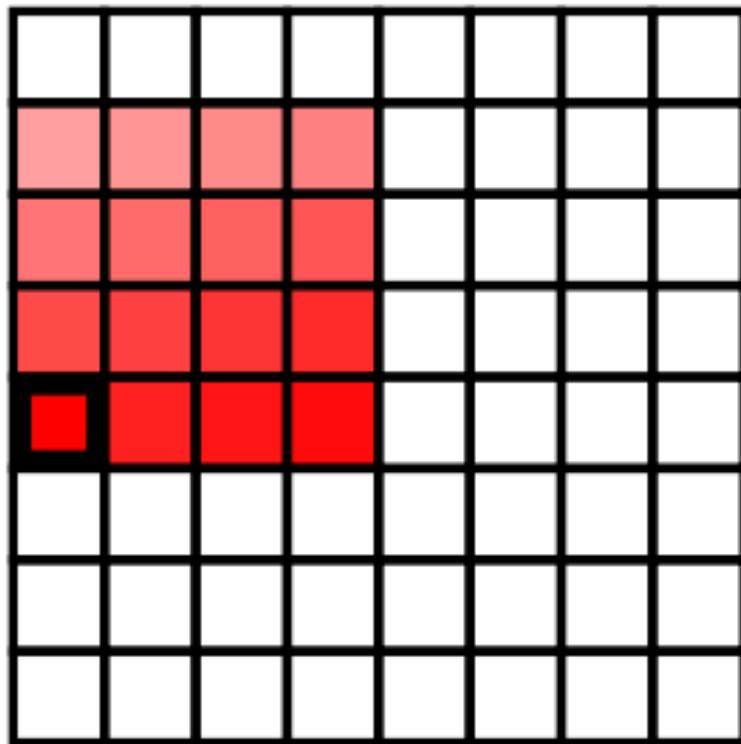


cache hits:151 $\approx 74\%$

C mem:68 cache hit:51

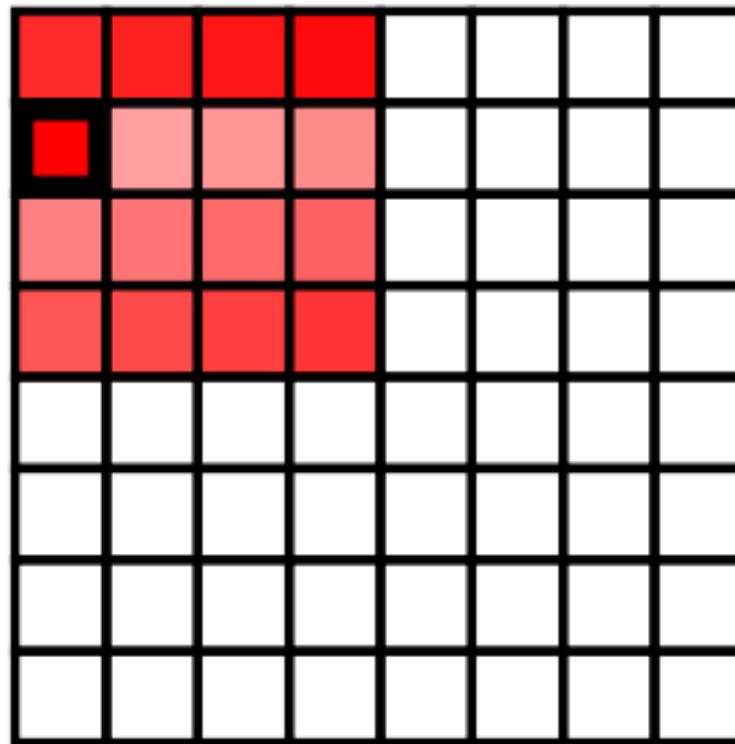


A mem:69 cache hit:49



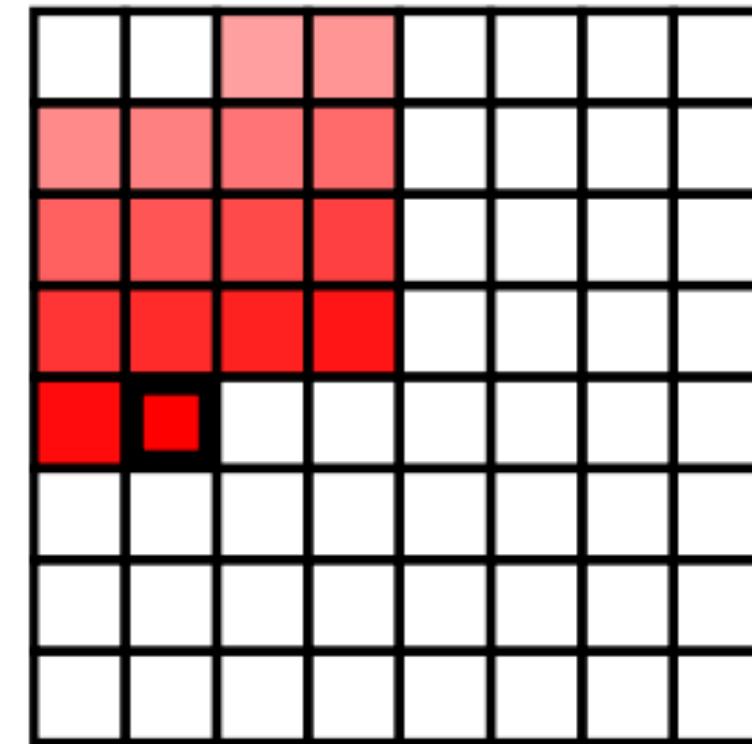
mem:207

B mem:69 cache hit:53

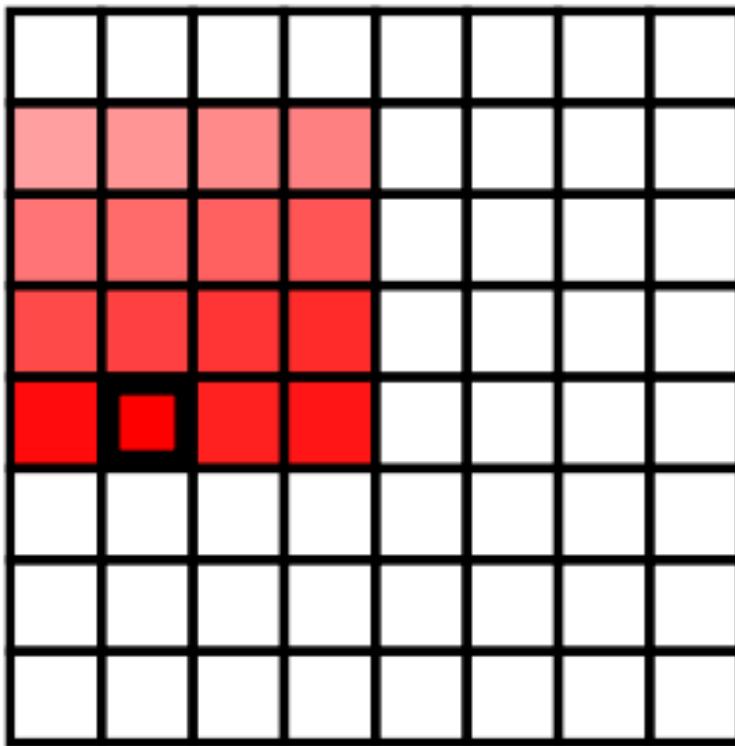


cache hits:153 ≈73%

C mem:69 cache hit:51

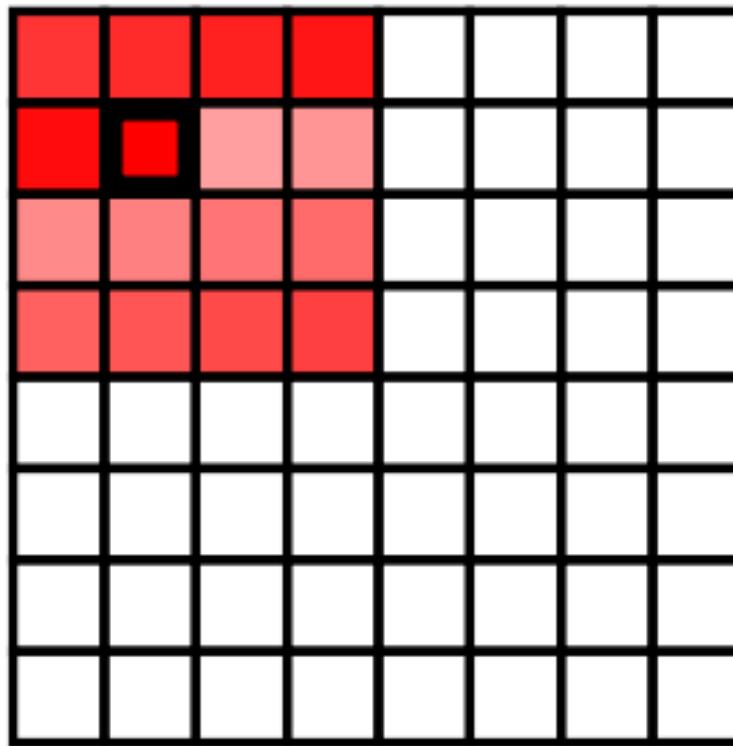


A mem:70 cache hit:50



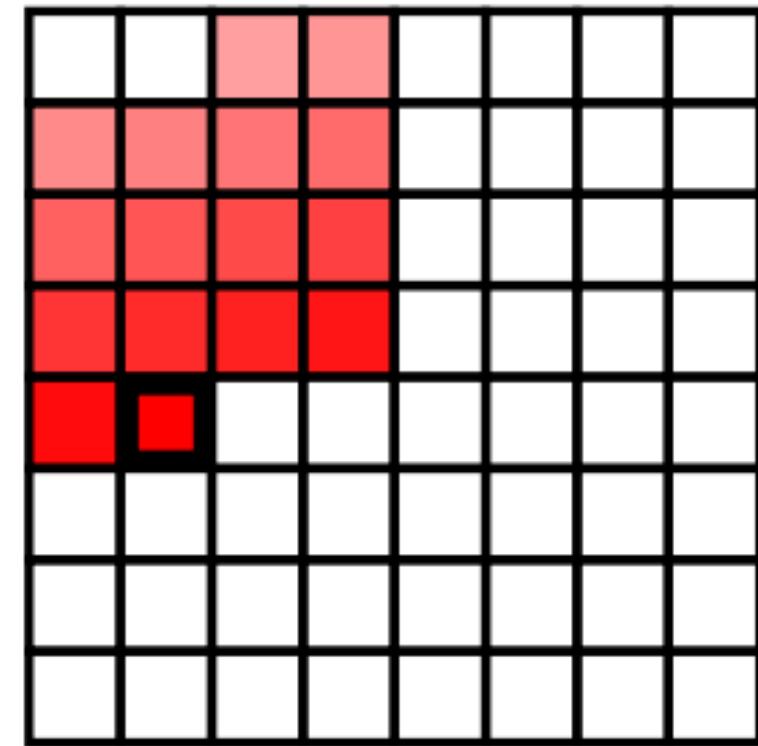
mem:210

B mem:70 cache hit:54

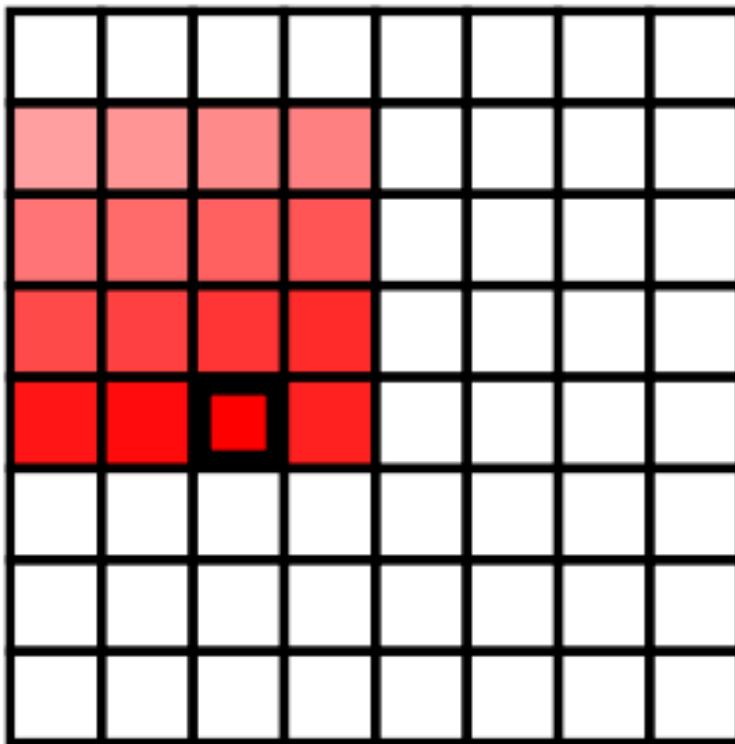


cache hits:156 ≈74%

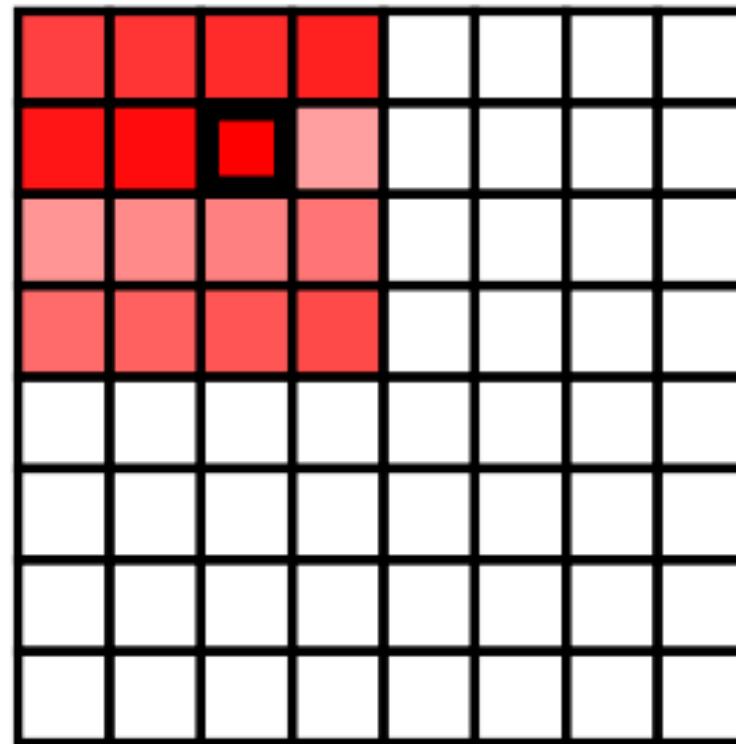
C mem:70 cache hit:52



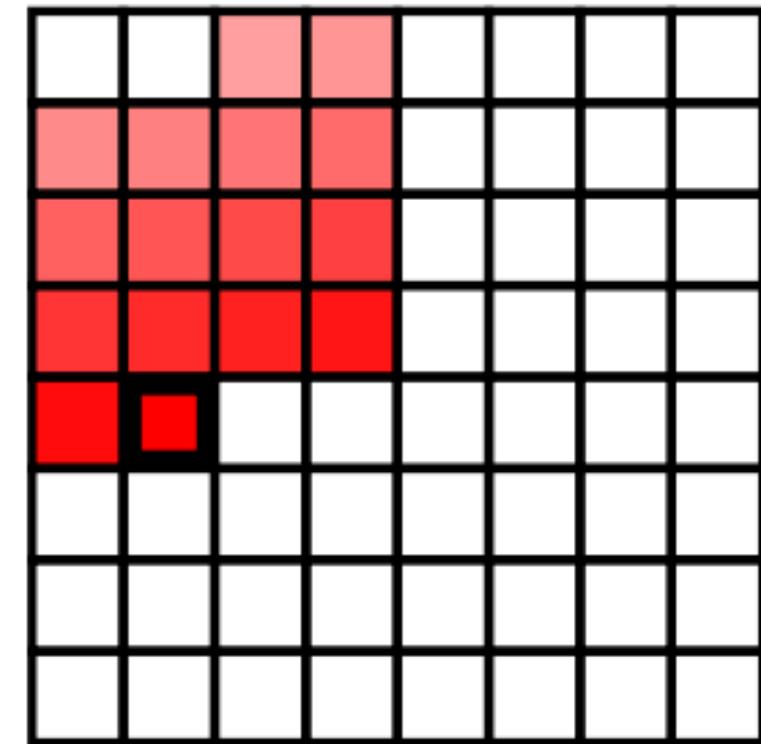
A mem:71 cache hit:51



B mem:71 cache hit:55



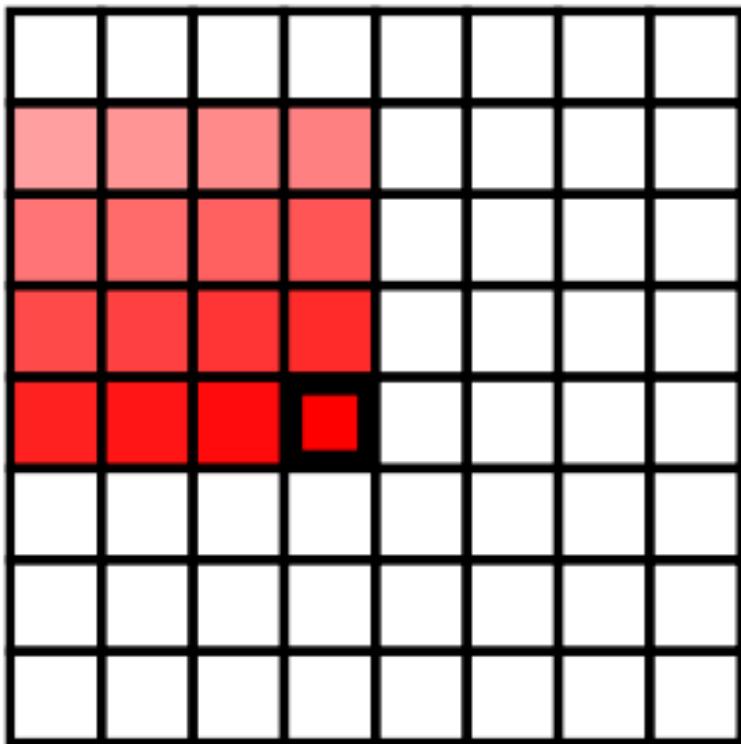
C mem:71 cache hit:53



mem:213

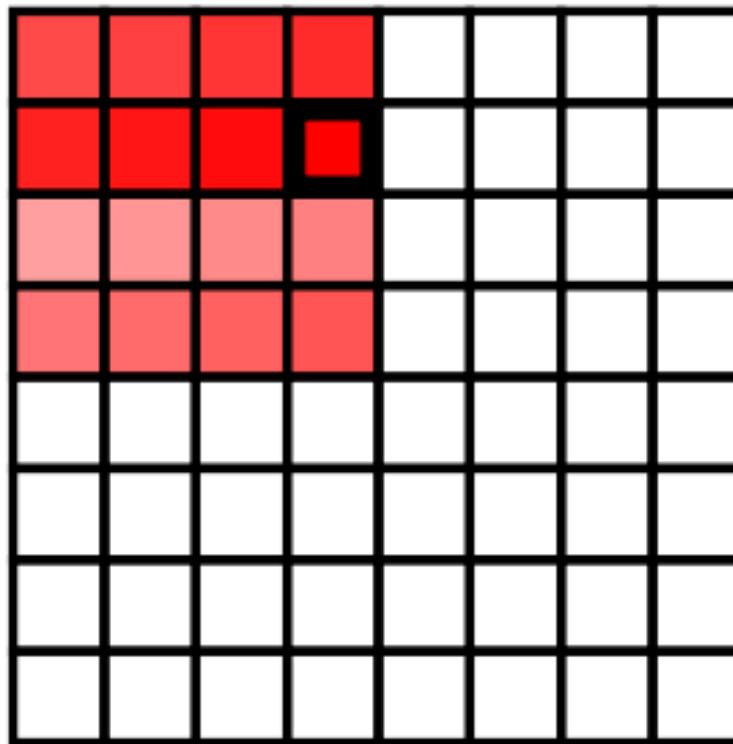
cache hits:159 ≈74%

A mem:72 cache hit:52



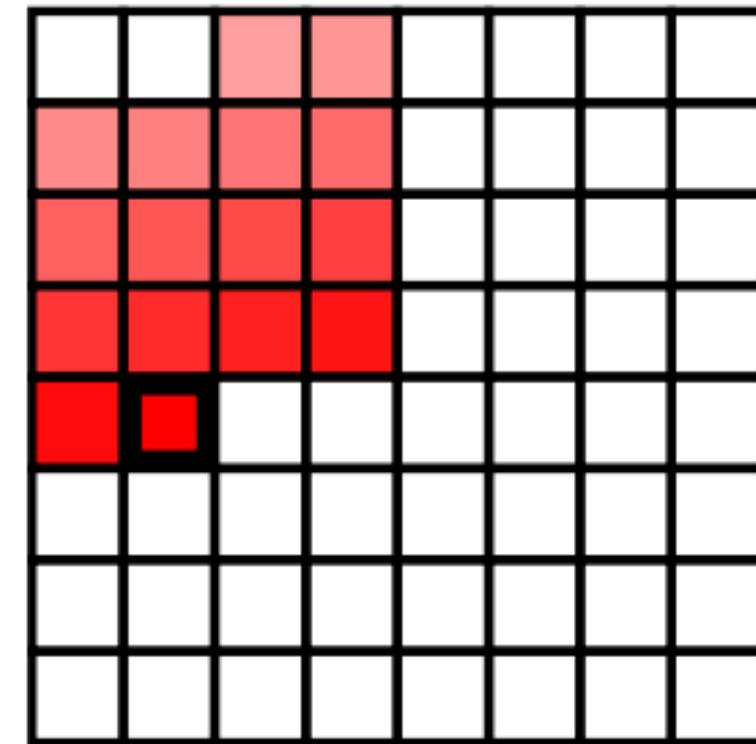
mem:216

B mem:72 cache hit:56

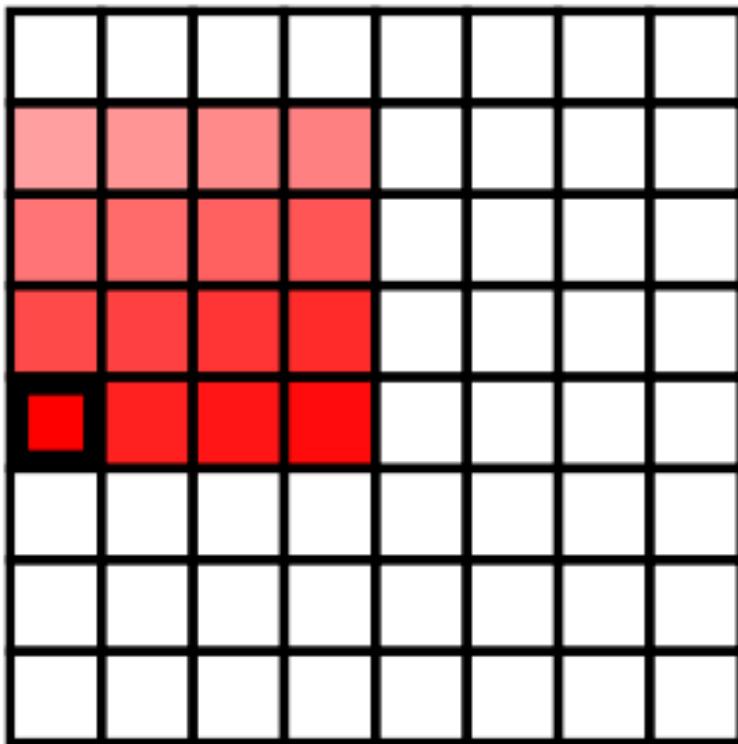


cache hits:162 ≈75%

C mem:72 cache hit:54

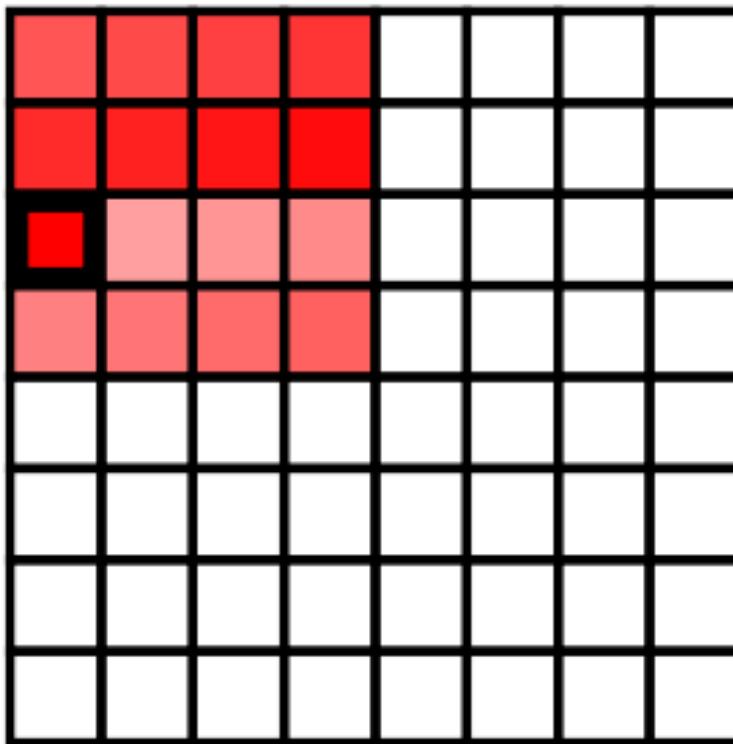


A mem:73 cache hit:53



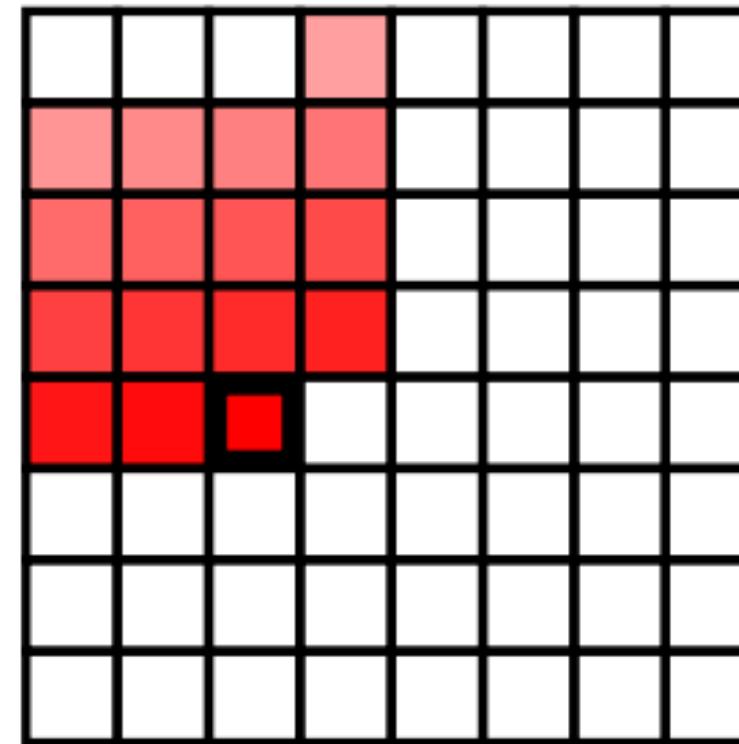
mem:219

B mem:73 cache hit:57

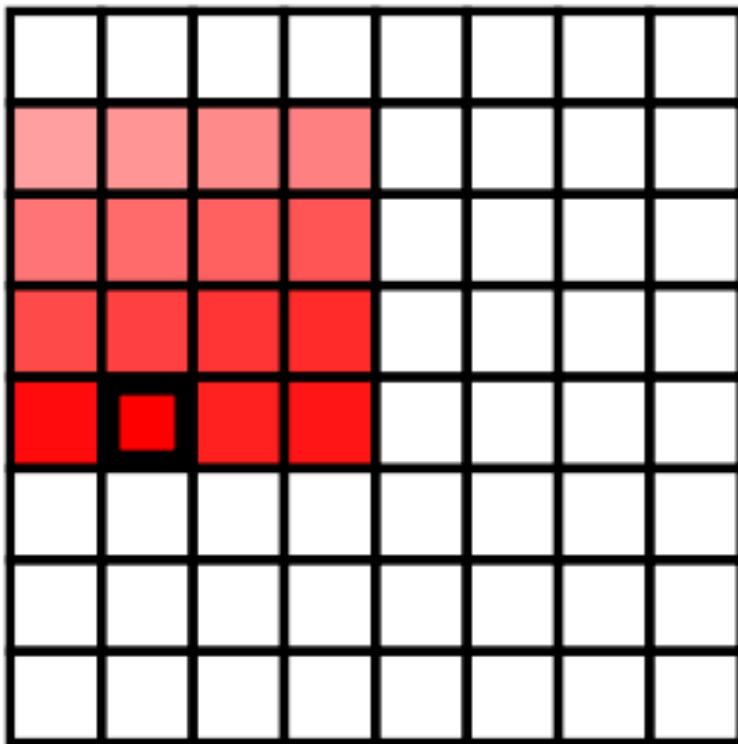


cache hits:164 ≈74%

C mem:73 cache hit:54

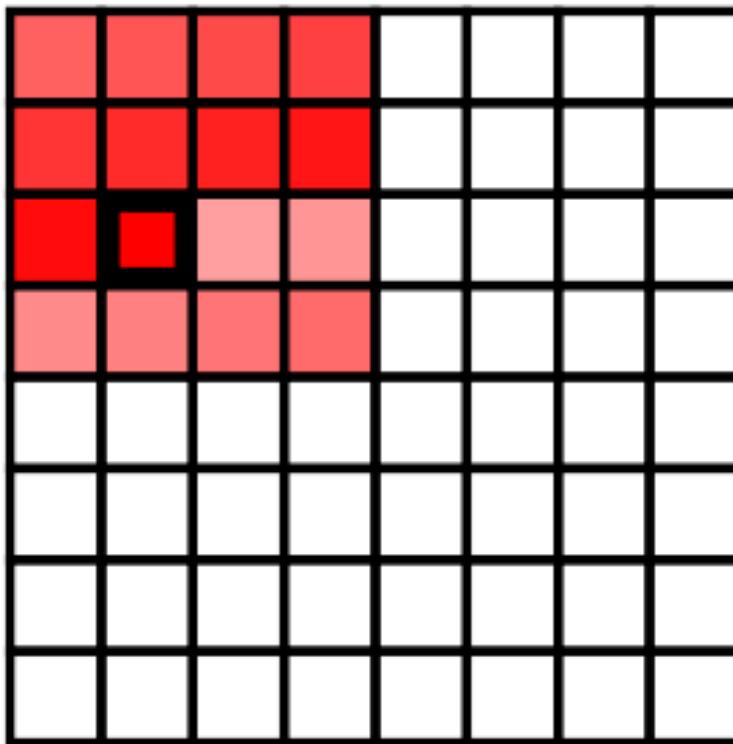


A mem:74 cache hit:54



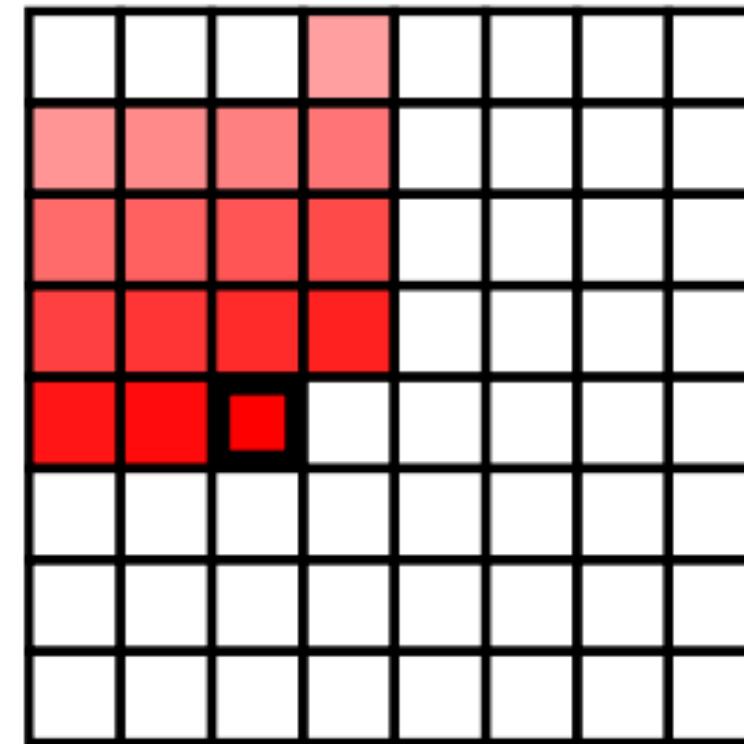
mem:222

B mem:74 cache hit:58

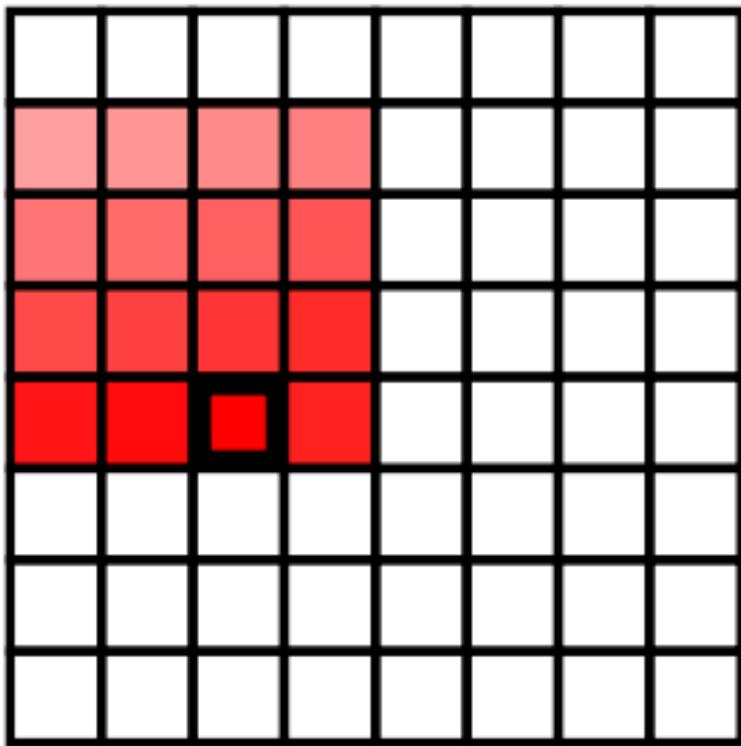


cache hits:167 ≈75%

C mem:74 cache hit:55

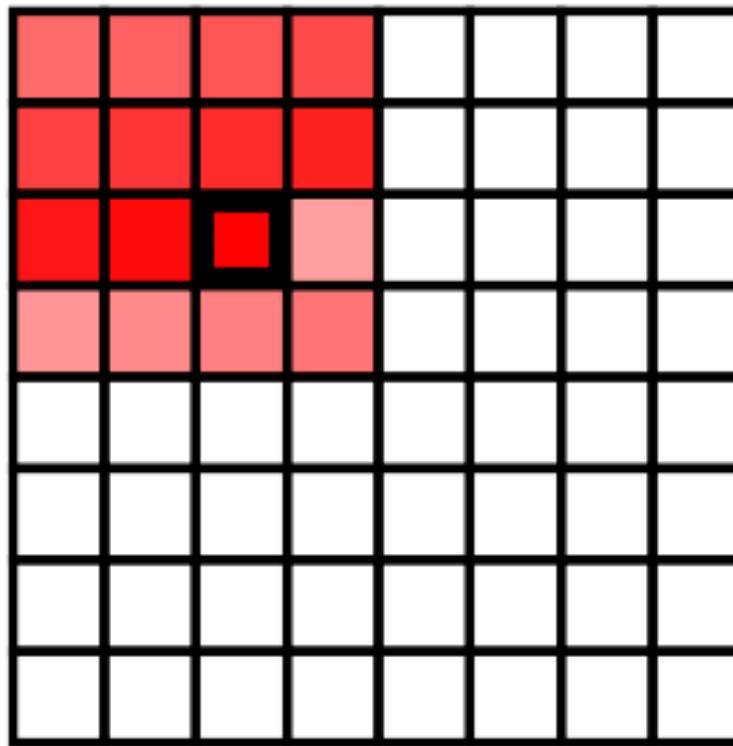


A mem:75 cache hit:55



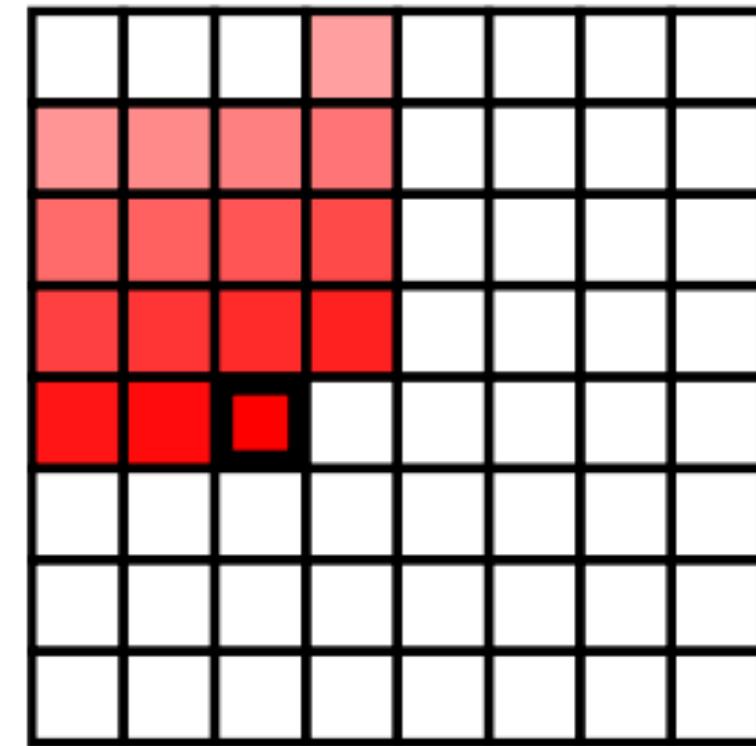
mem:225

B mem:75 cache hit:59

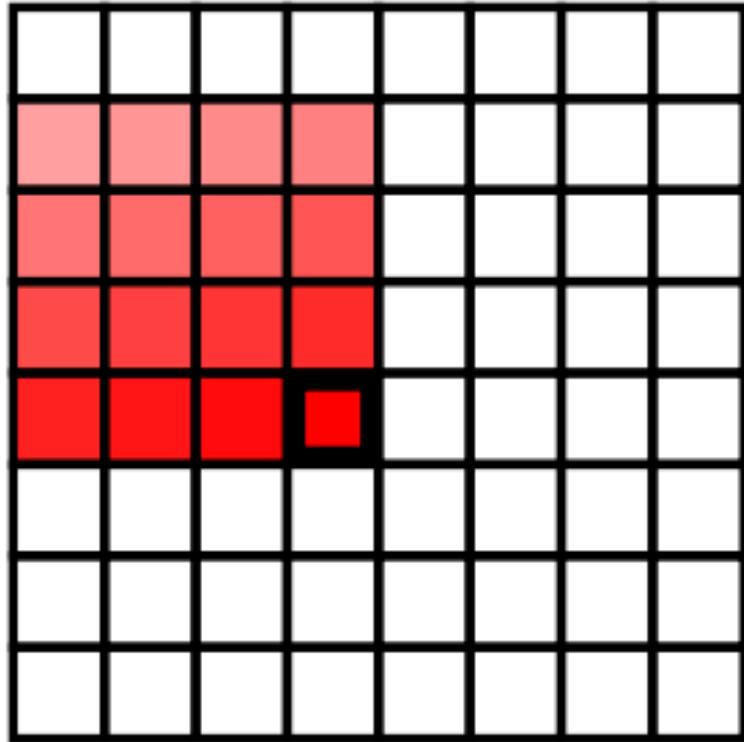


cache hits:170 ≈75%

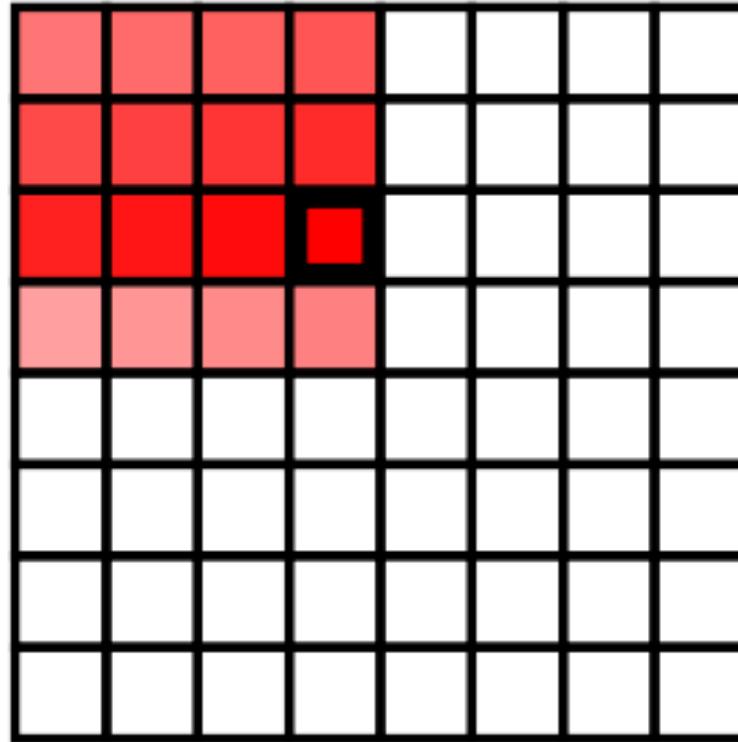
C mem:75 cache hit:56



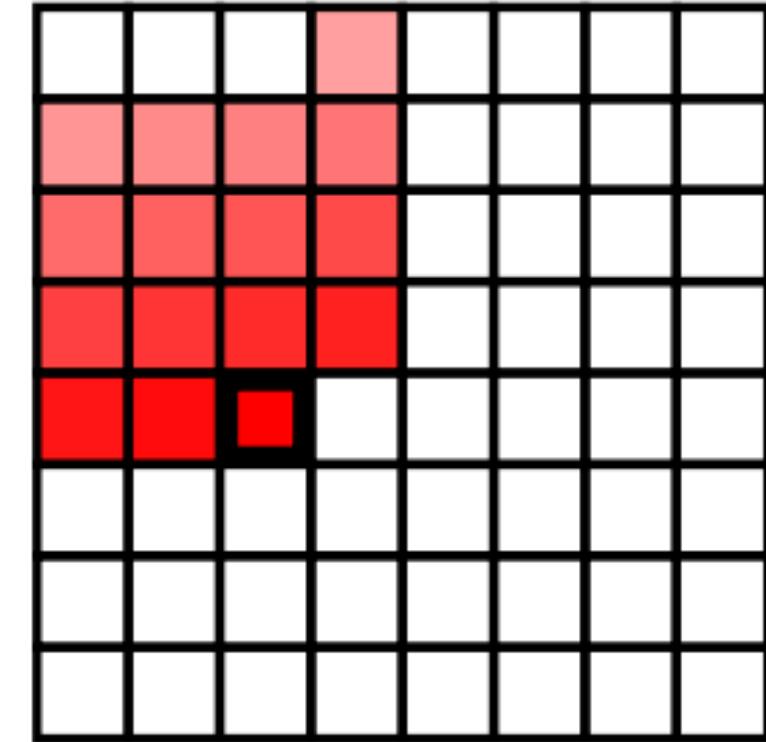
A mem:76 cache hit:56



B mem:76 cache hit:60



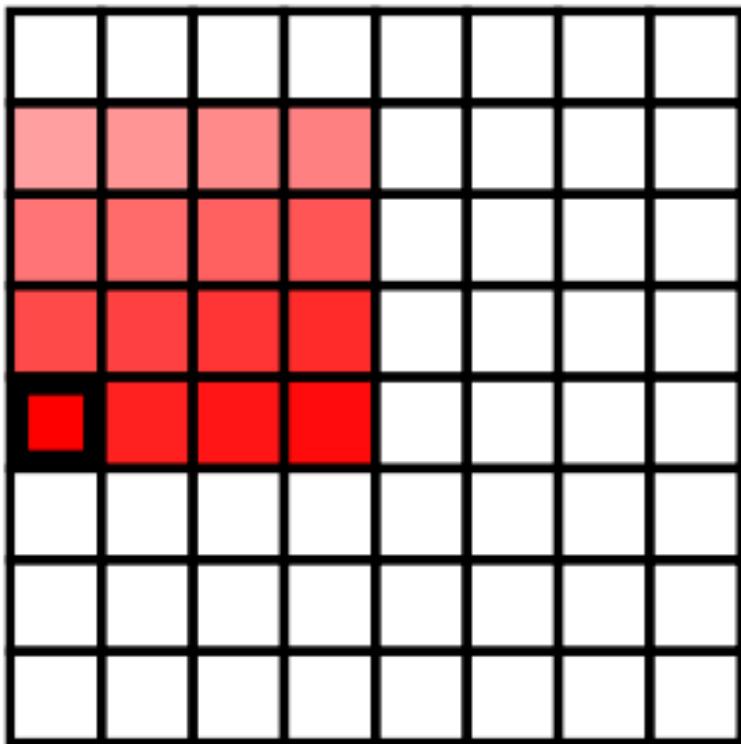
C mem:76 cache hit:57



mem:228

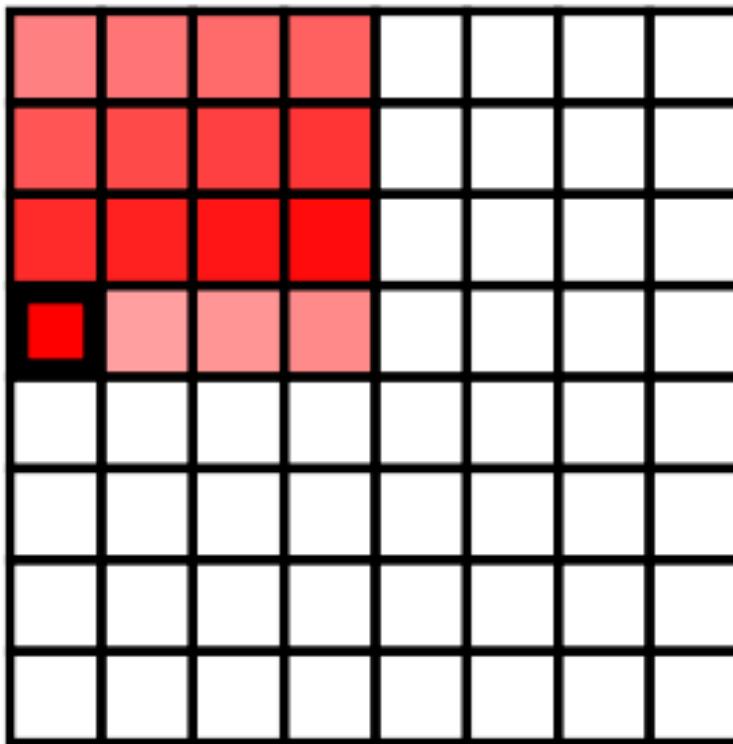
cache hits:173 $\approx 75\%$

A mem:77 cache hit:57



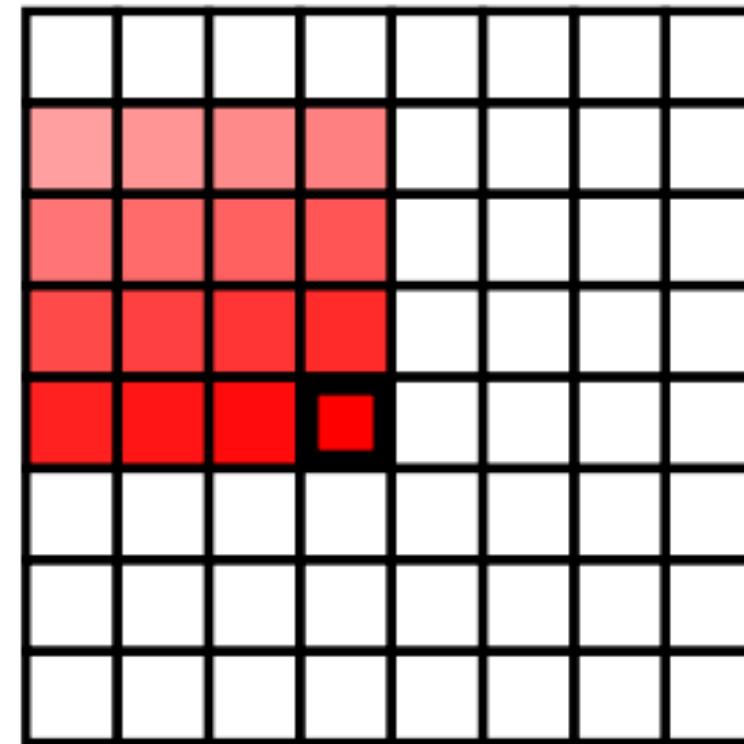
mem:231

B mem:77 cache hit:61

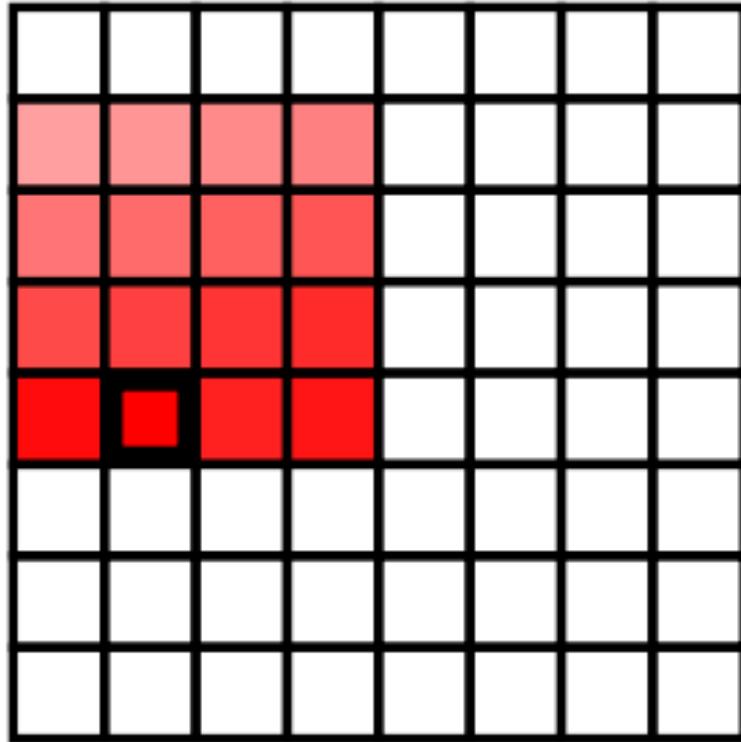


cache hits:175 ≈75%

C mem:77 cache hit:57

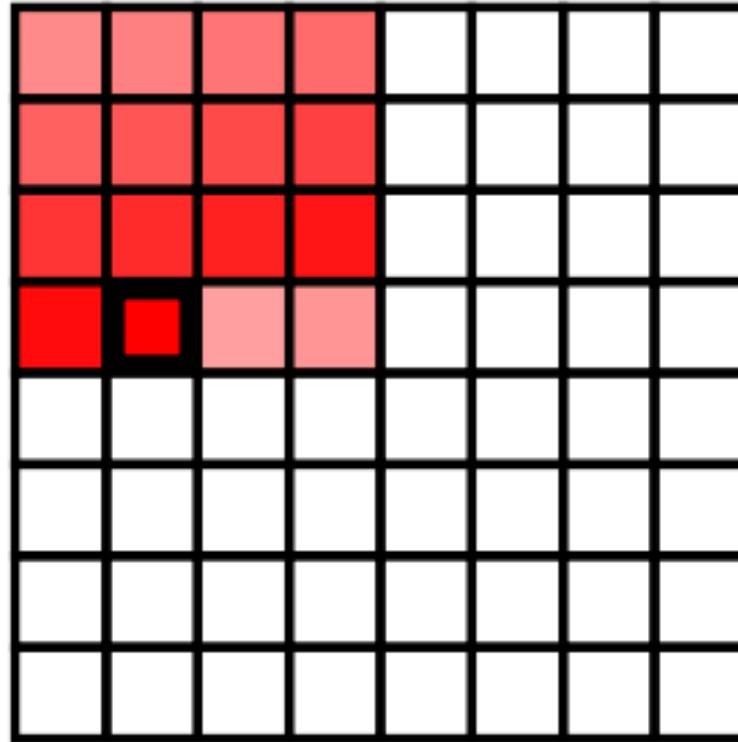


A mem:78 cache hit:58



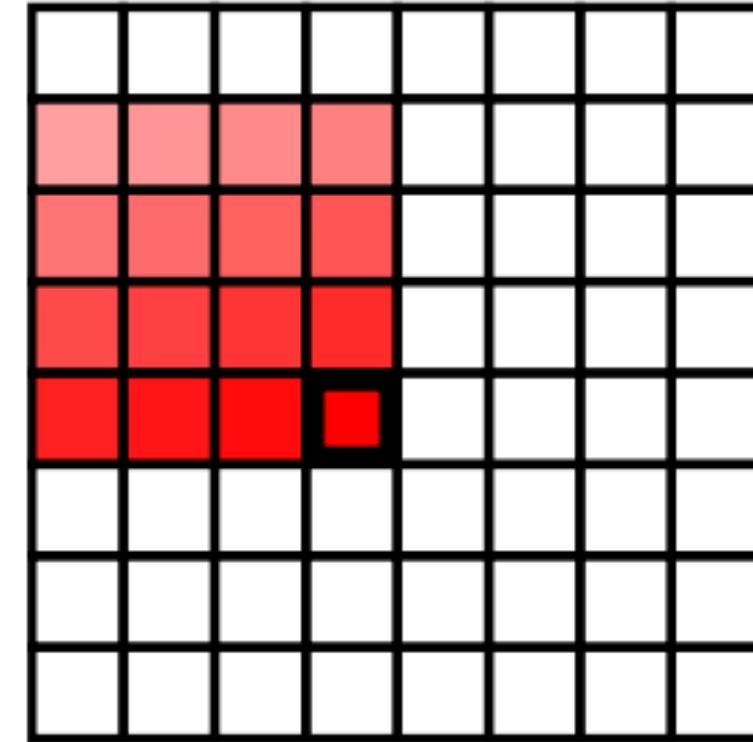
mem:234

B mem:78 cache hit:62

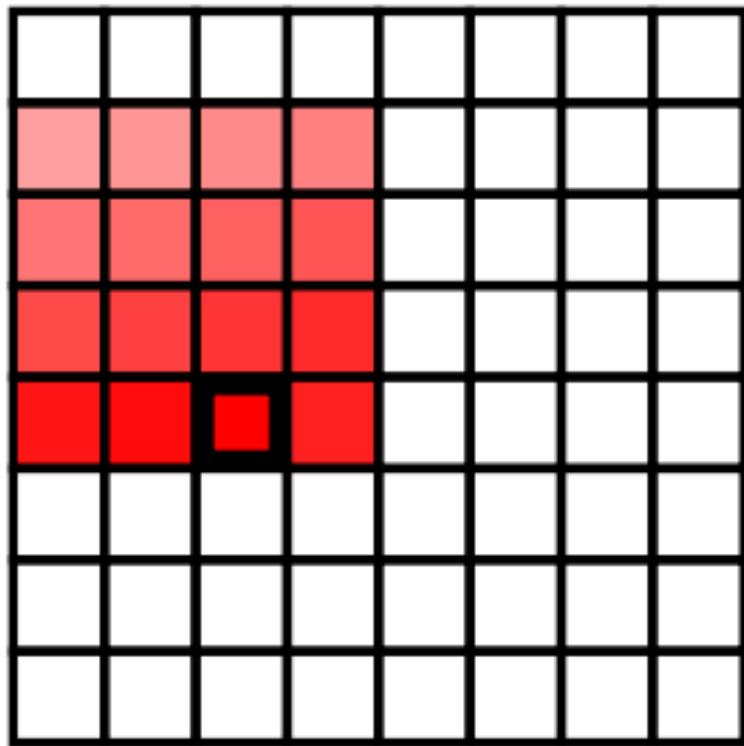


cache hits:178 ≈76%

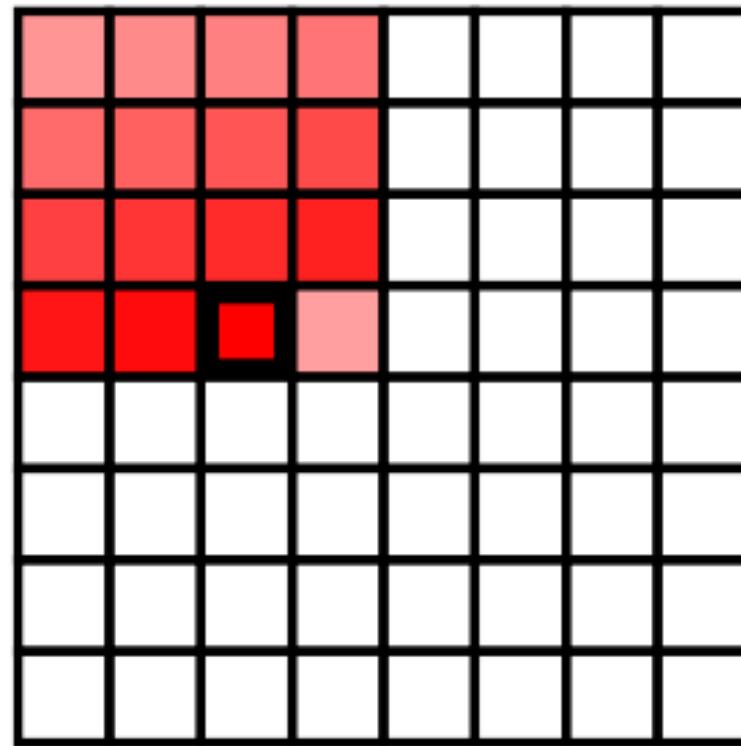
C mem:78 cache hit:58



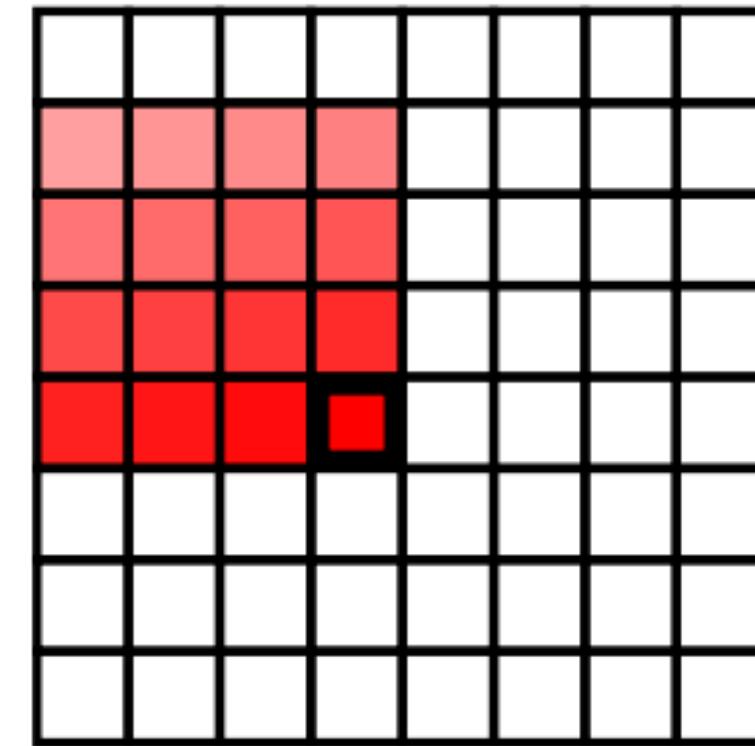
A mem:79 cache hit:59



B mem:79 cache hit:63



C mem:79 cache hit:59



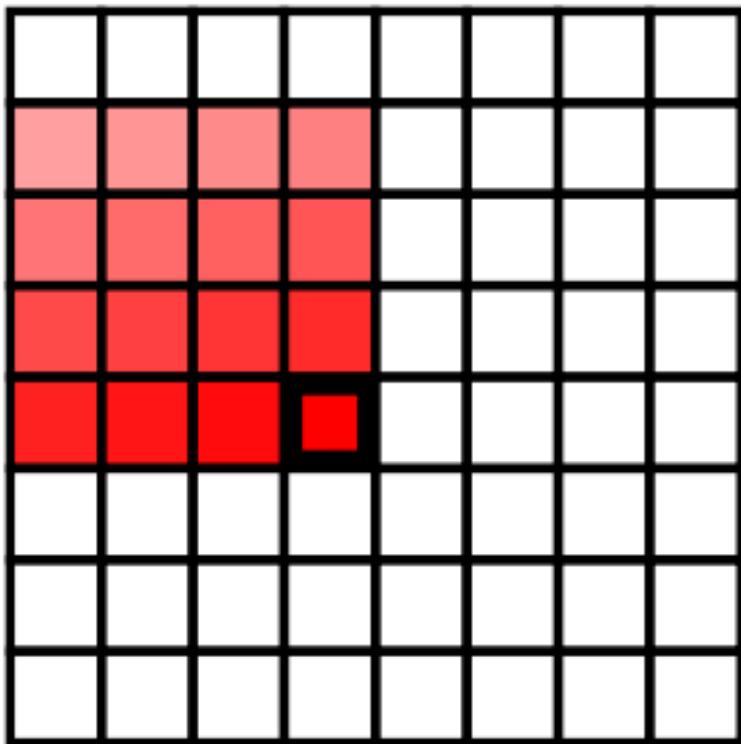
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mem:237

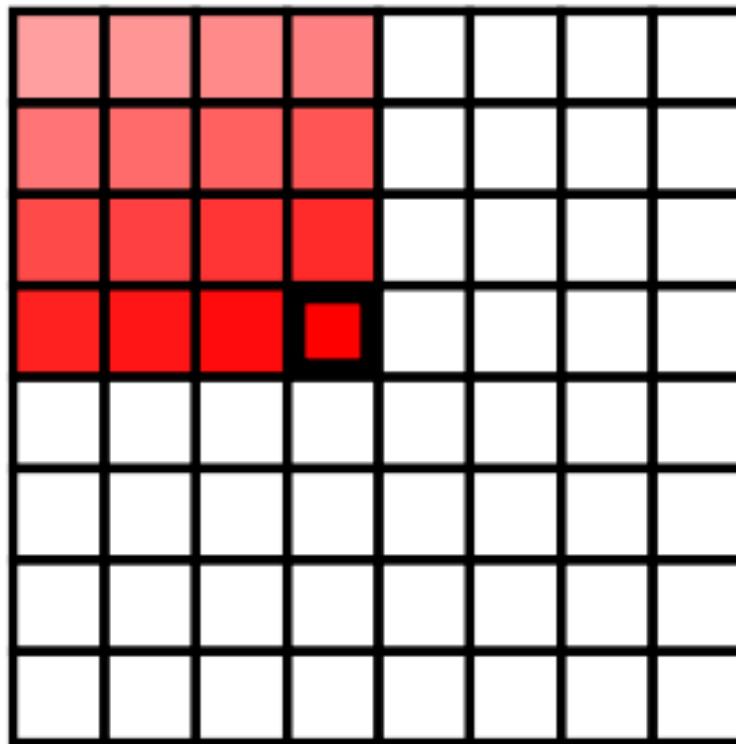
cache hits:181 $\approx 76\%$

A mem:80 cache hit:60



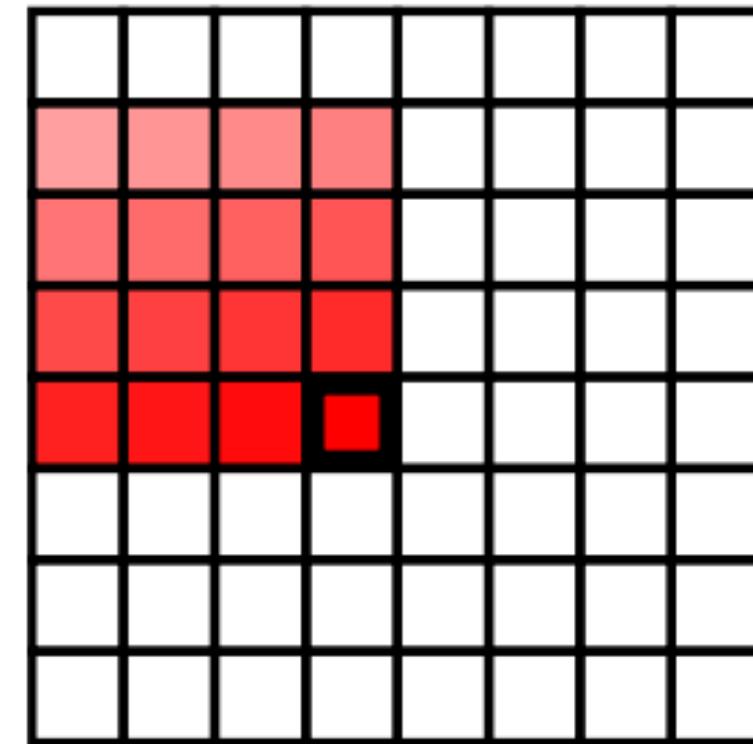
mem:240

B mem:80 cache hit:64

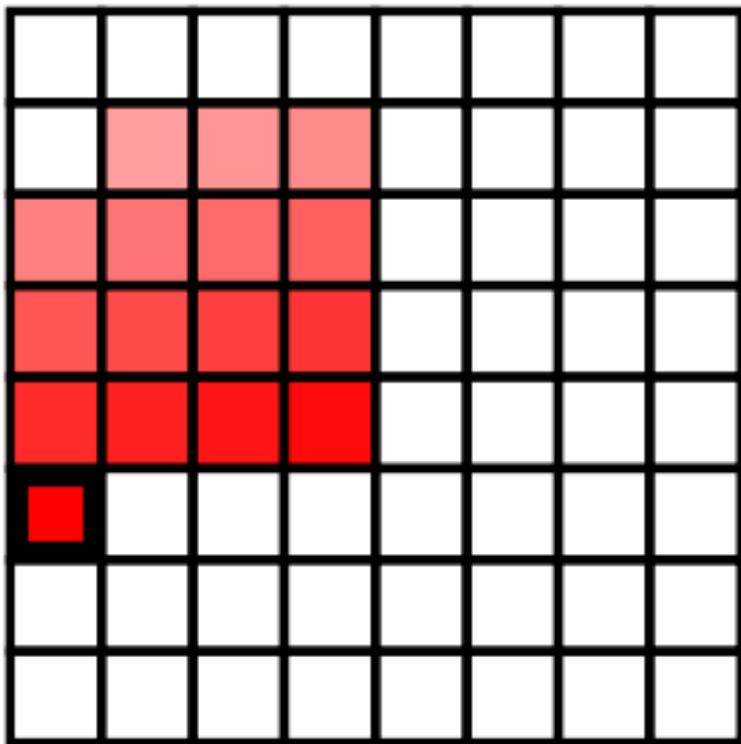


cache hits:184 ≈76%

C mem:80 cache hit:60

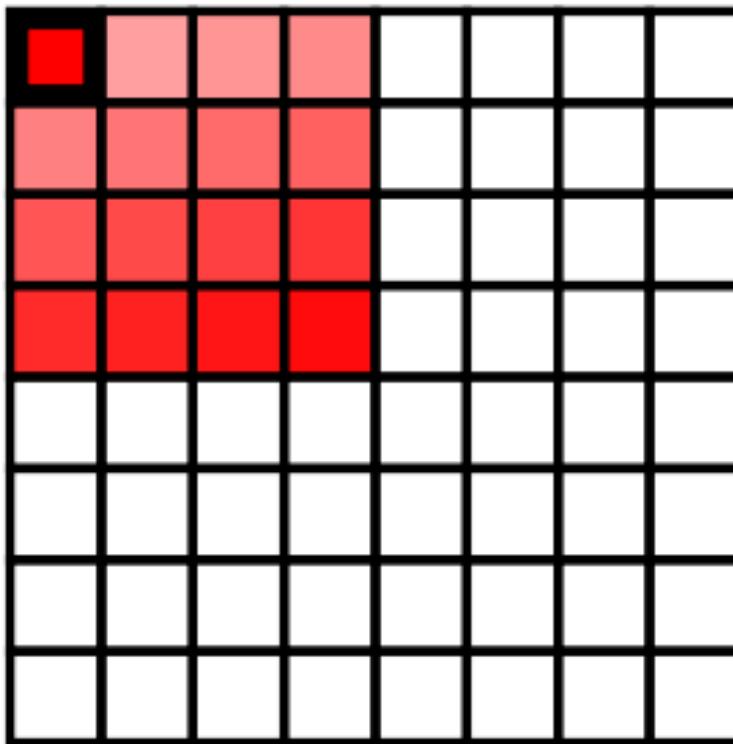


A mem:81 cache hit:60



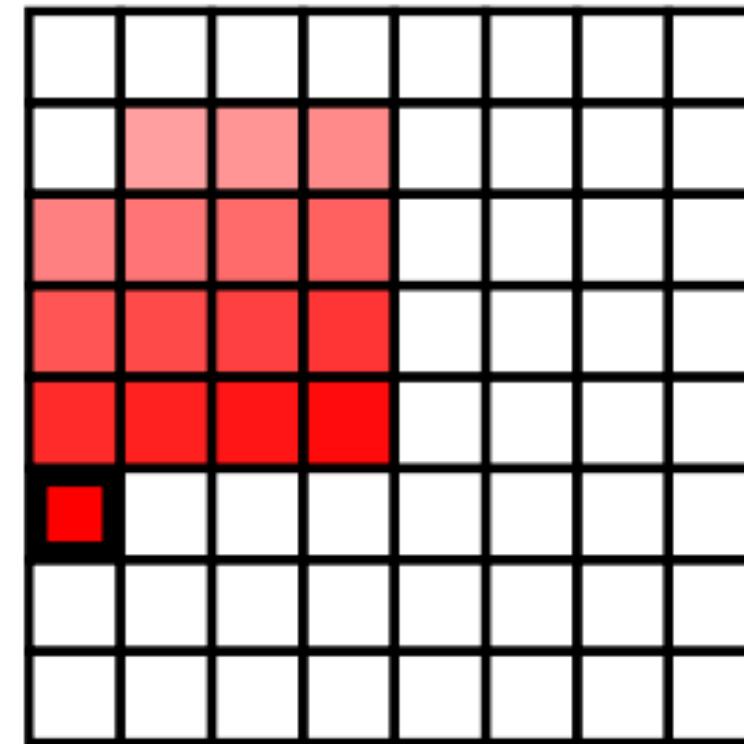
mem:243

B mem:81 cache hit:65

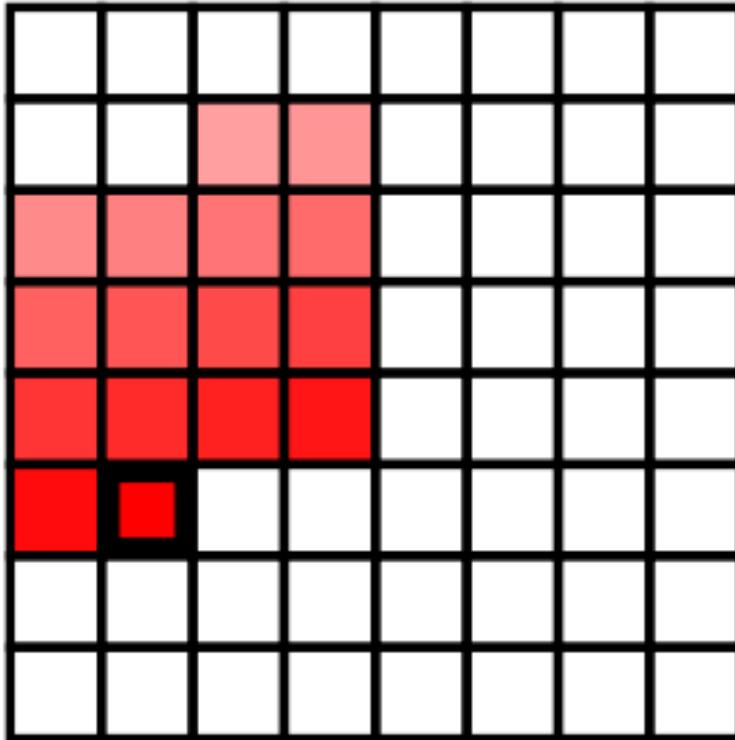


cache hits:185 ≈76%

C mem:81 cache hit:60

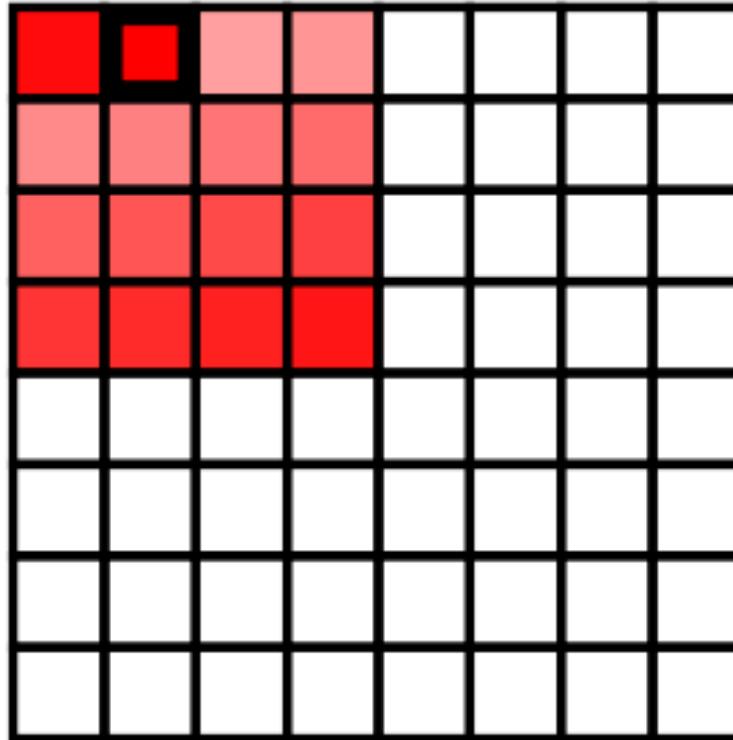


A mem:82 cache hit:60



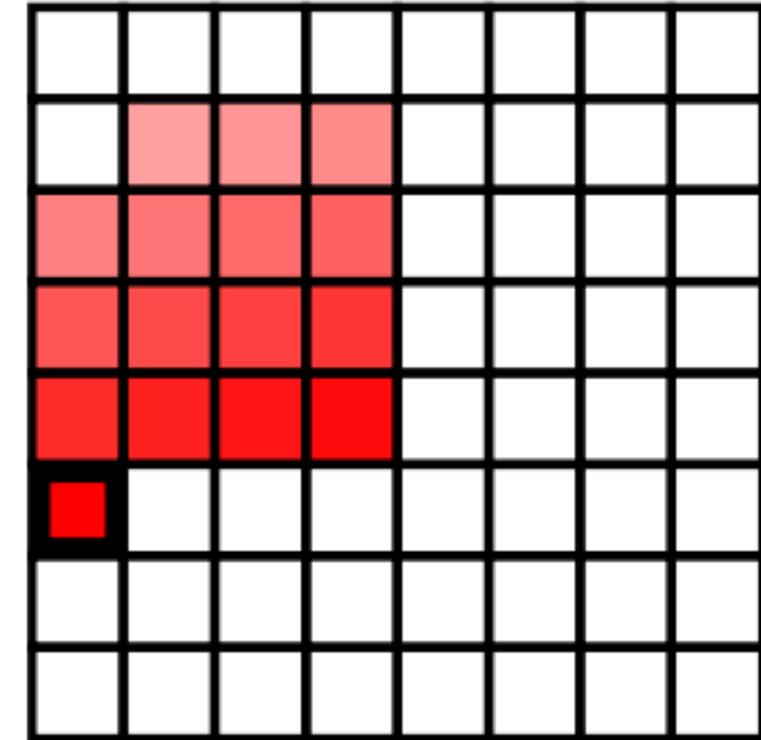
mem:246

B mem:82 cache hit:66

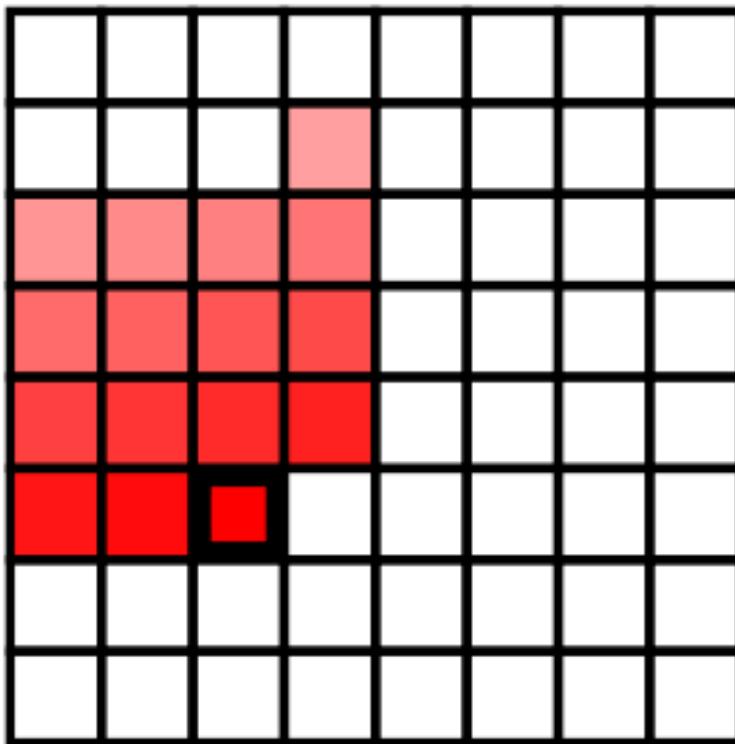


cache hits:187 ≈76%

C mem:82 cache hit:61

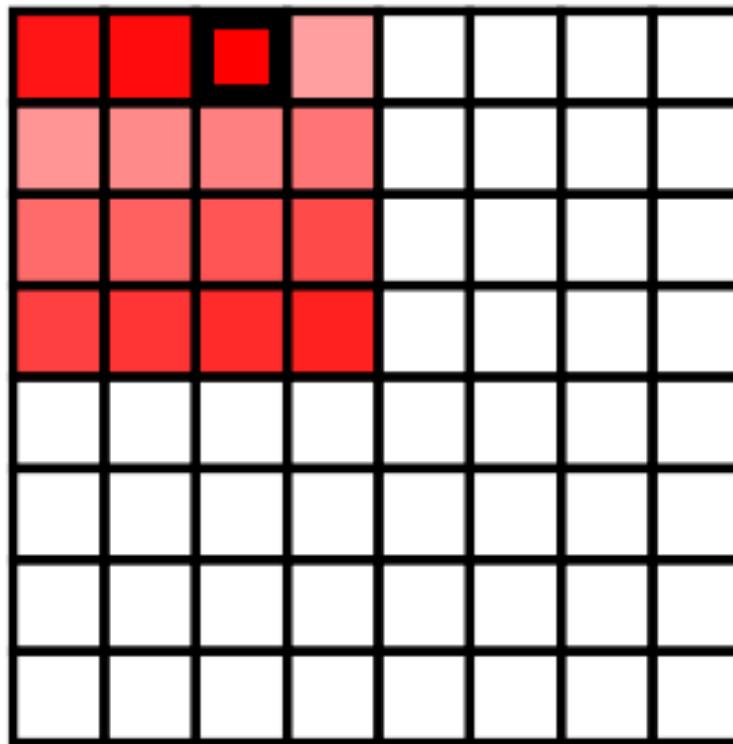


A mem:83 cache hit:60



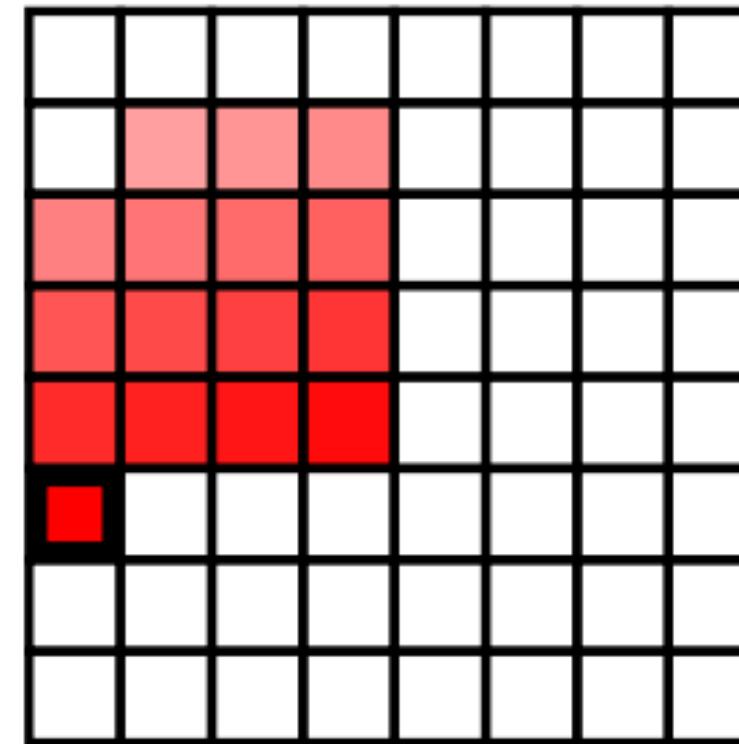
mem:249

B mem:83 cache hit:67

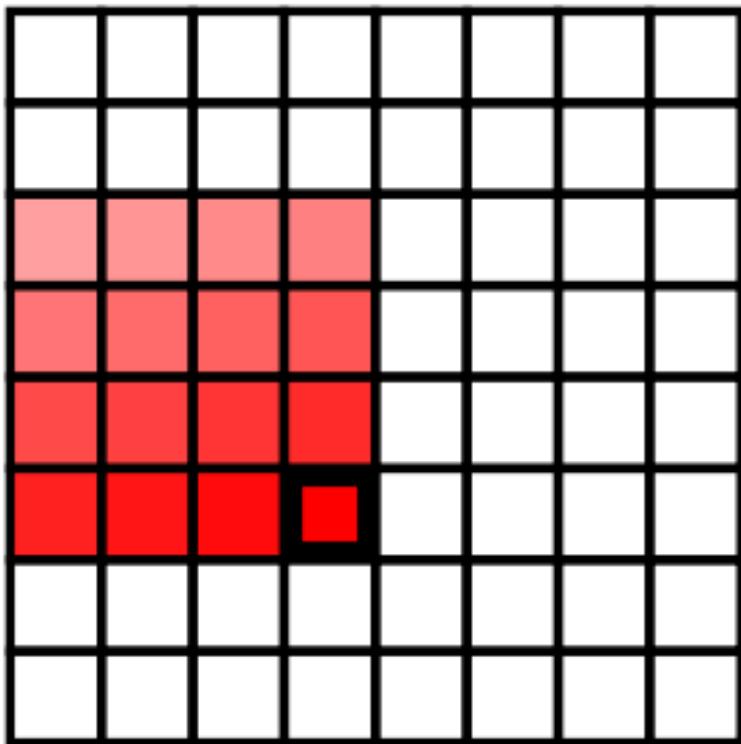


cache hits:189 $\approx 75\%$

C mem:83 cache hit:62

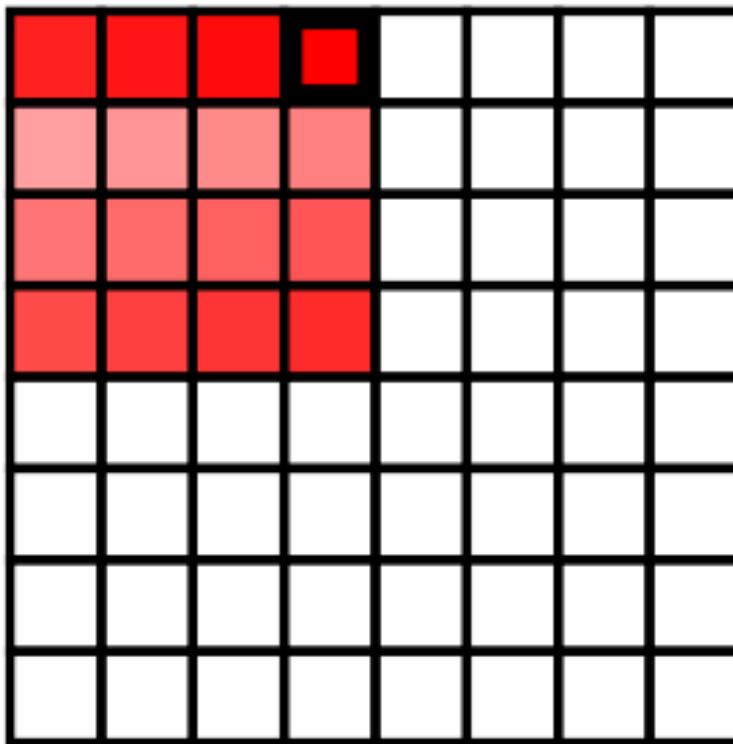


A mem:84 cache hit:60



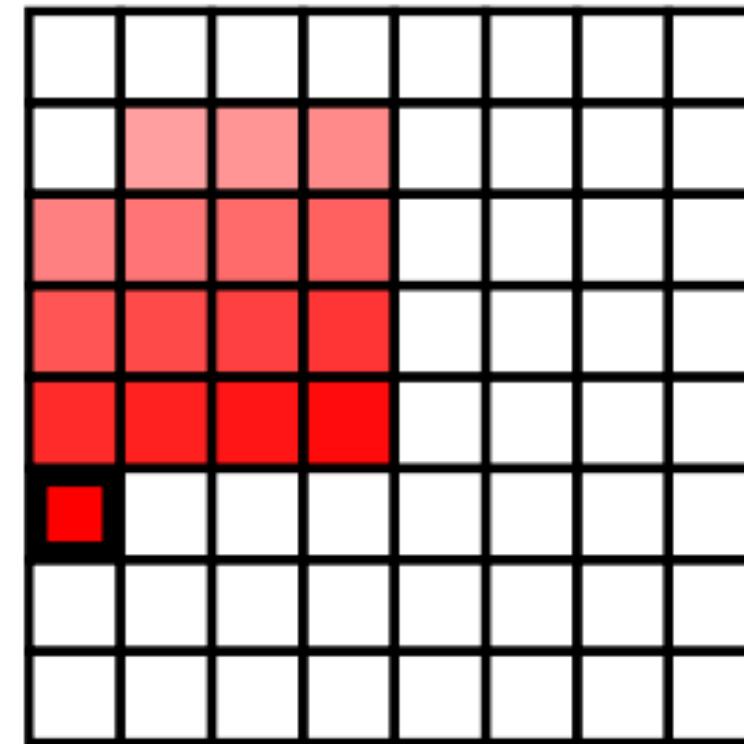
mem:252

B mem:84 cache hit:68

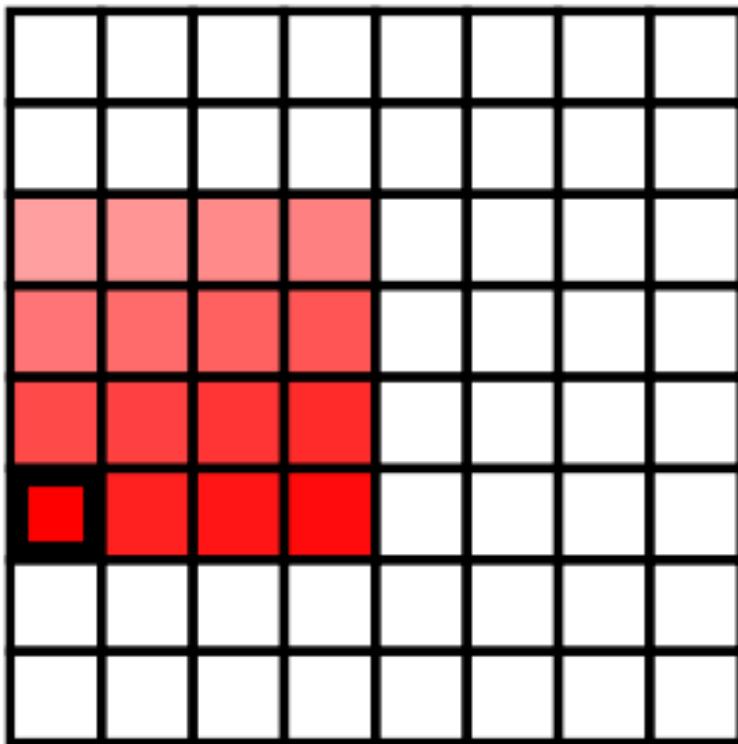


cache hits:191 $\approx 75\%$

C mem:84 cache hit:63

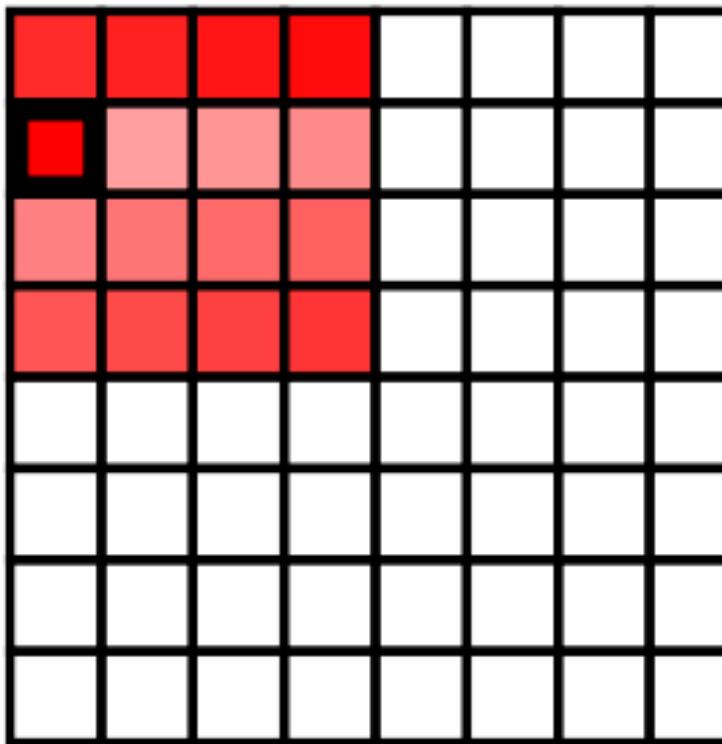


A mem:85 cache hit:61



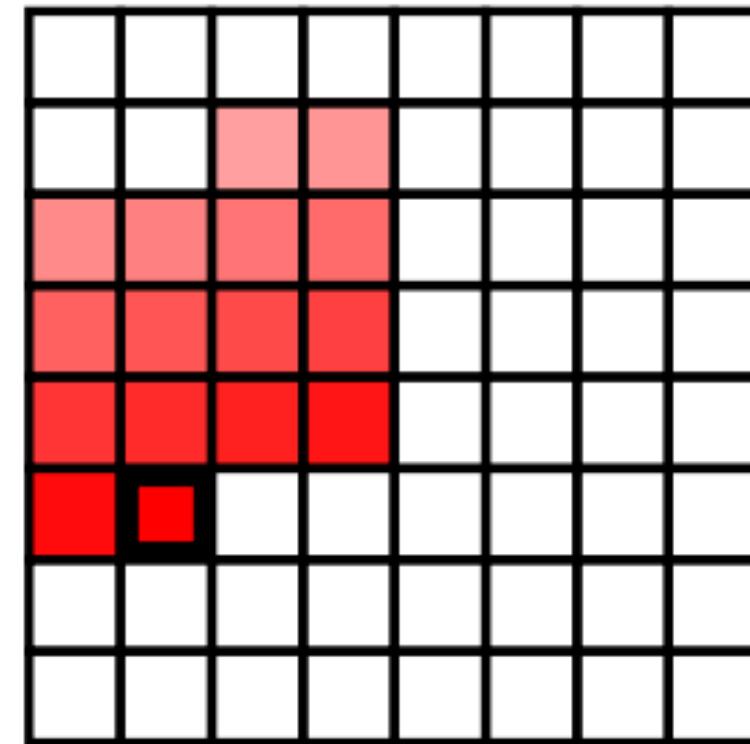
mem:255

B mem:85 cache hit:69

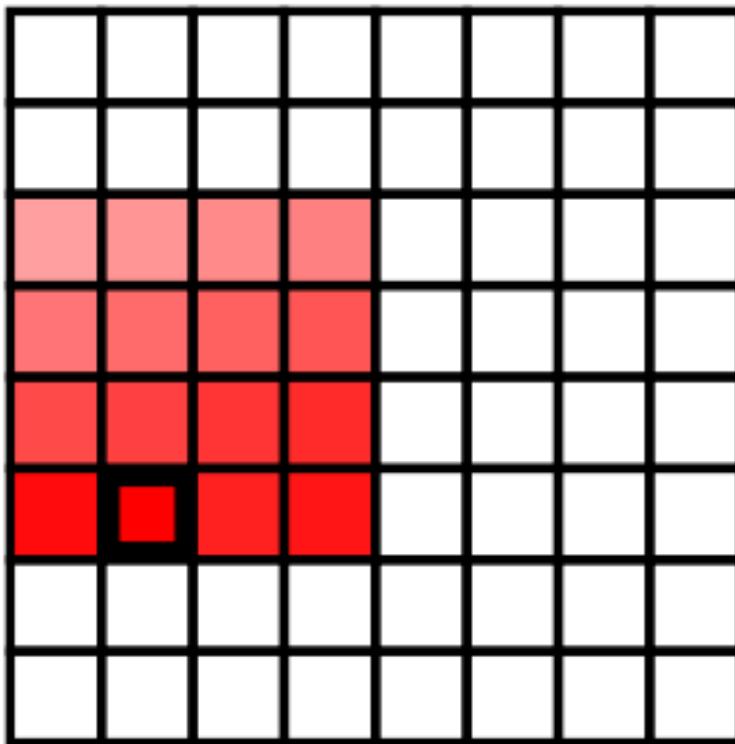


cache hits:193 ≈75%

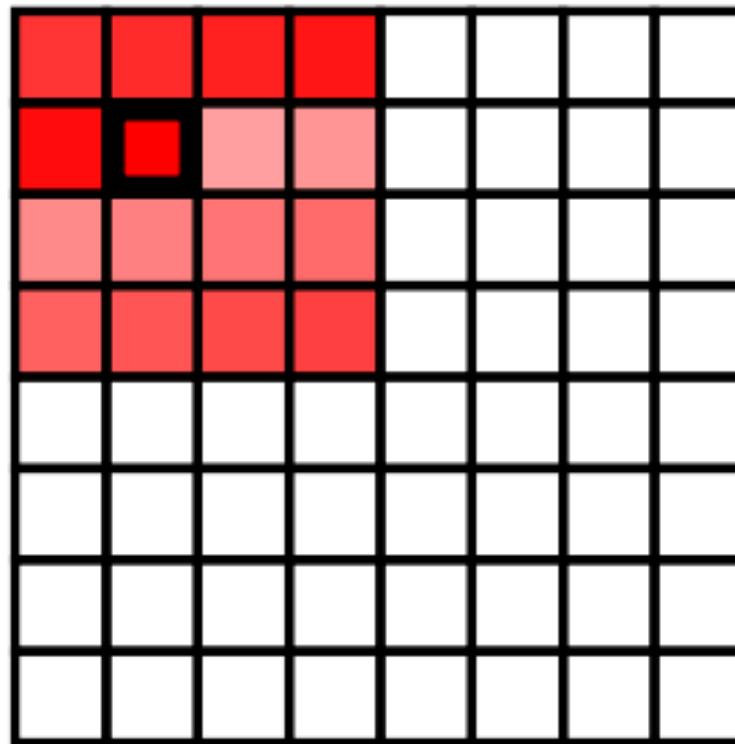
C mem:85 cache hit:63



A mem:86 cache hit:62

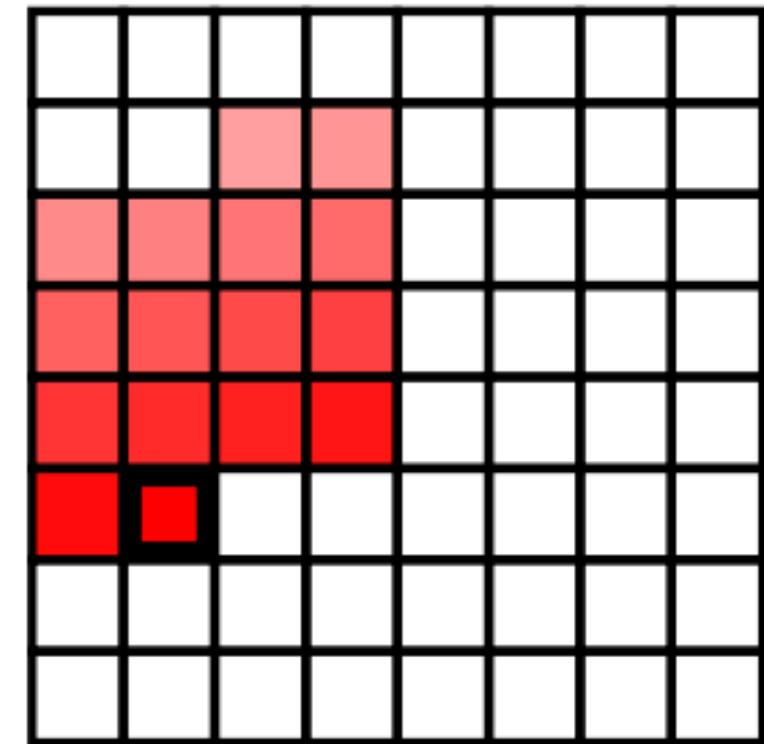


B mem:86 cache hit:70



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C mem:86 cache hit:64

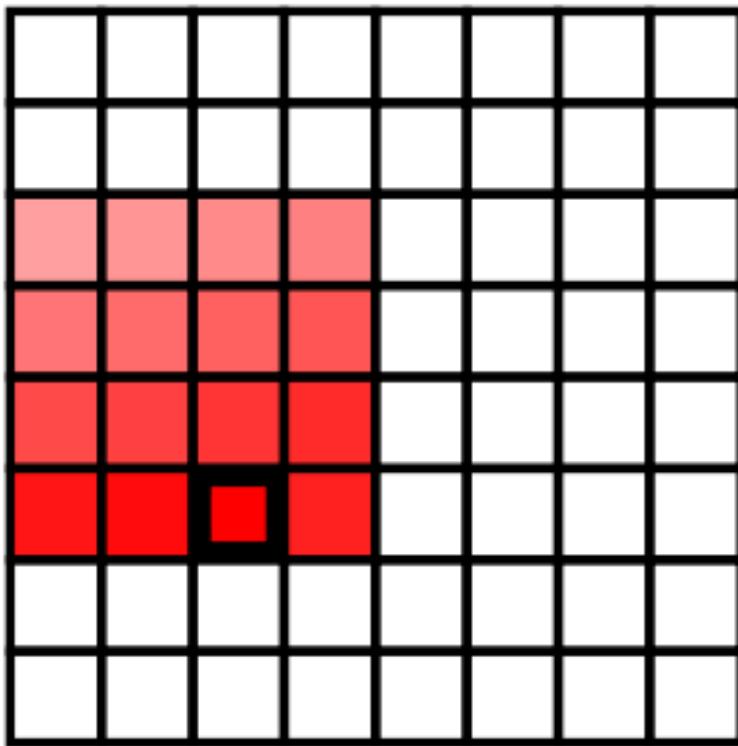


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mem:258

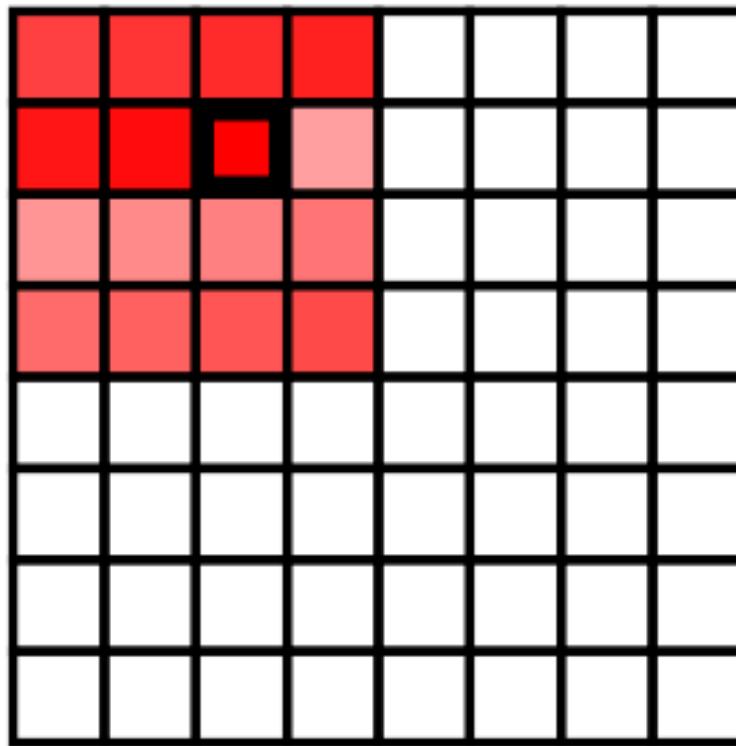
cache hits:196 $\approx 75\%$

A mem:87 cache hit:63



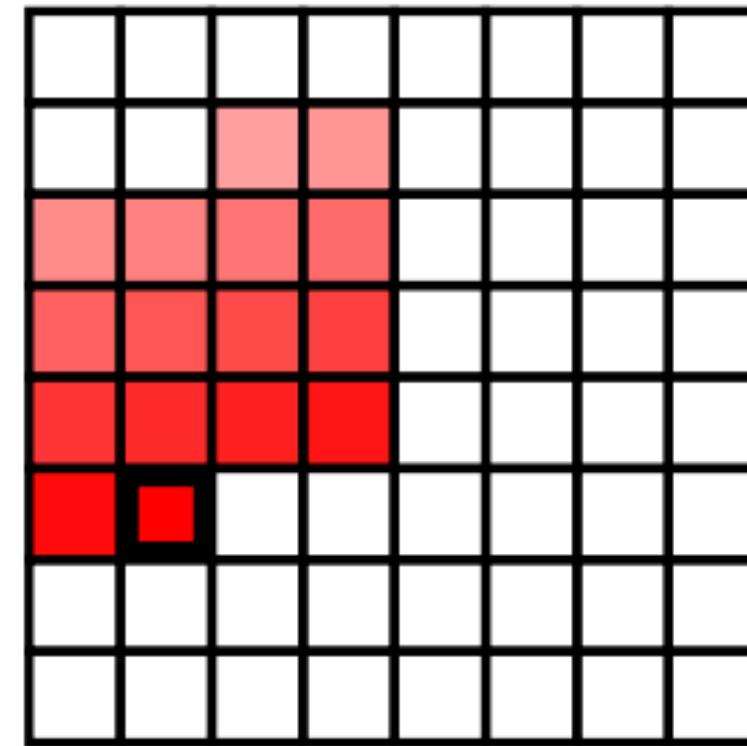
mem:261

B mem:87 cache hit:71

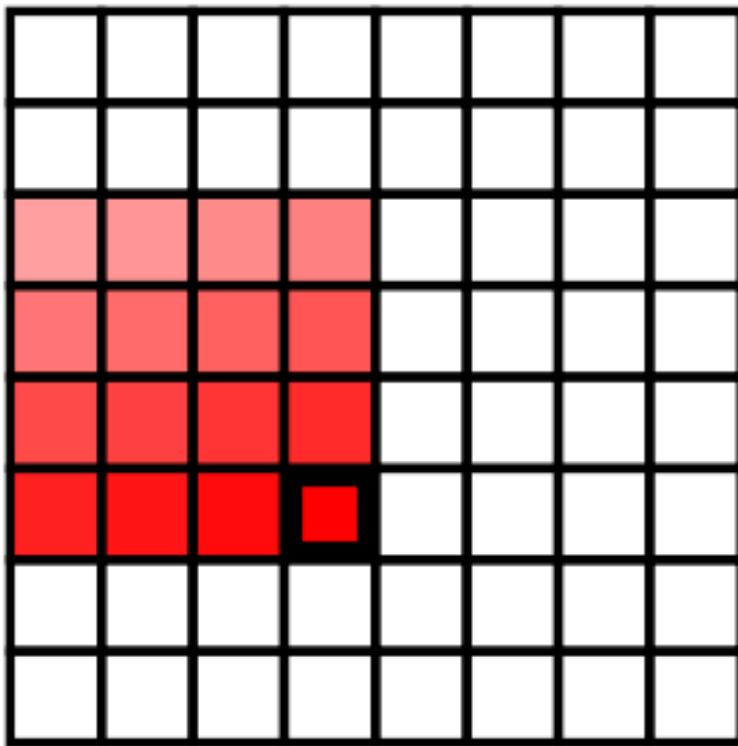


cache hits:199 ≈76%

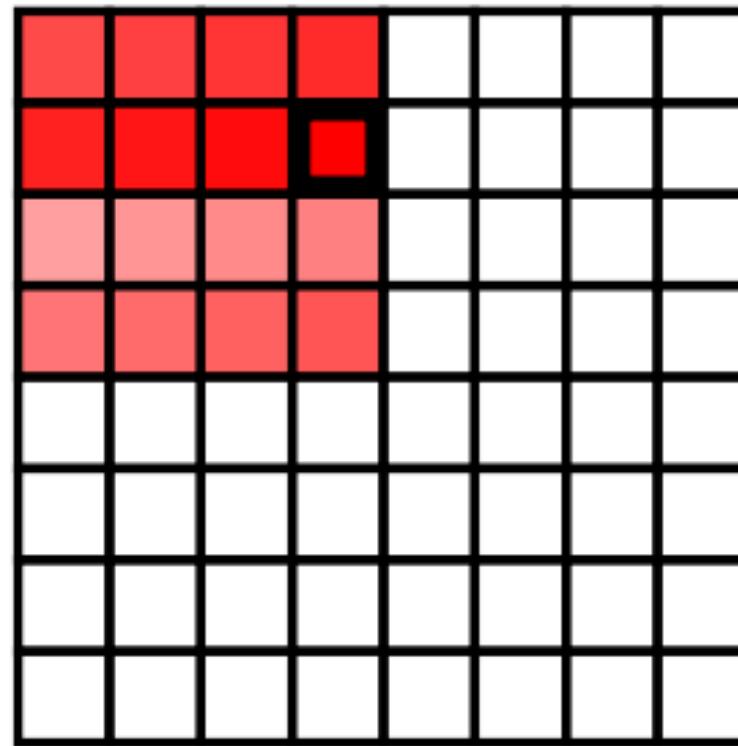
C mem:87 cache hit:65



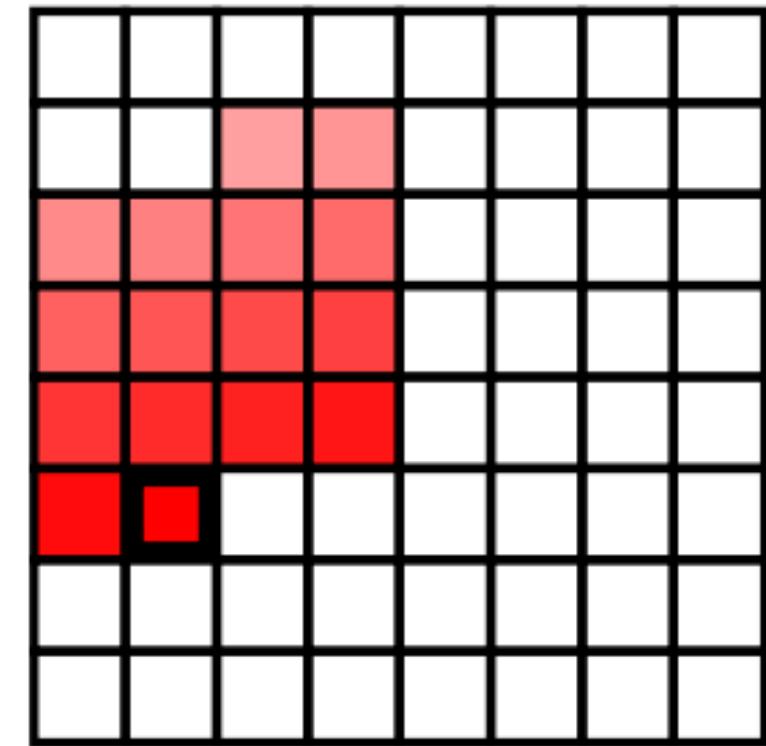
A mem:88 cache hit:64



B mem:88 cache hit:72



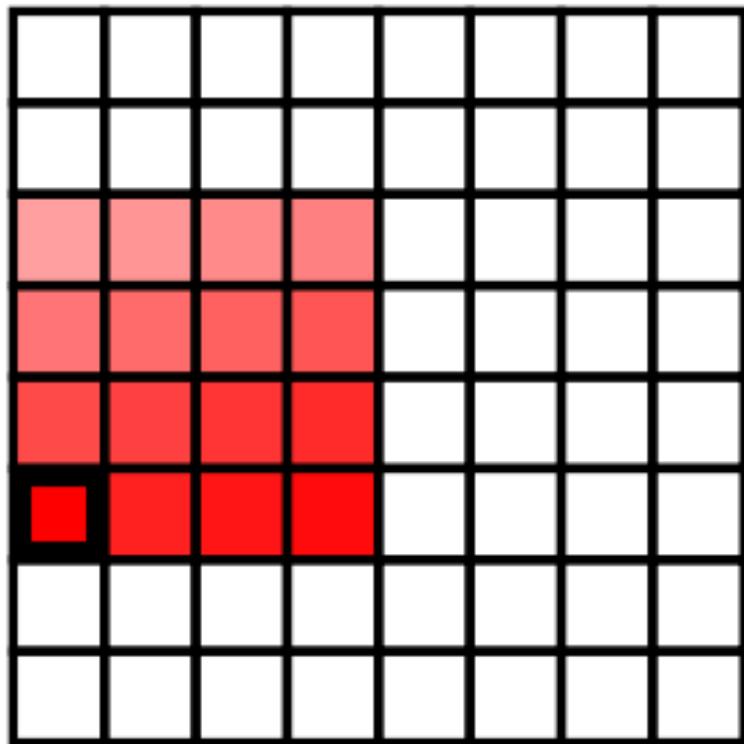
C mem:88 cache hit:66



mem:264

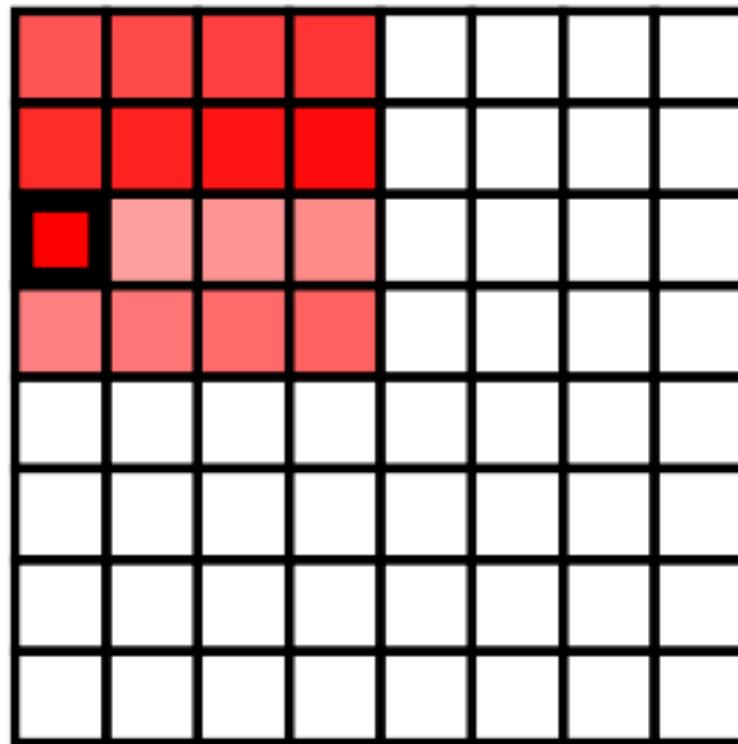
cache hits:202 $\approx 76\%$

A mem:89 cache hit:65



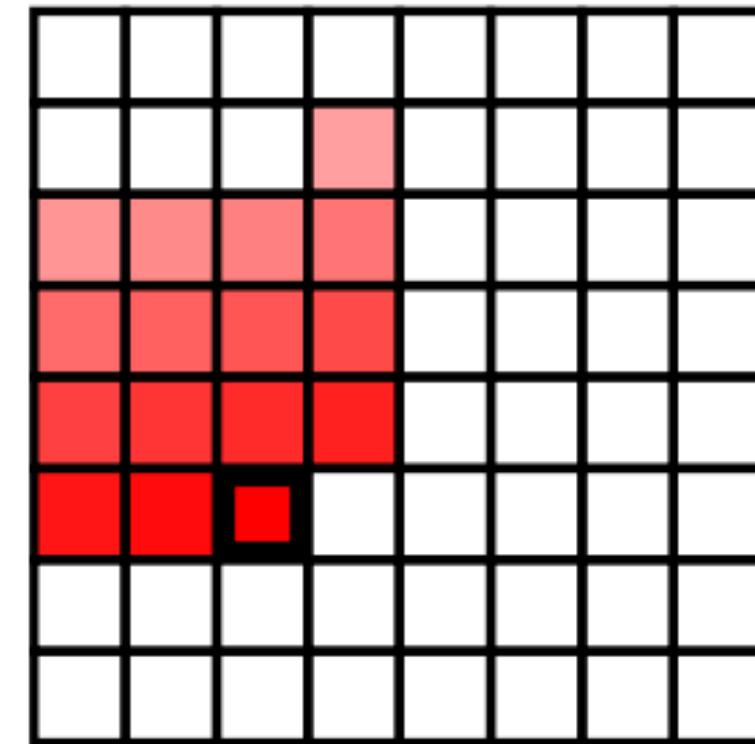
mem:267

B mem:89 cache hit:73

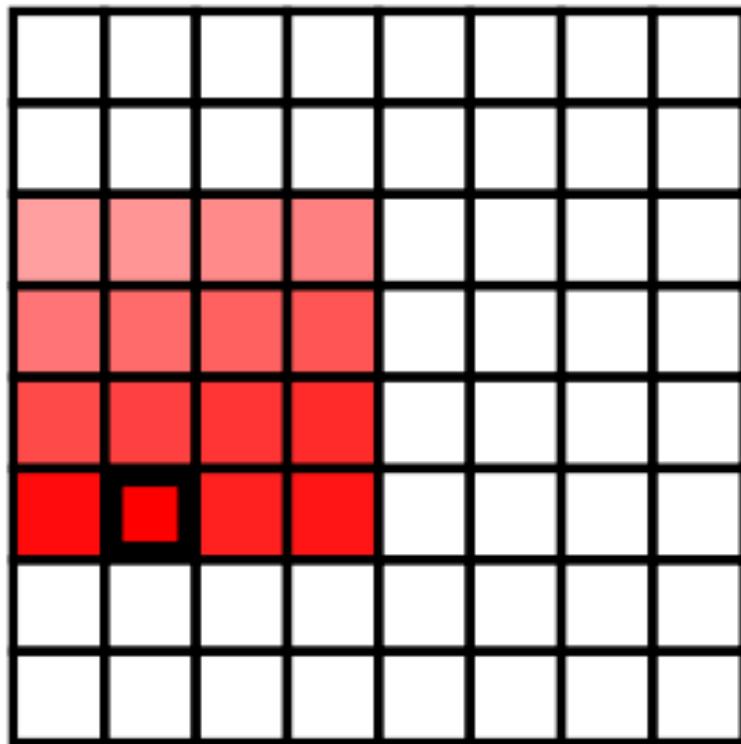


cache hits:204 ≈76%

C mem:89 cache hit:66

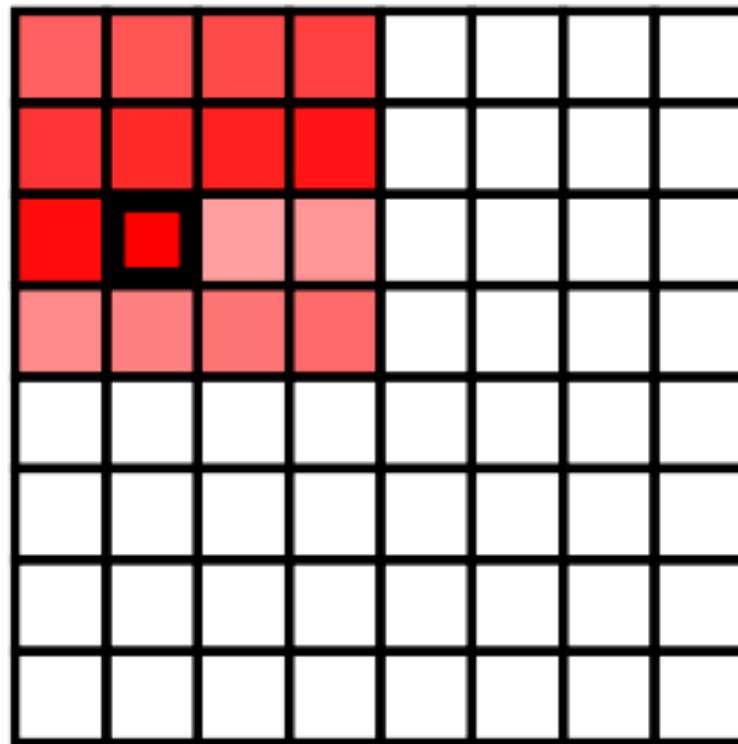


A mem:90 cache hit:66



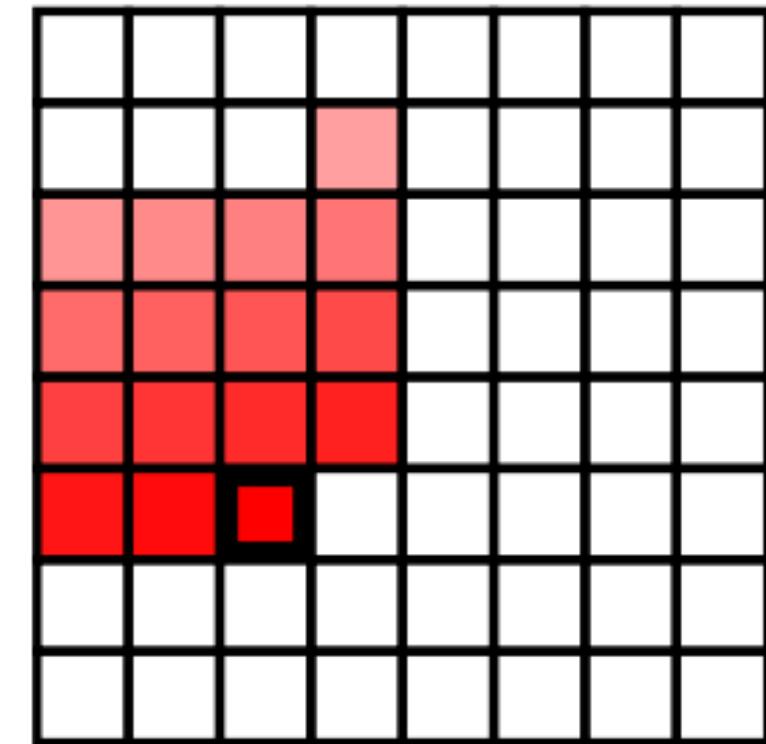
mem:270

B mem:90 cache hit:74

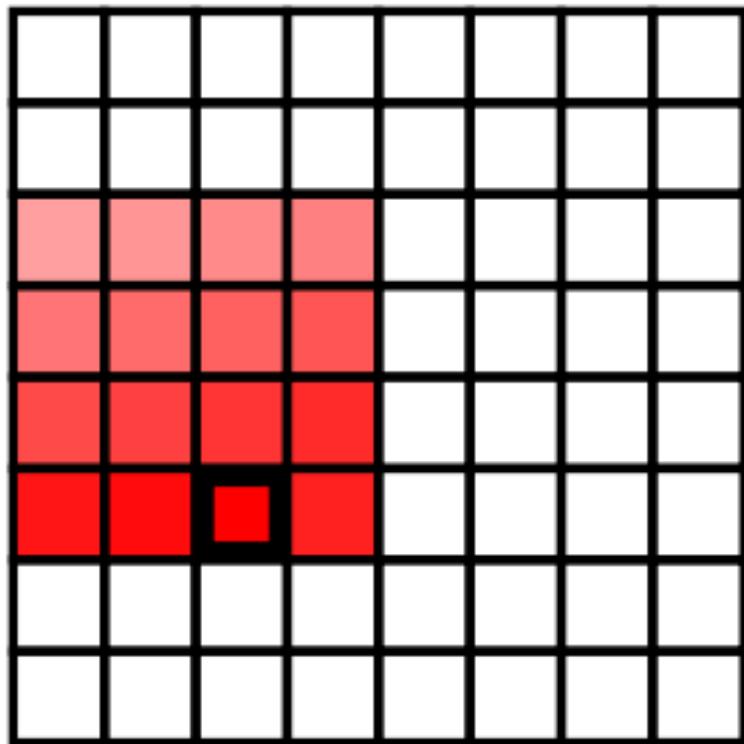


cache hits:207 ≈76%

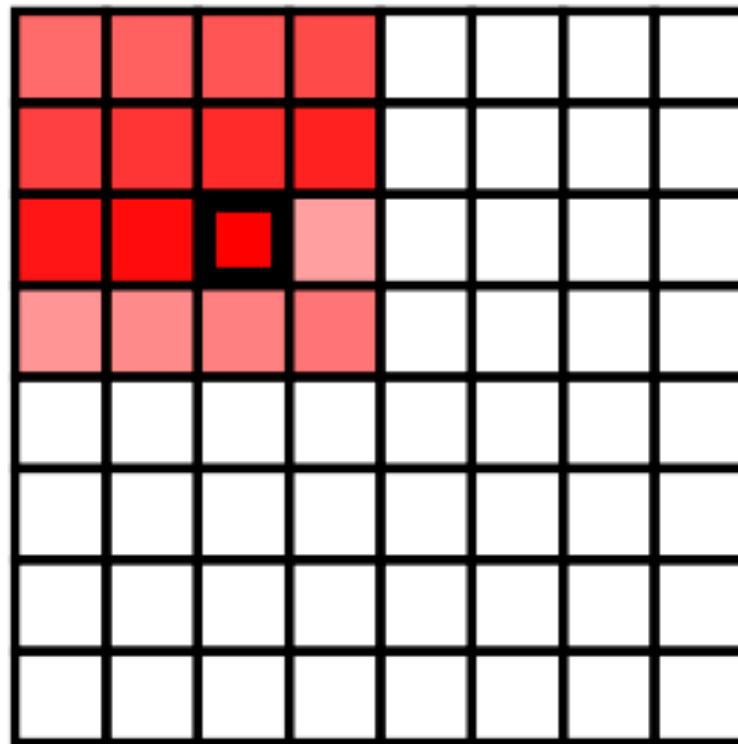
C mem:90 cache hit:67



A mem:91 cache hit:67



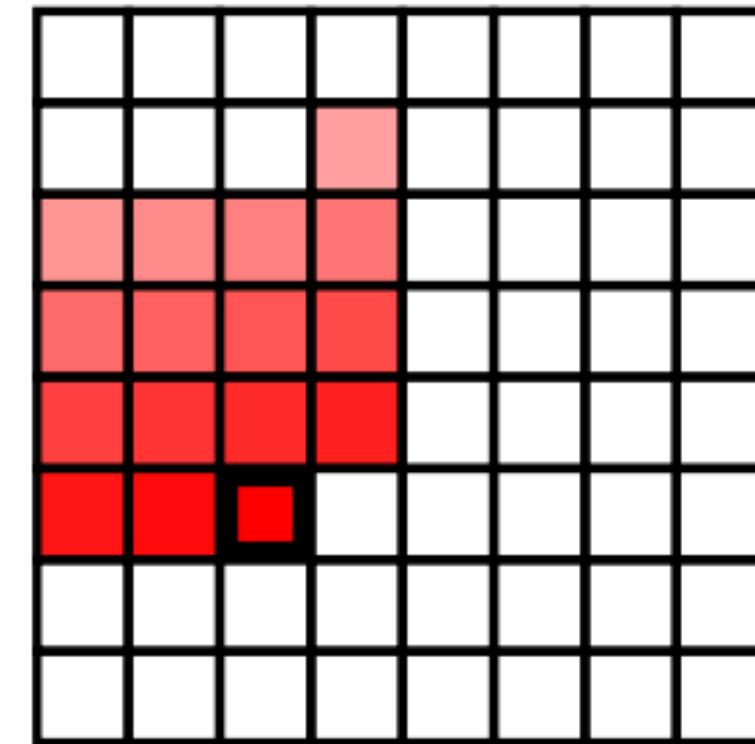
B mem:91 cache hit:75



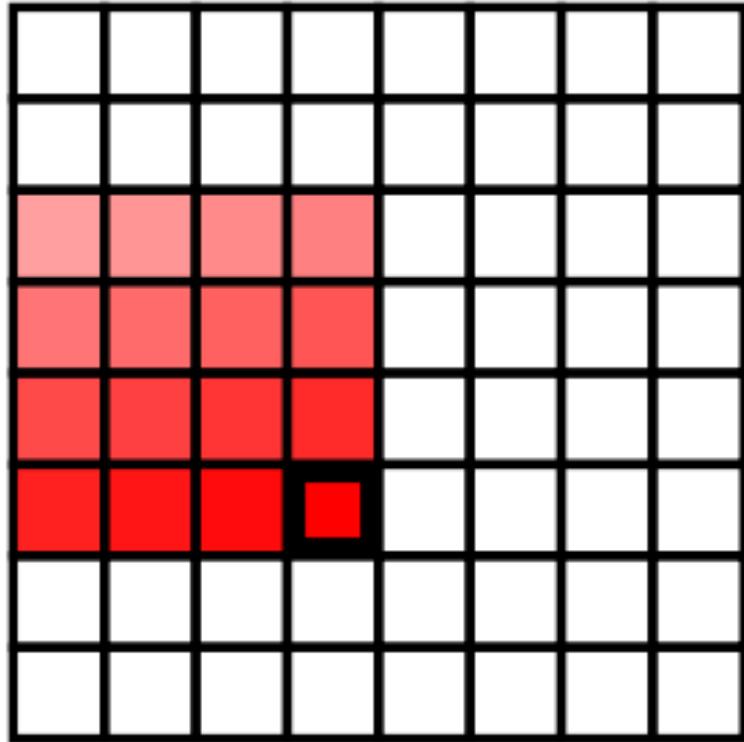
mem:273

cache hits:210 $\approx 76\%$

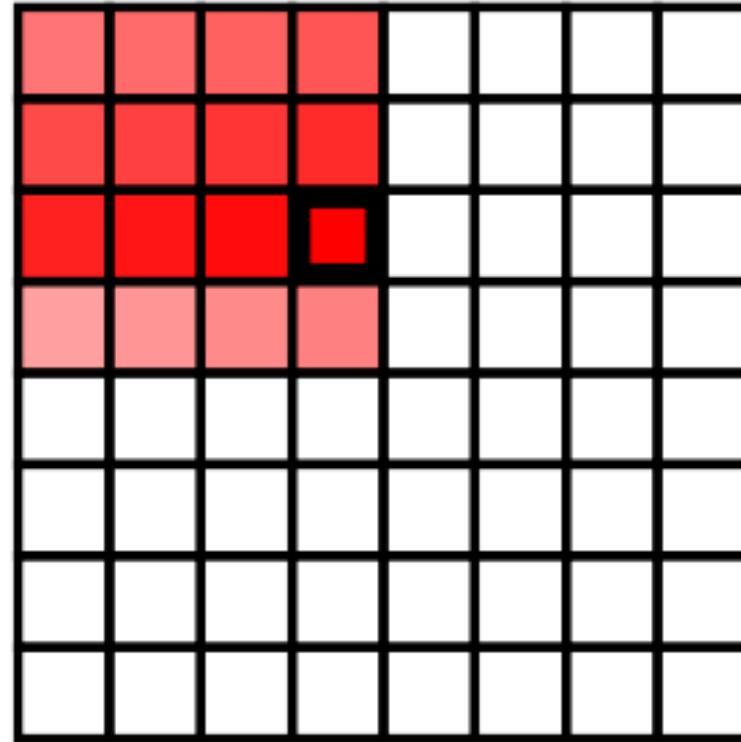
C mem:91 cache hit:68



A mem:92 cache hit:68



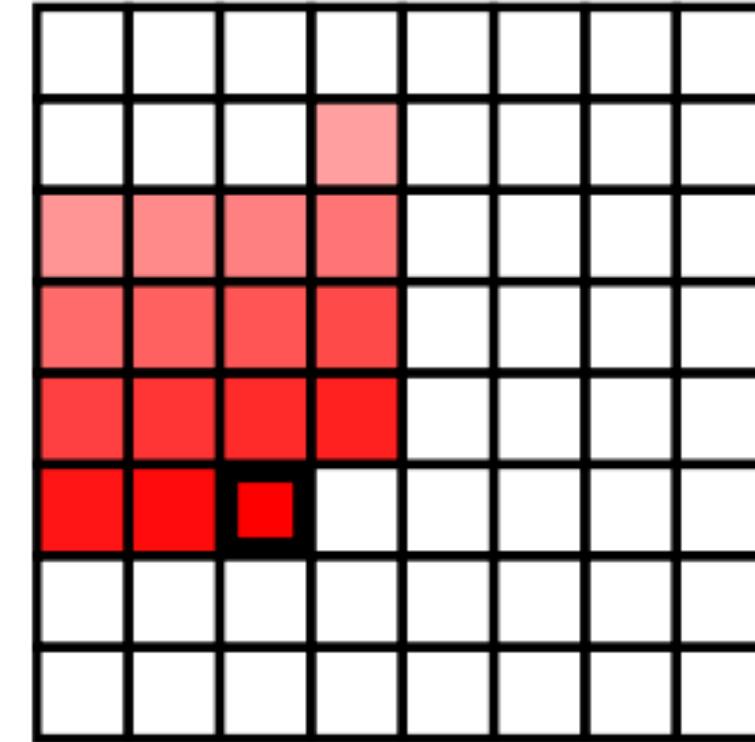
B mem:92 cache hit:76



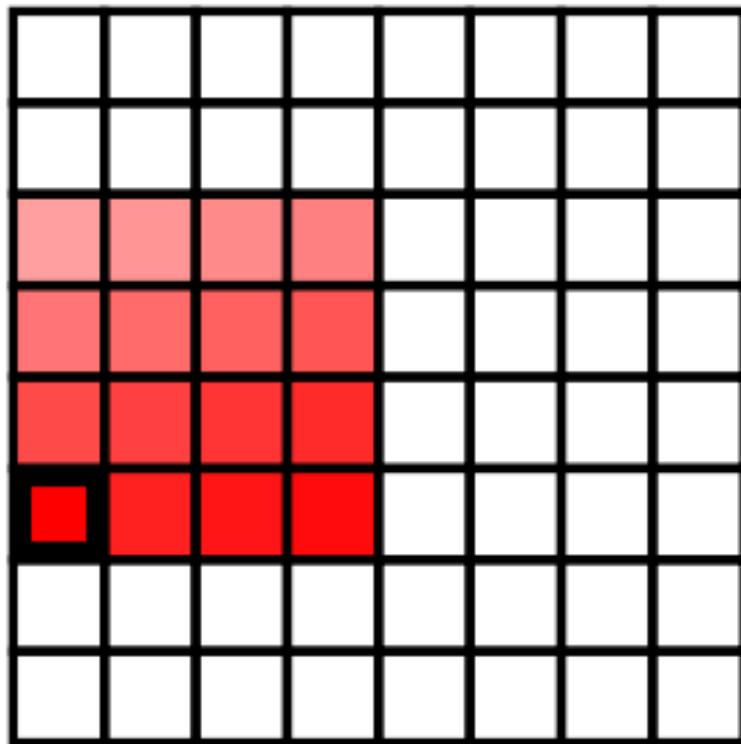
mem:276

cache hits:213 $\approx 77\%$

C mem:92 cache hit:69

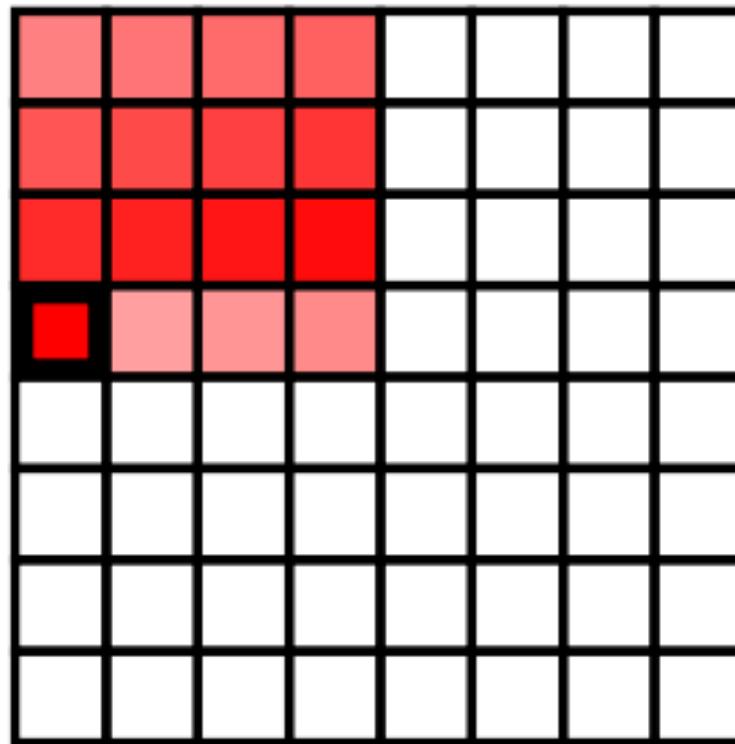


A mem:93 cache hit:69



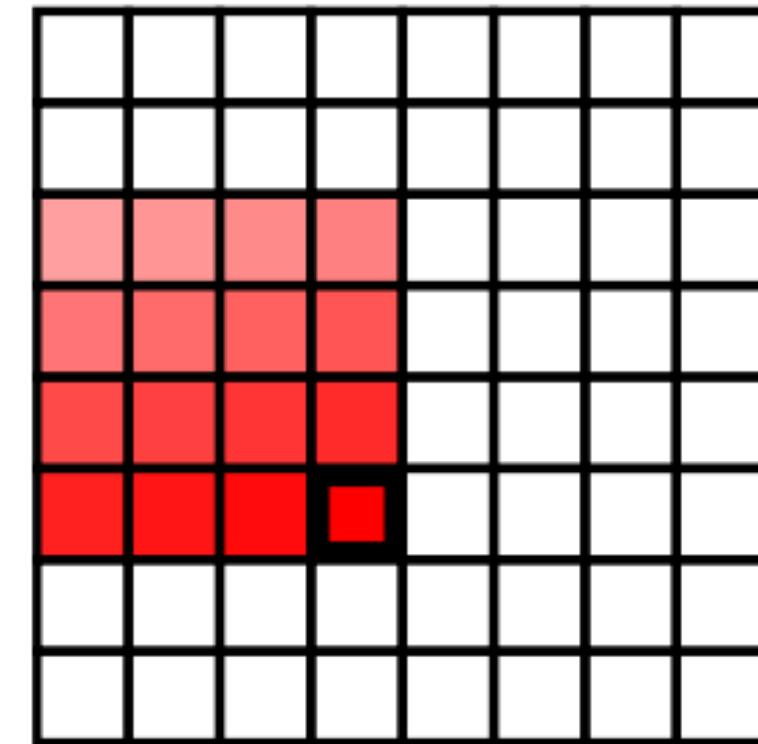
mem:279

B mem:93 cache hit:77

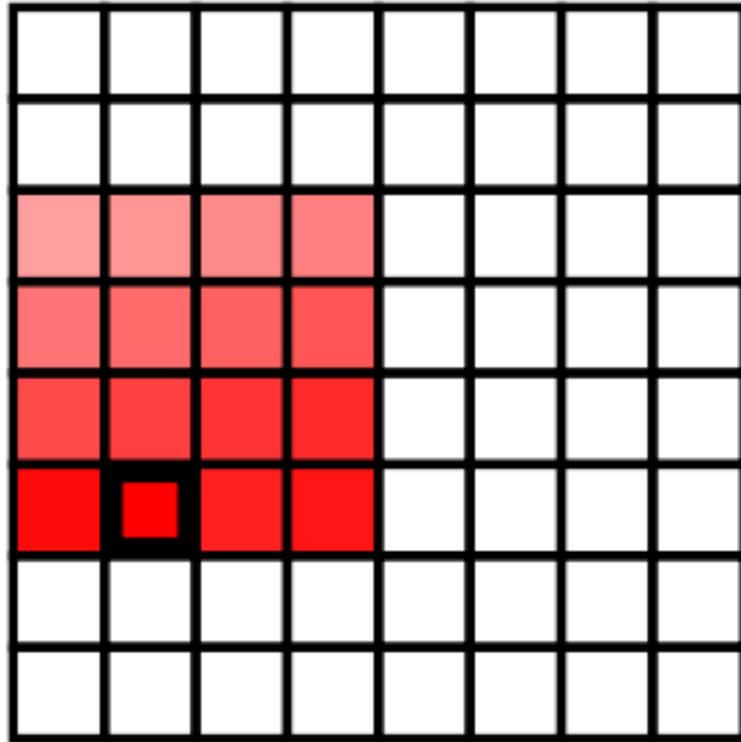


cache hits:215 ≈77%

C mem:93 cache hit:69

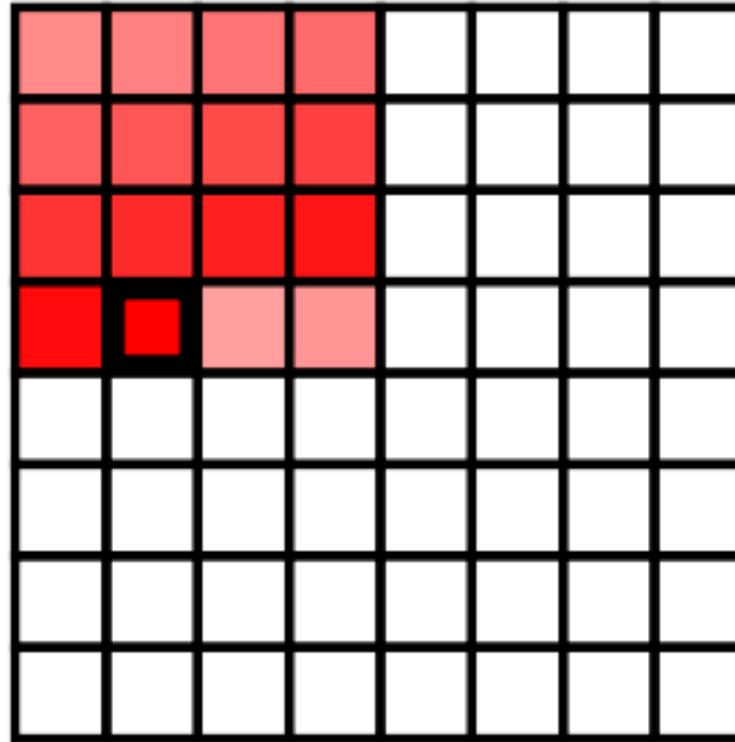


A mem:94 cache hit:70



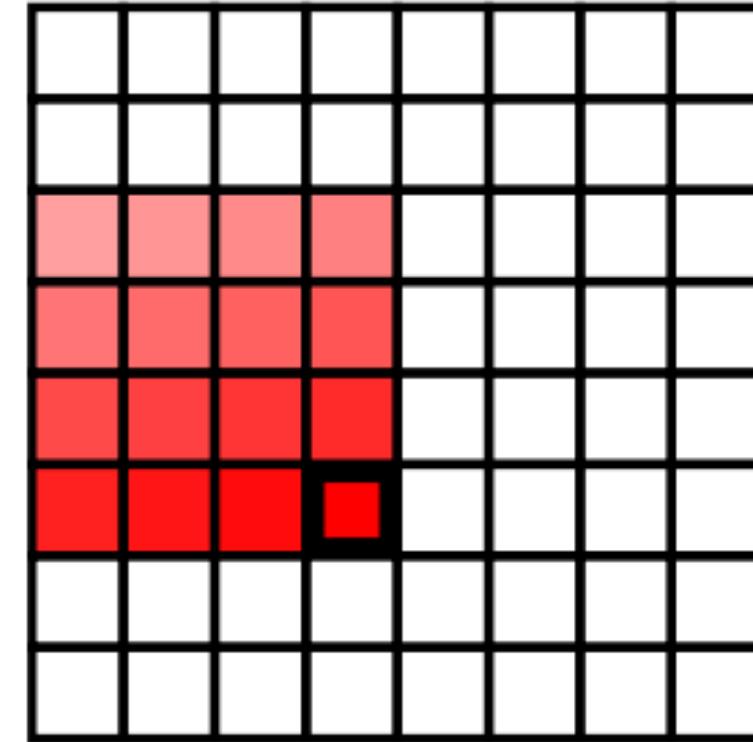
mem:282

B mem:94 cache hit:78

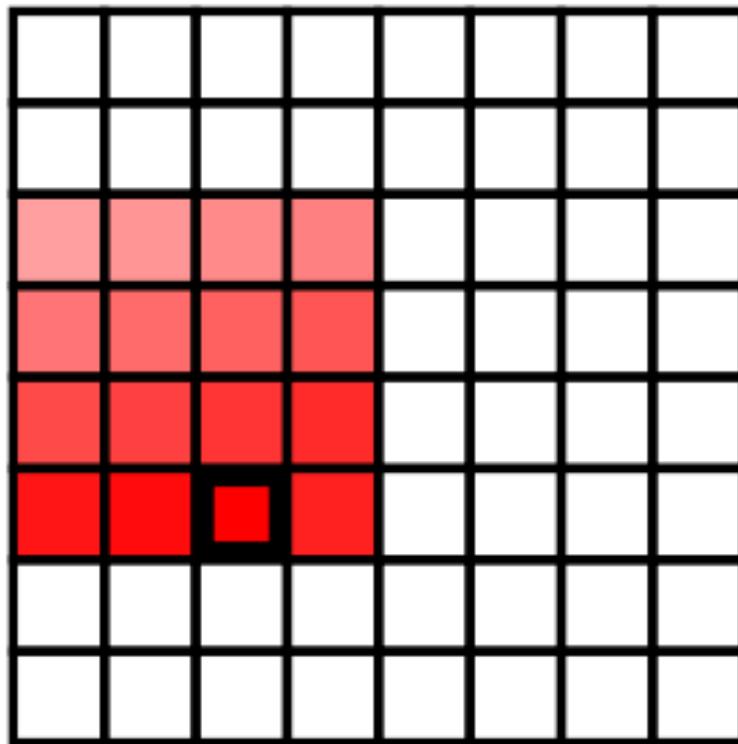


cache hits:218 ≈77%

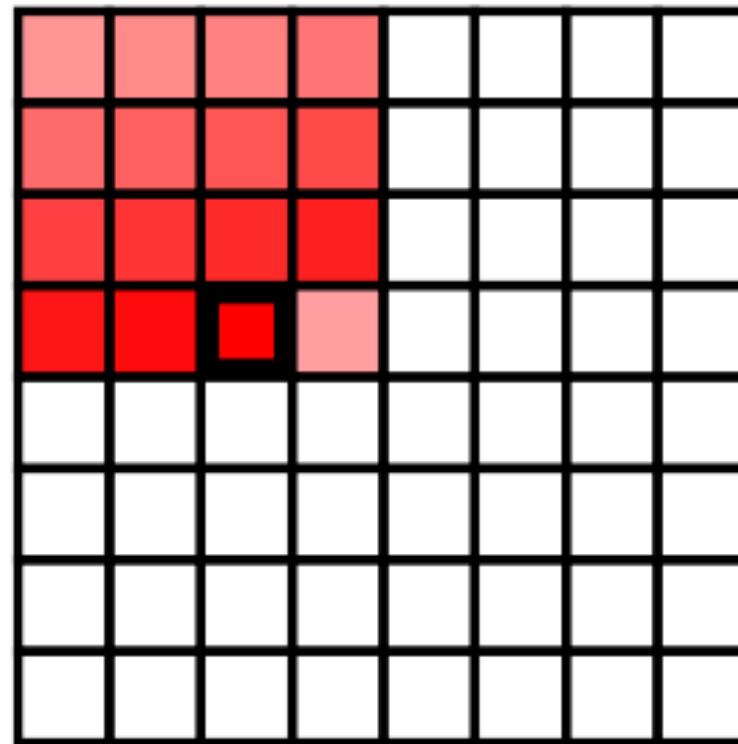
C mem:94 cache hit:70



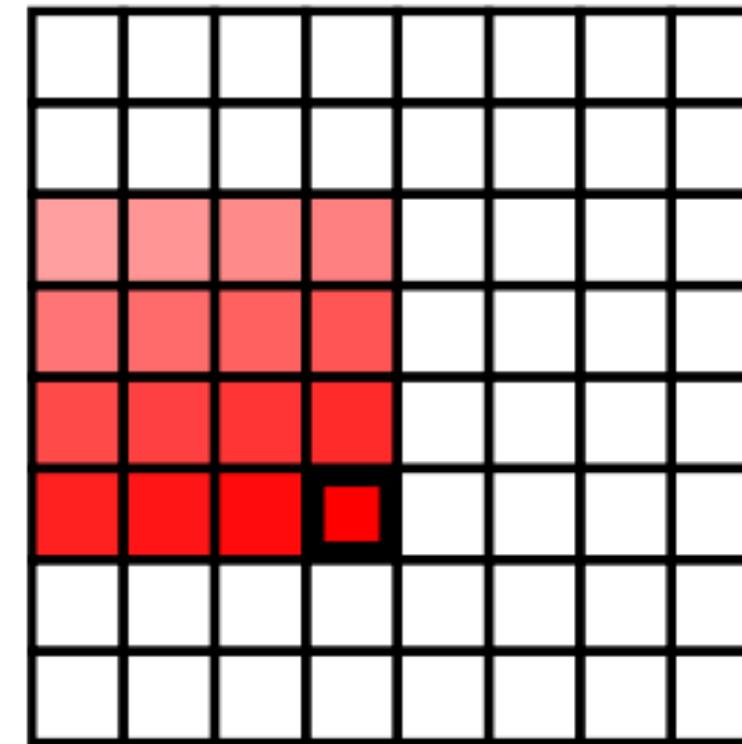
A mem:95 cache hit:71



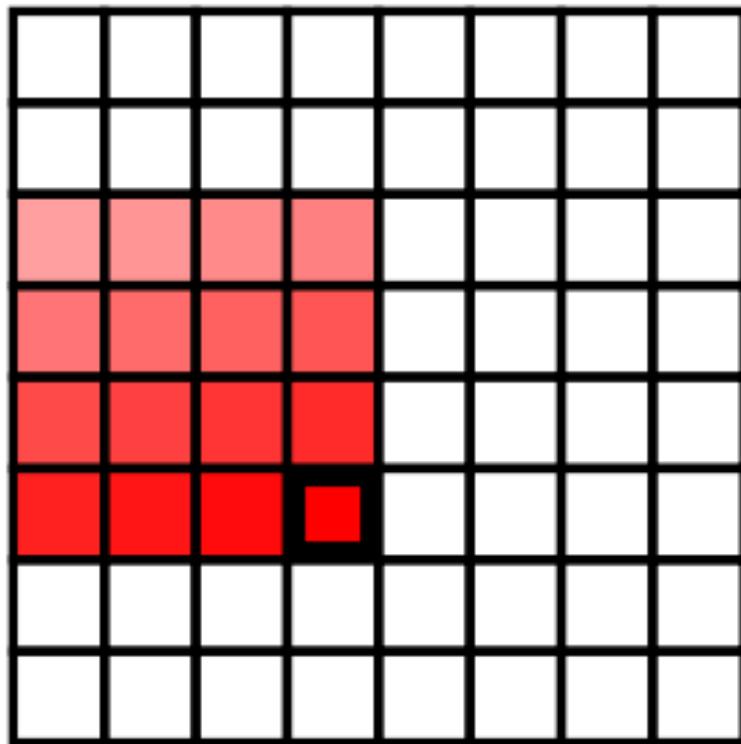
B mem:95 cache hit:79



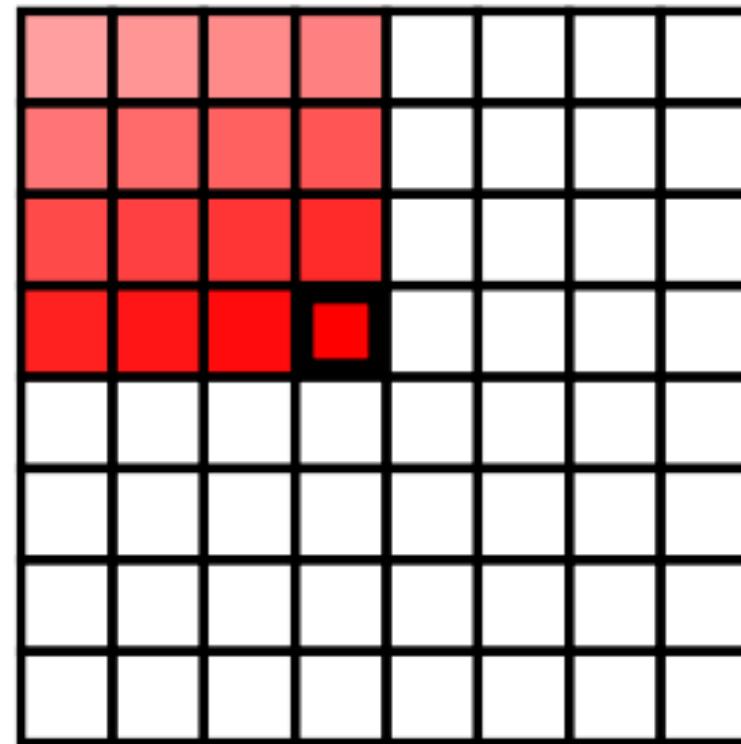
C mem:95 cache hit:71



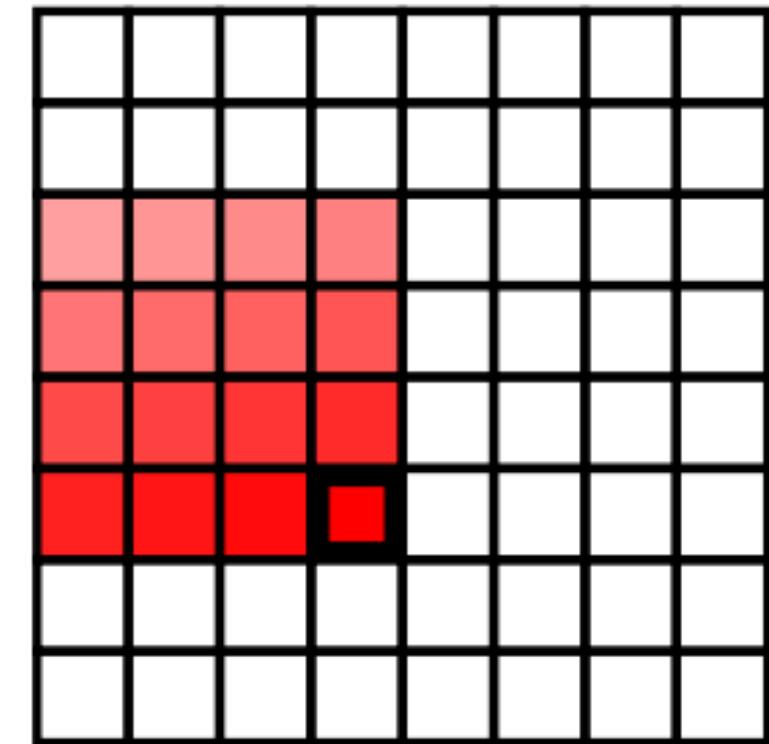
A mem:96 cache hit:72



B mem:96 cache hit:80



C mem:96 cache hit:72



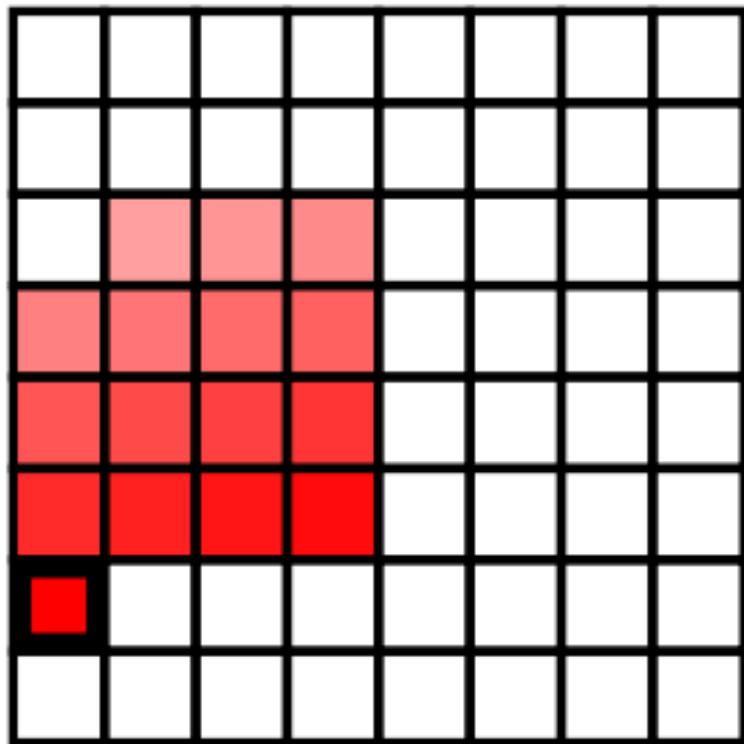
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mem:288

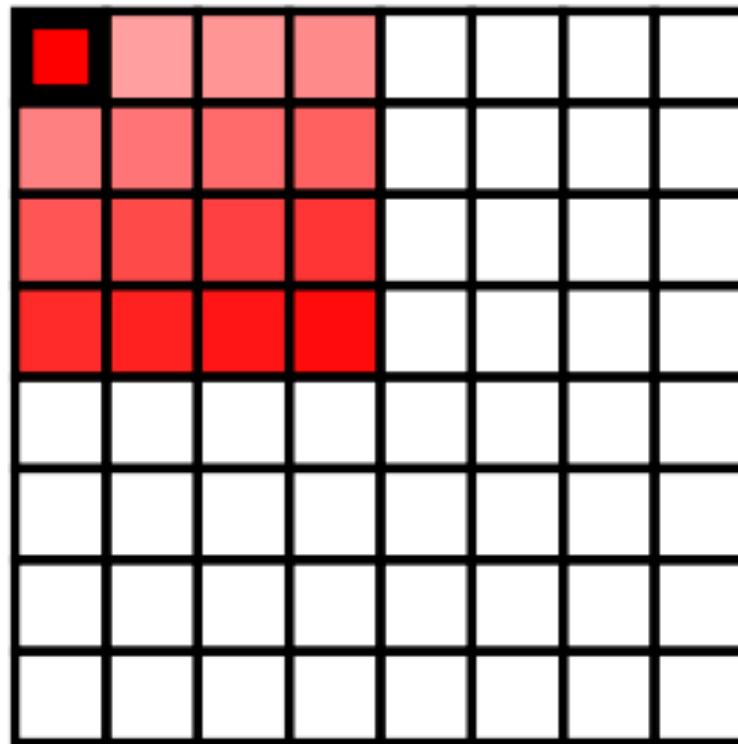
cache hits:224 ≈77%

A mem:97 cache hit:72



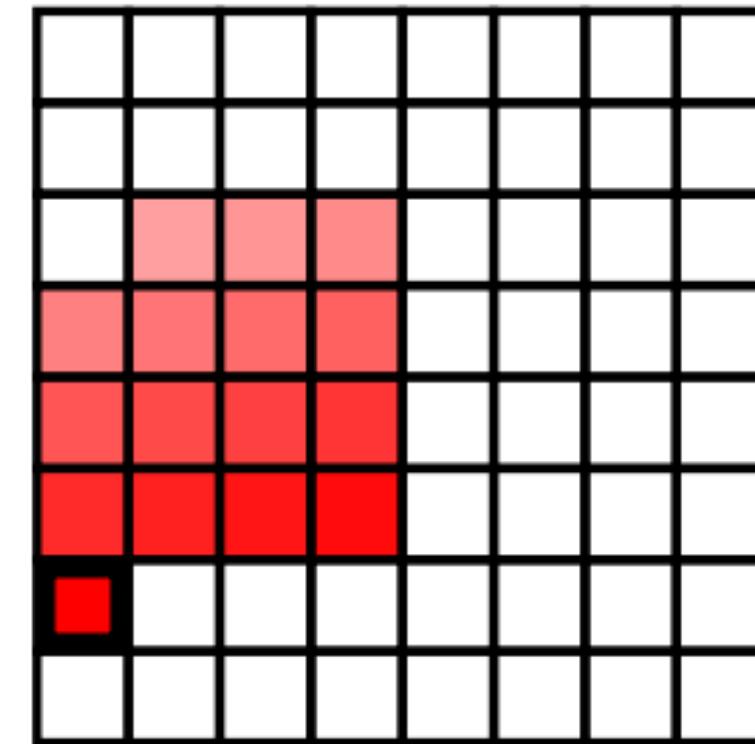
mem:291

B mem:97 cache hit:81

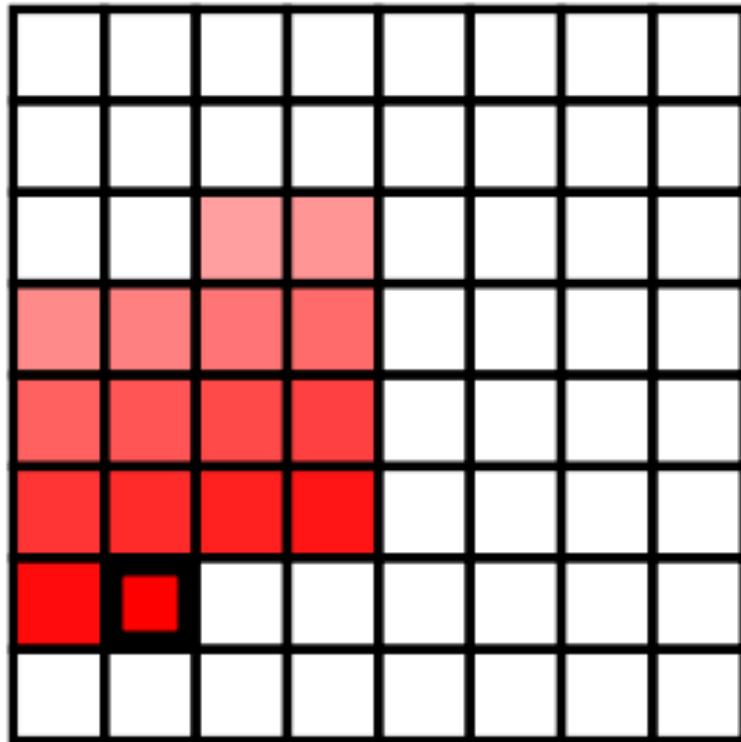


cache hits:225 ≈77%

C mem:97 cache hit:72

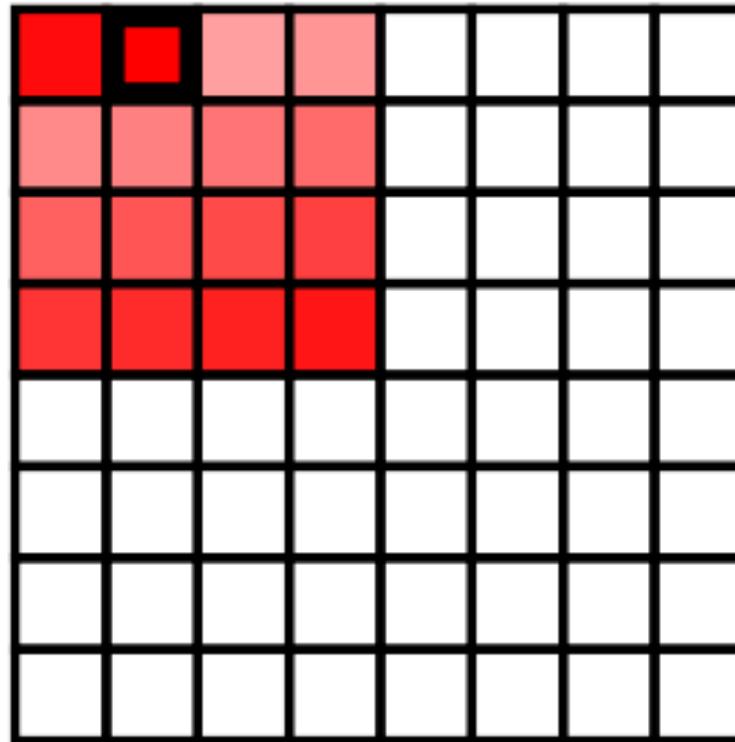


A mem:98 cache hit:72



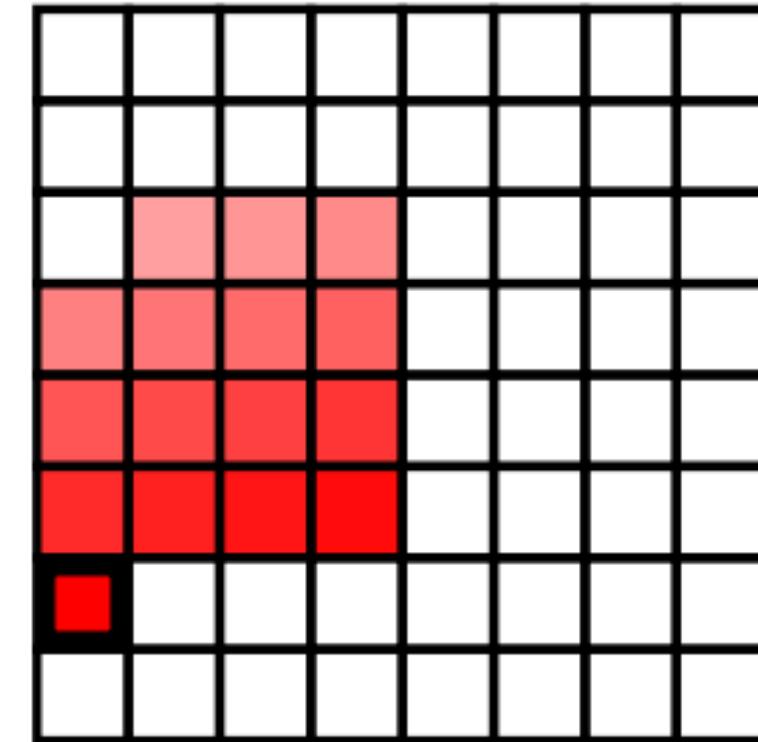
mem:294

B mem:98 cache hit:82

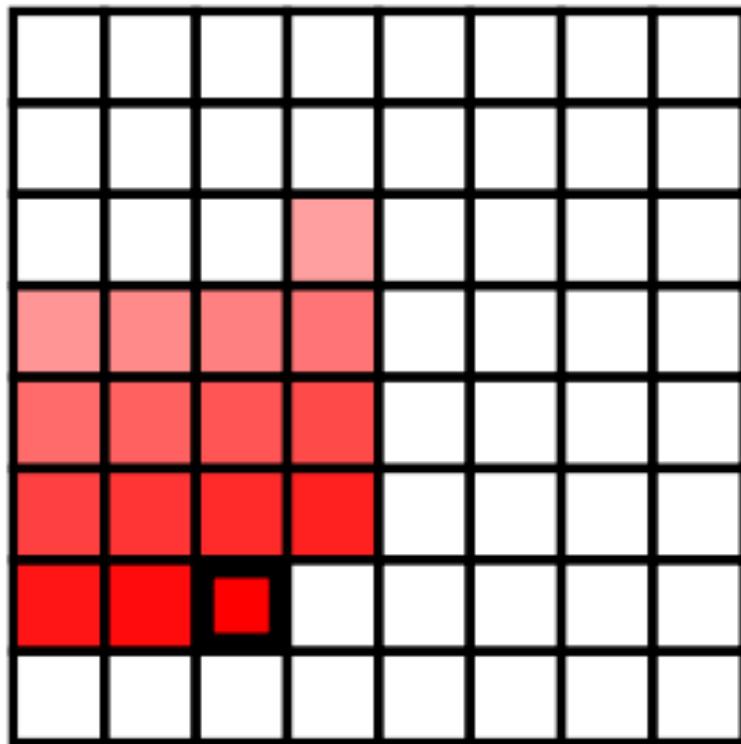


cache hits:227 ≈77%

C mem:98 cache hit:73

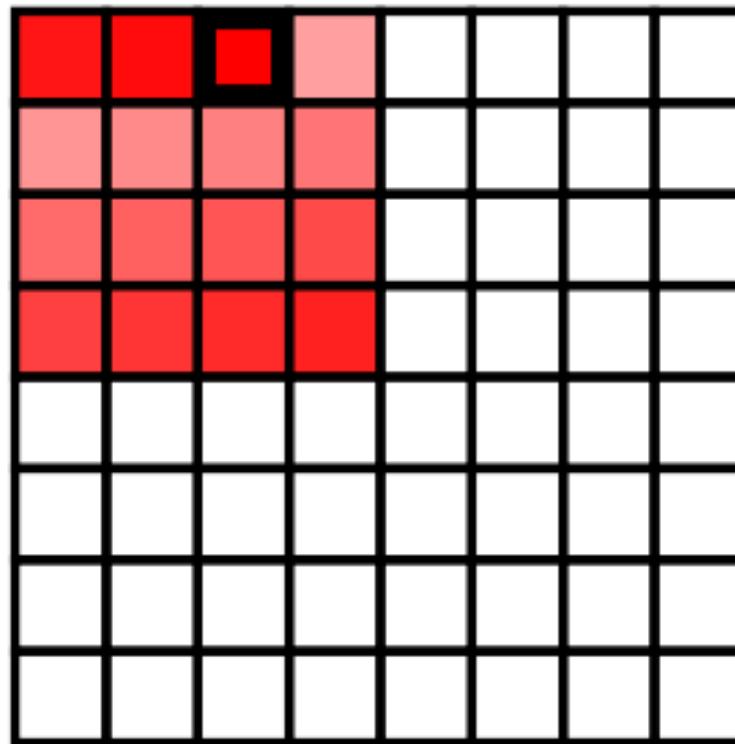


A mem:99 cache hit:72



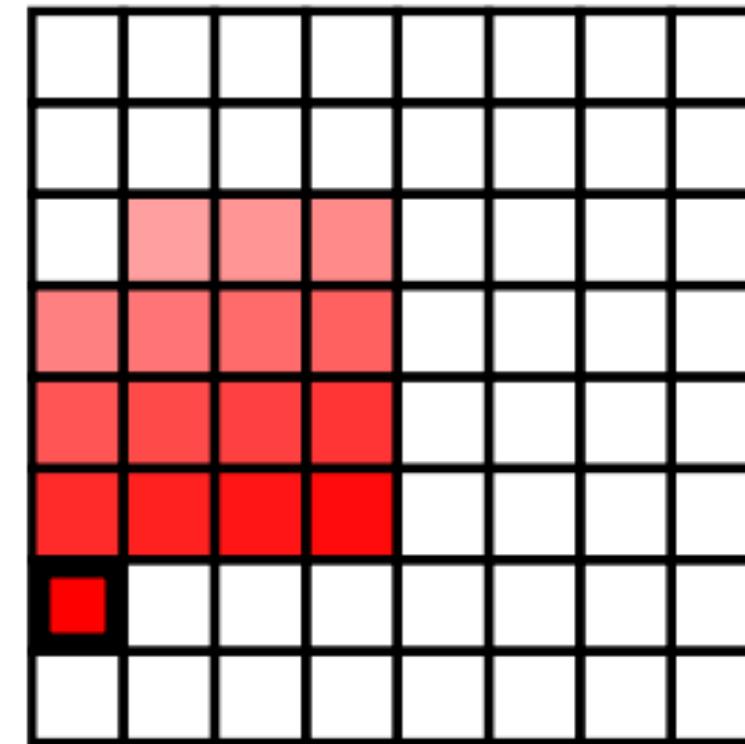
mem:297

B mem:99 cache hit:83

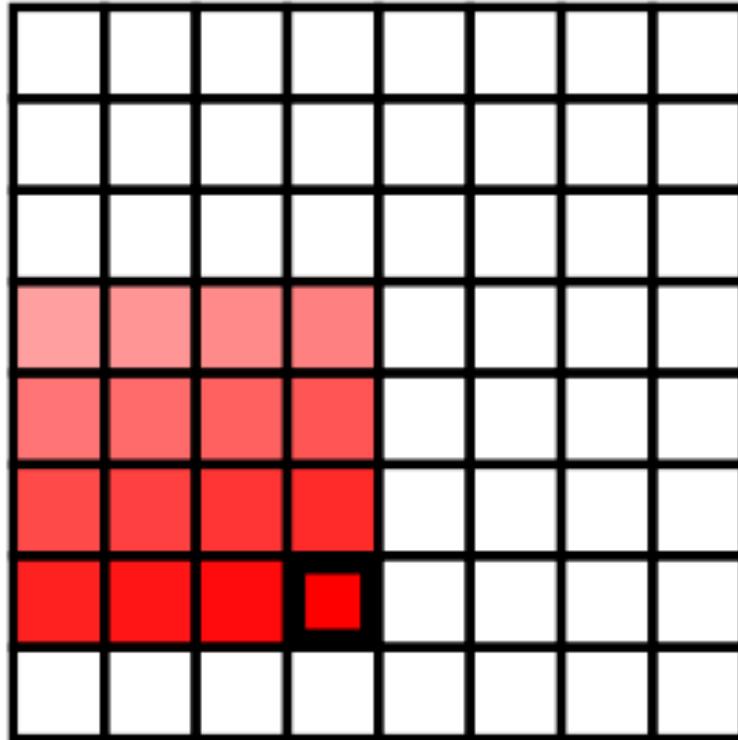


cache hits:229 ≈77%

C mem:99 cache hit:74

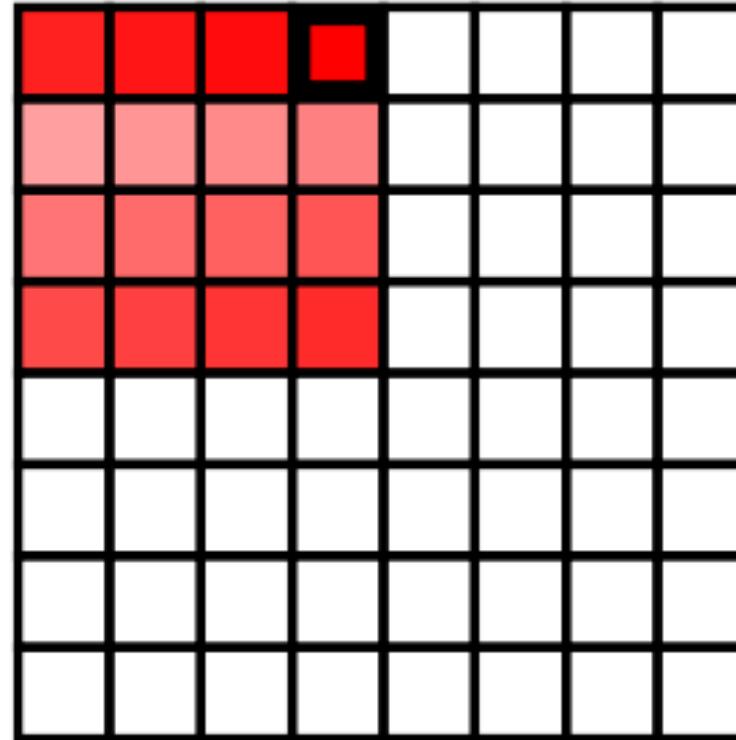


A mem:100 cache hit:72



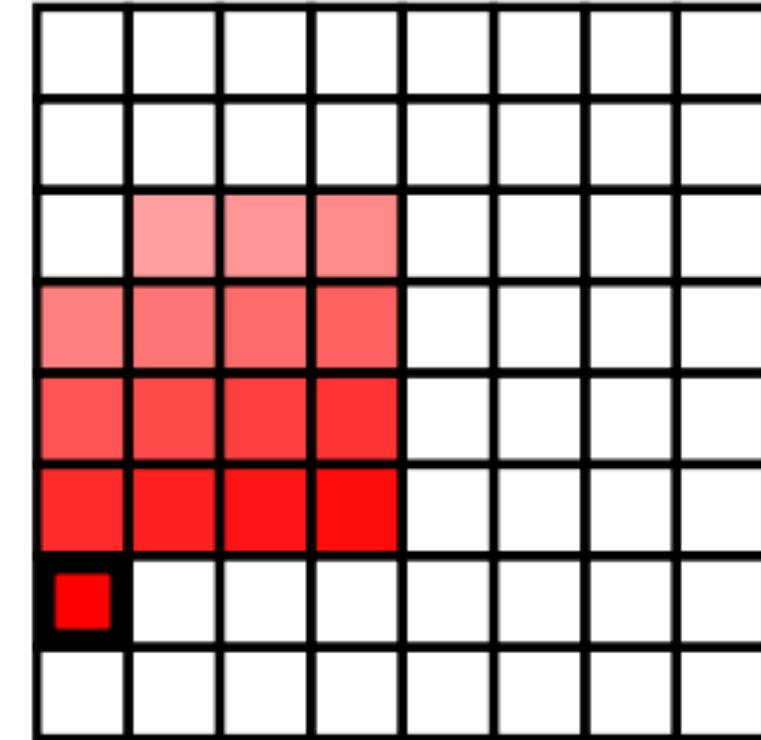
mem:300

B mem:100 cache hit:84

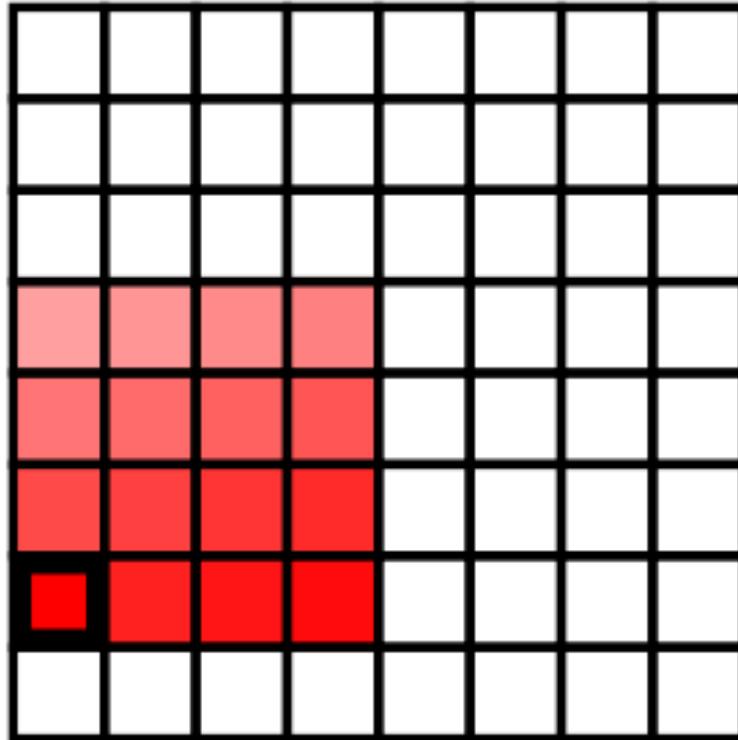


cache hits:231 ≈77%

C mem:100 cache hit:75

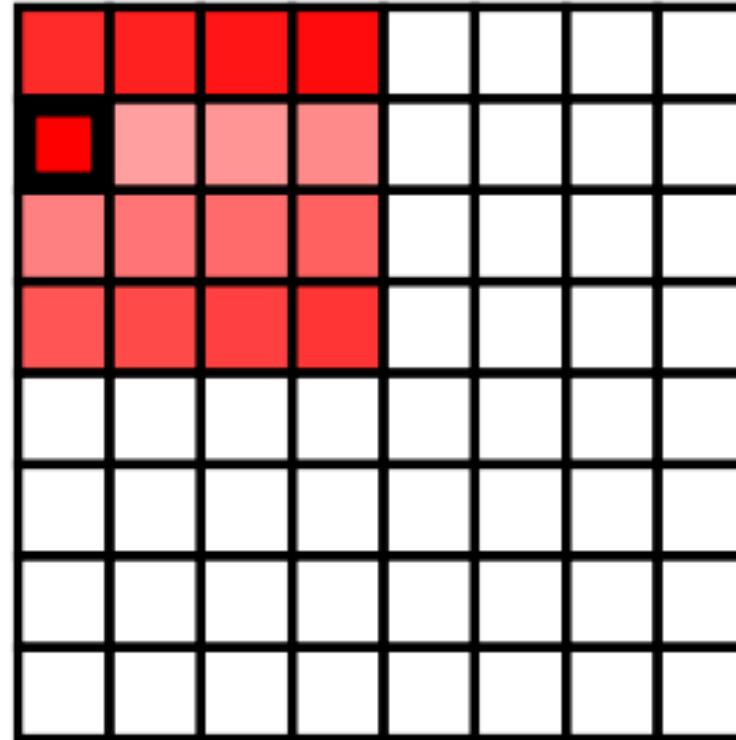


A mem:101 cache hit:73



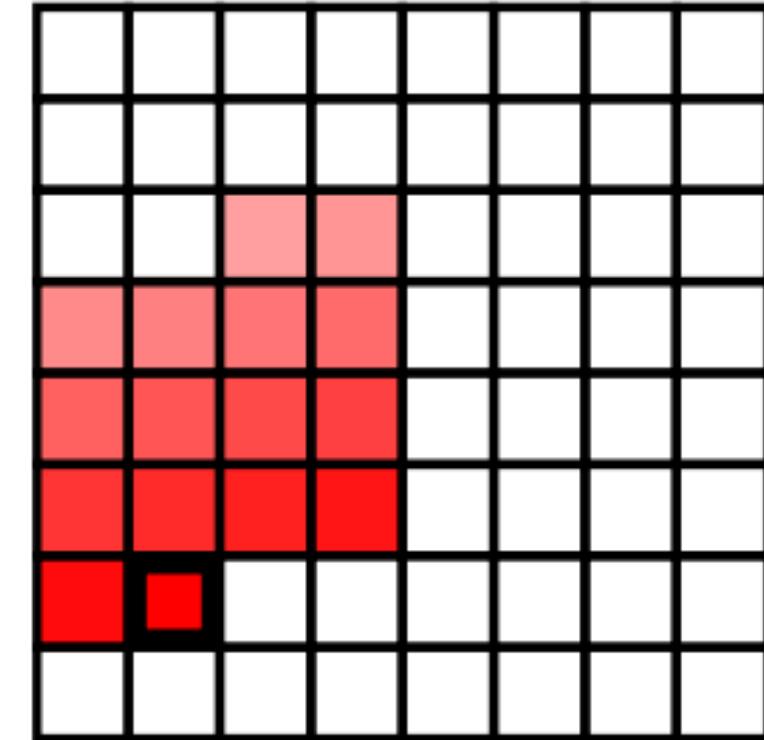
mem:303

B mem:101 cache hit:85

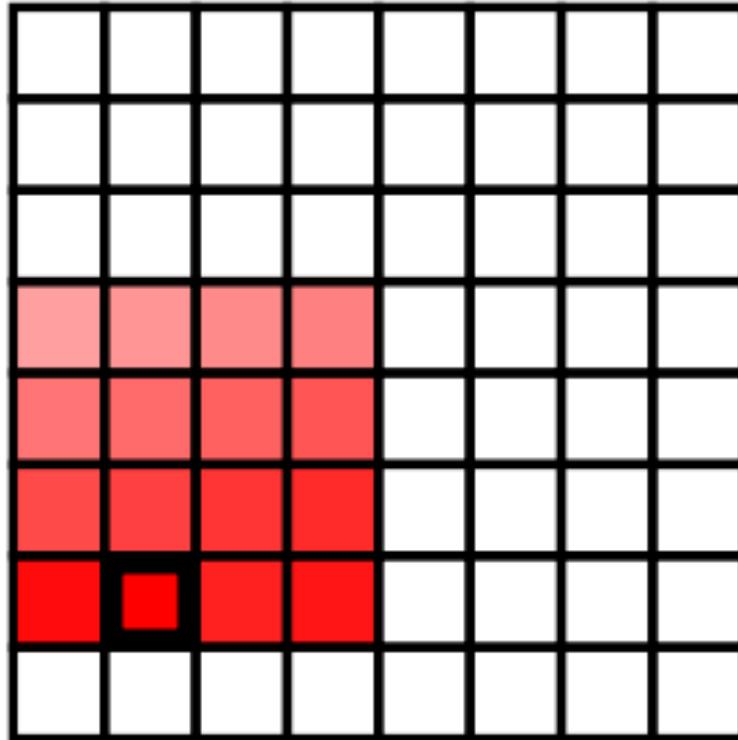


cache hits:233 ≈76%

C mem:101 cache hit:75

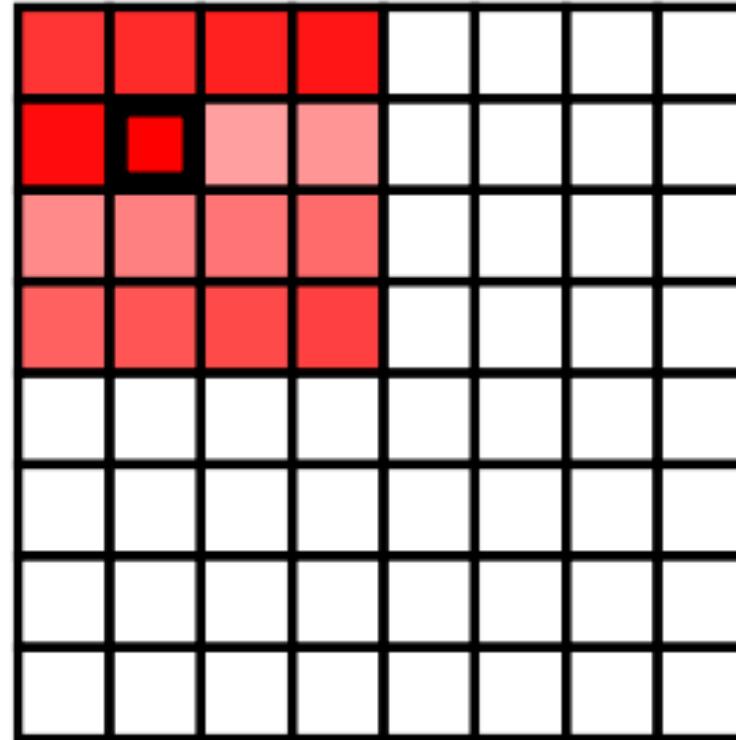


A mem:102 cache hit:74



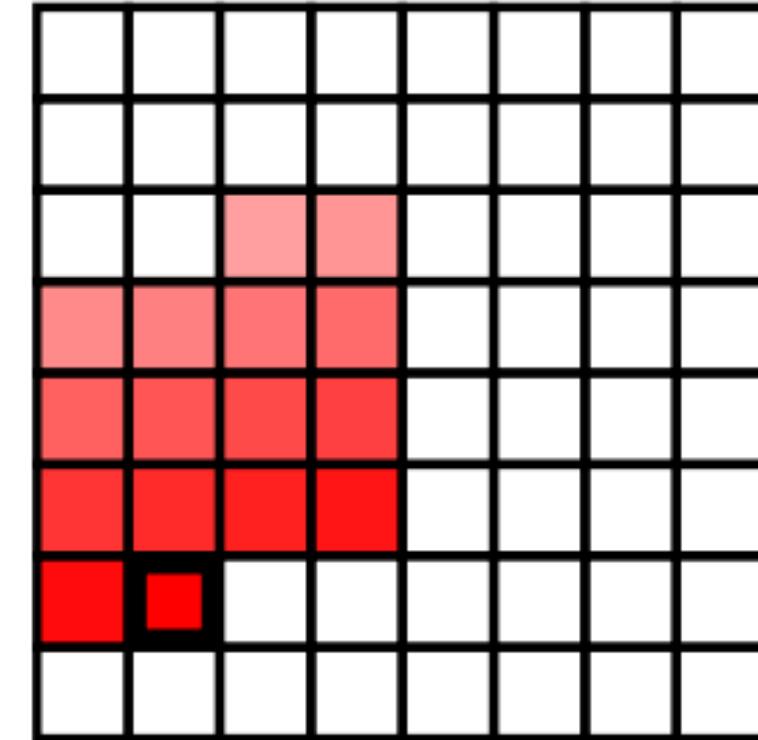
mem:306

B mem:102 cache hit:86

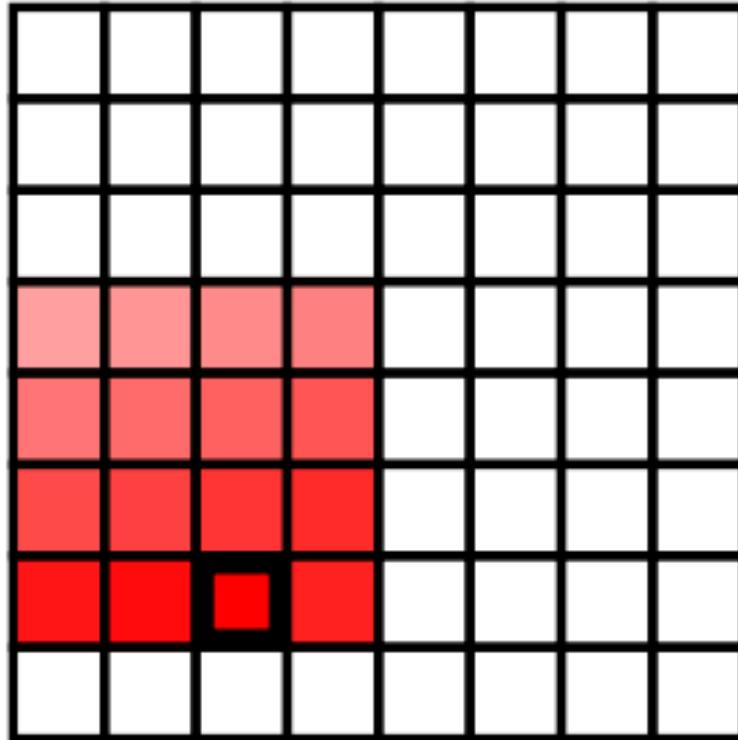


cache hits:236 ≈77%

C mem:102 cache hit:76

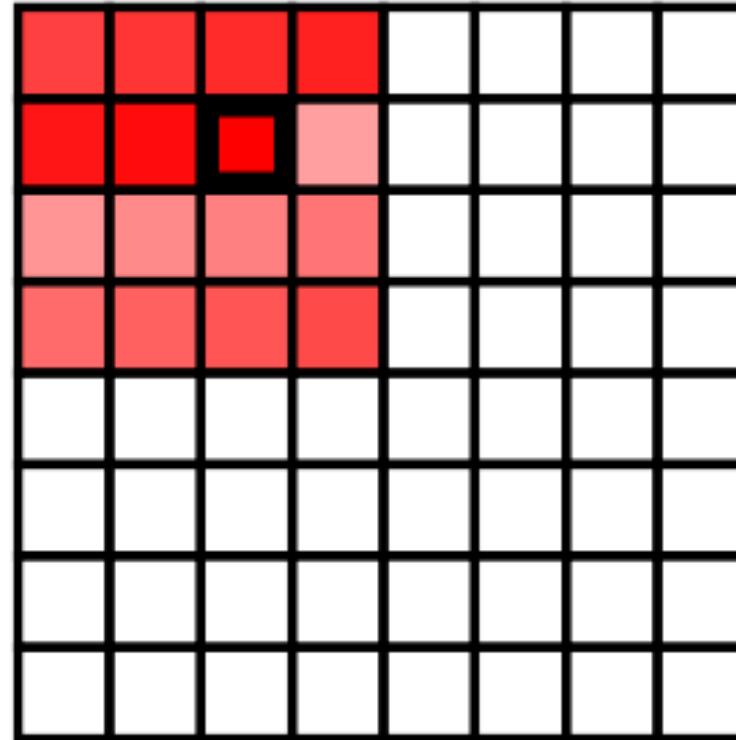


A mem:103 cache hit:75



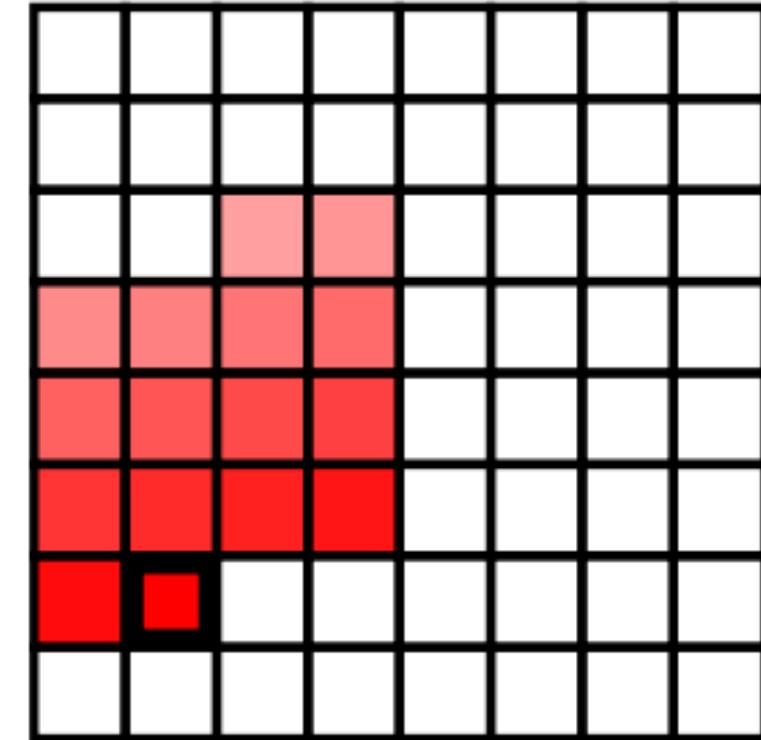
mem:309

B mem:103 cache hit:87

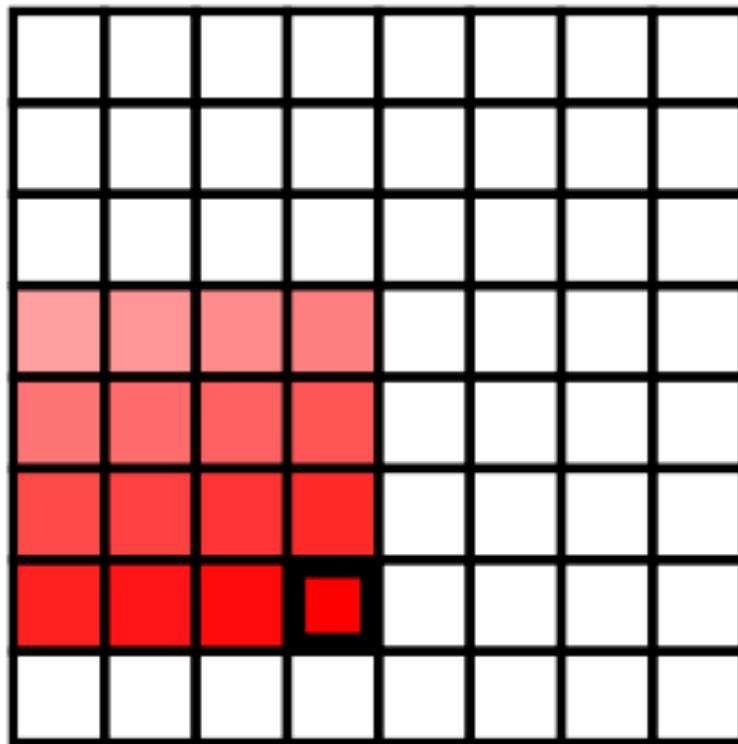


cache hits:239 ≈77%

C mem:103 cache hit:77

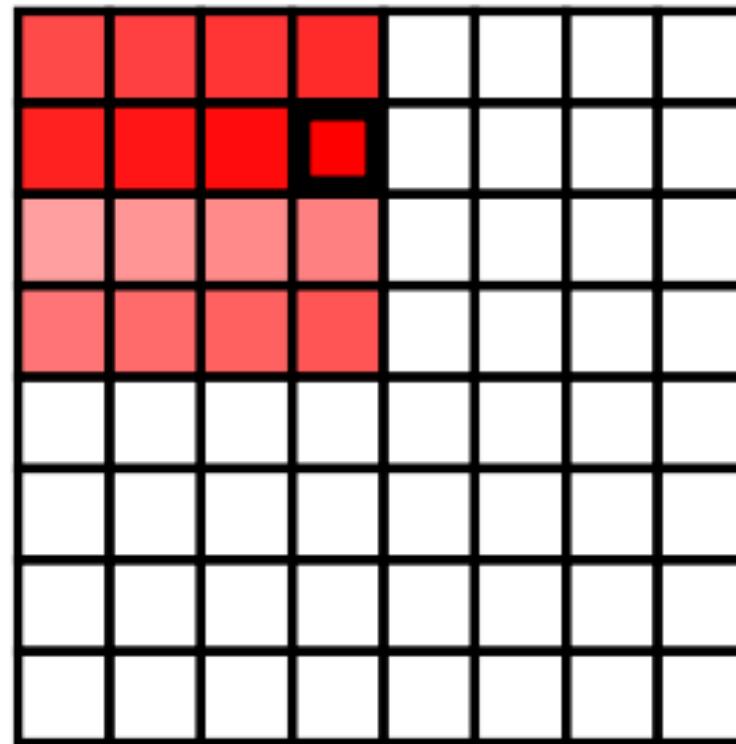


A mem:104 cache hit:76



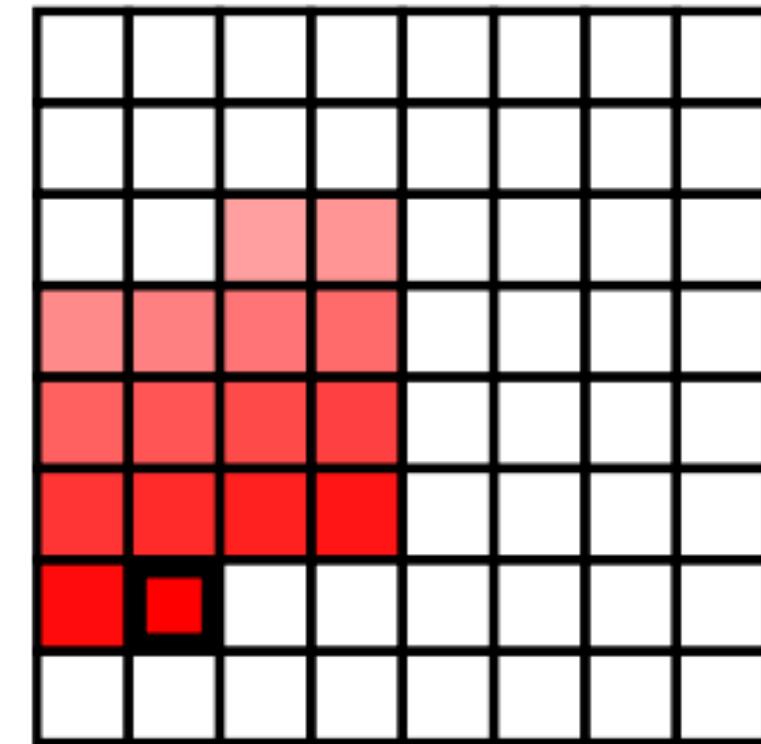
mem:312

B mem:104 cache hit:88

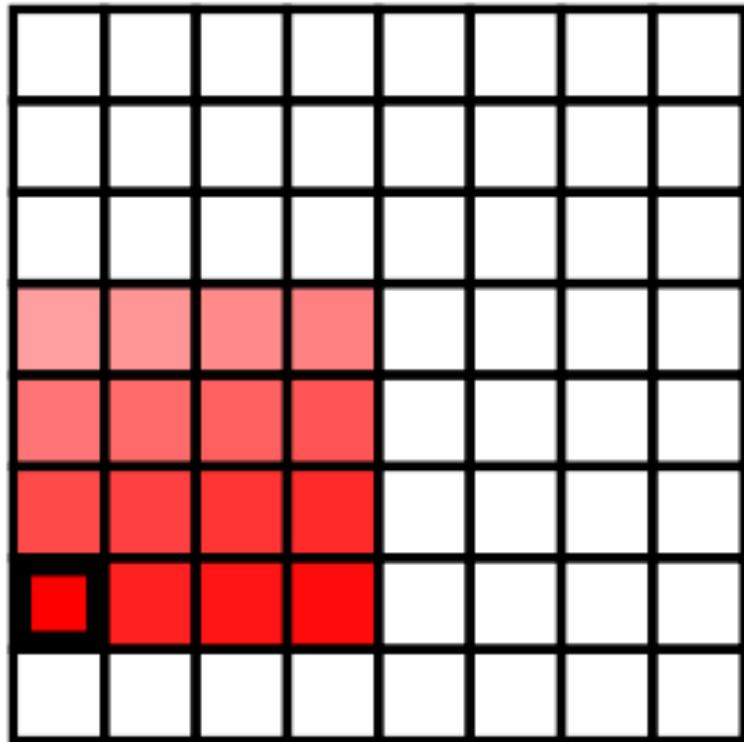


cache hits:242 ≈77%

C mem:104 cache hit:78

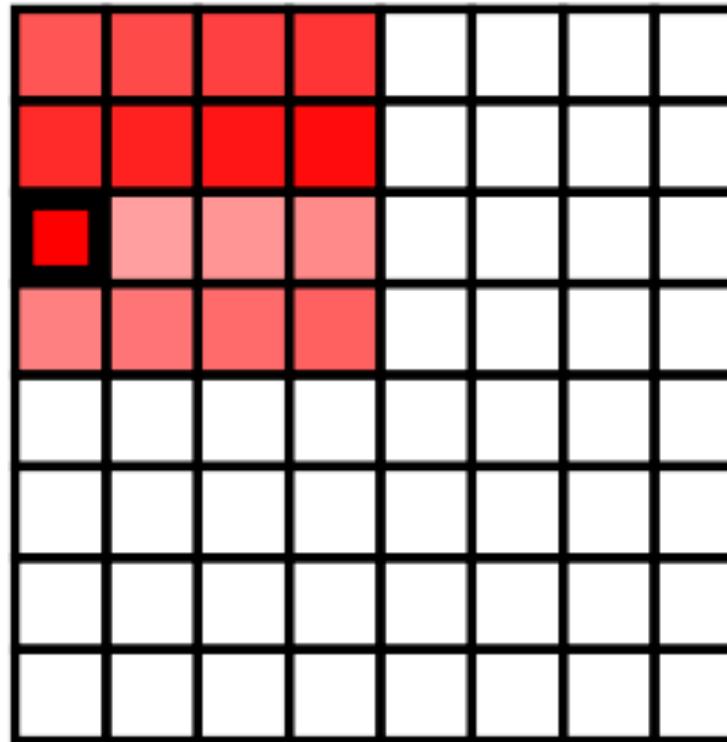


A mem:105 cache hit:77



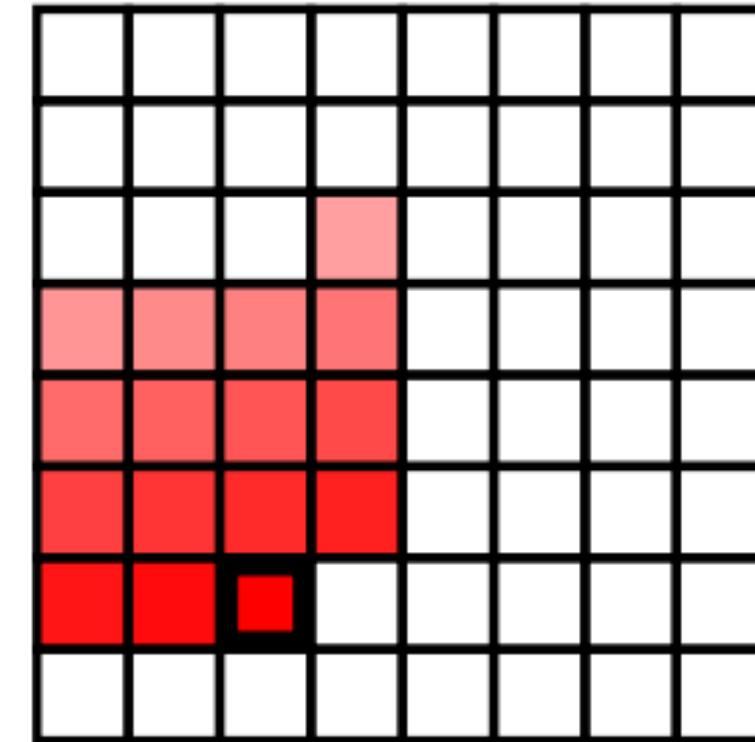
mem:315

B mem:105 cache hit:89

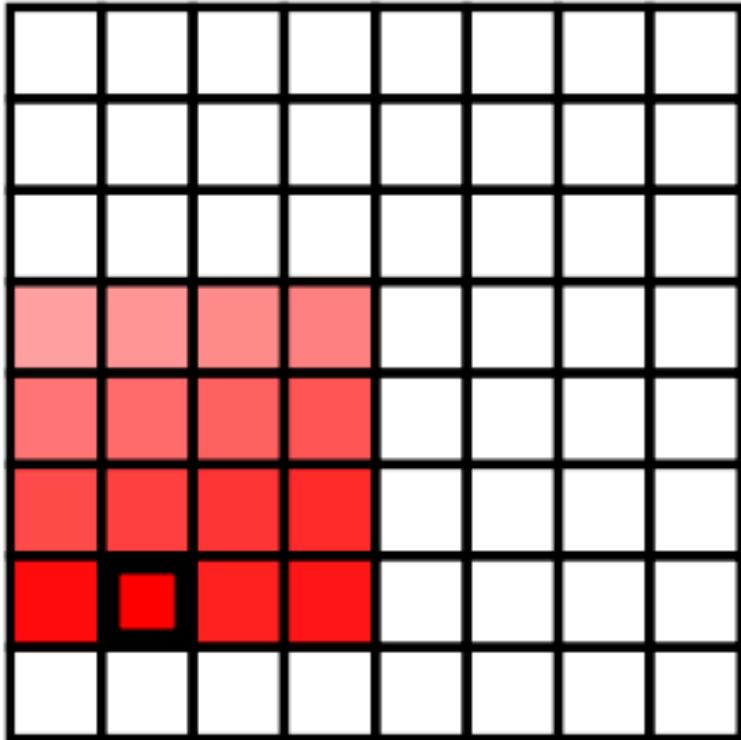


cache hits:244 ≈77%

C mem:105 cache hit:78

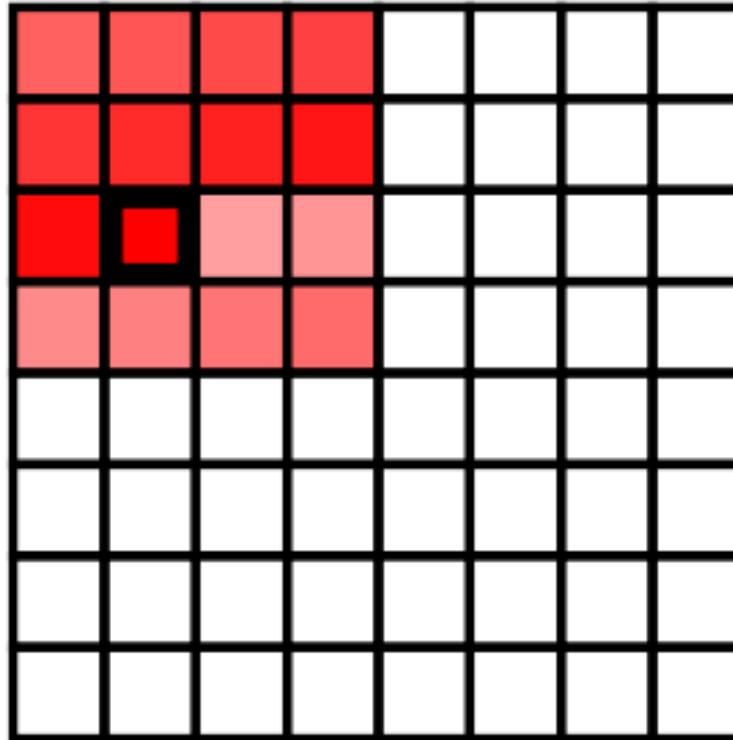


A mem:106 cache hit:78



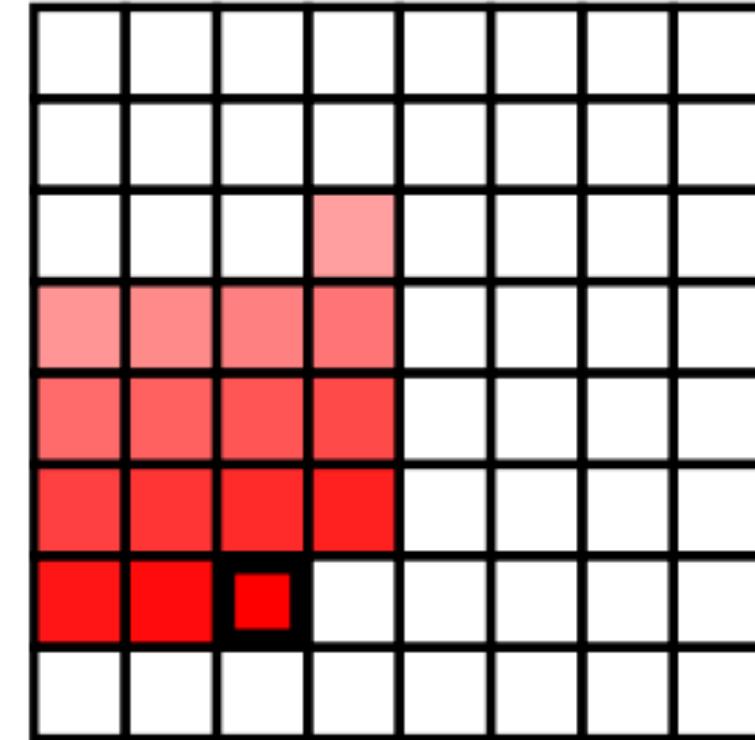
mem:318

B mem:106 cache hit:90

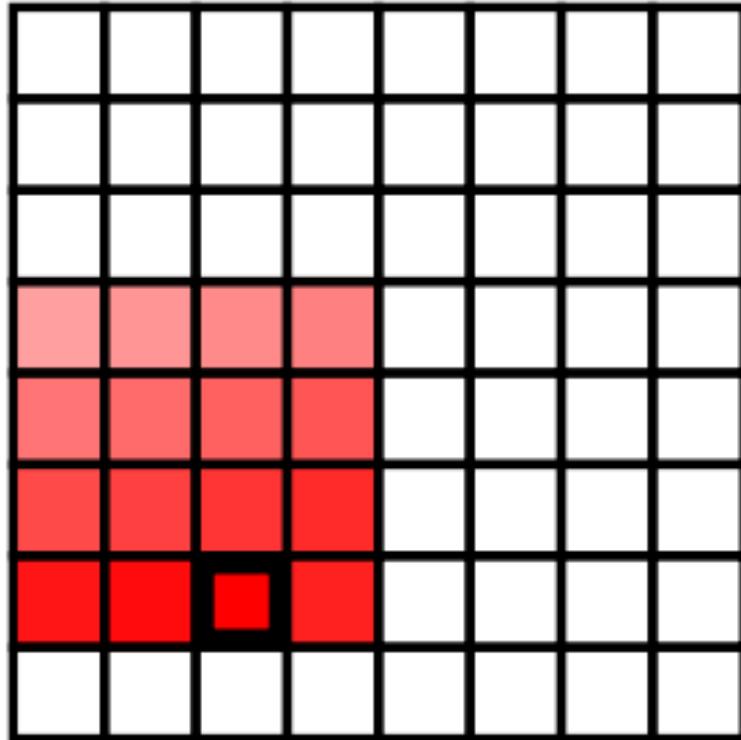


cache hits:247 ≈77%

C mem:106 cache hit:79

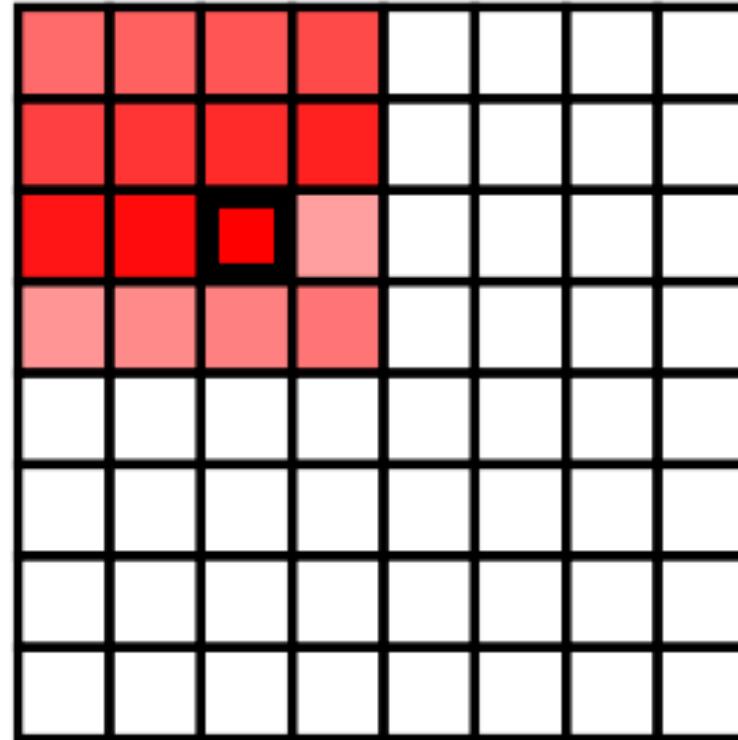


A mem:107 cache hit:79



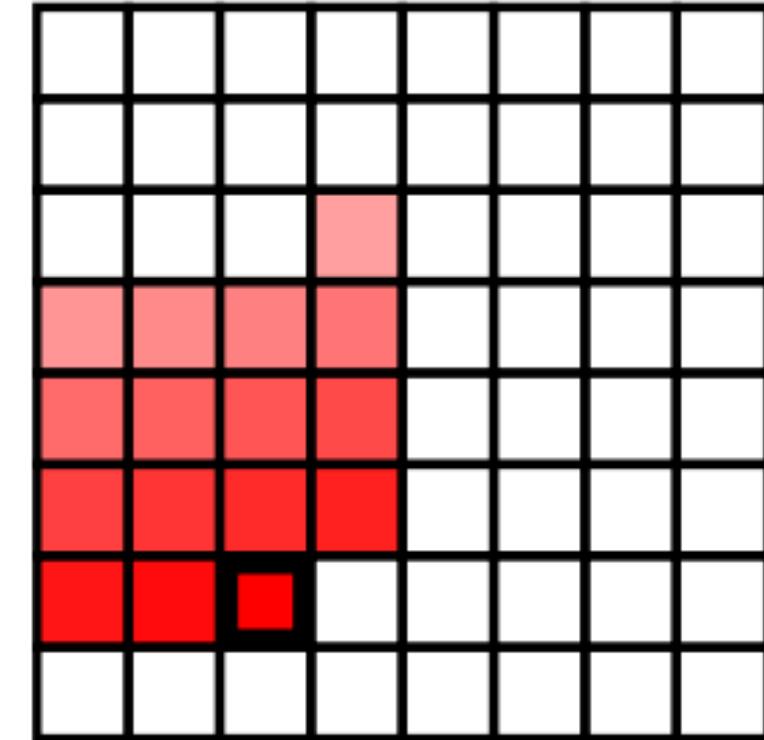
mem:321

B mem:107 cache hit:91

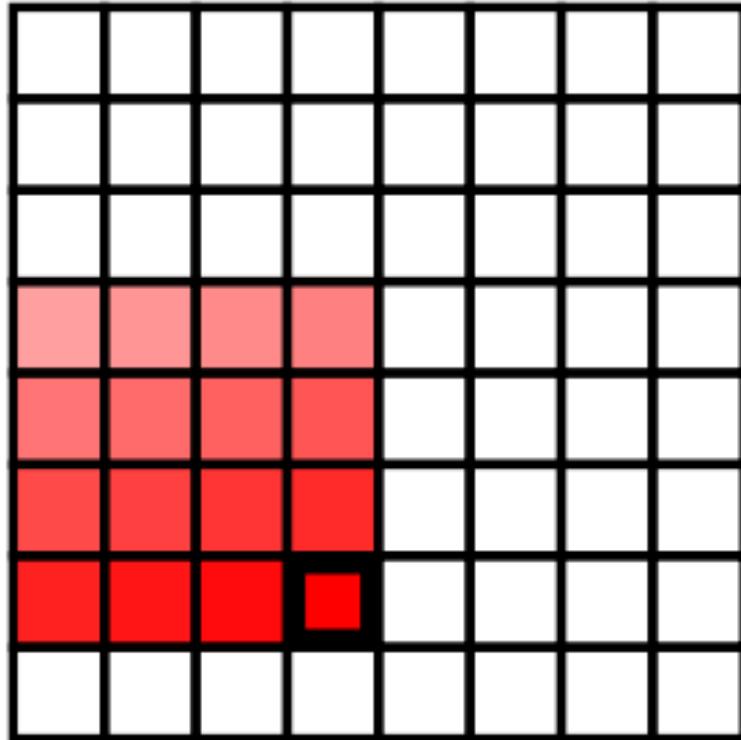


cache hits:250 ≈77%

C mem:107 cache hit:80

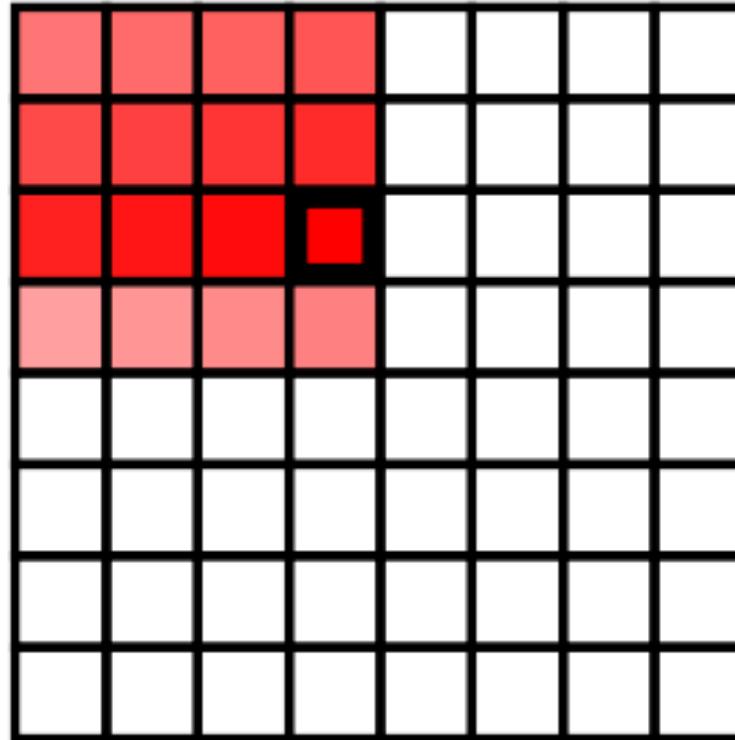


A mem:108 cache hit:80



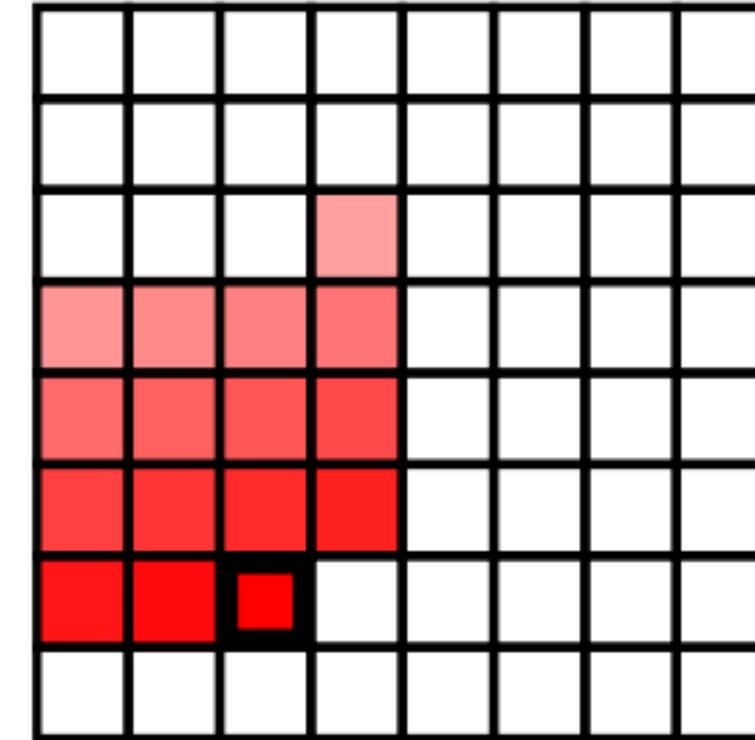
mem:324

B mem:108 cache hit:92

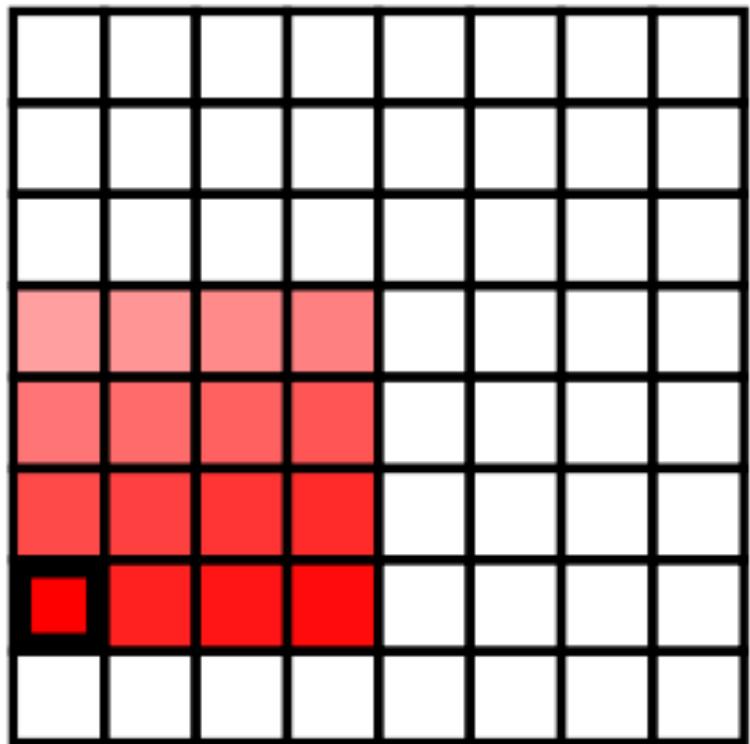


cache hits:253 ≈78%

C mem:108 cache hit:81

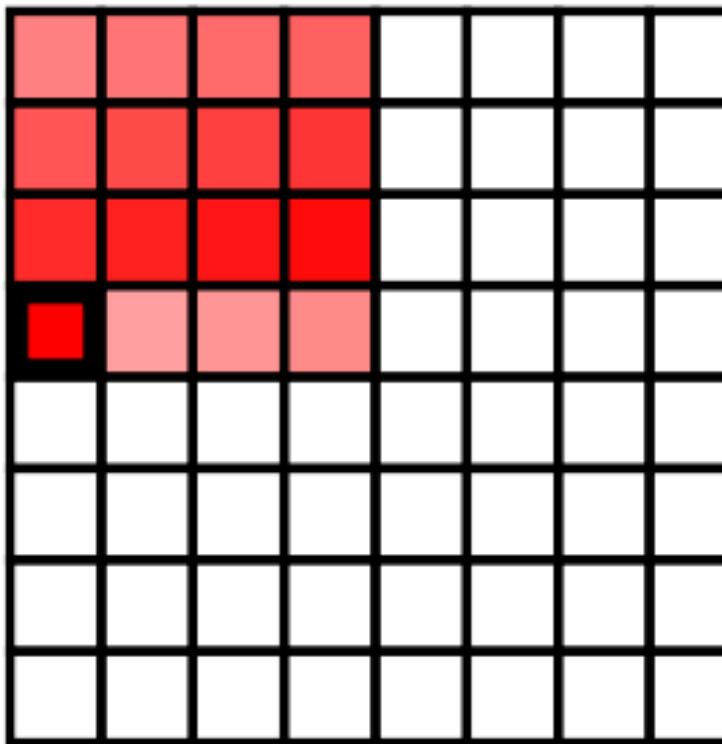


A mem:109 cache hit:81



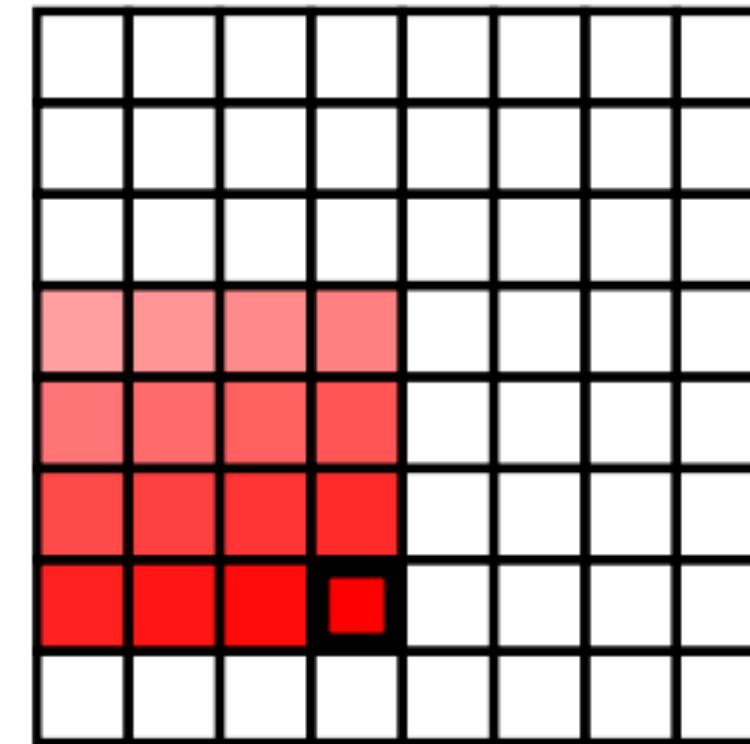
mem:327

B mem:109 cache hit:93

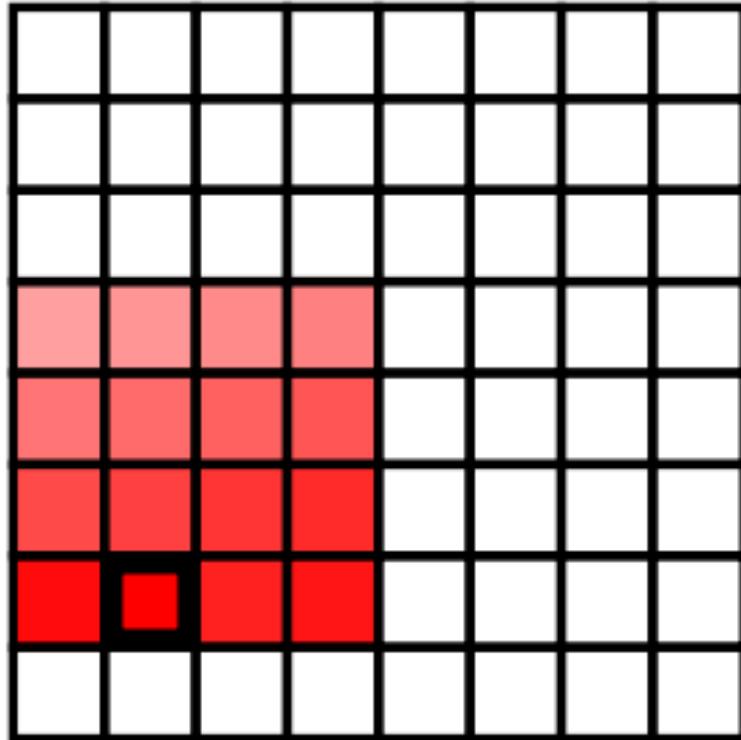


cache hits:255 ≈77%

C mem:109 cache hit:81

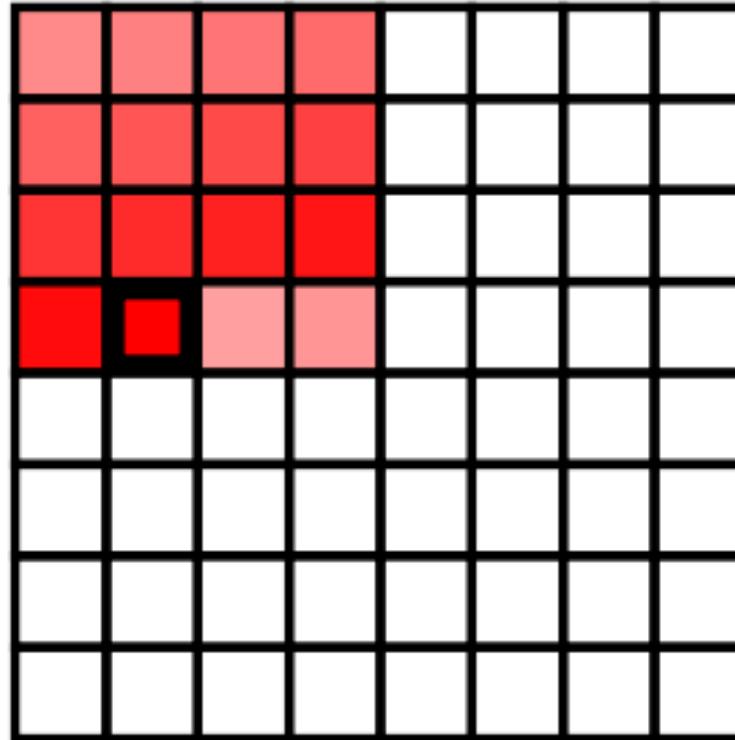


A mem:110 cache hit:82



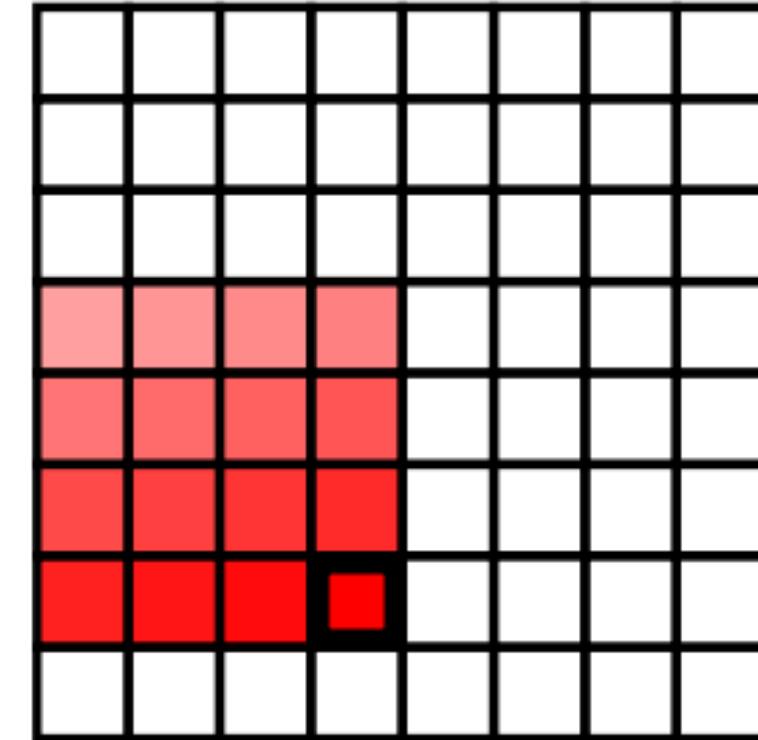
mem:330

B mem:110 cache hit:94

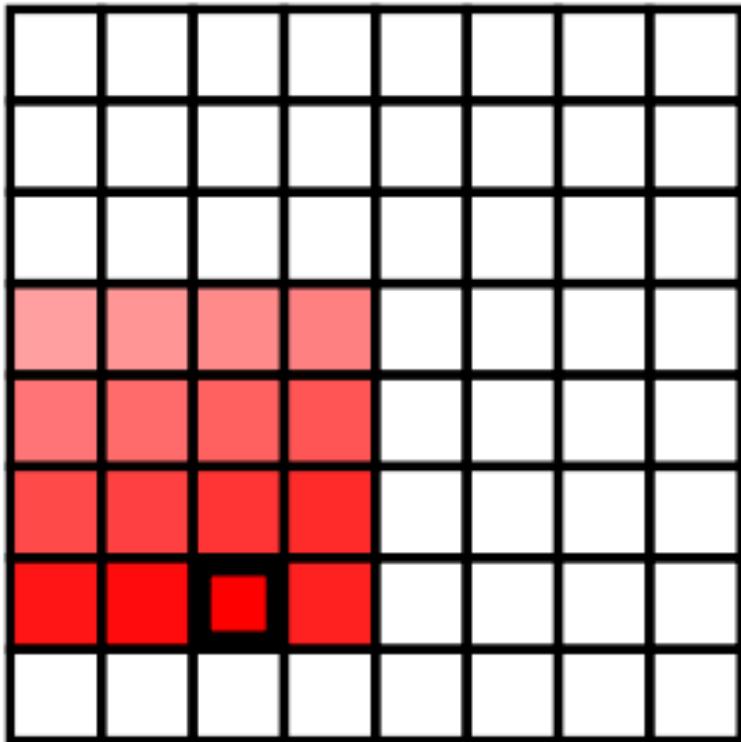


cache hits:258 $\approx 78\%$

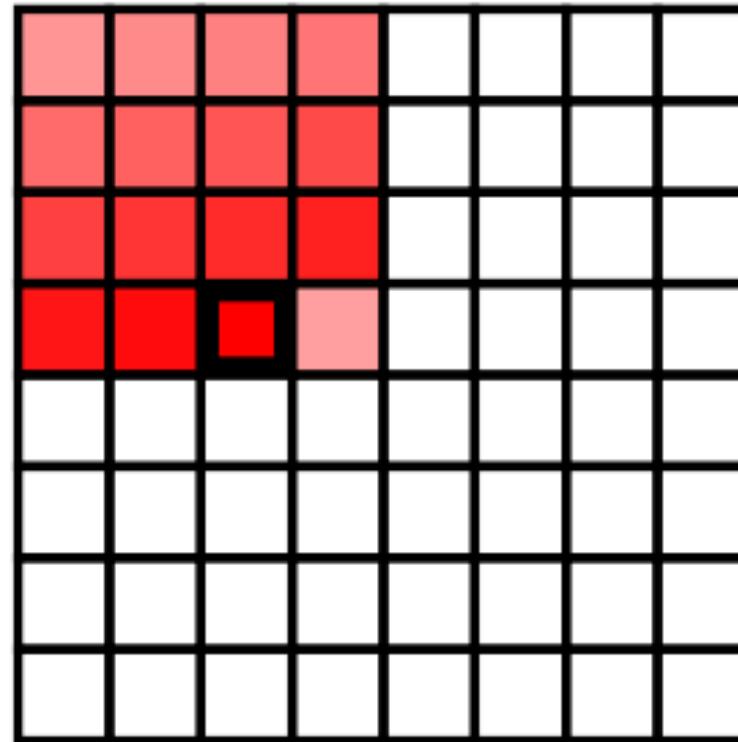
C mem:110 cache hit:82



A mem:111 cache hit:83



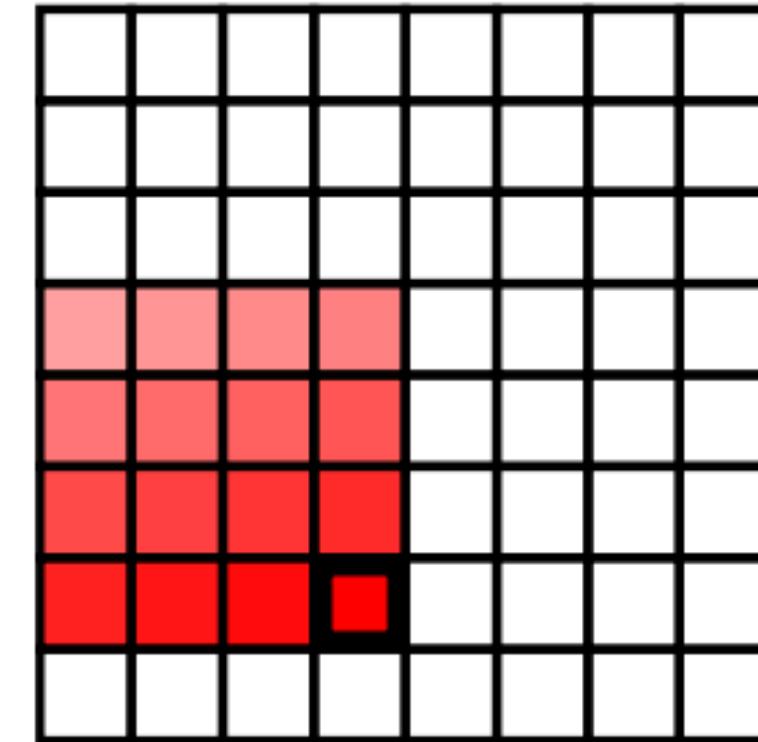
B mem:111 cache hit:95



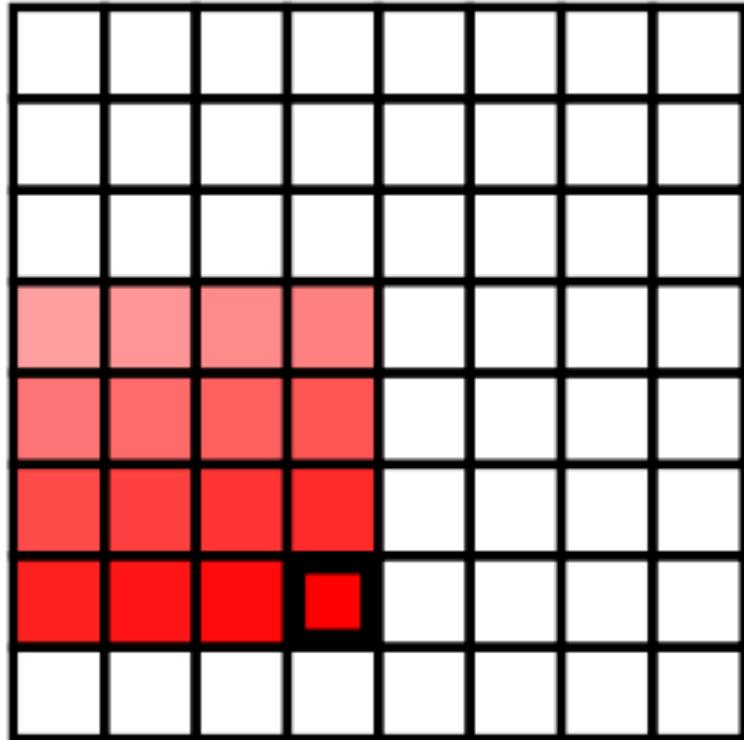
mem:333

cache hits:261 $\approx 78\%$

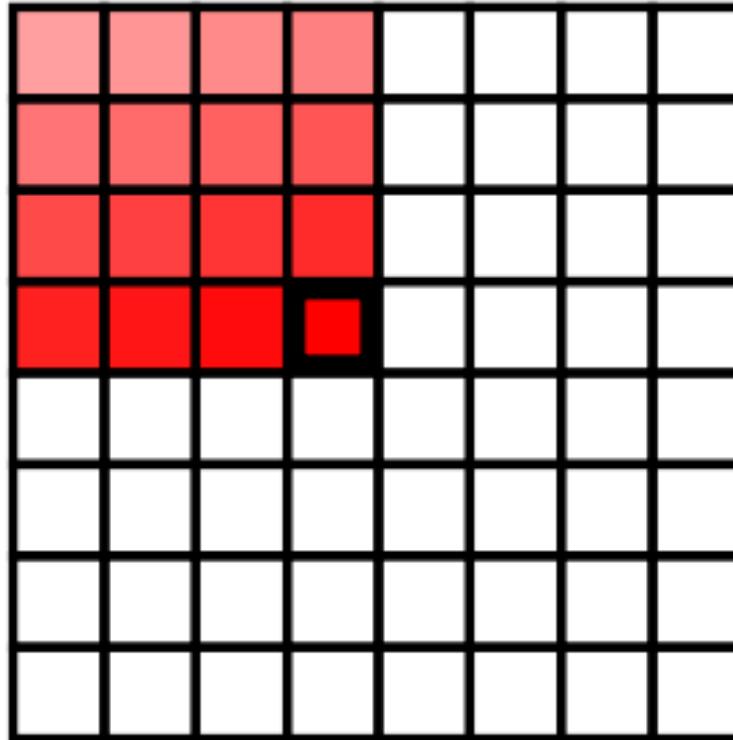
C mem:111 cache hit:83



A mem:112 cache hit:84



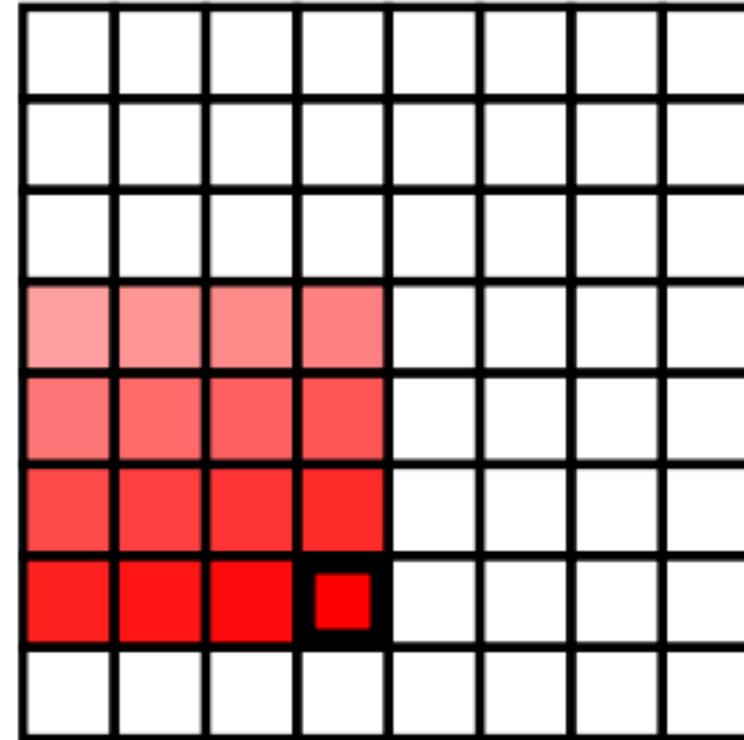
B mem:112 cache hit:96



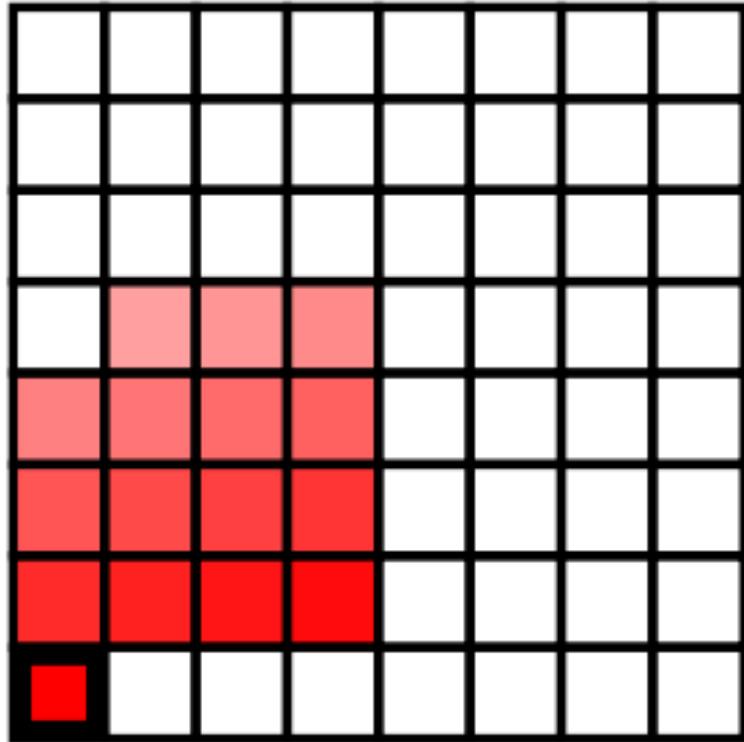
mem:336

cache hits:264 $\approx 78\%$

C mem:112 cache hit:84

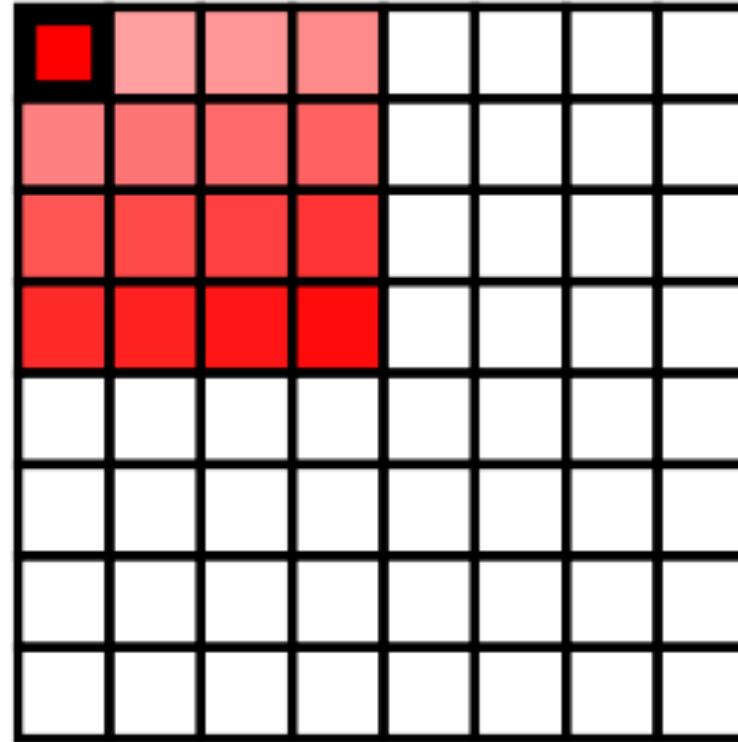


A mem:113 cache hit:84



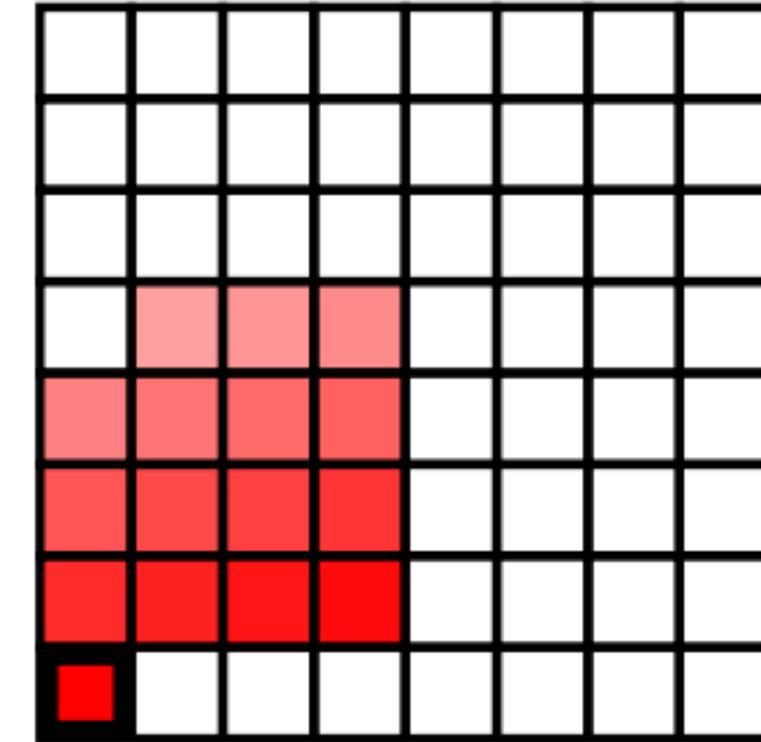
mem:339

B mem:113 cache hit:97

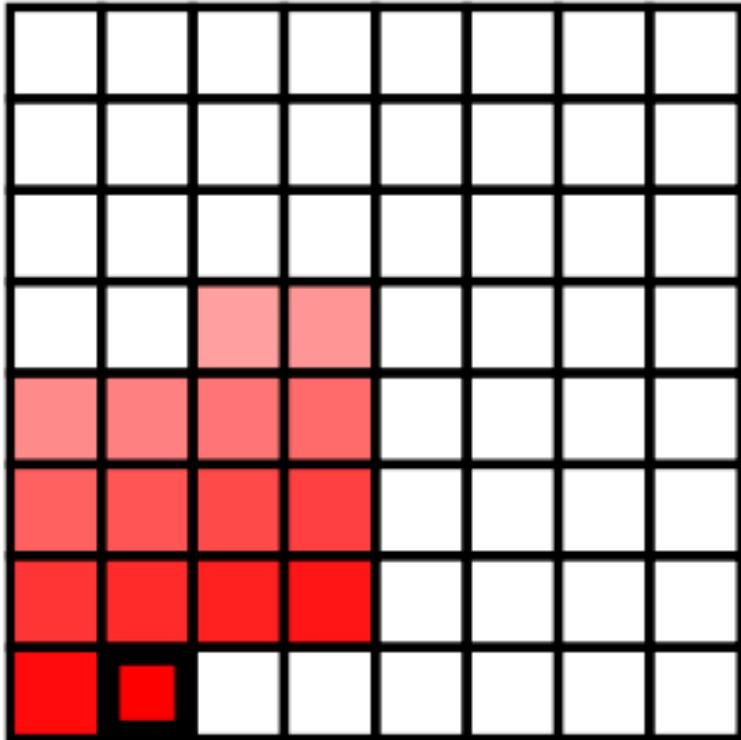


cache hits:265 ≈78%

C mem:113 cache hit:84

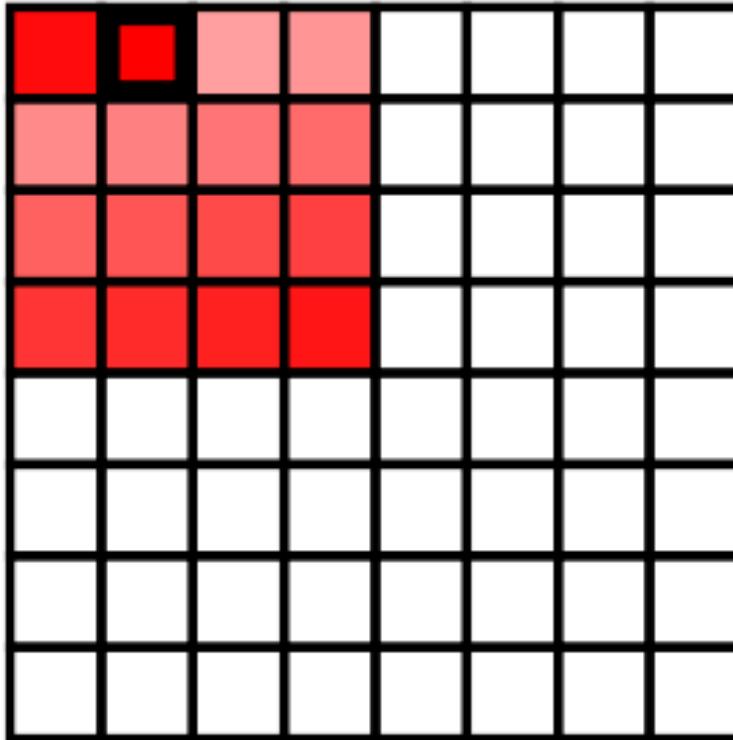


A mem:114 cache hit:84



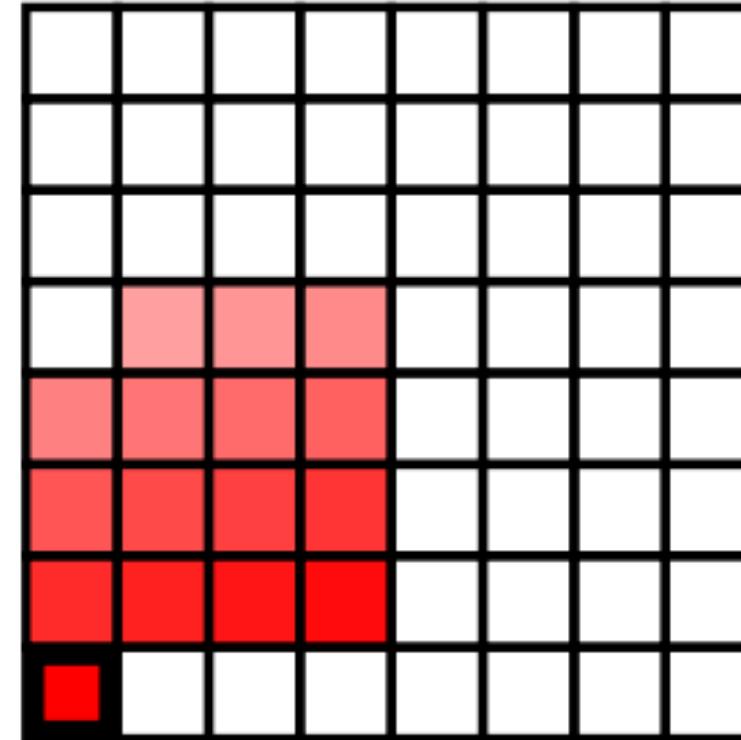
mem:342

B mem:114 cache hit:98

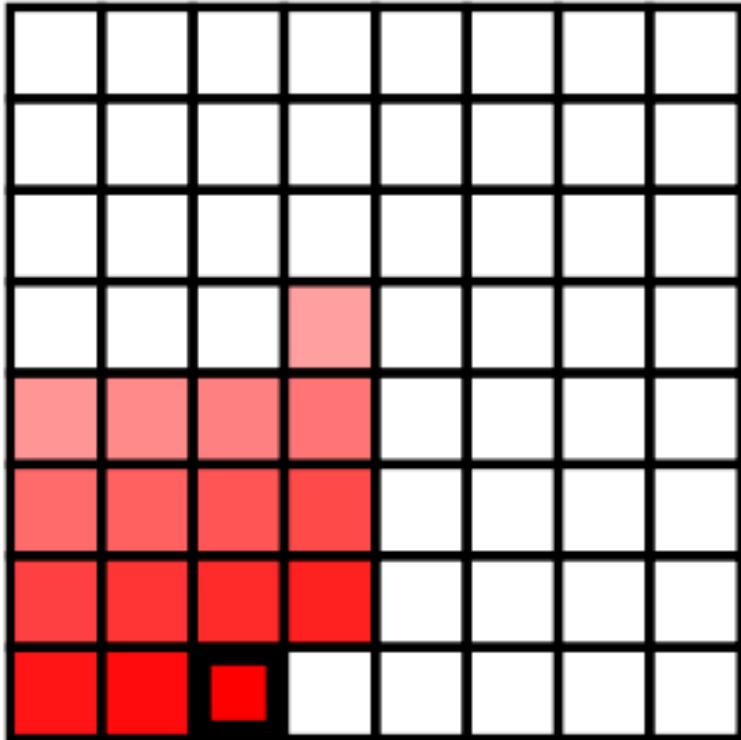


cache hits:267 ≈78%

C mem:114 cache hit:85

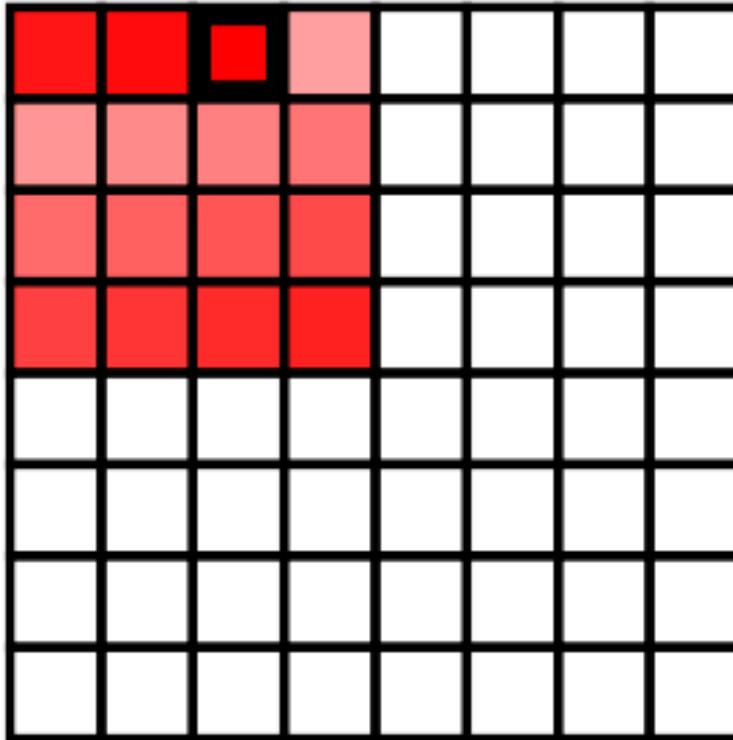


A mem:115 cache hit:84



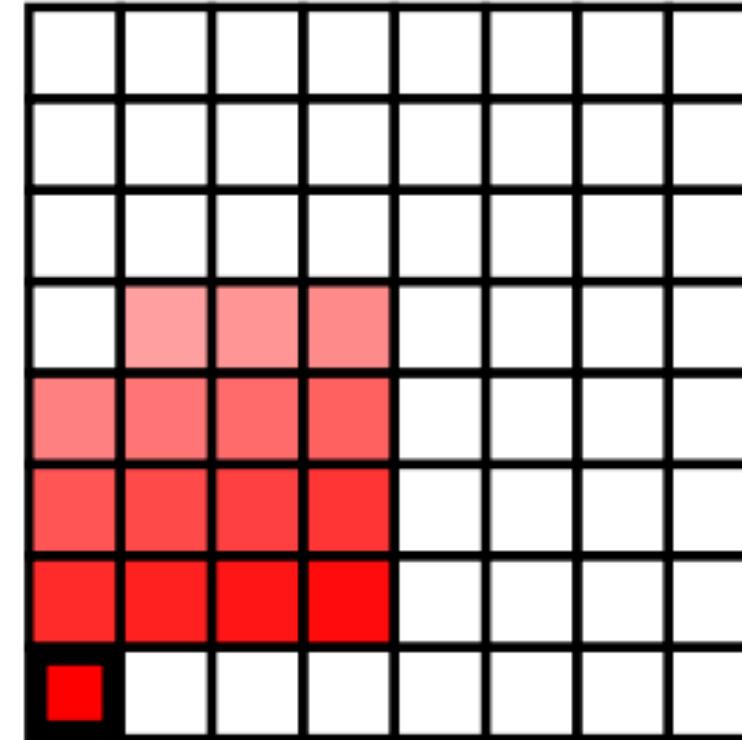
mem:345

B mem:115 cache hit:99

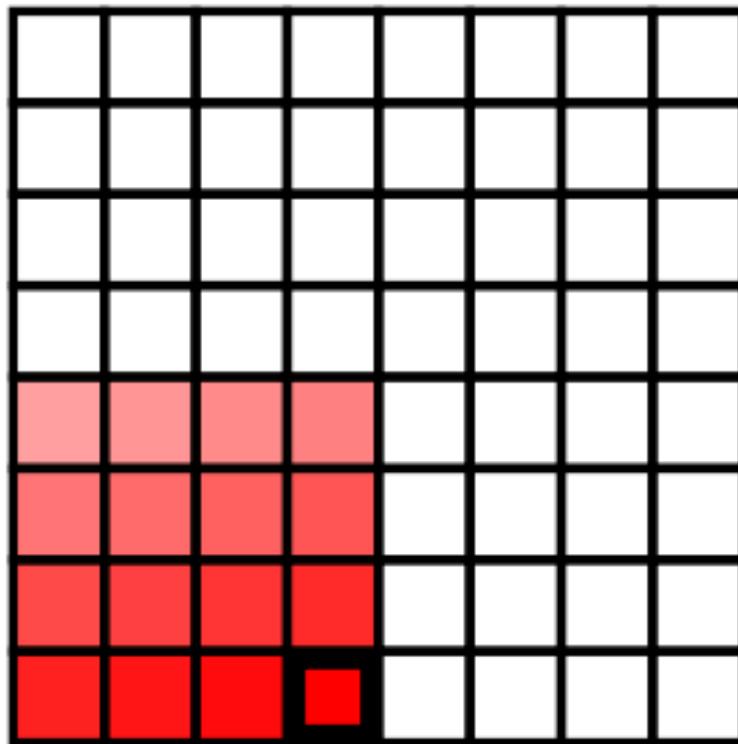


cache hits:269 ≈77%

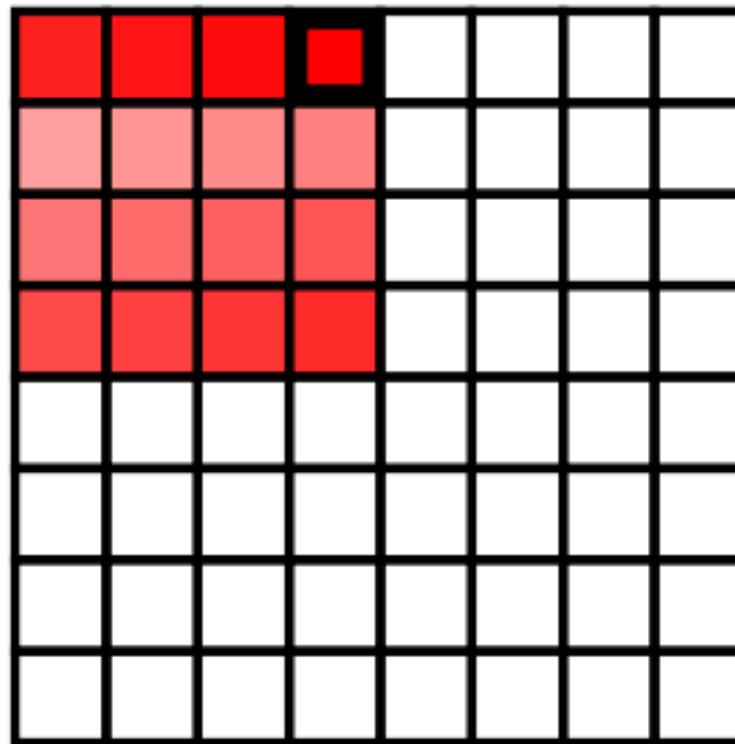
C mem:115 cache hit:86



A mem:116 cache hit:84

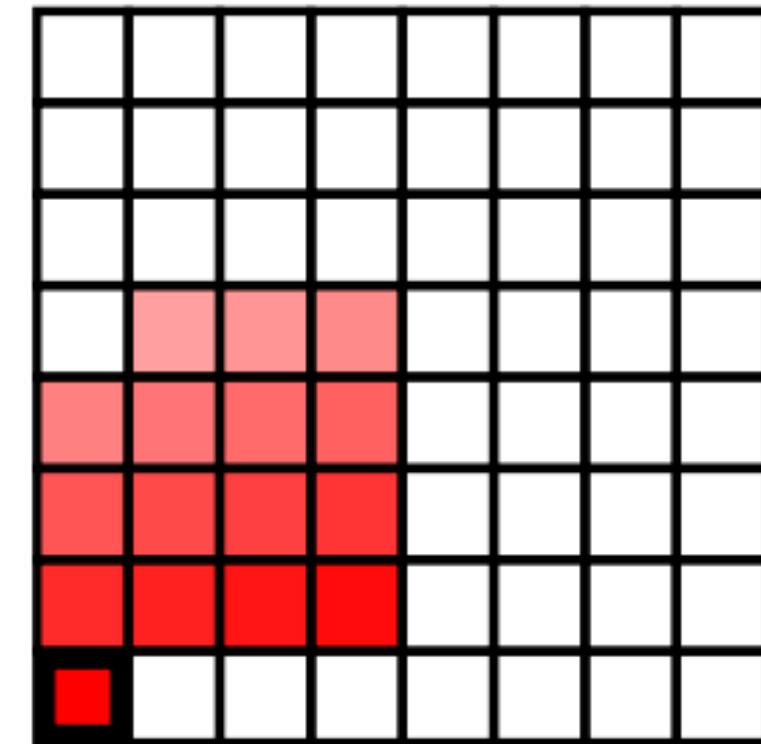


B mem:116 cache hit:100



×

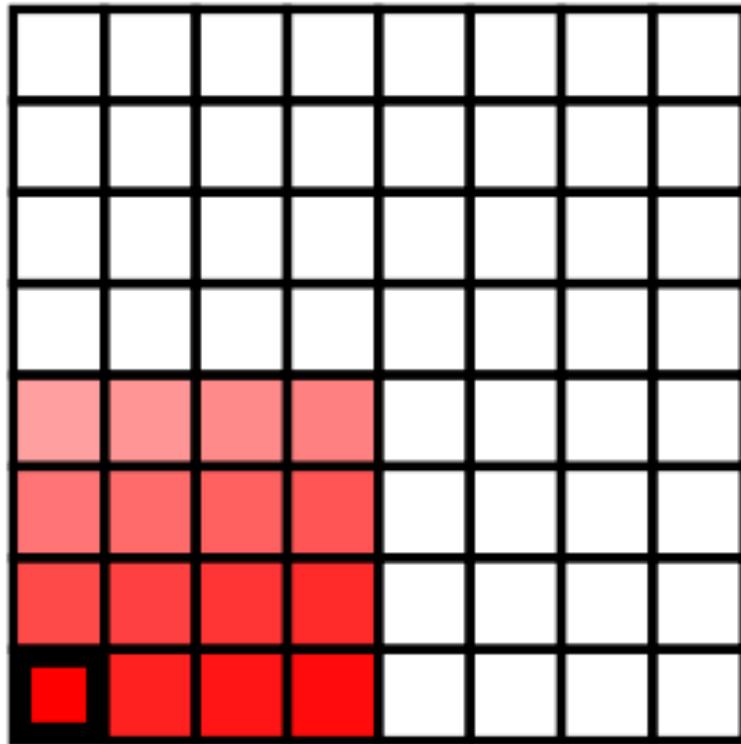
C mem:116 cache hit:87



mem:348

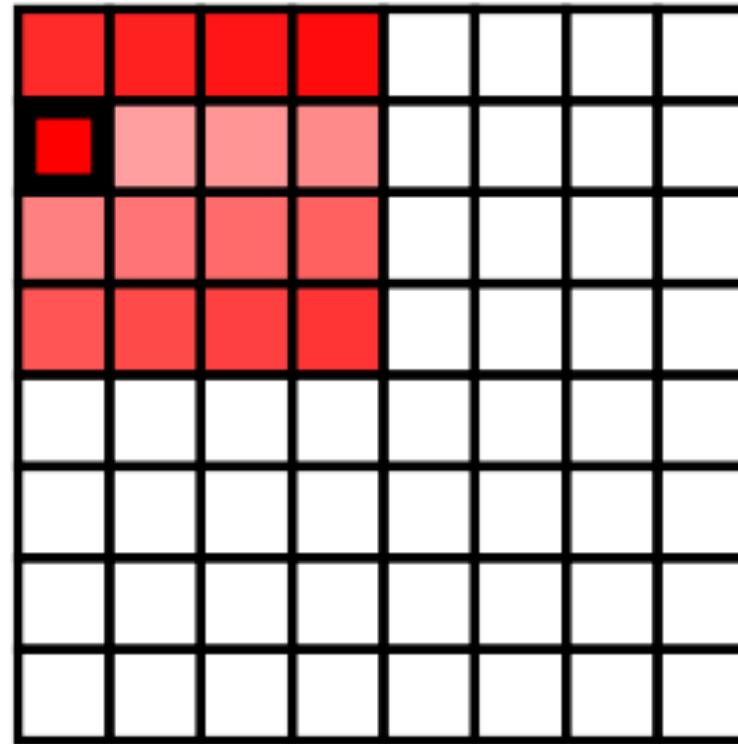
cache hits:271 $\approx 77\%$

A mem:117 cache hit:85



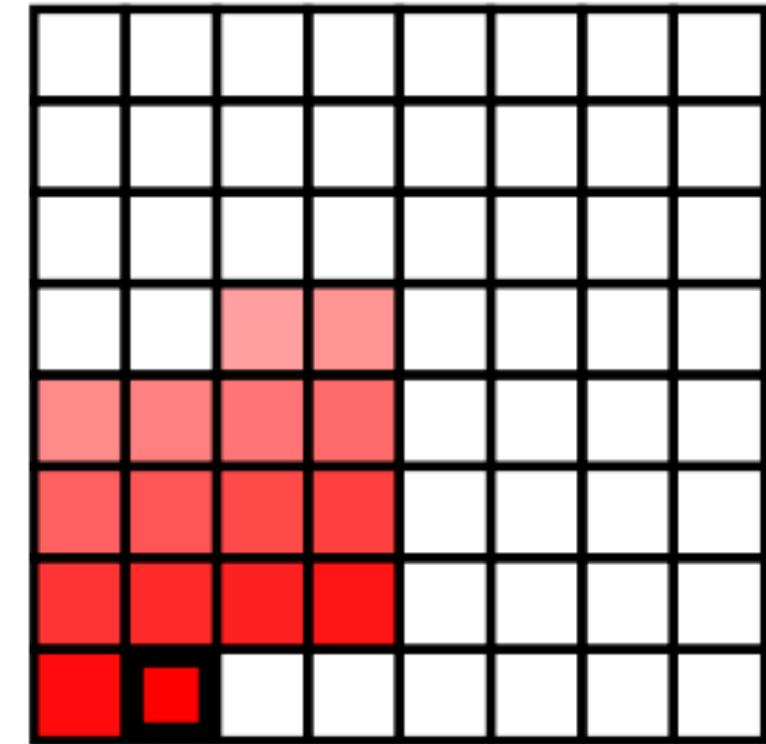
mem:351

B mem:117 cache hit:101

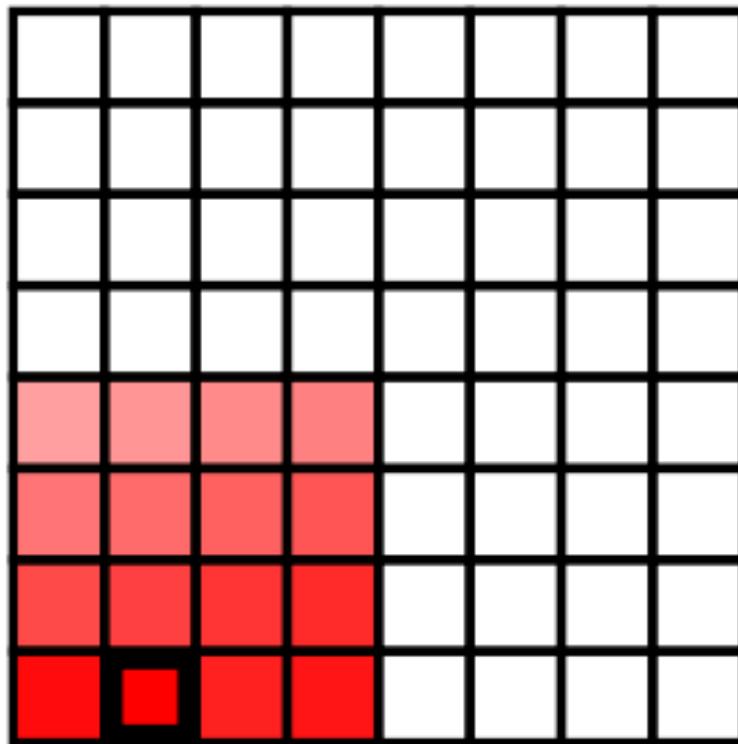


cache hits:273 ≈77%

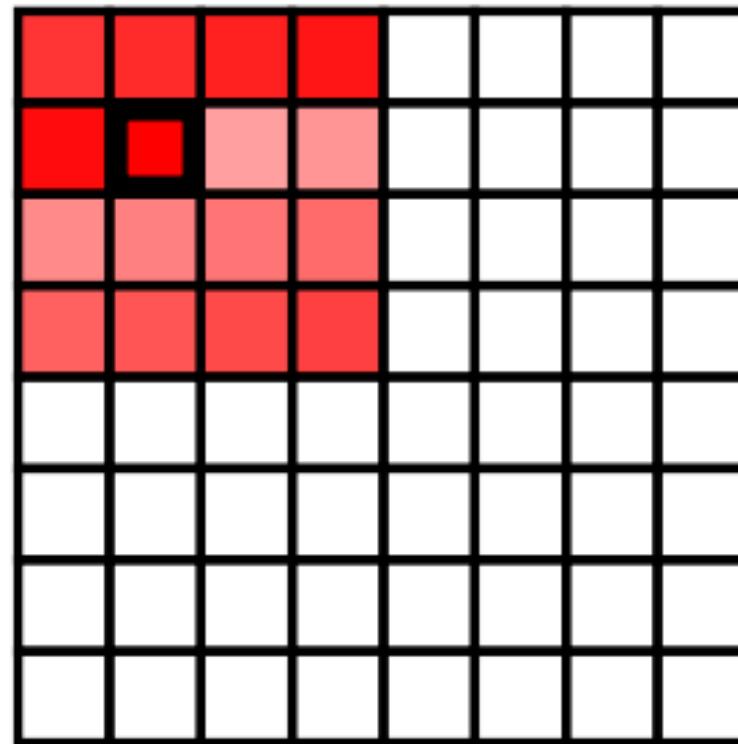
C mem:117 cache hit:87



A mem:118 cache hit:86



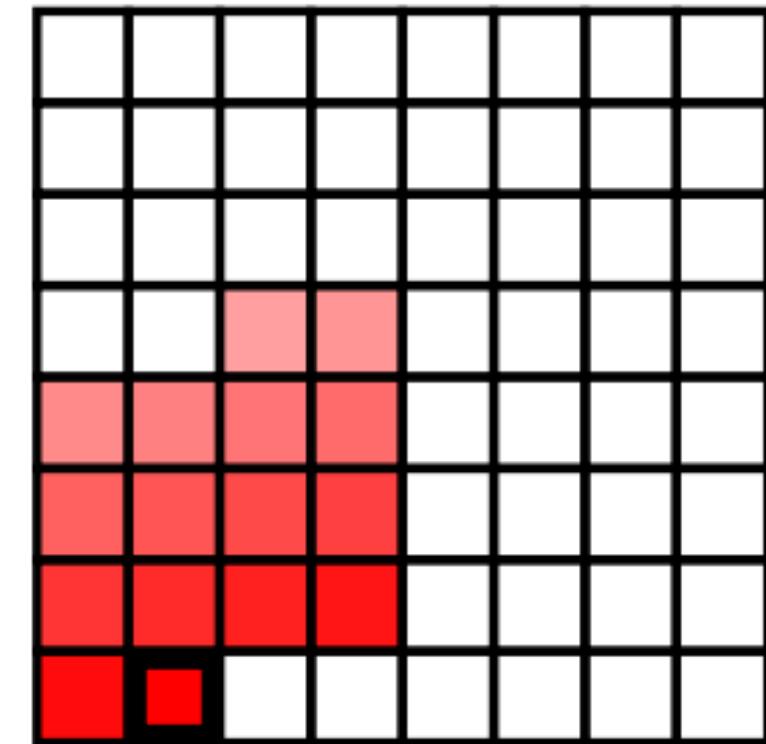
B mem:118 cache hit:102



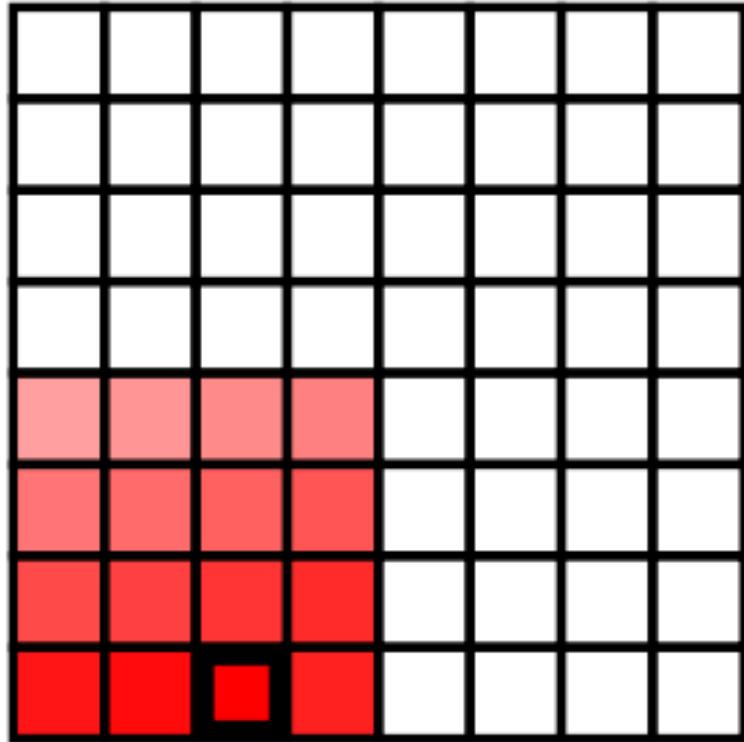
mem:354

cache hits:276 ≈77%

C mem:118 cache hit:88

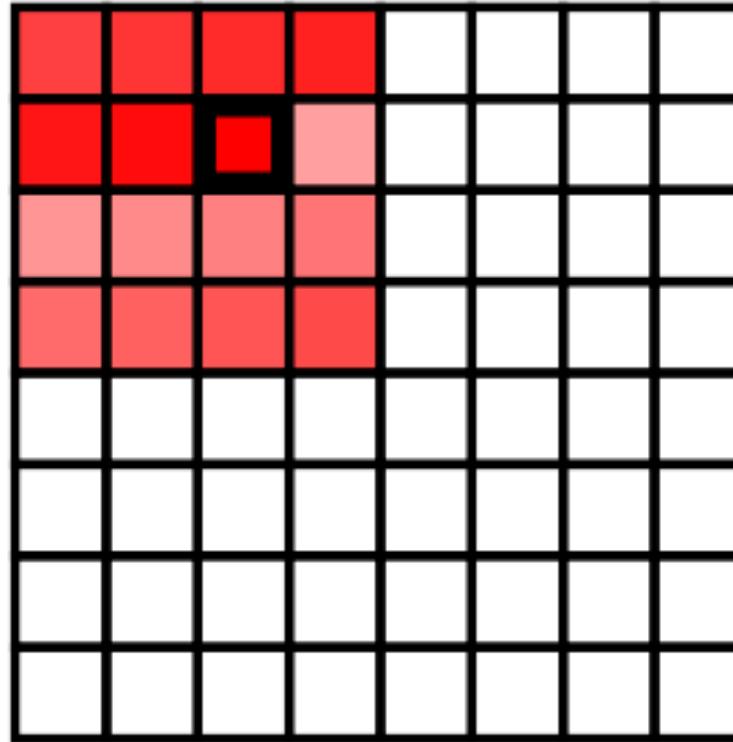


A mem:119 cache hit:87



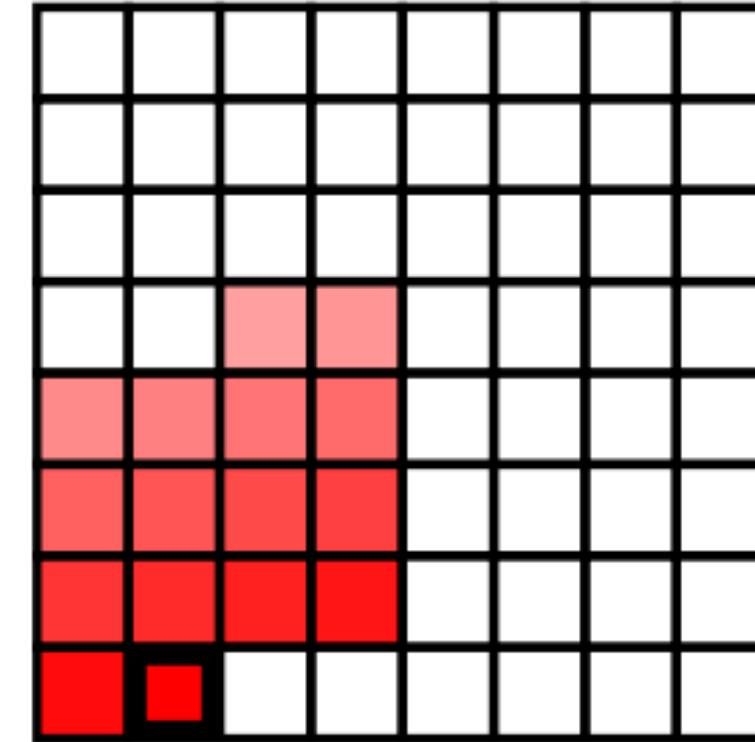
mem:357

B mem:119 cache hit:103

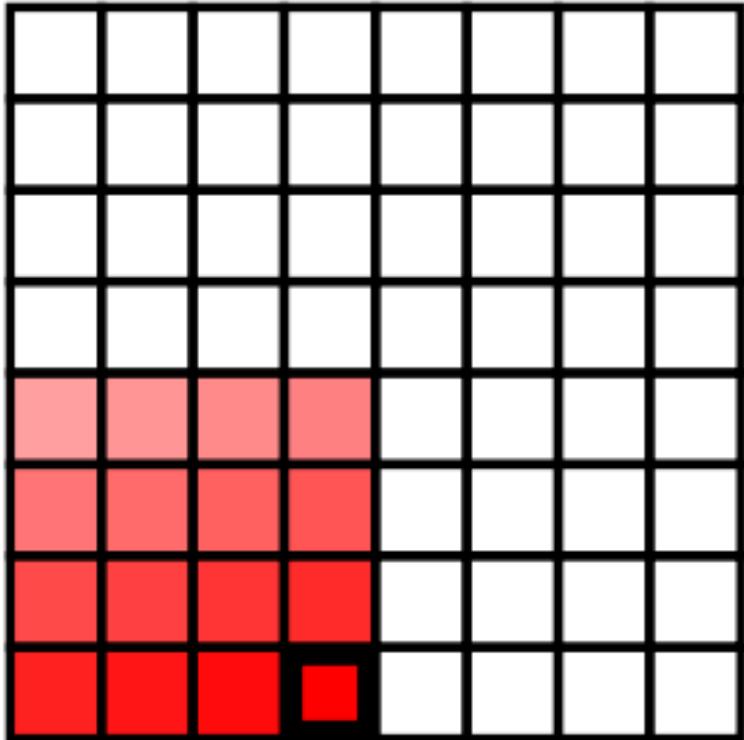


cache hits:279 ≈78%

C mem:119 cache hit:89

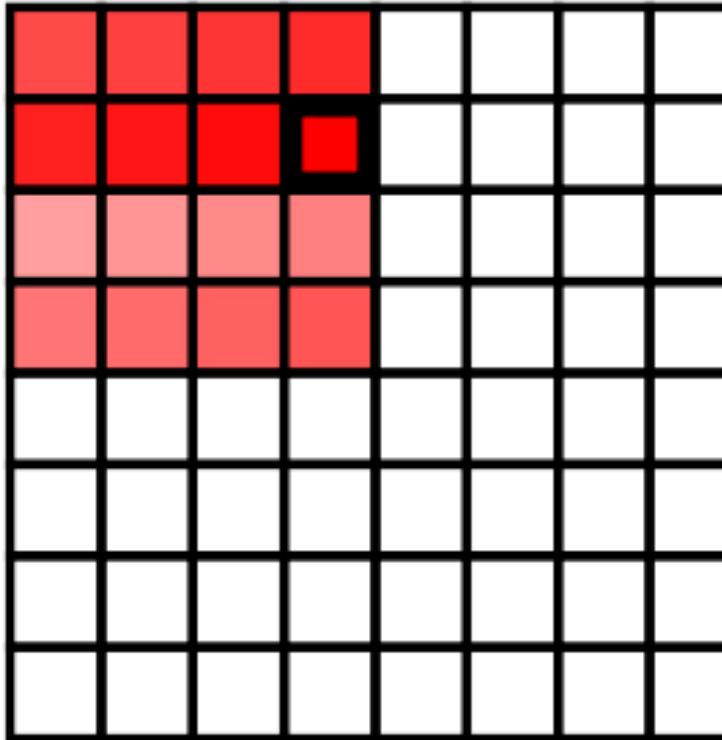


A mem:120 cache hit:88



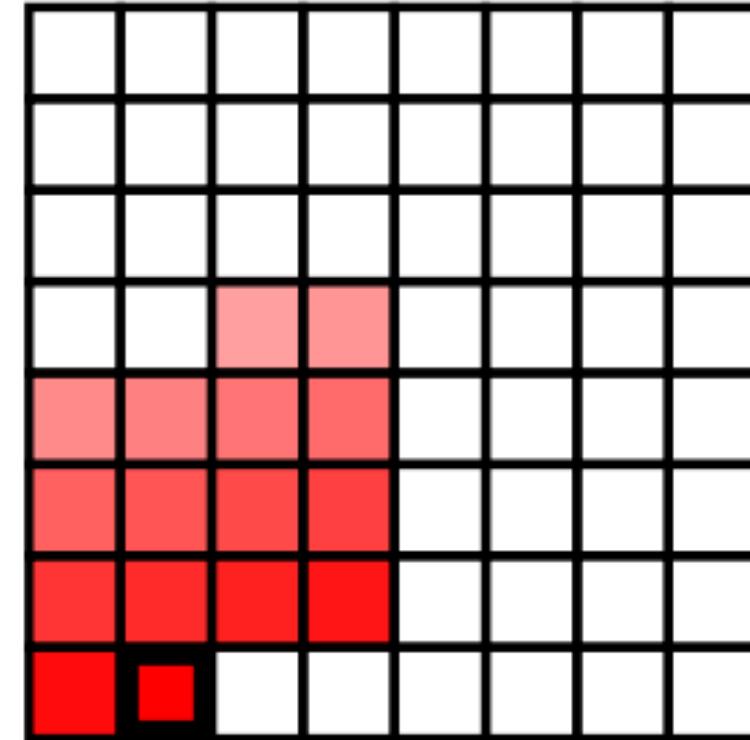
mem:360

B mem:120 cache hit:104

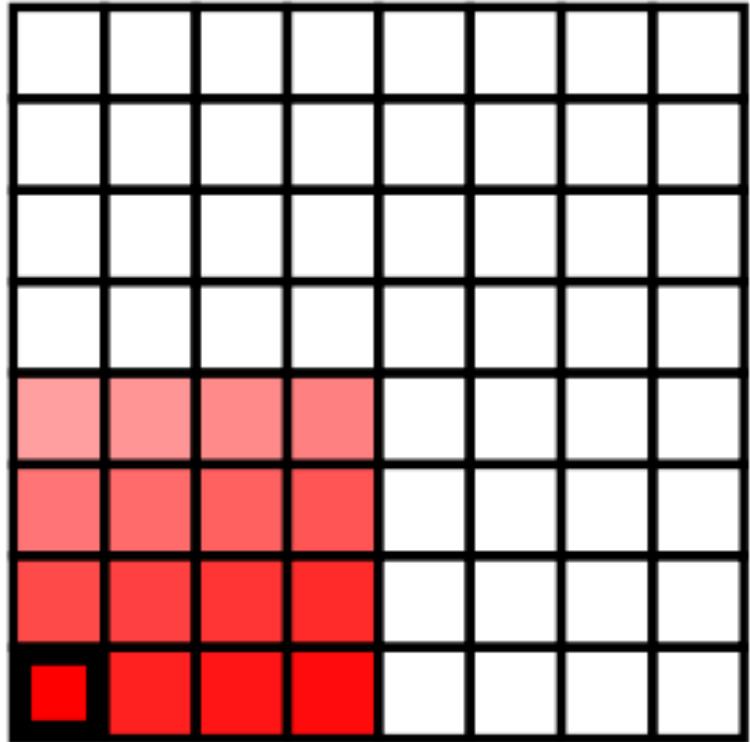


cache hits:282 $\approx 78\%$

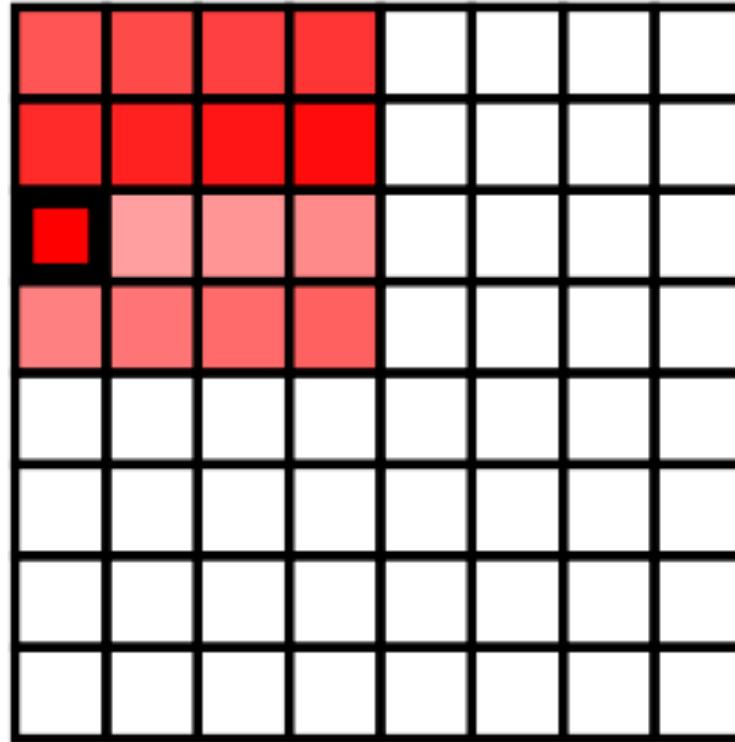
C mem:120 cache hit:90



A mem:121 cache hit:89



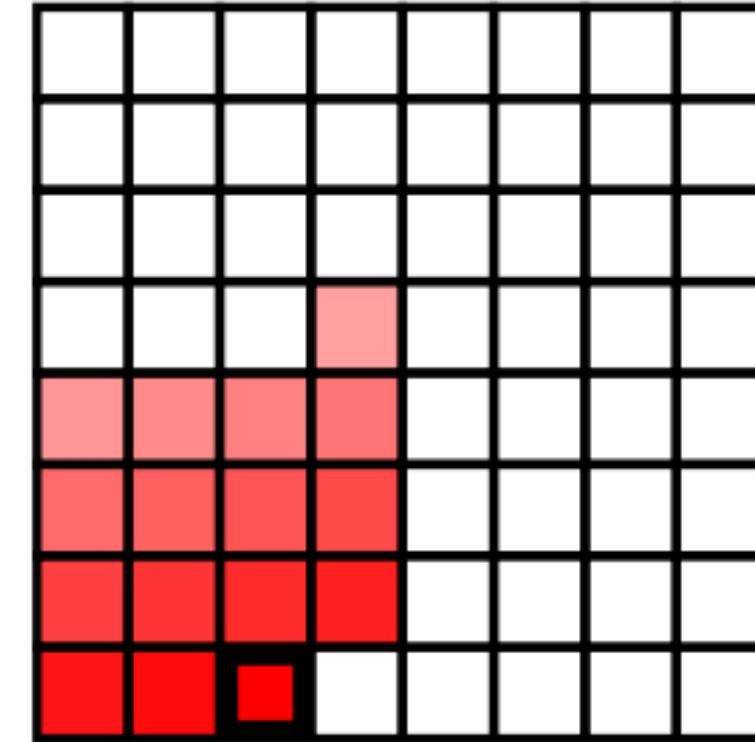
B mem:121 cache hit:105



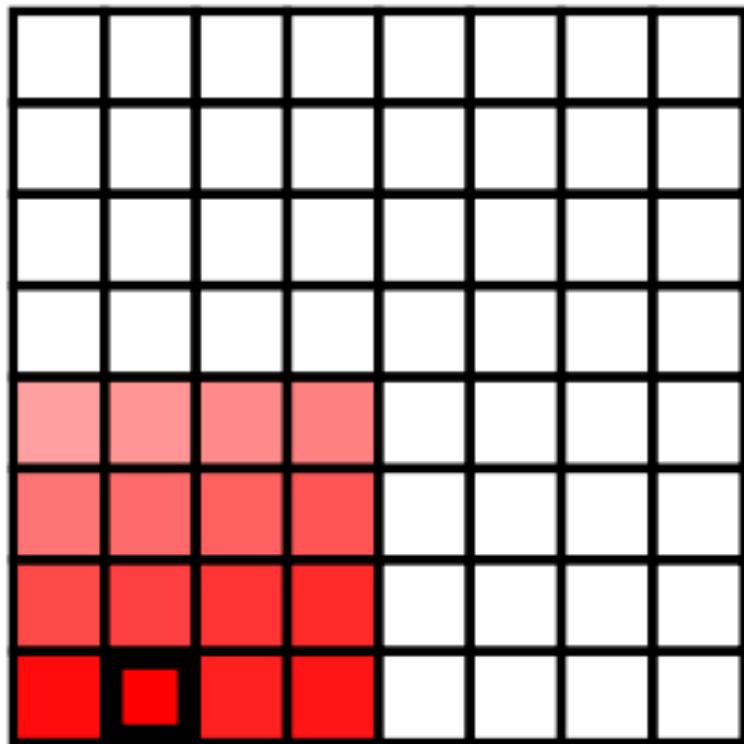
mem:363

cache hits:284 ≈78%

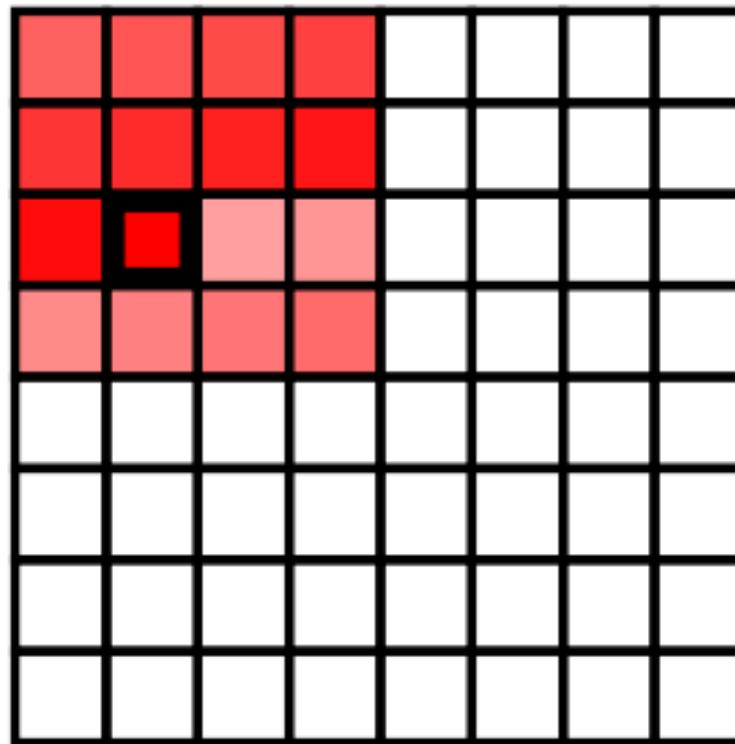
C mem:121 cache hit:90



A mem:122 cache hit:90



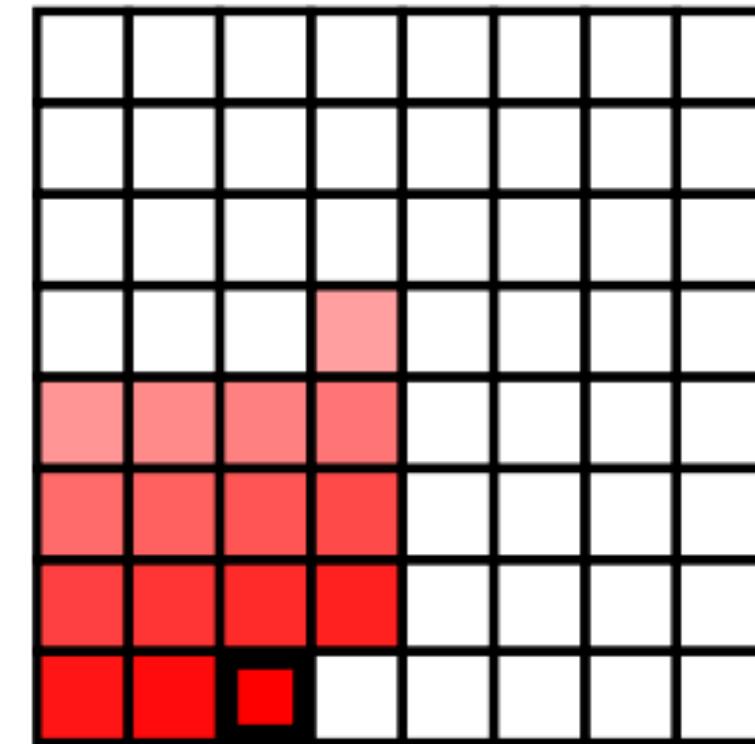
B mem:122 cache hit:106



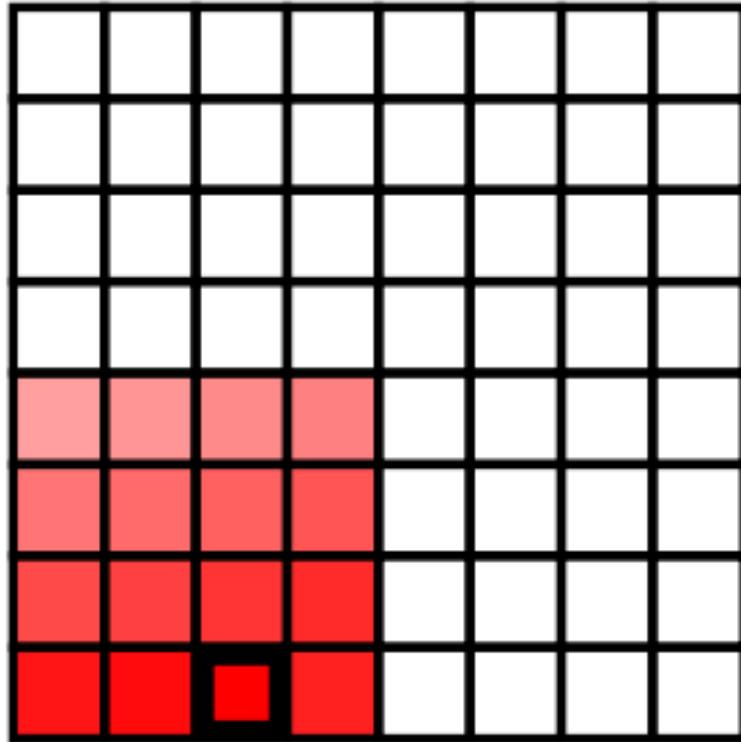
mem:366

cache hits:287 $\approx 78\%$

C mem:122 cache hit:91

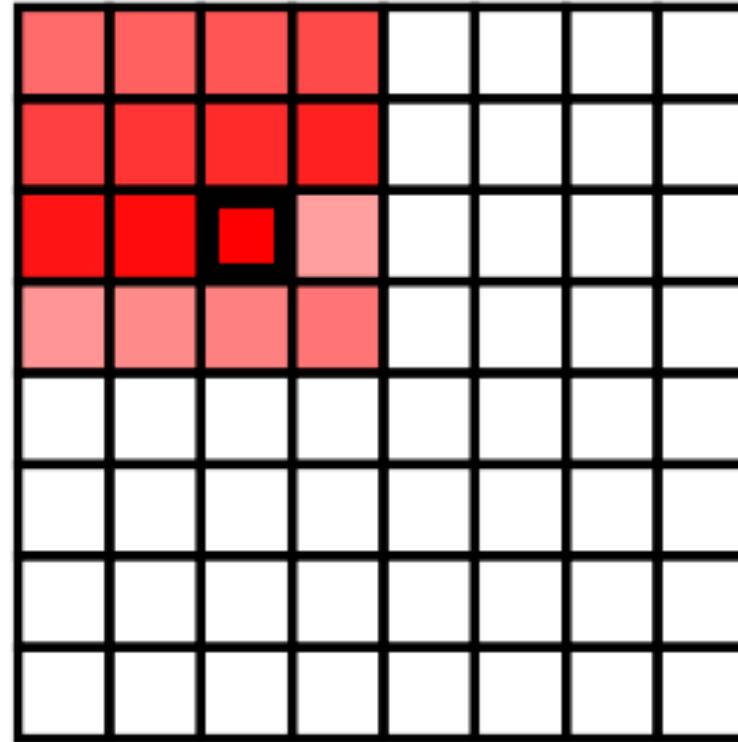


A mem:123 cache hit:91



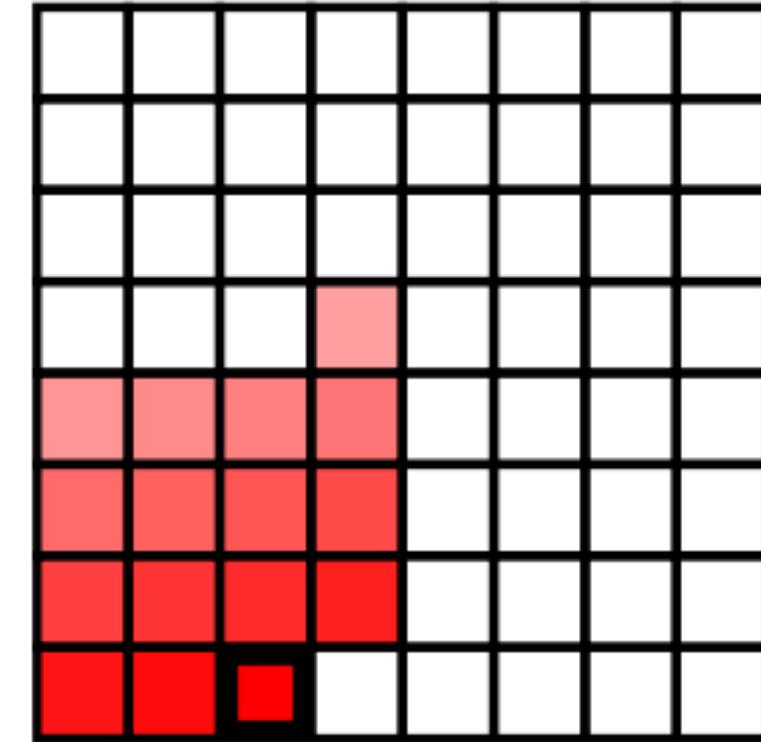
mem:369

B mem:123 cache hit:107

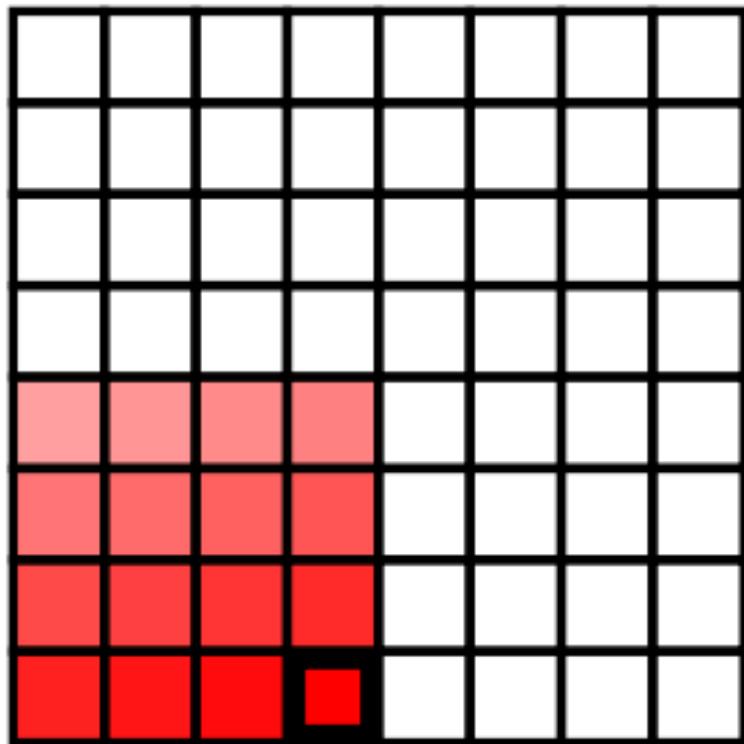


cache hits:290 $\approx 78\%$

C mem:123 cache hit:92

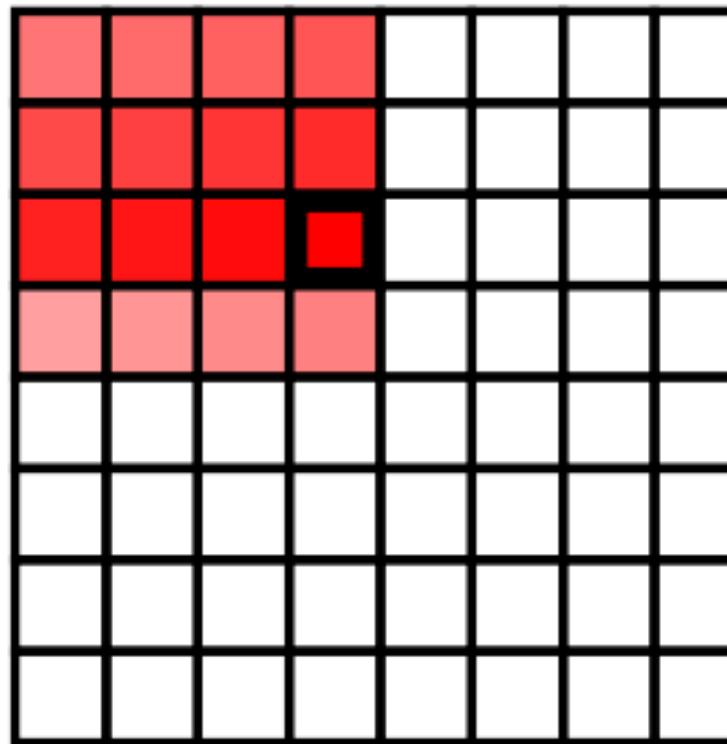


A mem:124 cache hit:92



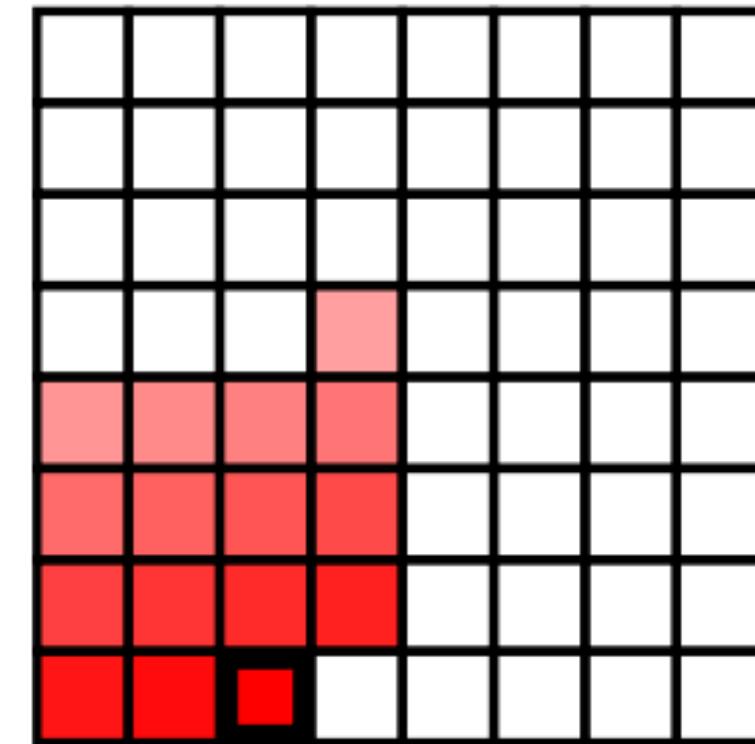
mem:372

B mem:124 cache hit:108

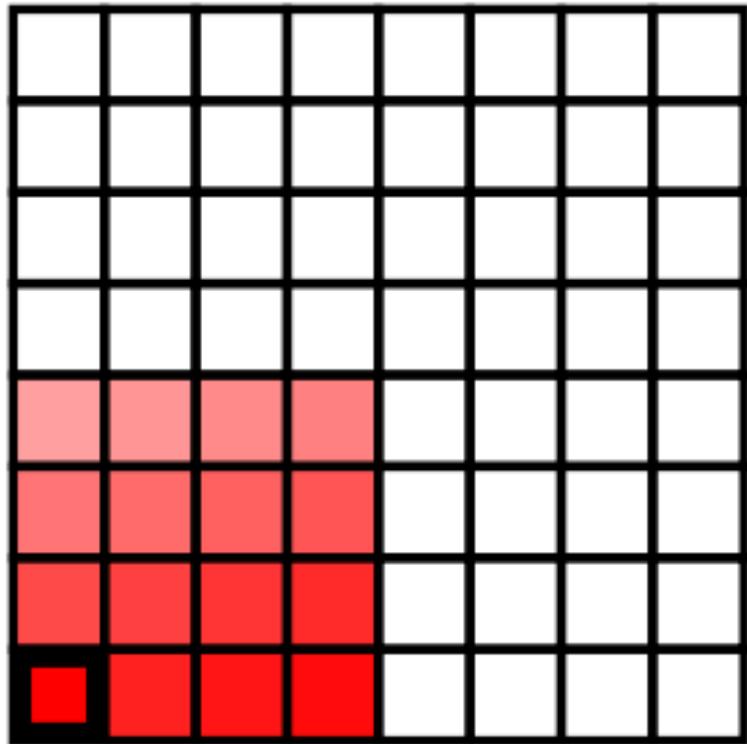


cache hits:293 ≈78%

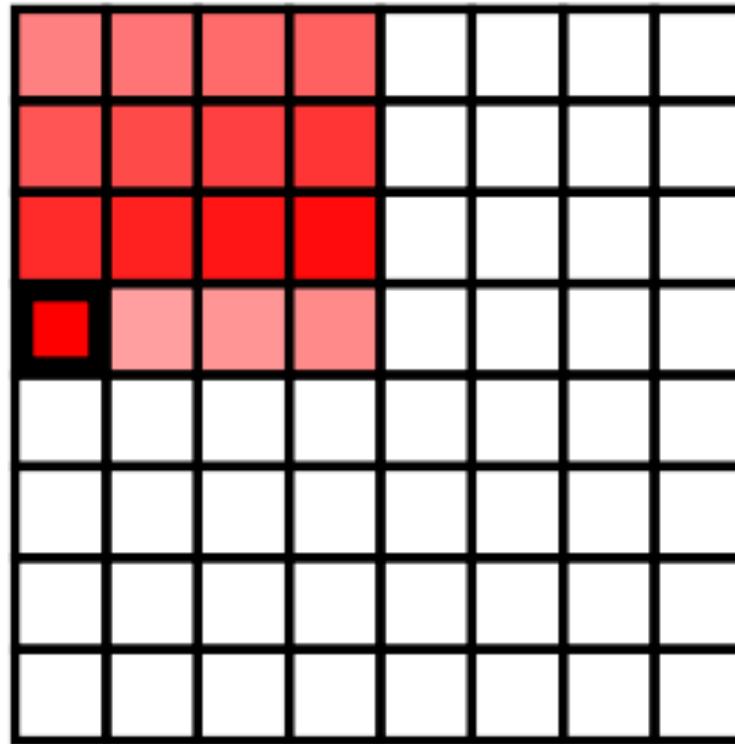
C mem:124 cache hit:93



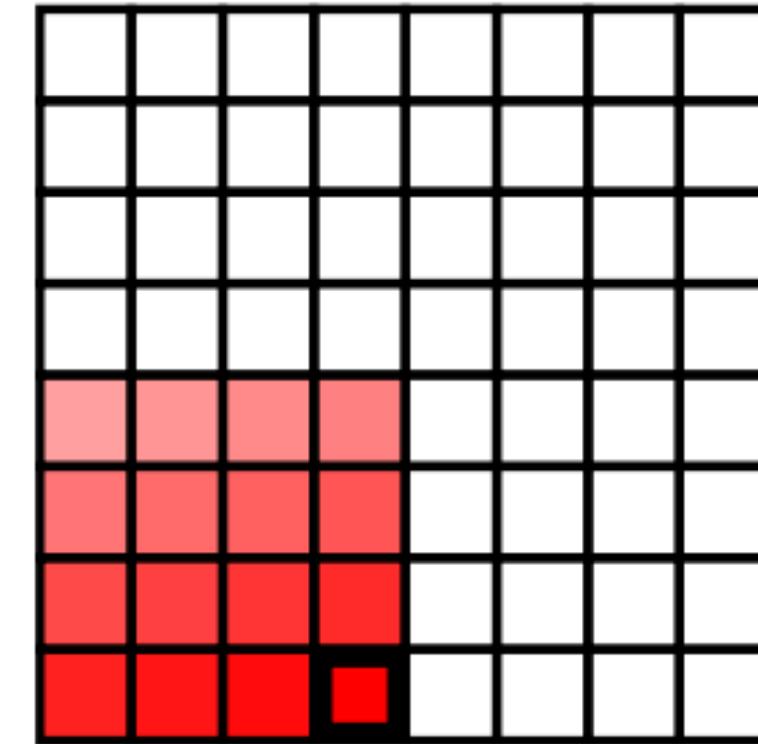
A mem:125 cache hit:93



B mem:125 cache hit:109



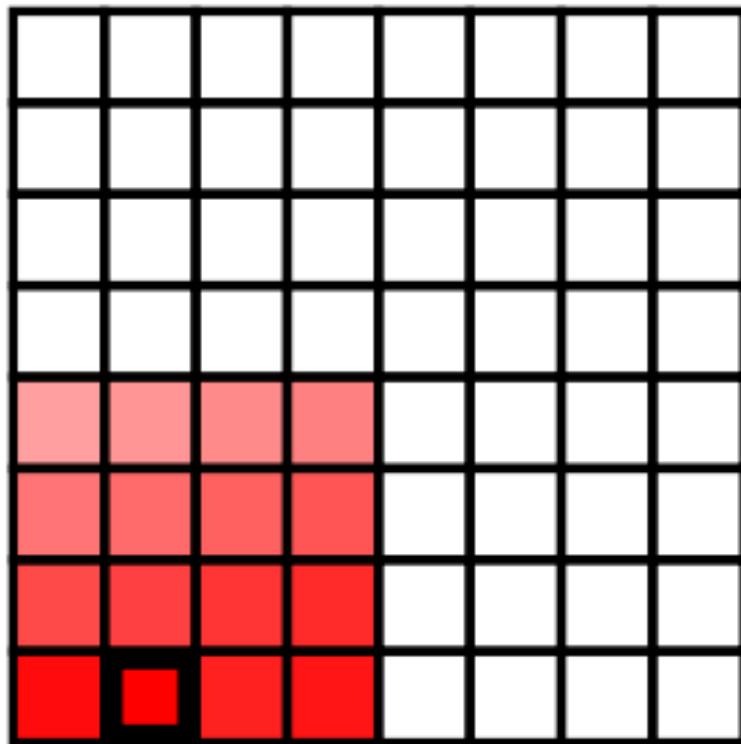
C mem:125 cache hit:93



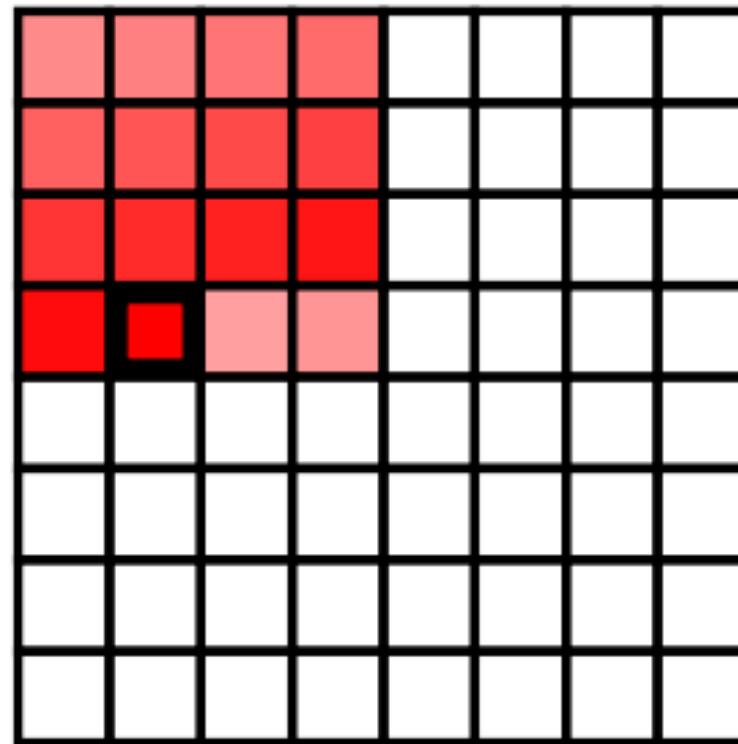
mem:375

cache hits:295 $\approx 78\%$

A mem:126 cache hit:94



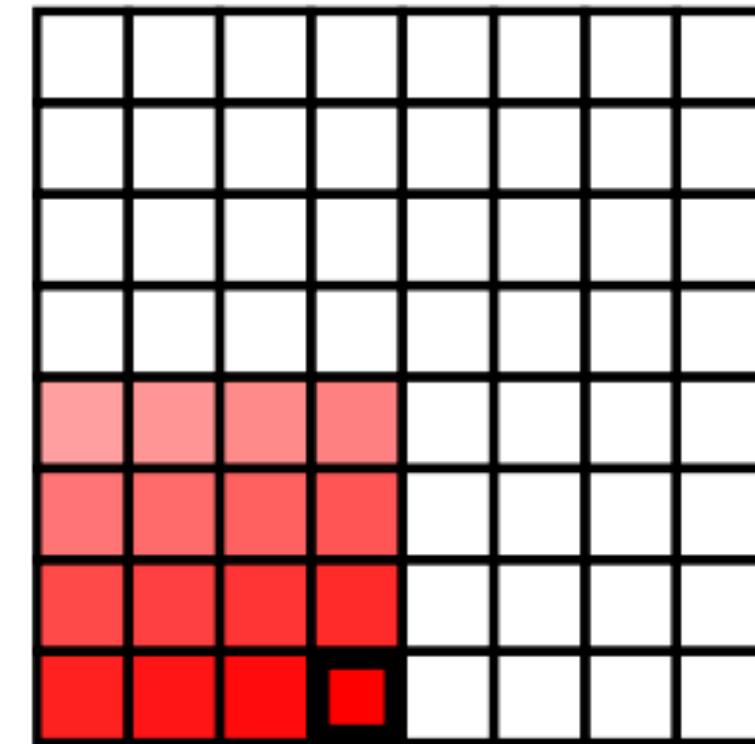
B mem:126 cache hit:110



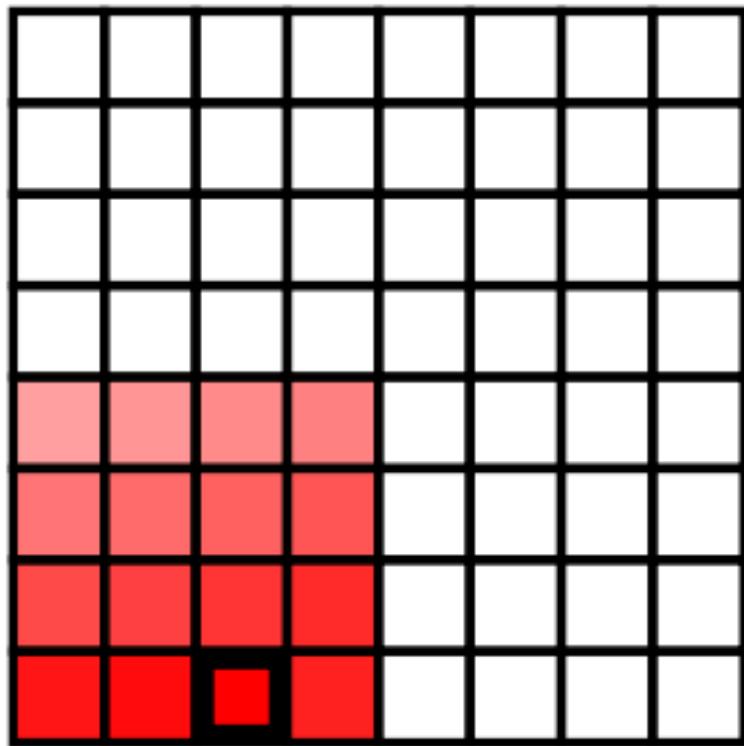
mem:378

cache hits:298 $\approx 78\%$

C mem:126 cache hit:94

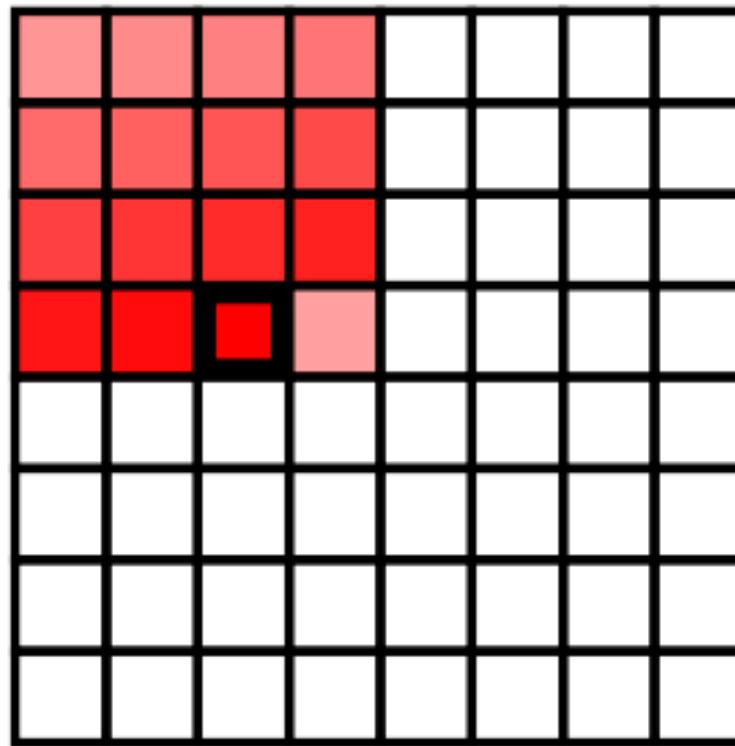


A mem:127 cache hit:95



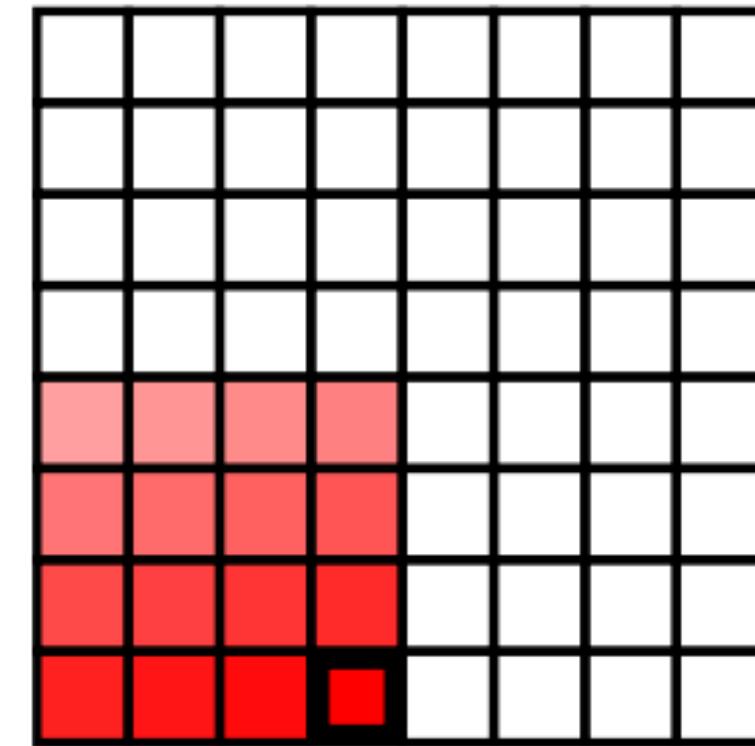
mem:381

B mem:127 cache hit:111

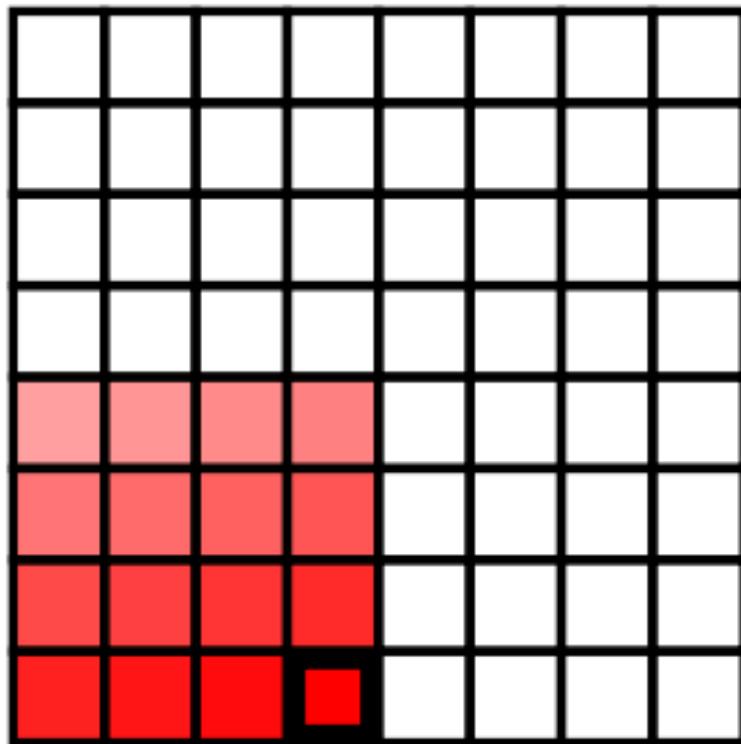


cache hits:301 $\approx 79\%$

C mem:127 cache hit:95

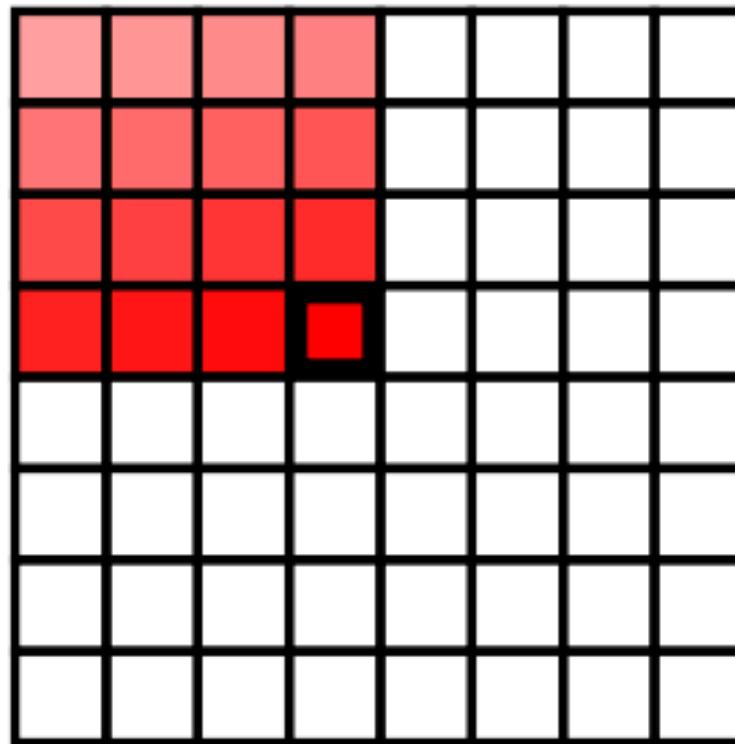


A mem:128 cache hit:96



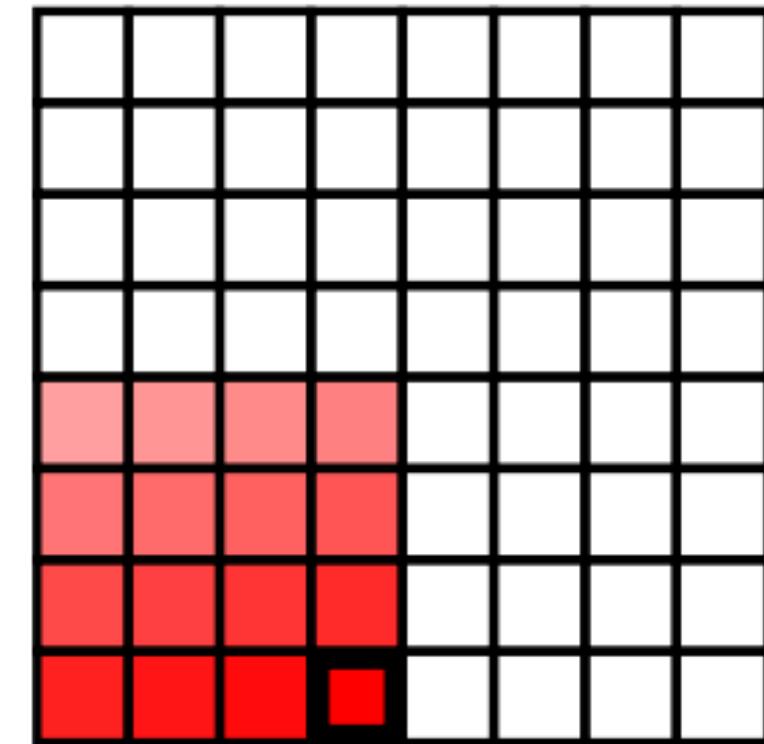
mem:384

B mem:128 cache hit:112

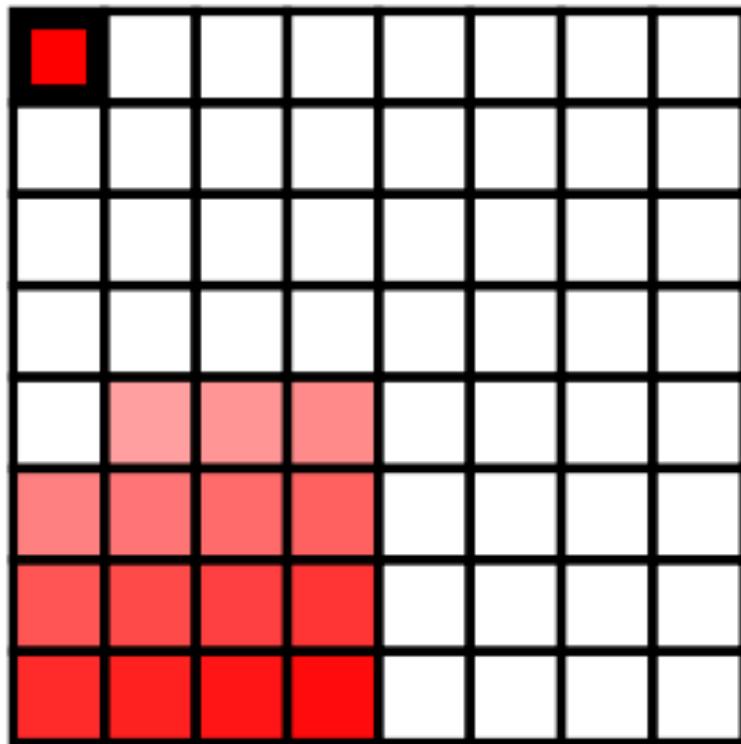


cache hits:304 ≈79%

C mem:128 cache hit:96

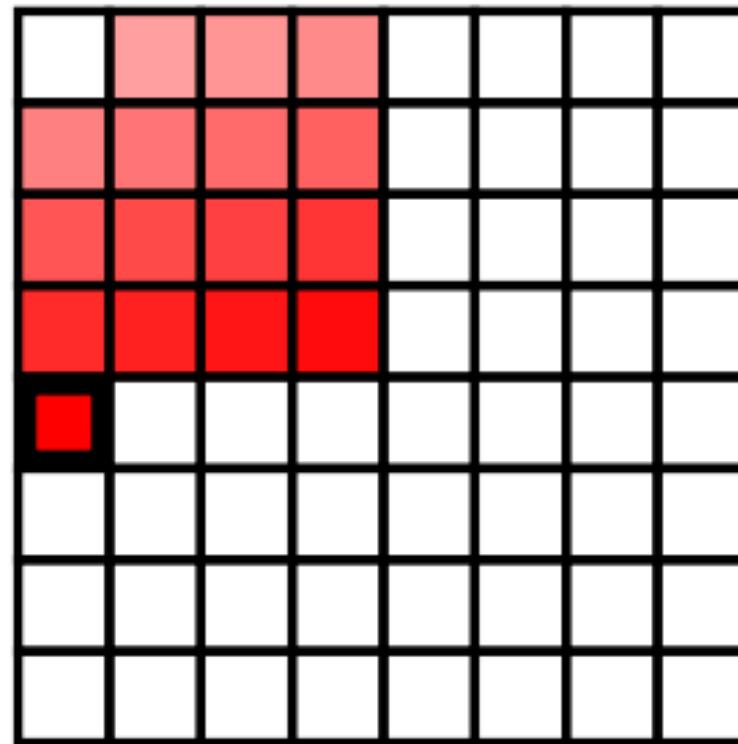


A mem:129 cache hit:96



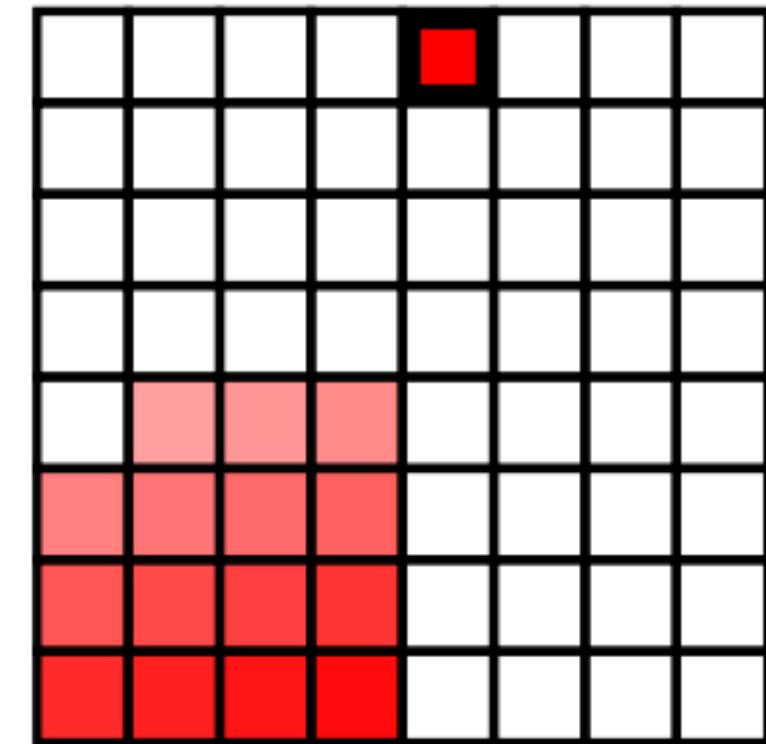
mem:387

B mem:129 cache hit:112

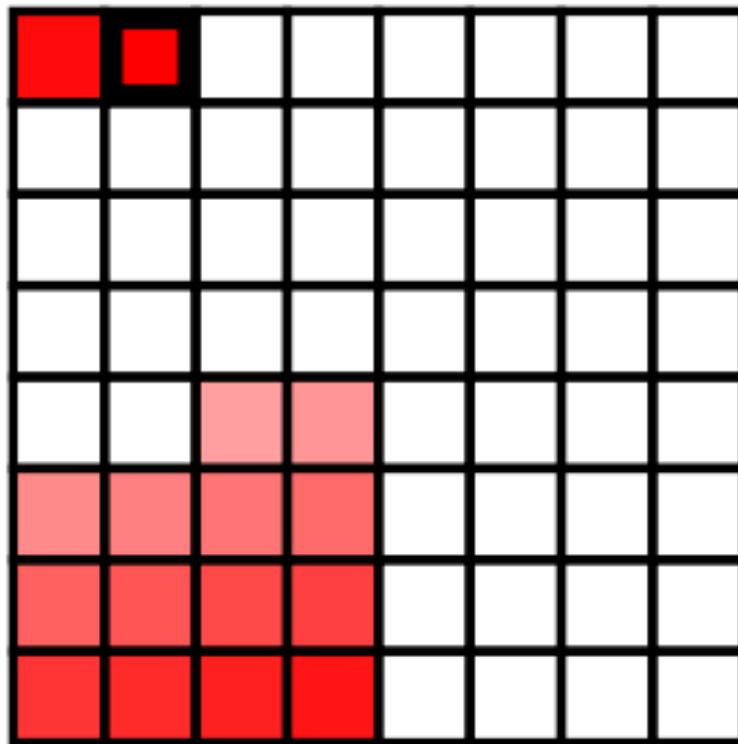


cache hits:304 ≈78%

C mem:129 cache hit:96

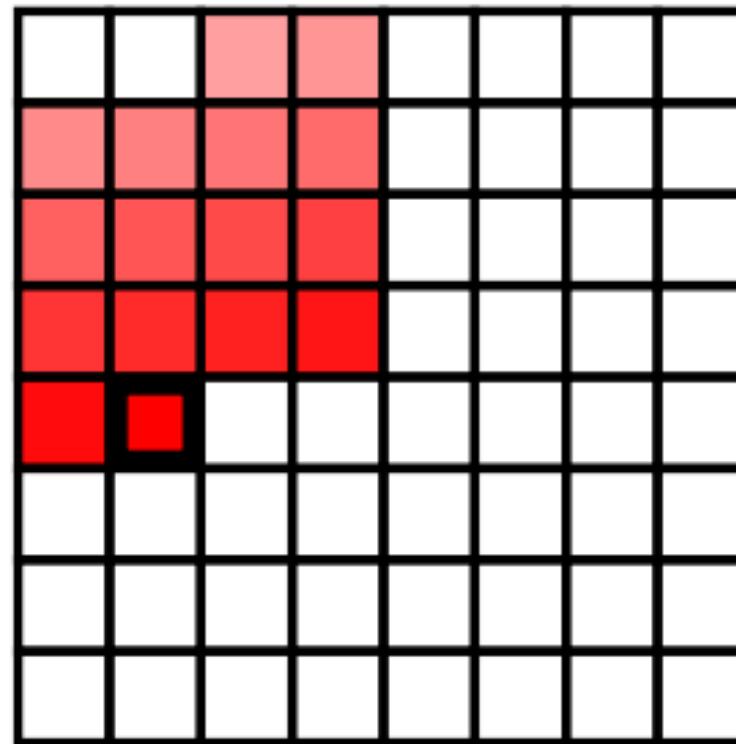


A mem:130 cache hit:96



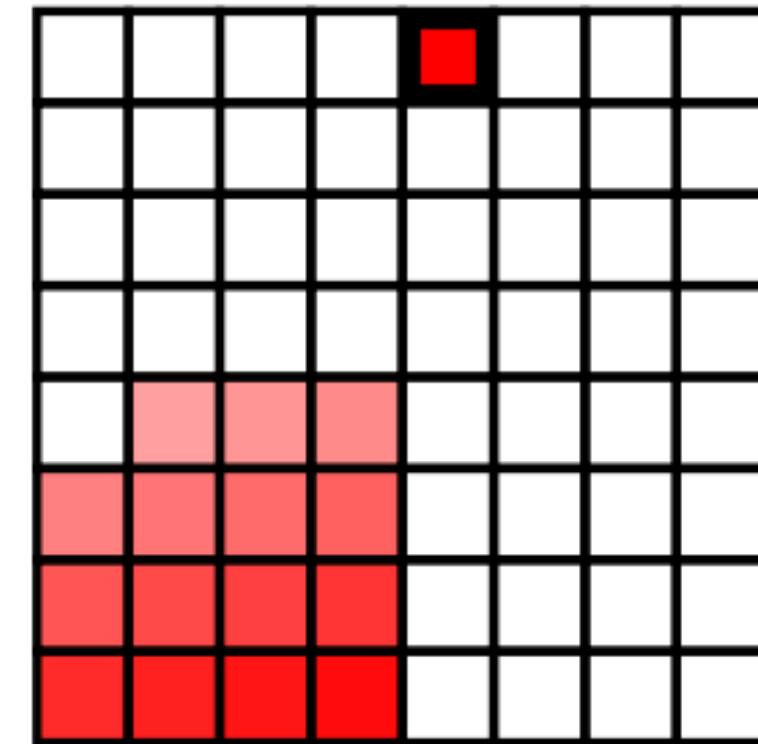
mem:390

B mem:130 cache hit:112

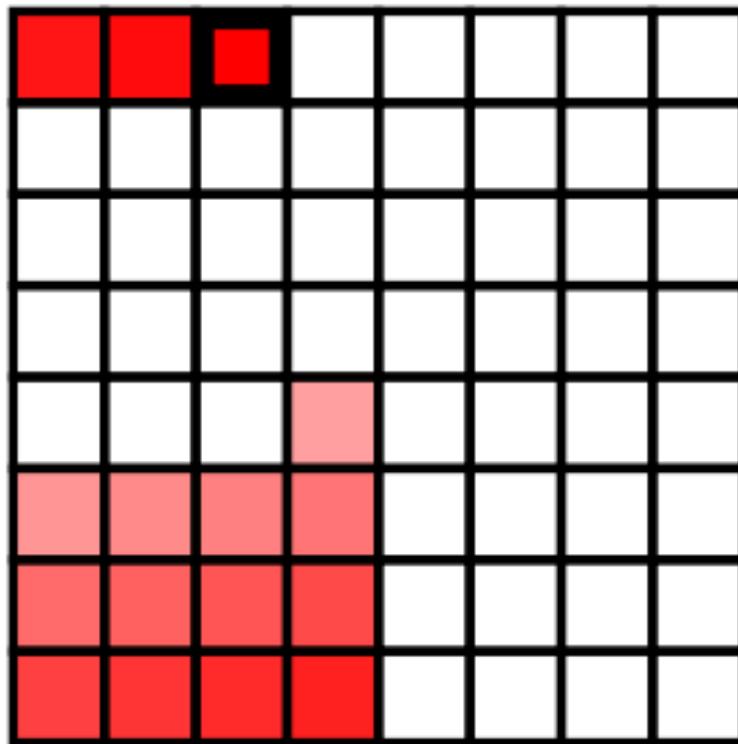


cache hits:305 $\approx 78\%$

C mem:130 cache hit:97

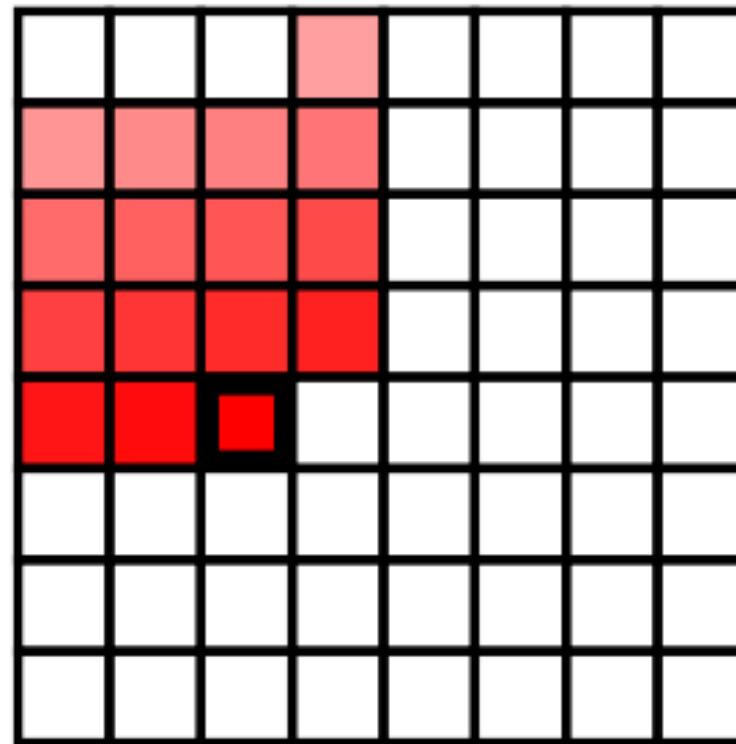


A mem:131 cache hit:96



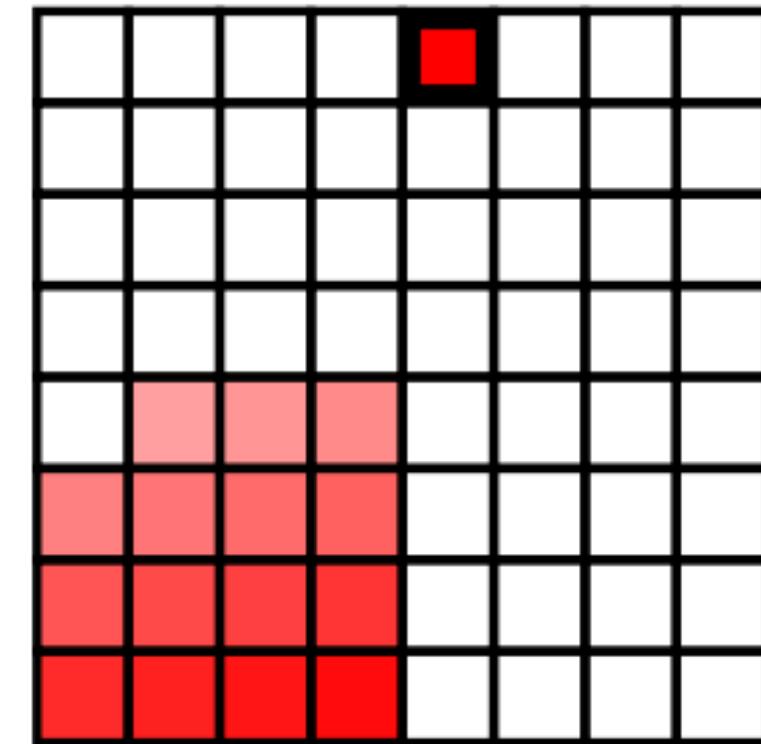
mem:393

B mem:131 cache hit:112

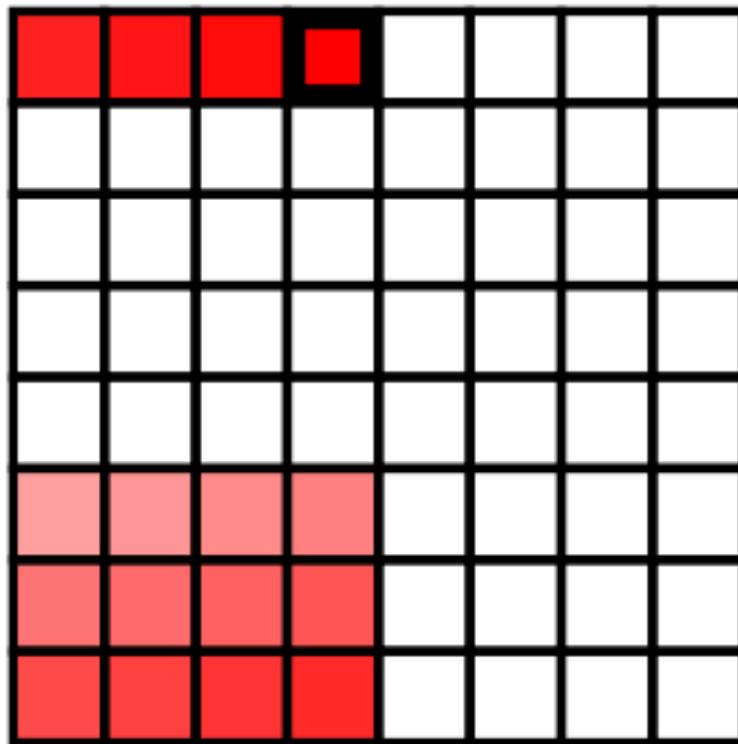


cache hits:306 ≈77%

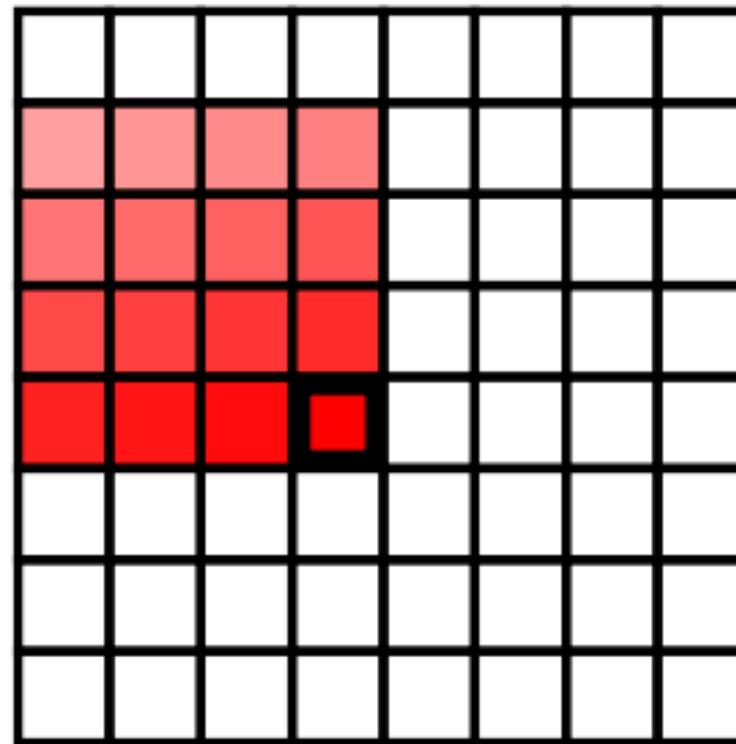
C mem:131 cache hit:98



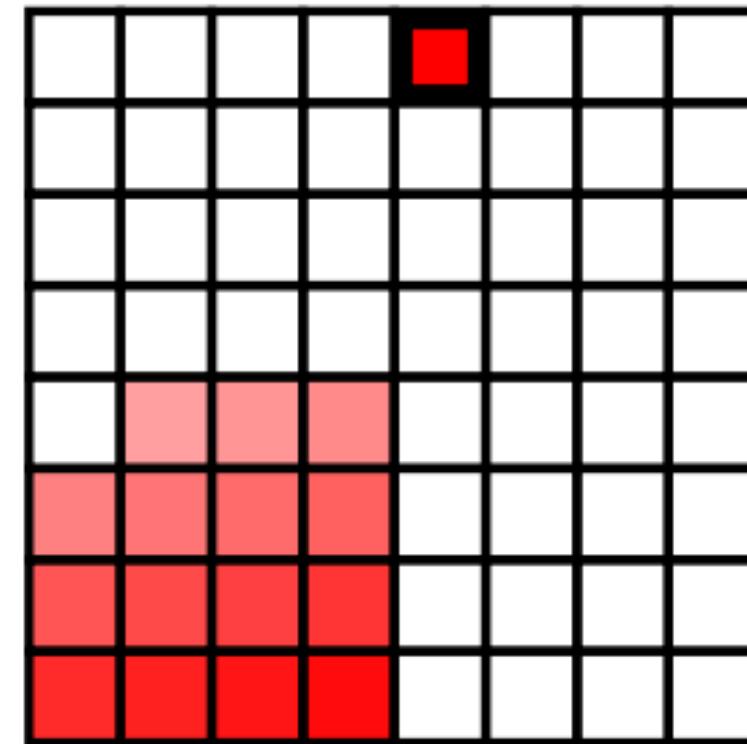
A mem:132 cache hit:96



B mem:132 cache hit:112



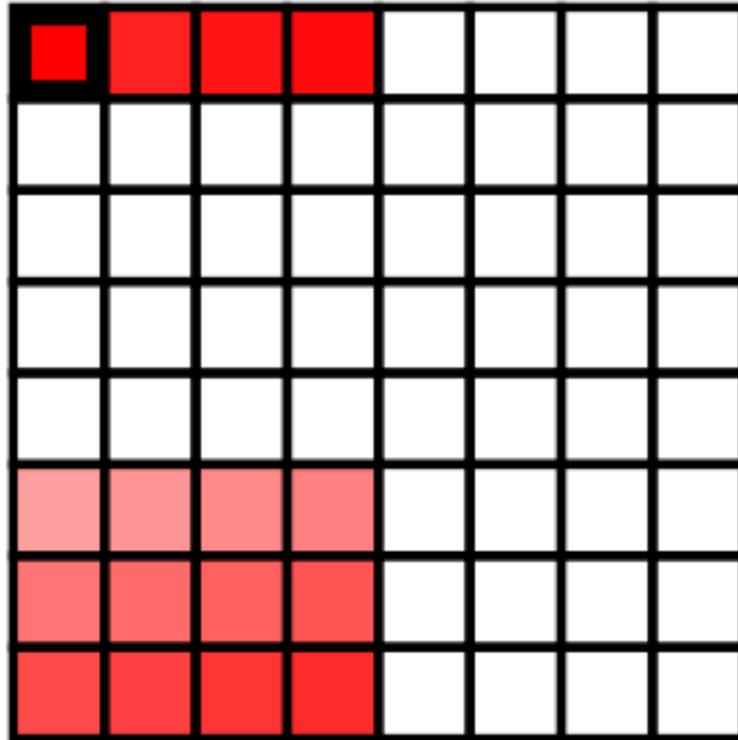
C mem:132 cache hit:99



mem:396

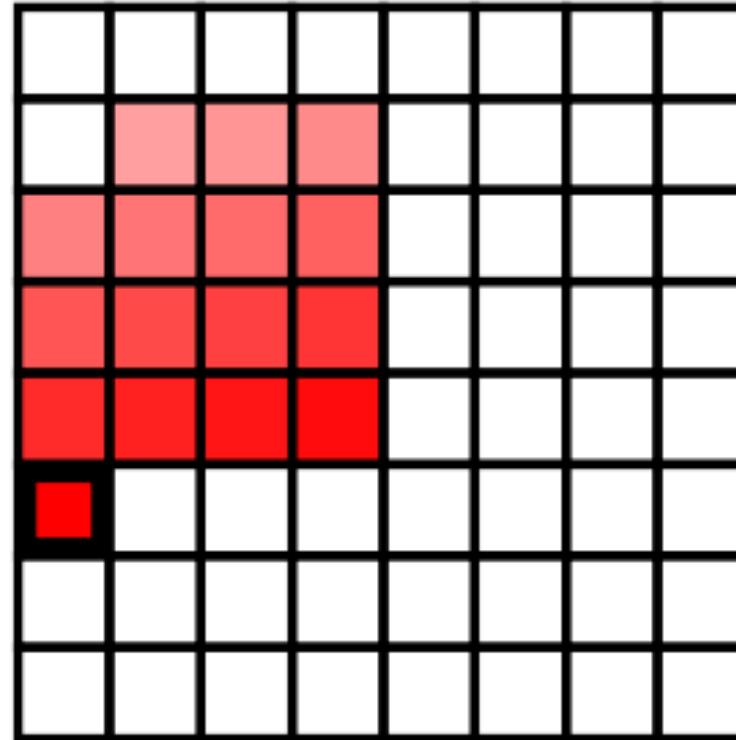
cache hits:307 $\approx 77\%$

A mem:133 cache hit:97



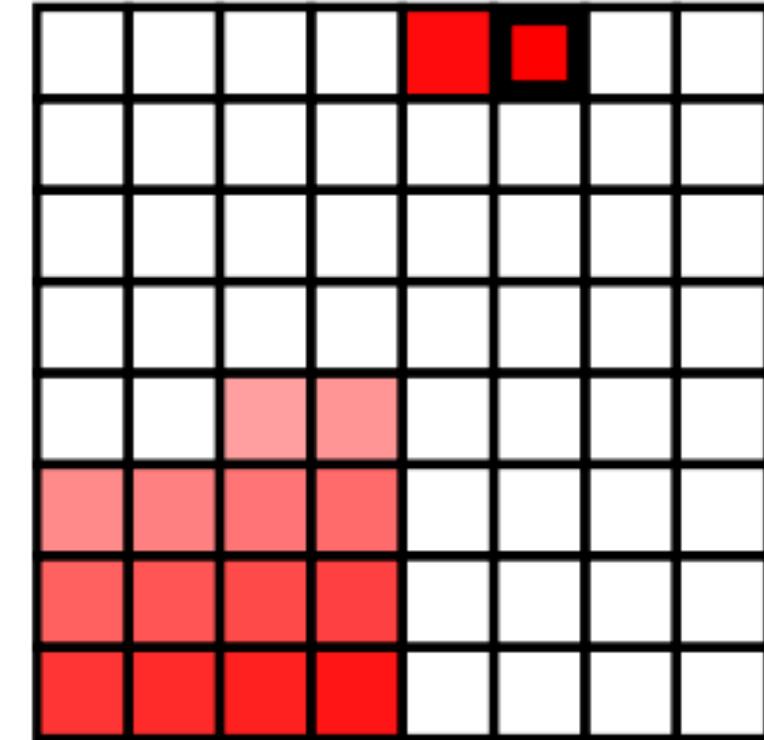
mem:399

B mem:133 cache hit:112

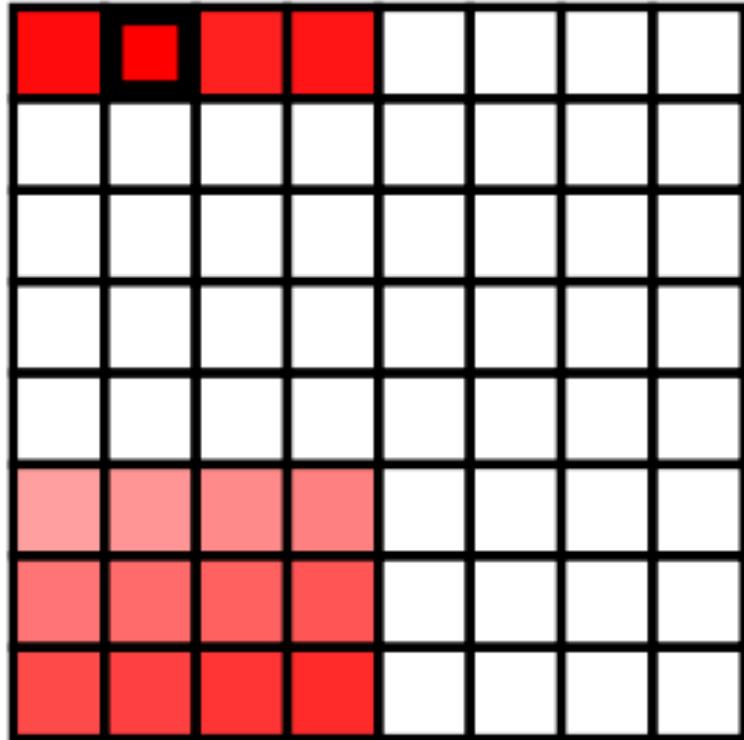


cache hits:308 ≈77%

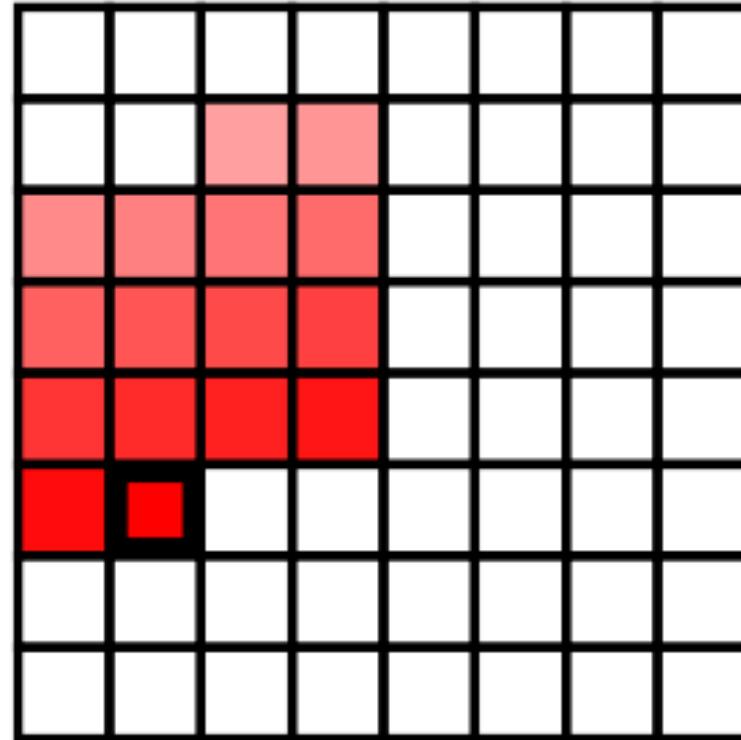
C mem:133 cache hit:99



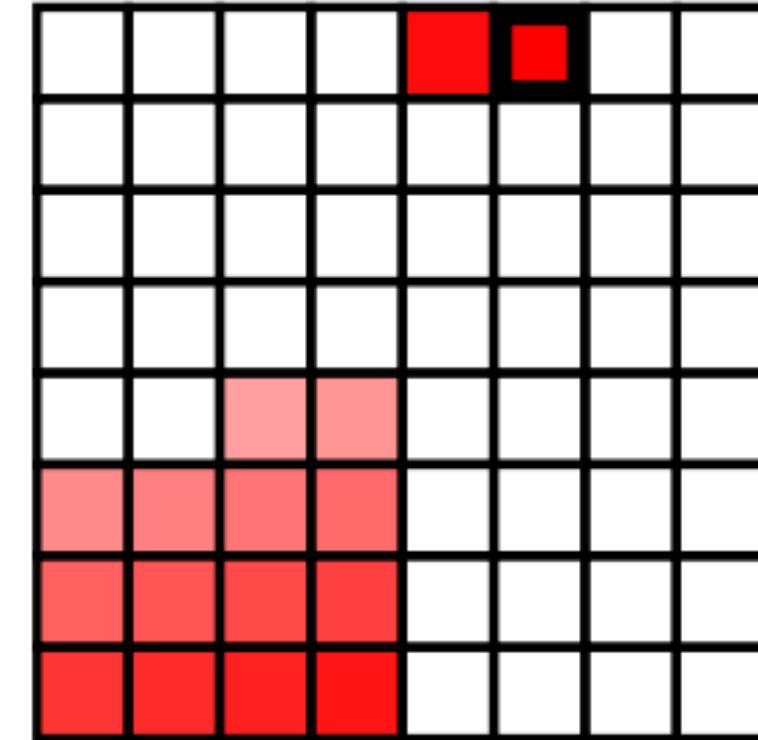
A mem:134 cache hit:98



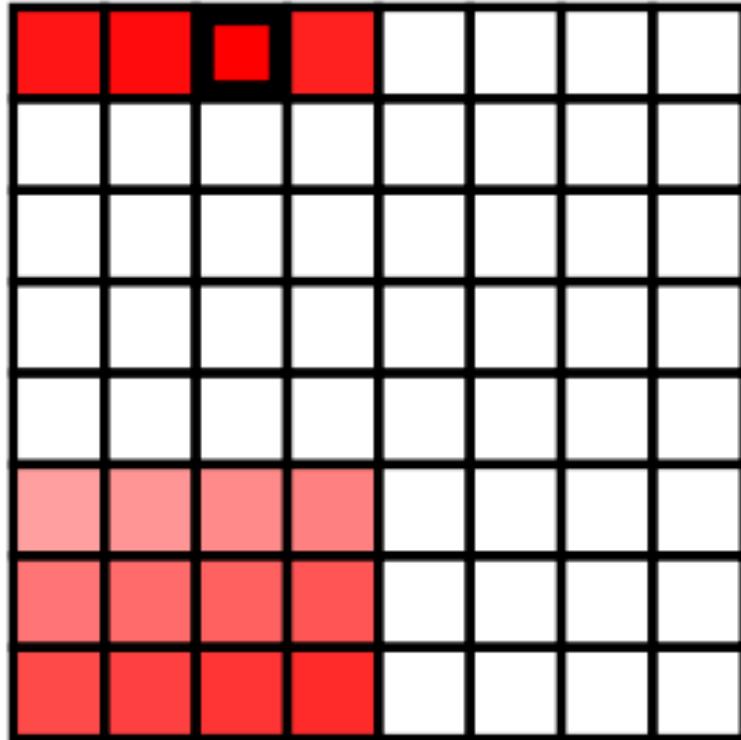
B mem:134 cache hit:112



C mem:134 cache hit:100

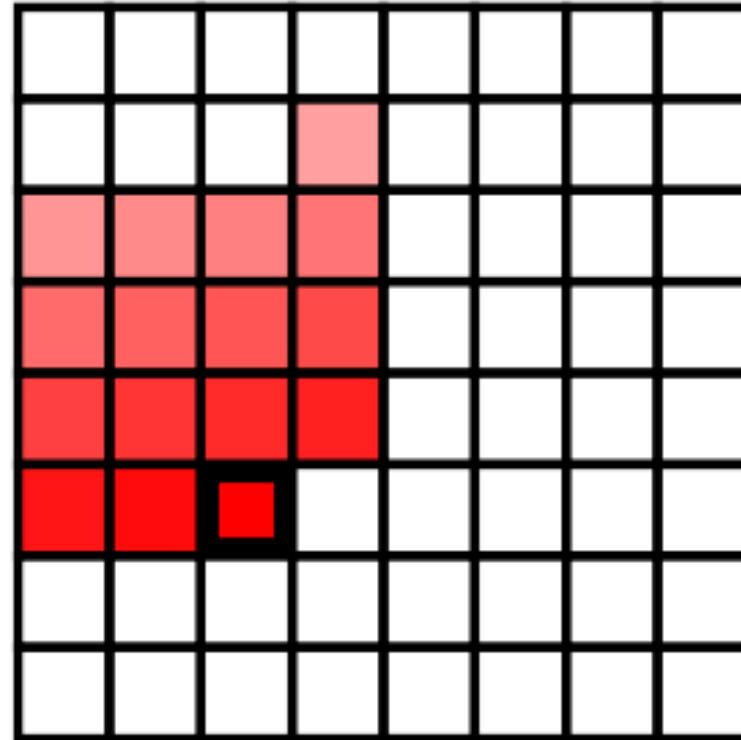


A mem:135 cache hit:99



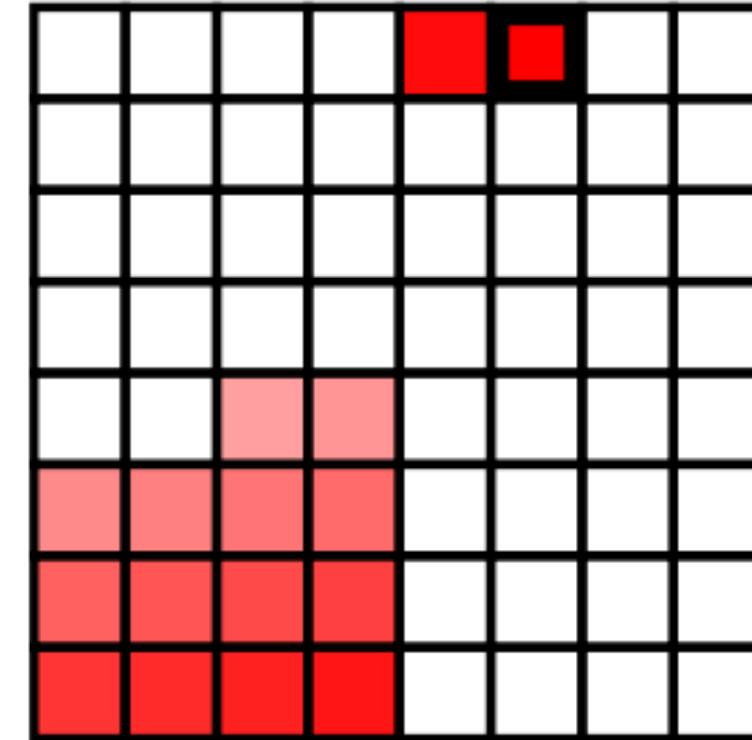
mem:405

B mem:135 cache hit:112

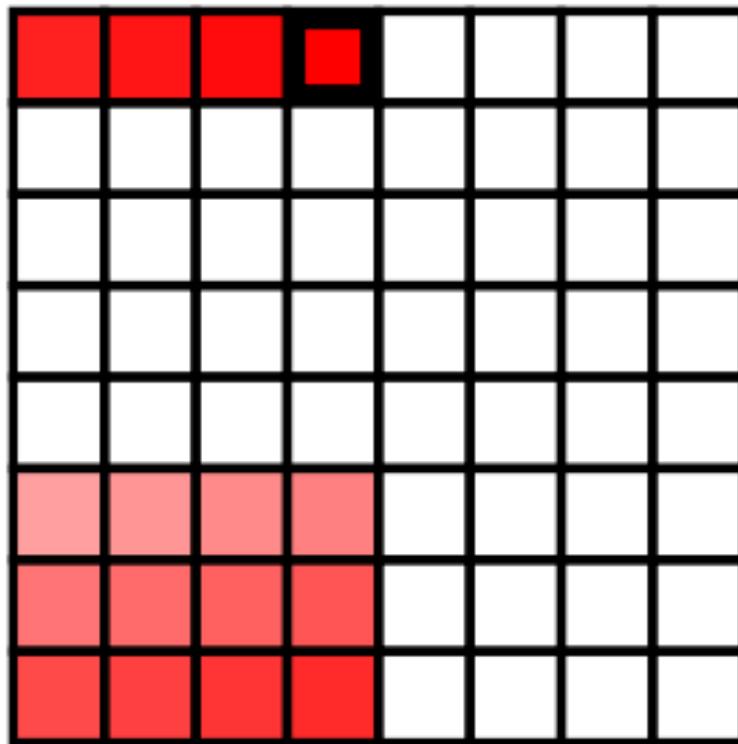


cache hits:312 ≈77%

C mem:135 cache hit:101

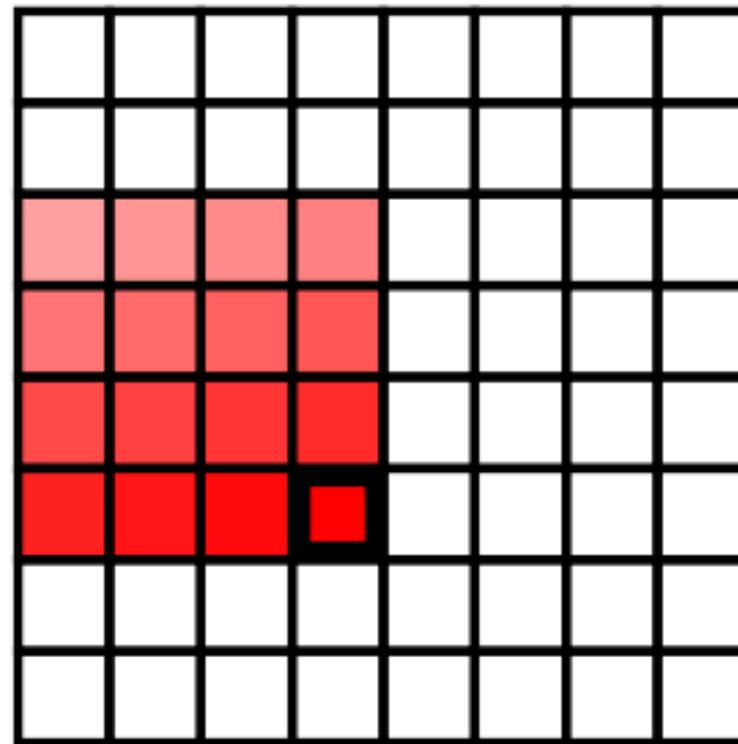


A mem:136 cache hit:100



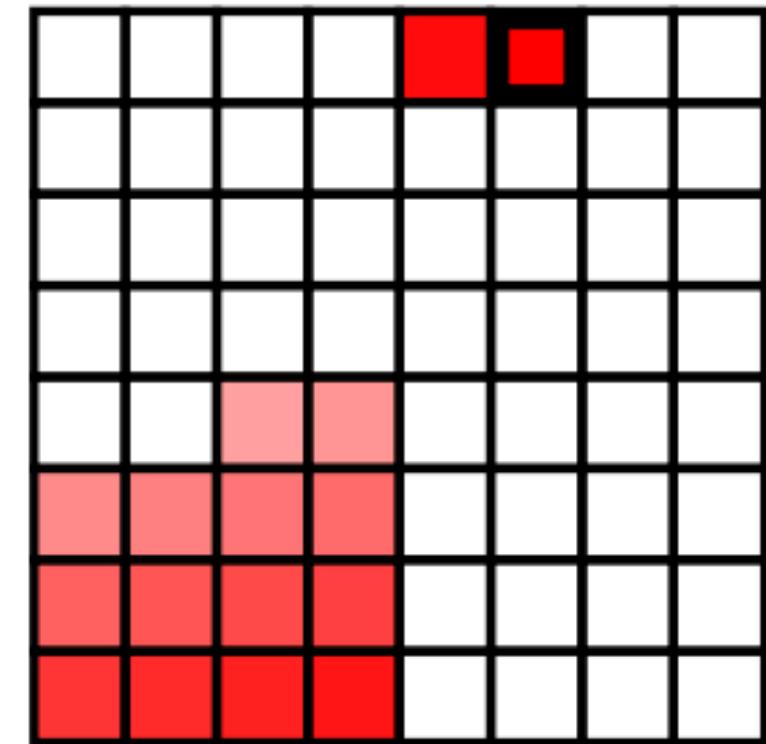
mem:408

B mem:136 cache hit:112

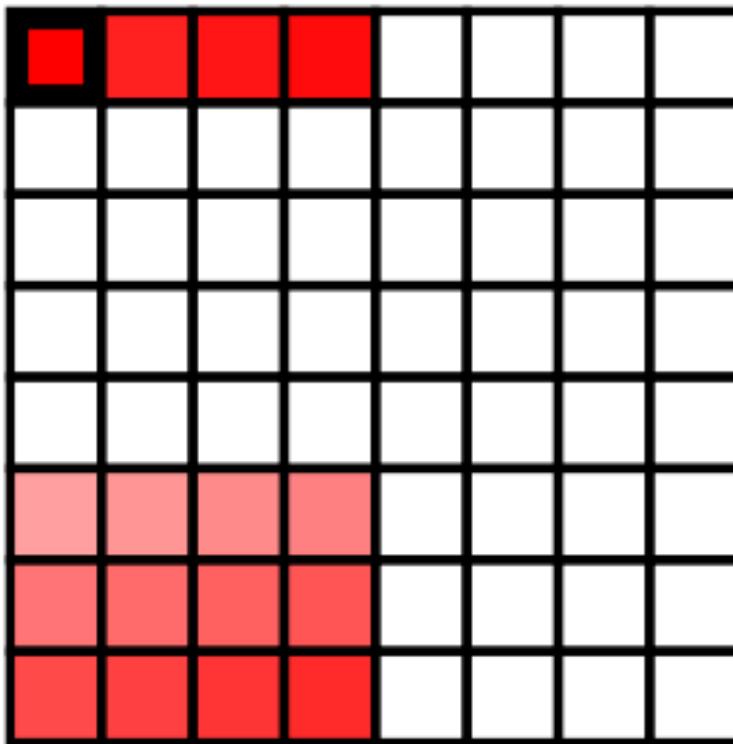


cache hits:314 ≈76%

C mem:136 cache hit:102

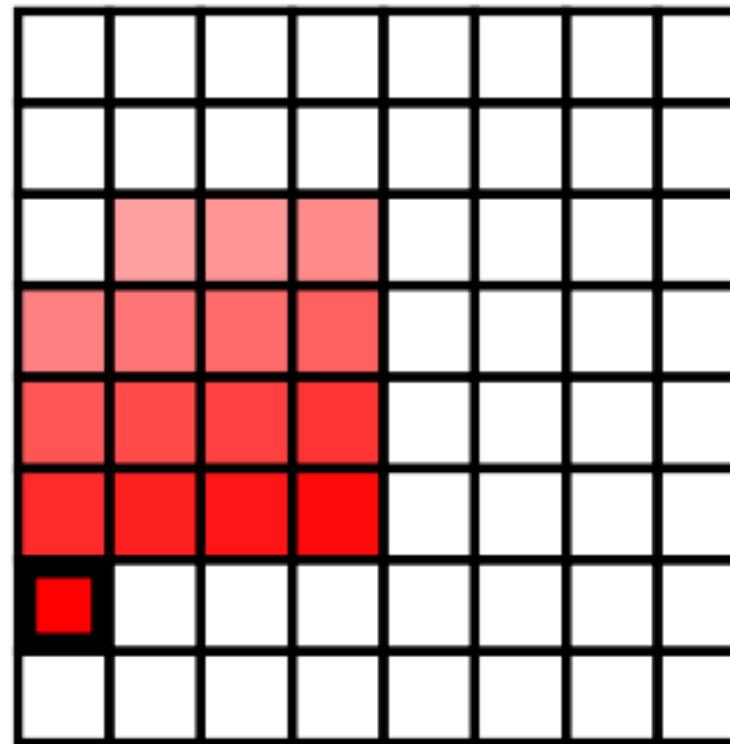


A mem:137 cache hit:101



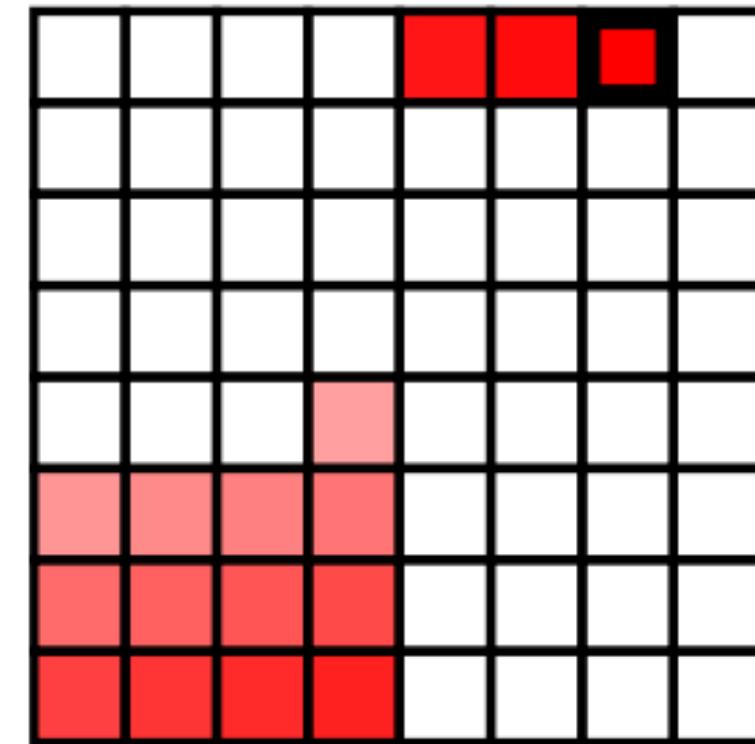
mem:411

B mem:137 cache hit:112

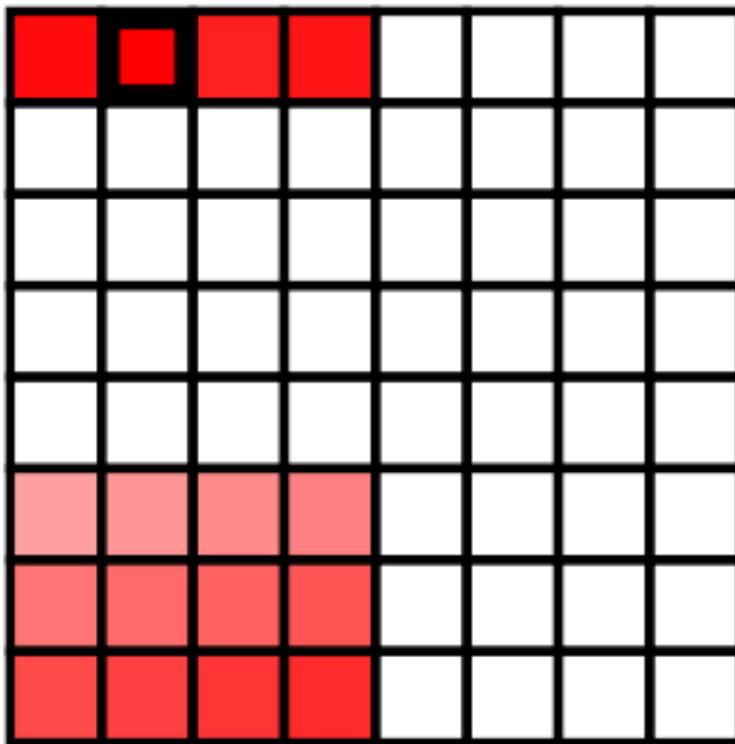


cache hits:315 ≈76%

C mem:137 cache hit:102

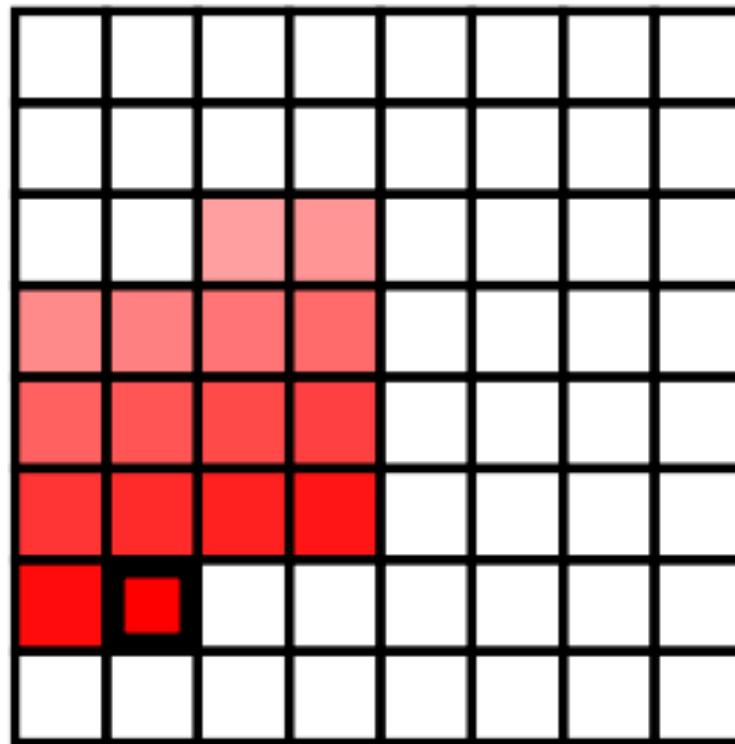


A mem:138 cache hit:102



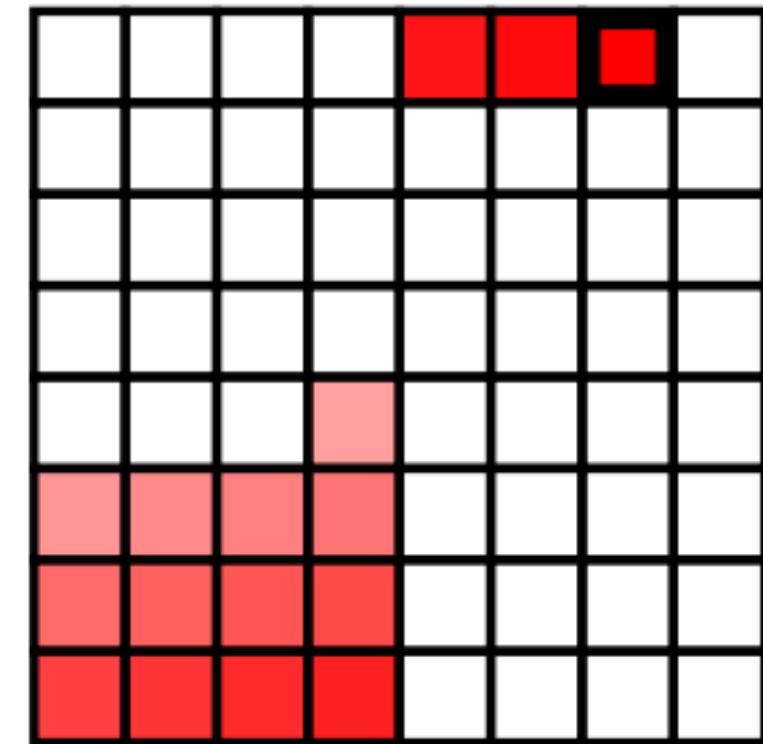
mem:414

B mem:138 cache hit:112

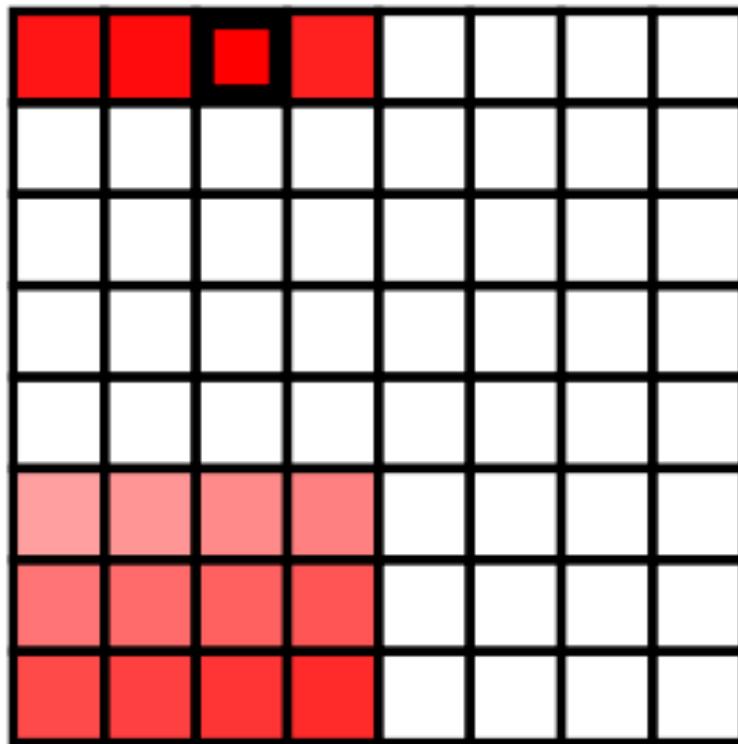


cache hits:317 ≈76%

C mem:138 cache hit:103

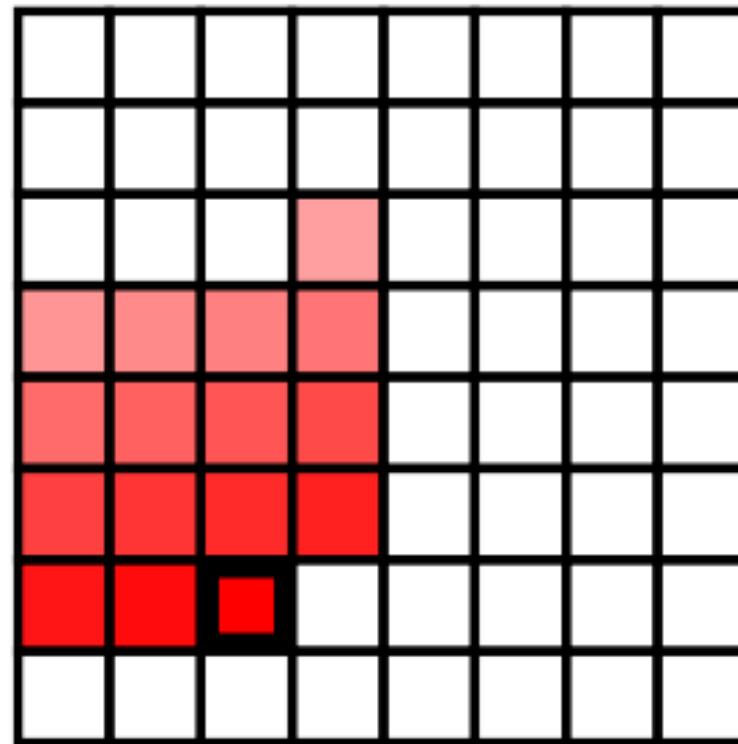


A mem:139 cache hit:103



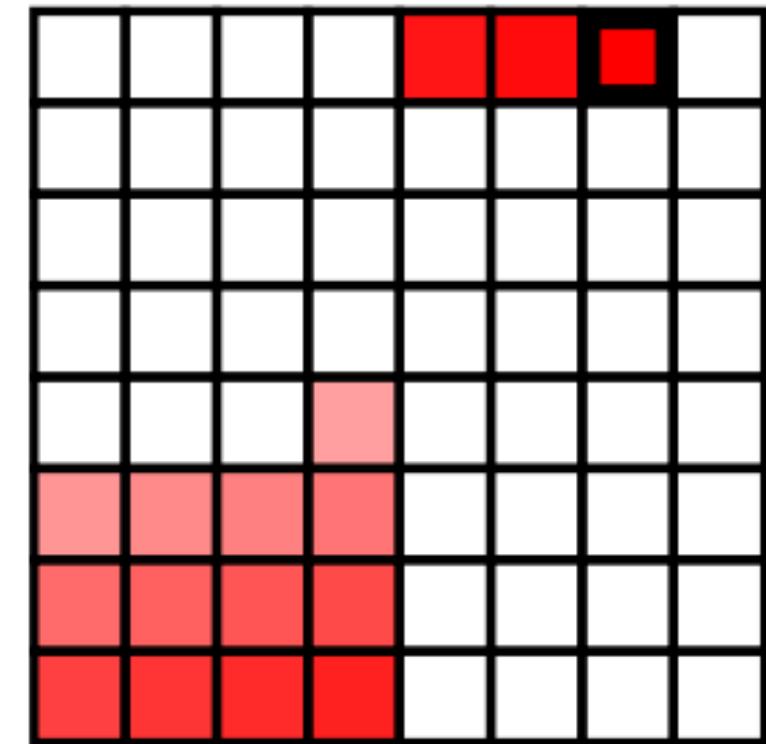
mem:417

B mem:139 cache hit:112

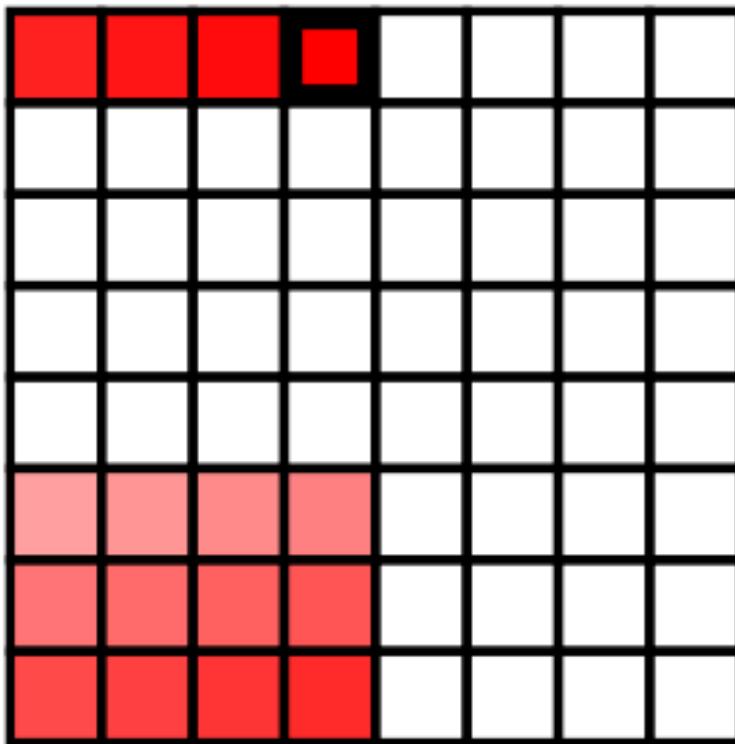


cache hits:319 ≈76%

C mem:139 cache hit:104

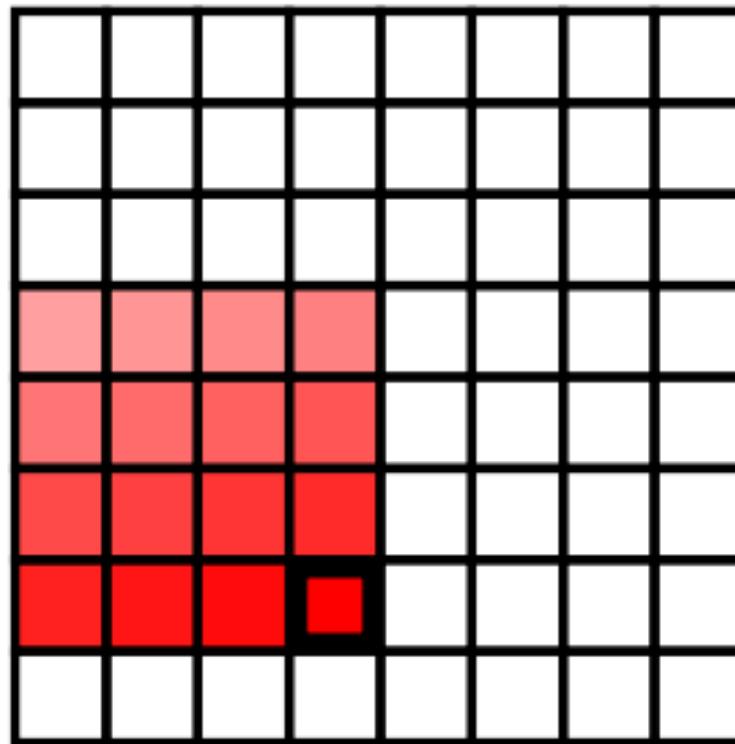


A mem:140 cache hit:104



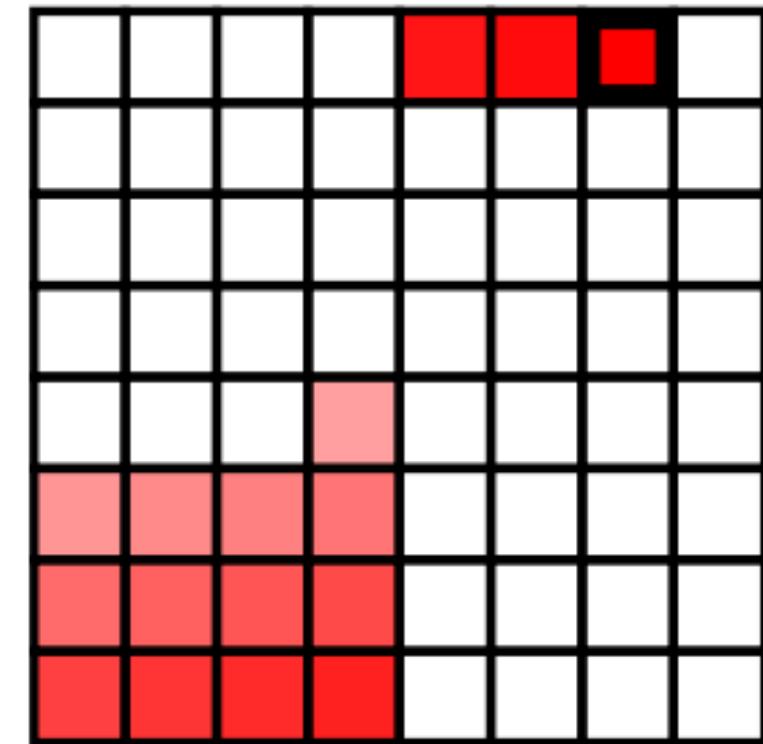
mem:420

B mem:140 cache hit:112

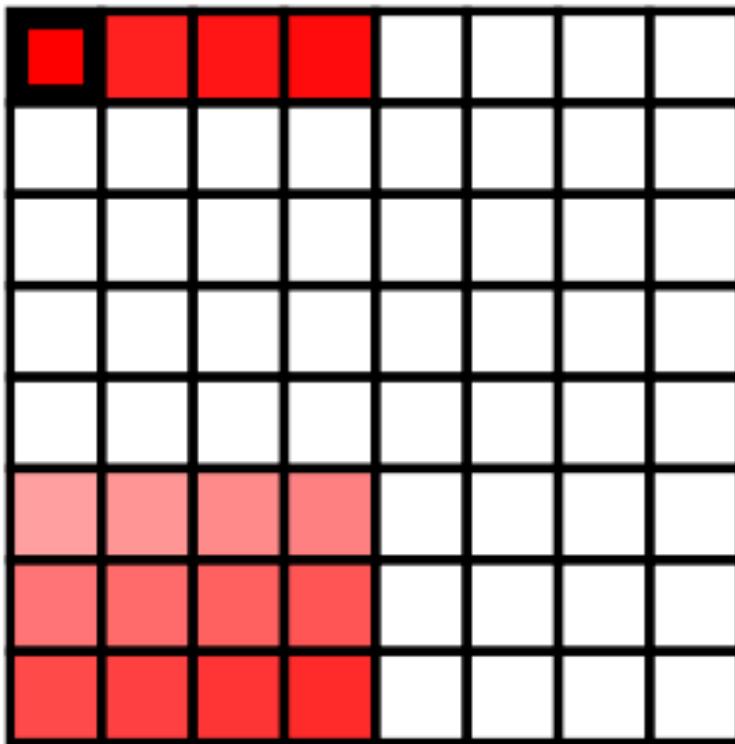


cache hits:321 $\approx 76\%$

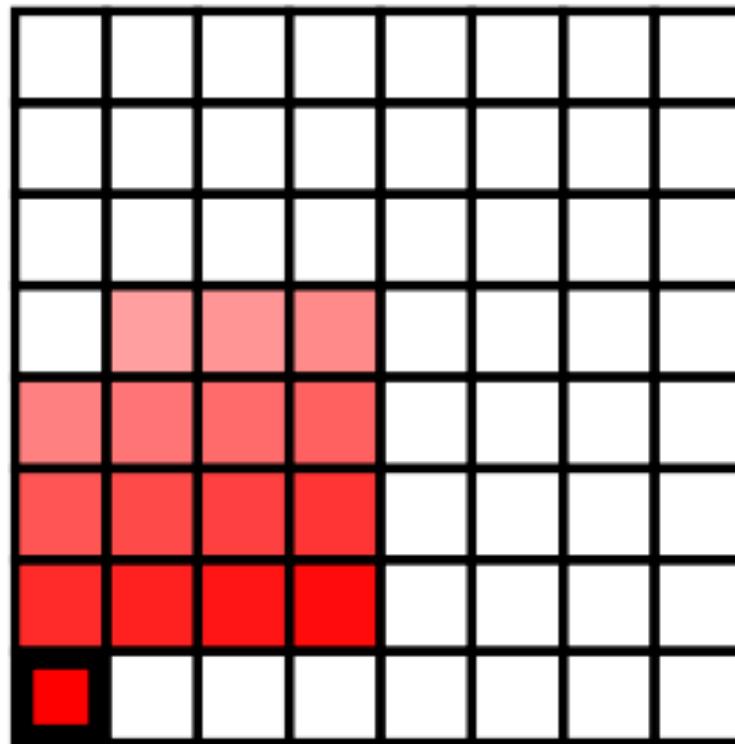
C mem:140 cache hit:105



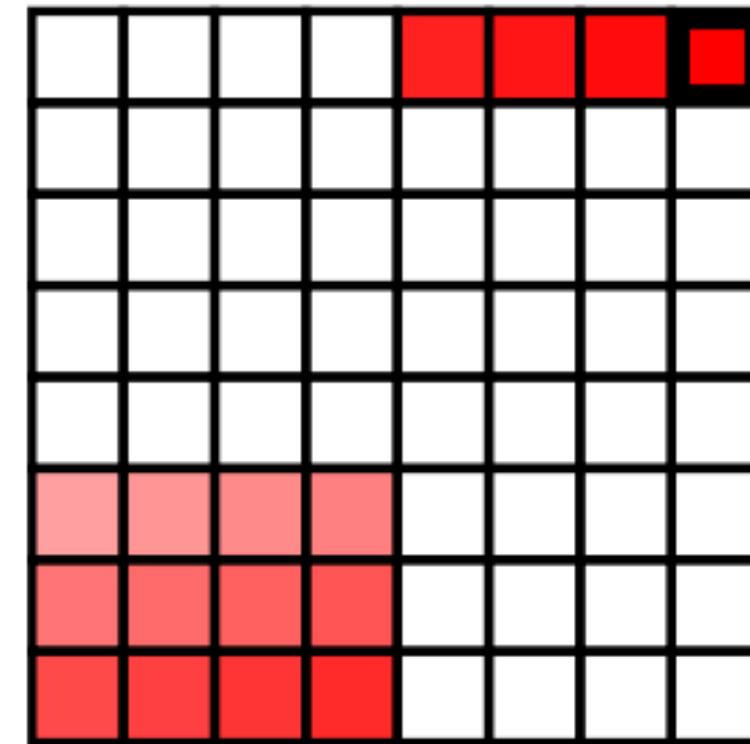
A mem:141 cache hit:105



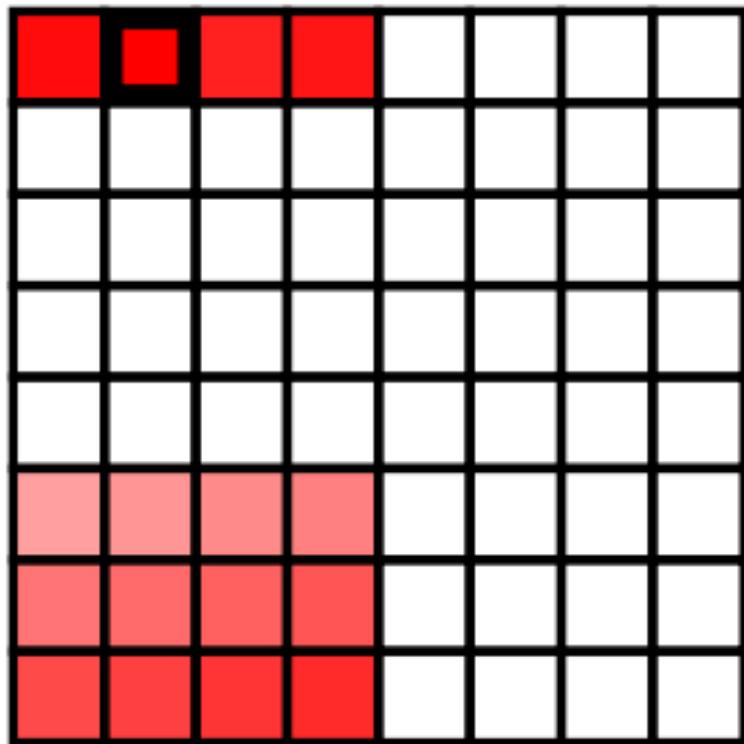
B mem:141 cache hit:112



C mem:141 cache hit:105

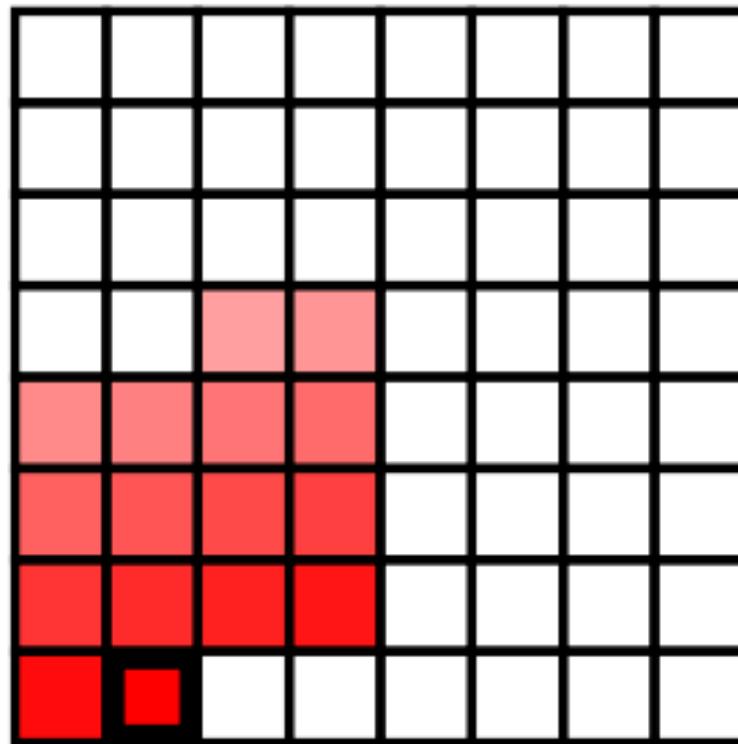


A mem:142 cache hit:106



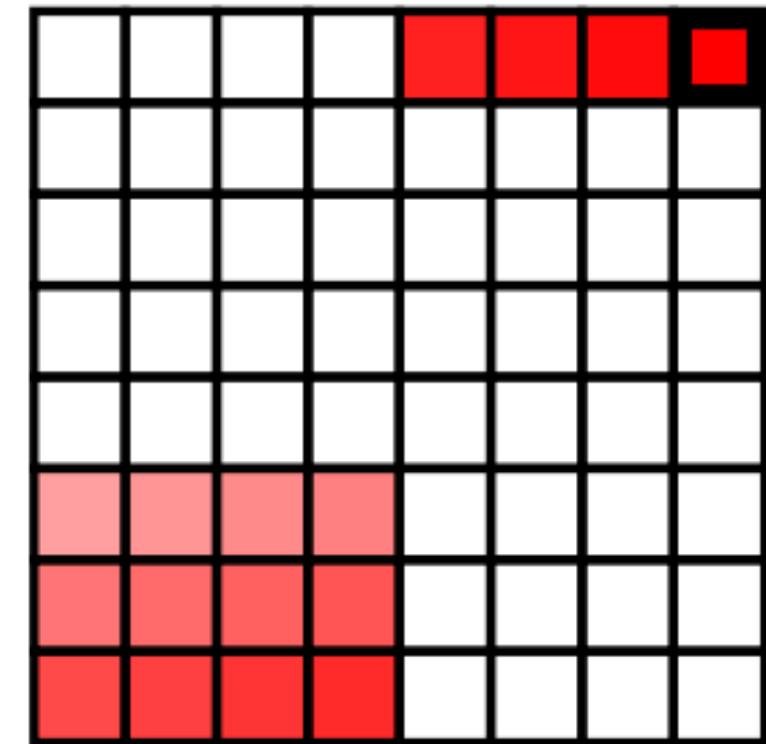
mem:426

B mem:142 cache hit:112

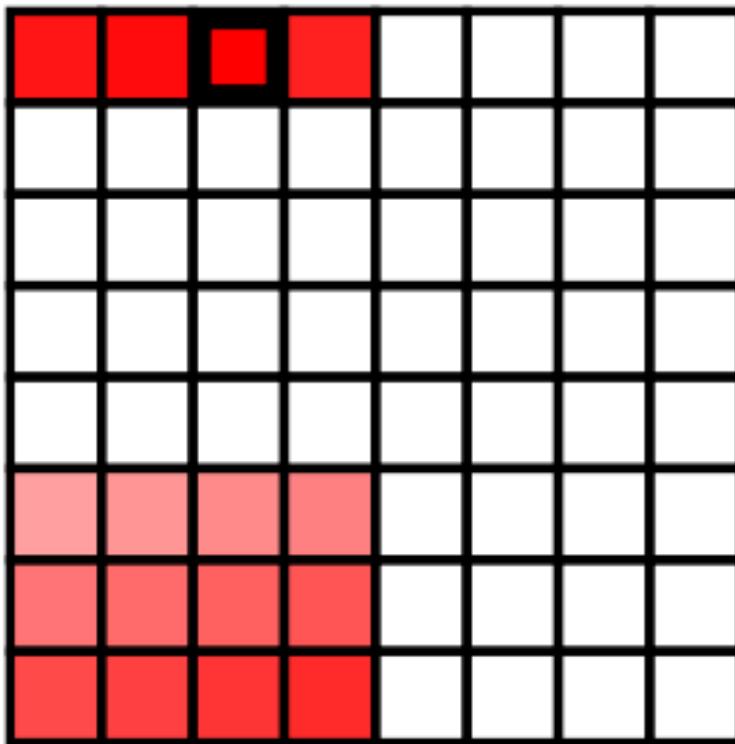


cache hits:324 ≈76%

C mem:142 cache hit:106

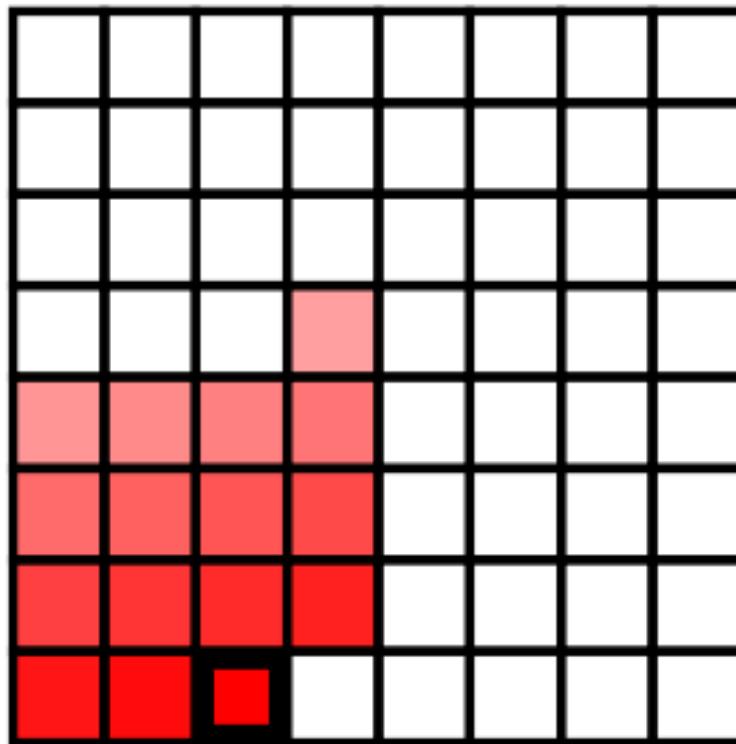


A mem:143 cache hit:107



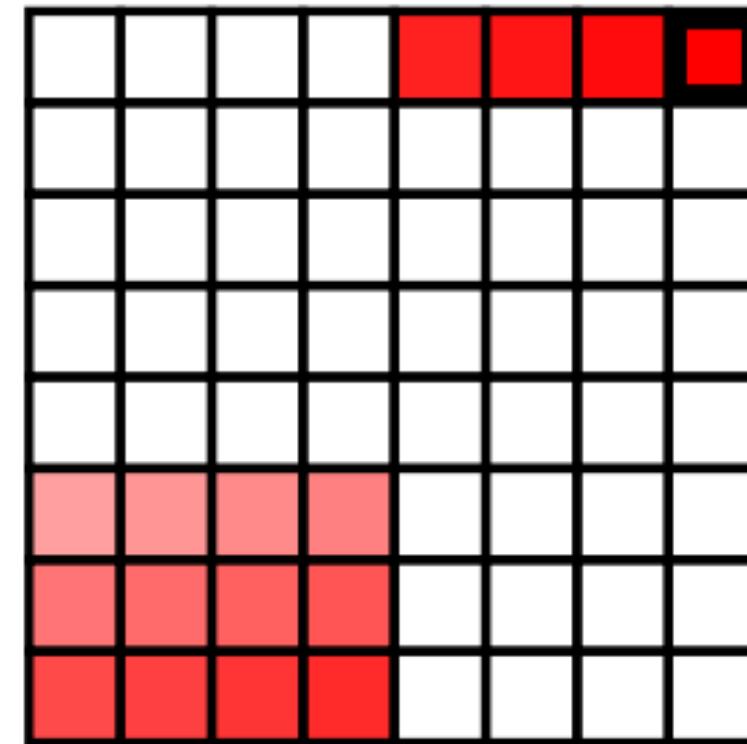
mem:429

B mem:143 cache hit:112

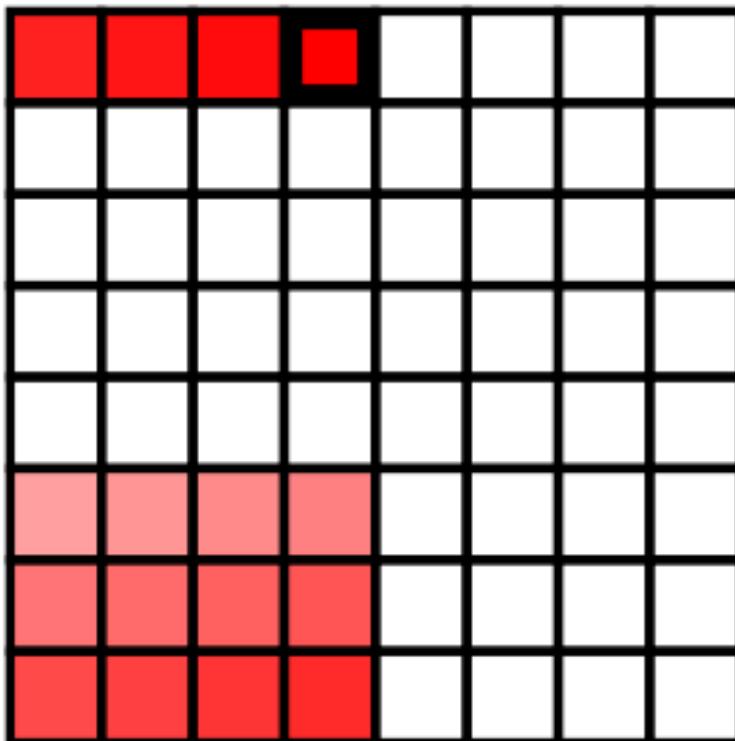


cache hits:326 ≈75%

C mem:143 cache hit:107

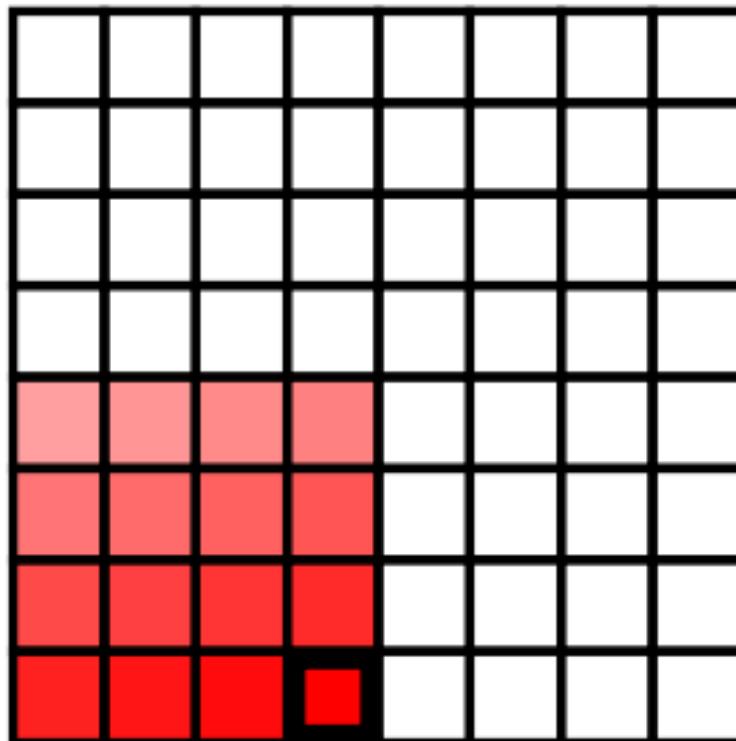


A mem:144 cache hit:108



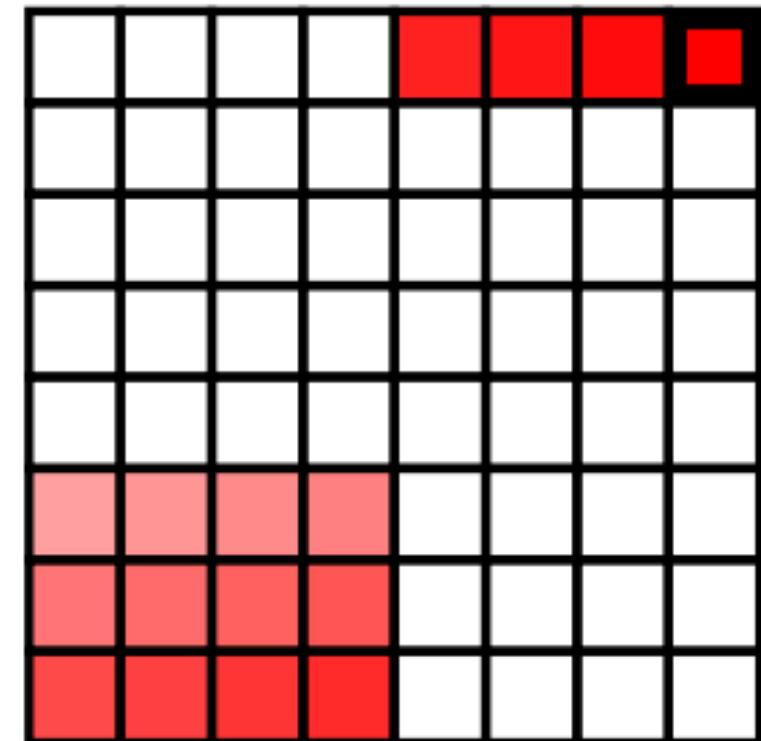
mem:432

B mem:144 cache hit:112

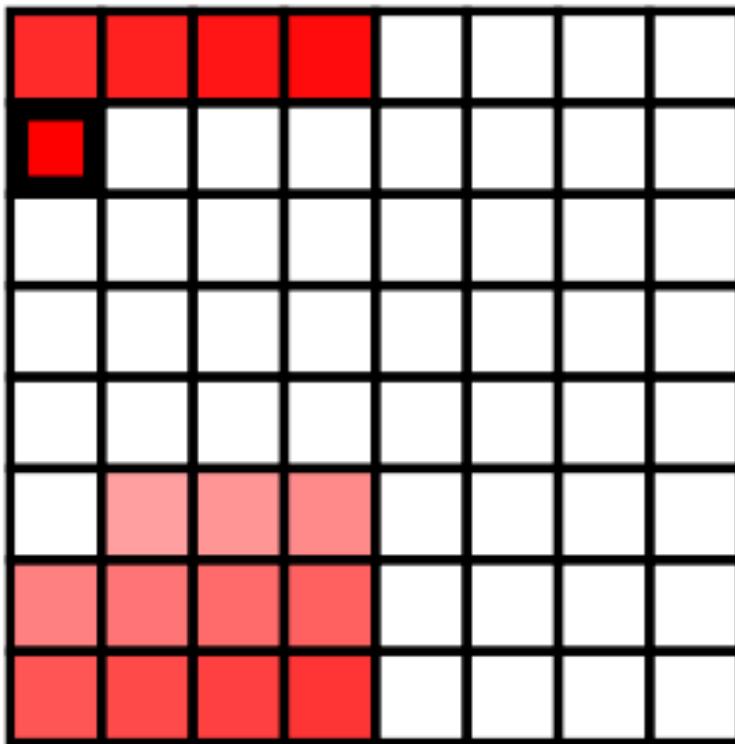


cache hits:328 ≈75%

C mem:144 cache hit:108

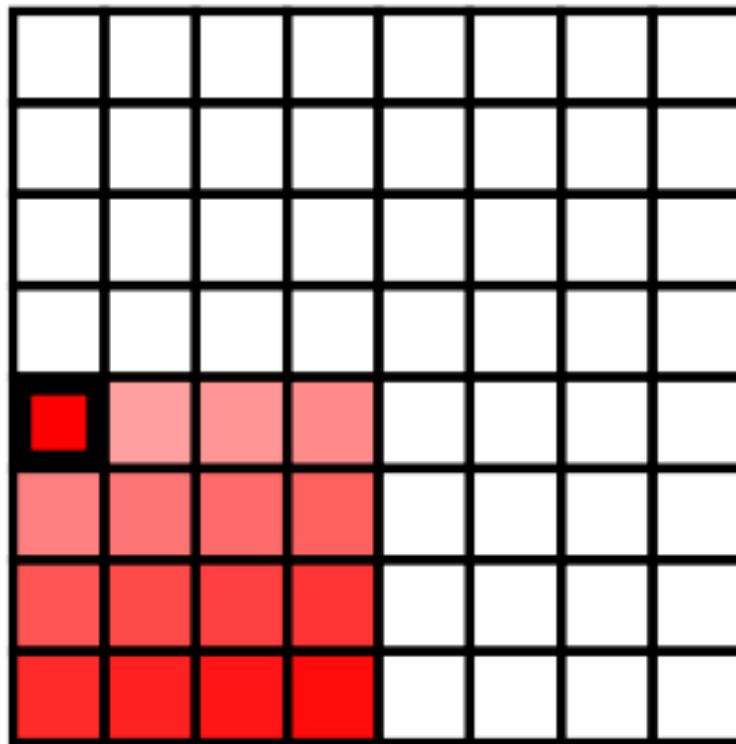


A mem:145 cache hit:108



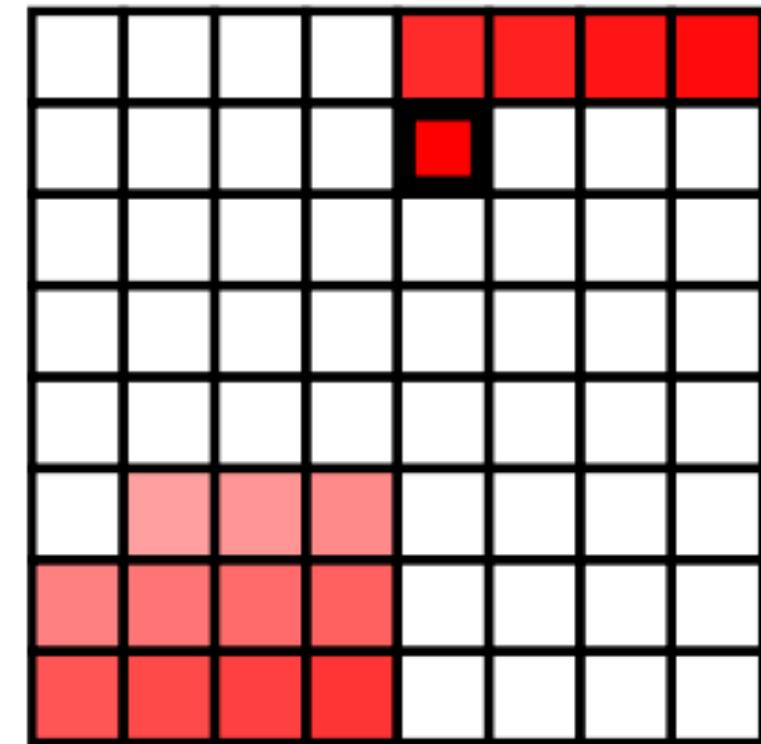
mem:435

B mem:145 cache hit:113

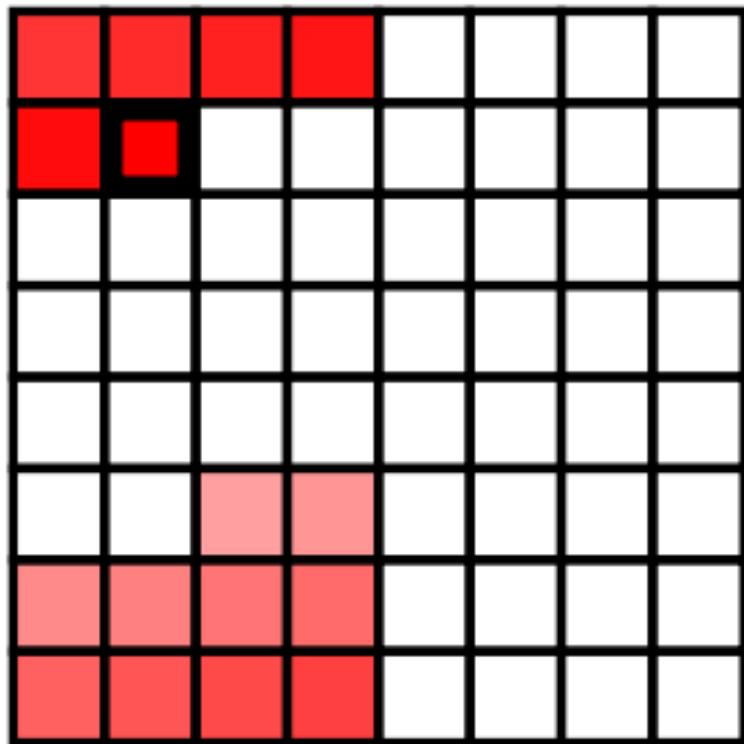


cache hits:329 ≈75%

C mem:145 cache hit:108

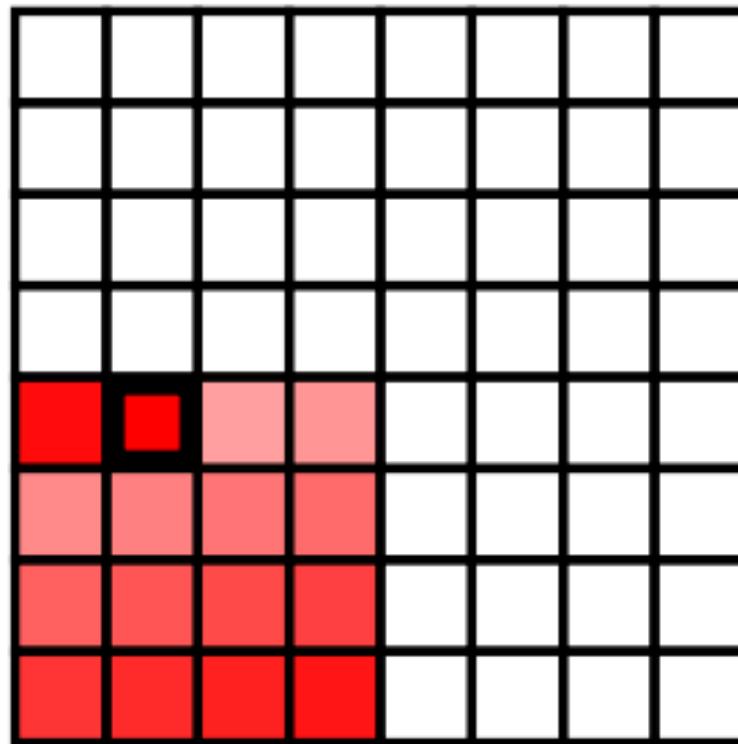


A mem:146 cache hit:108



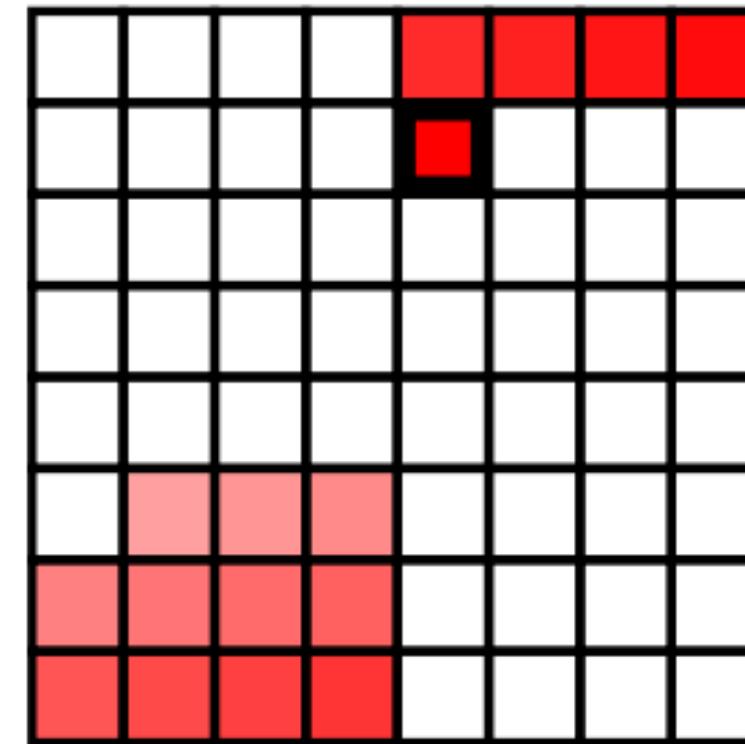
mem:438

B mem:146 cache hit:114

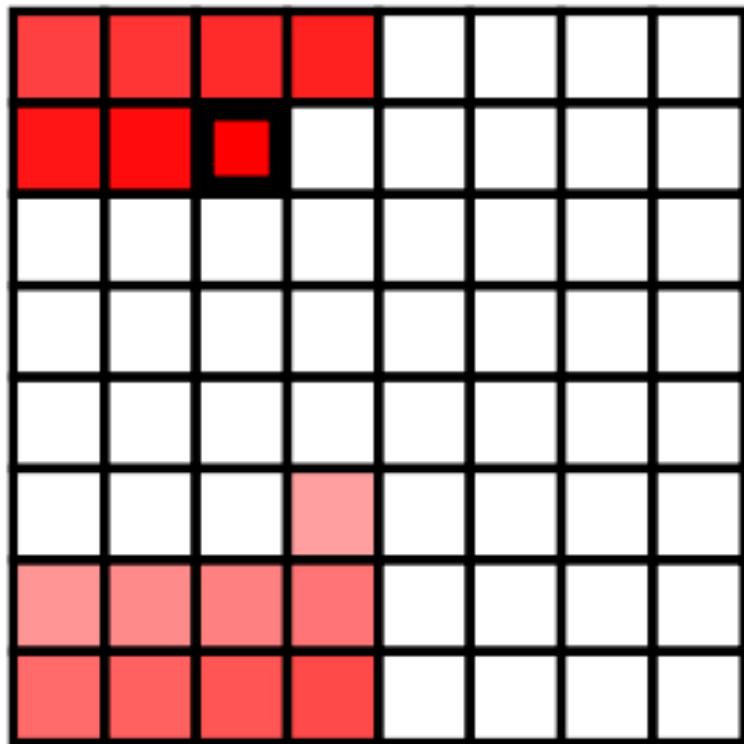


cache hits:331 $\approx 75\%$

C mem:146 cache hit:109

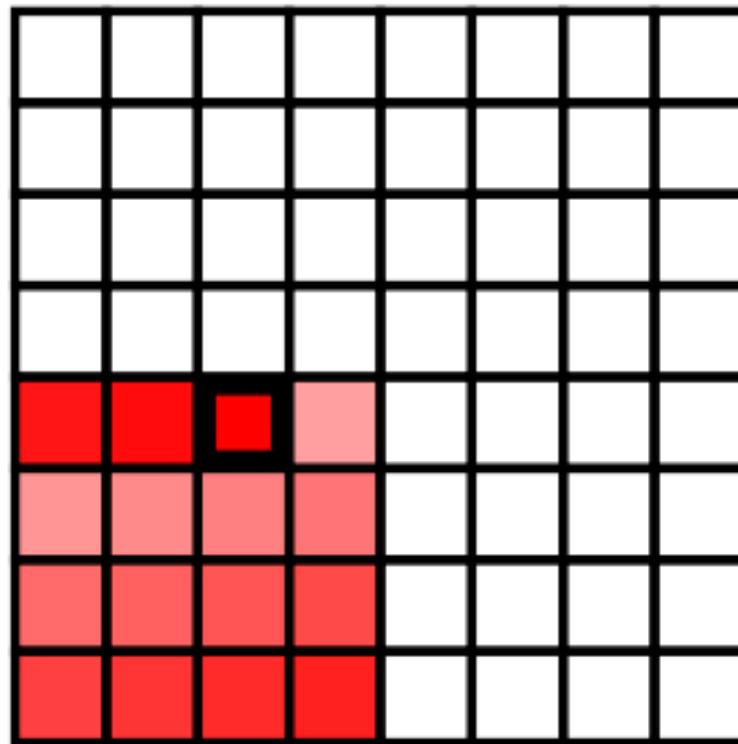


A mem:147 cache hit:108



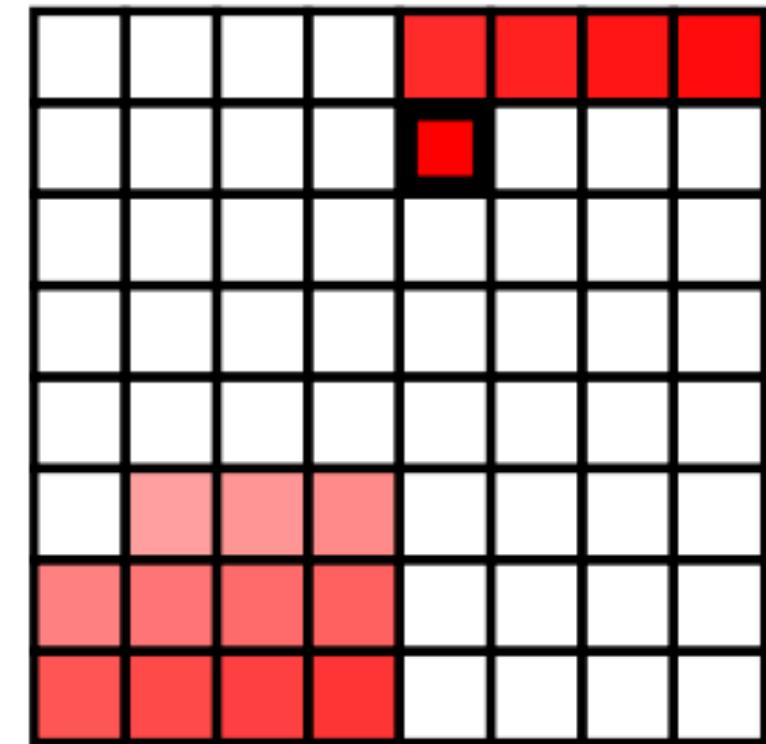
mem:441

B mem:147 cache hit:115

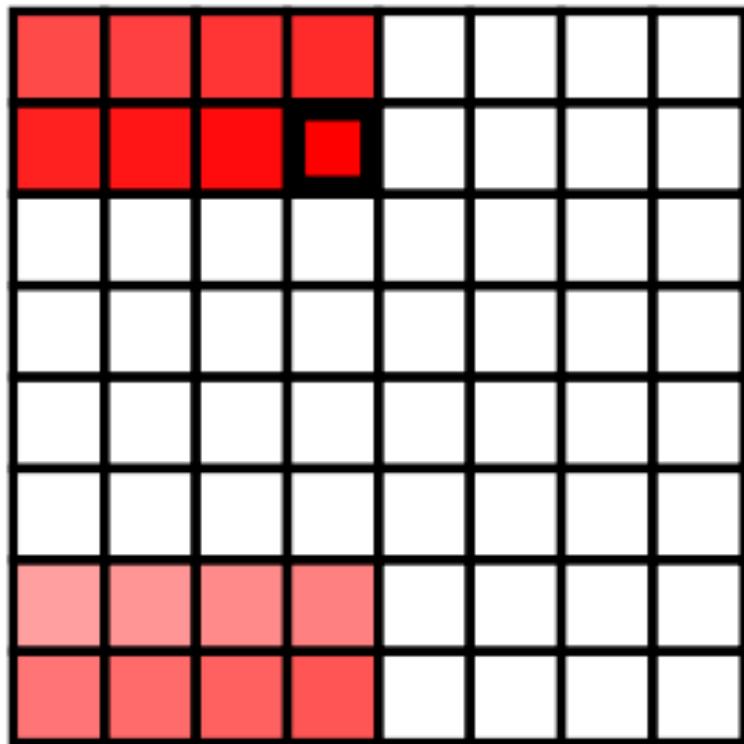


cache hits:333 $\approx 75\%$

C mem:147 cache hit:110

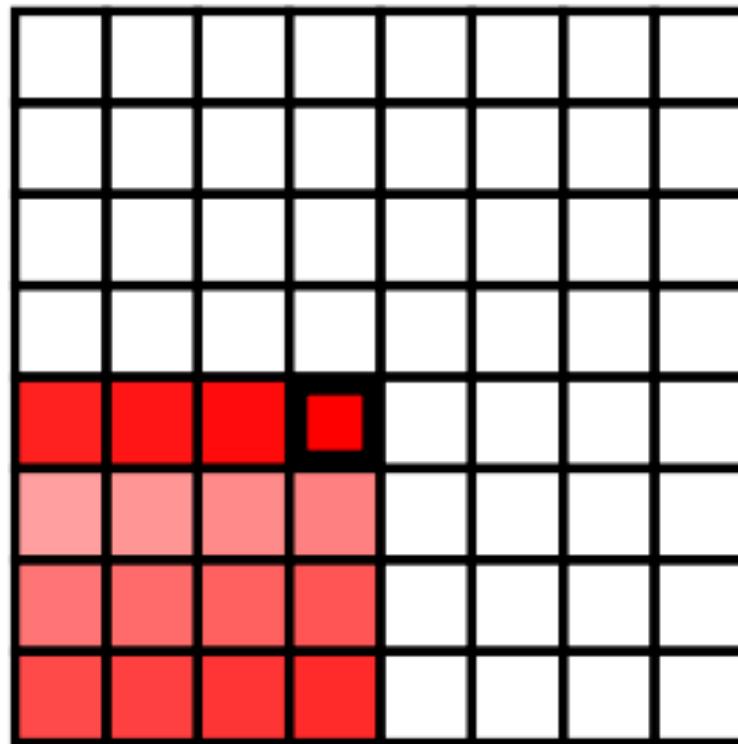


A mem:148 cache hit:108



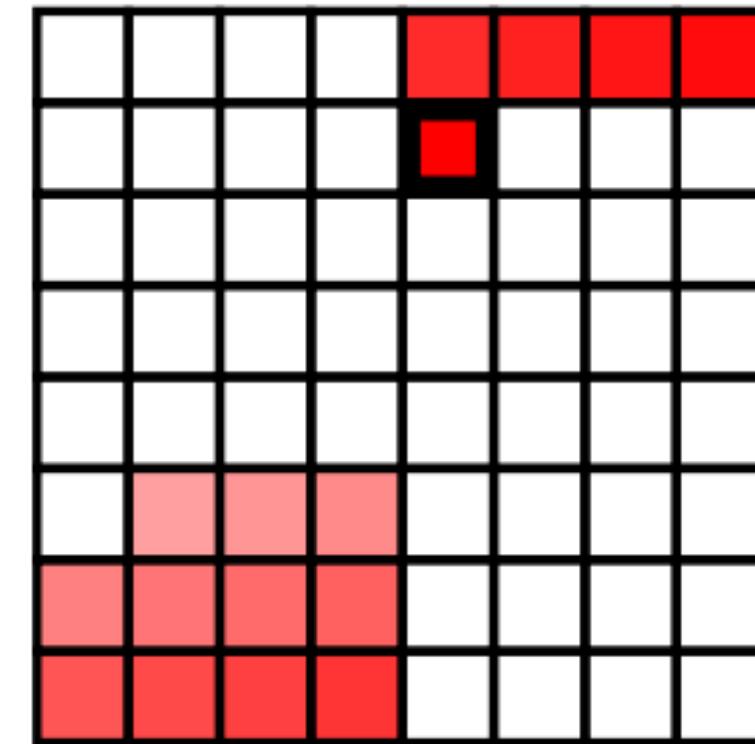
mem:444

B mem:148 cache hit:116

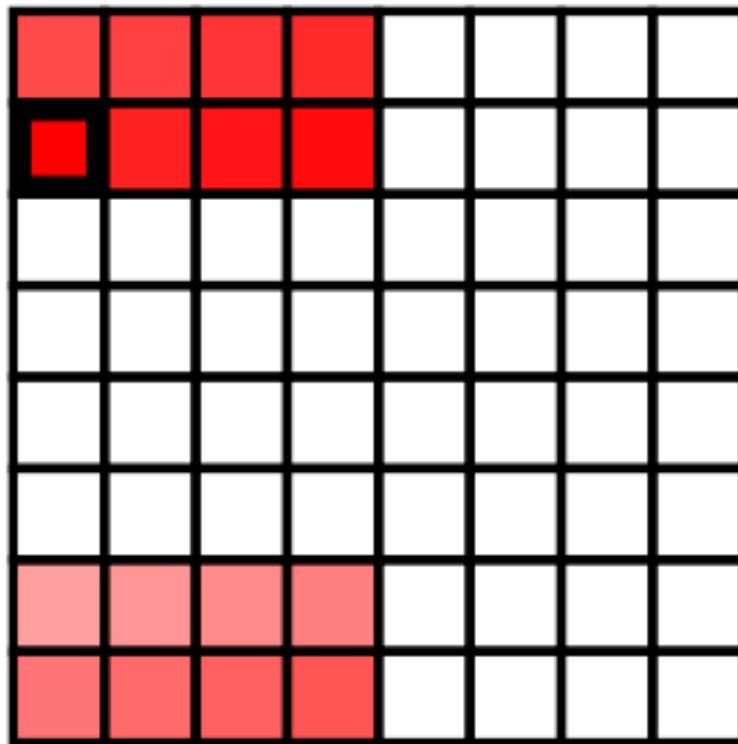


cache hits:335 $\approx 75\%$

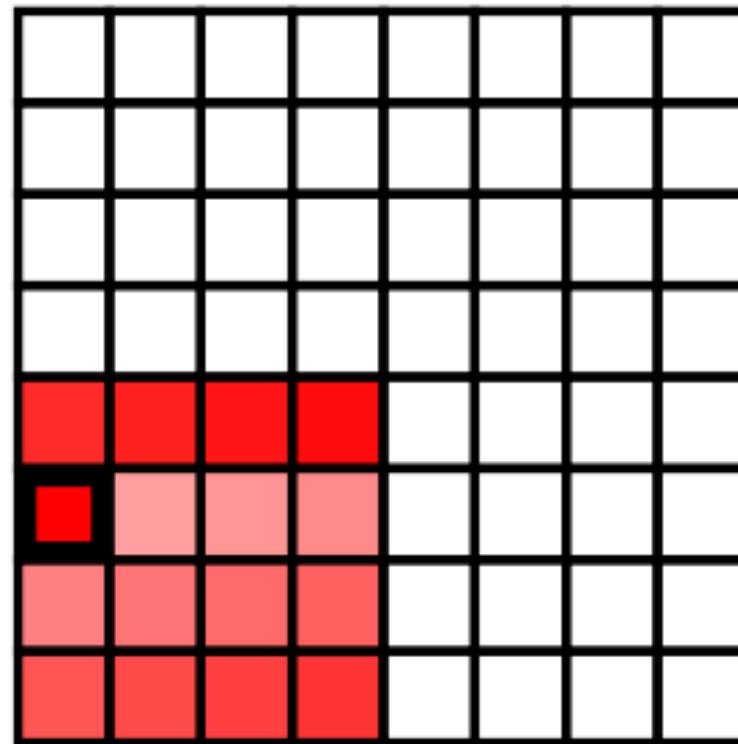
C mem:148 cache hit:111



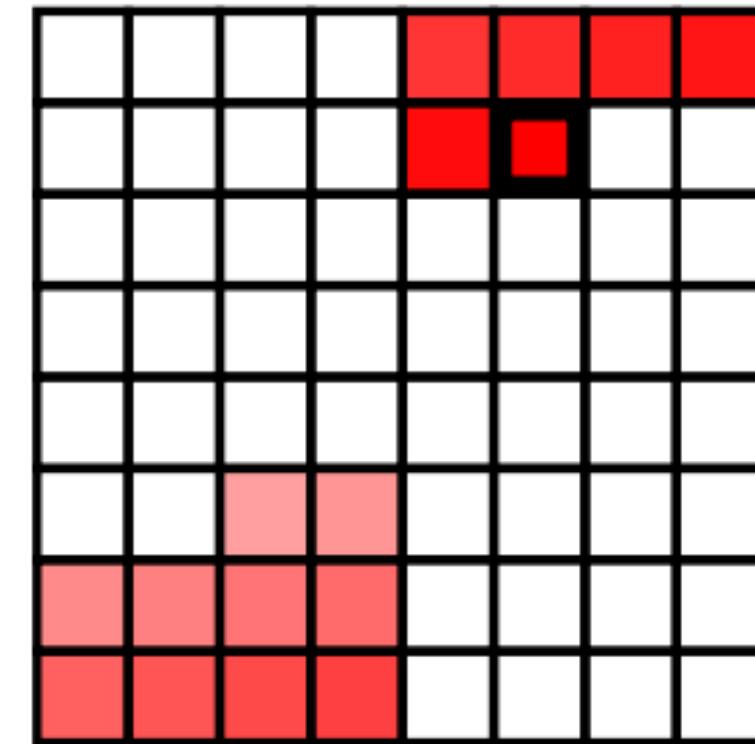
A mem:149 cache hit:109



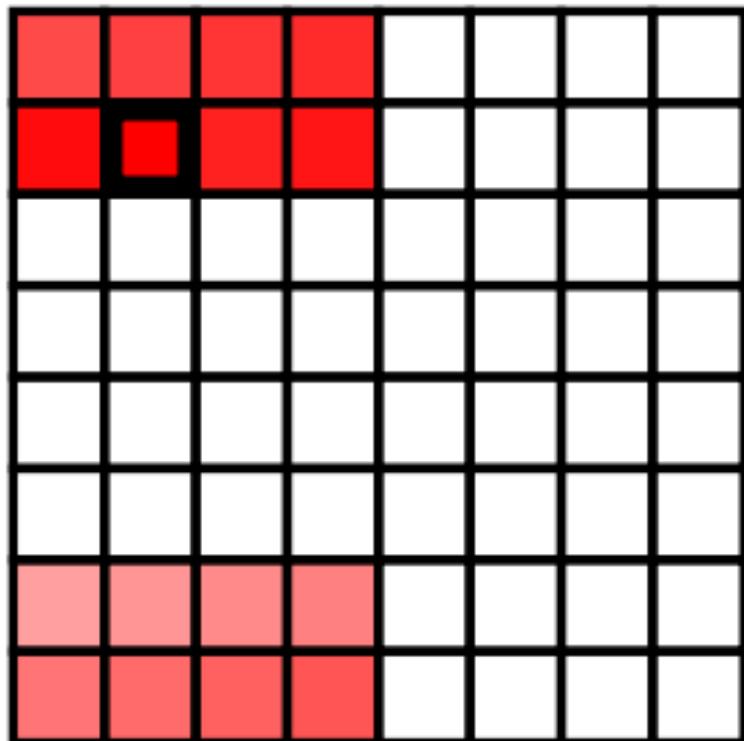
B mem:149 cache hit:117



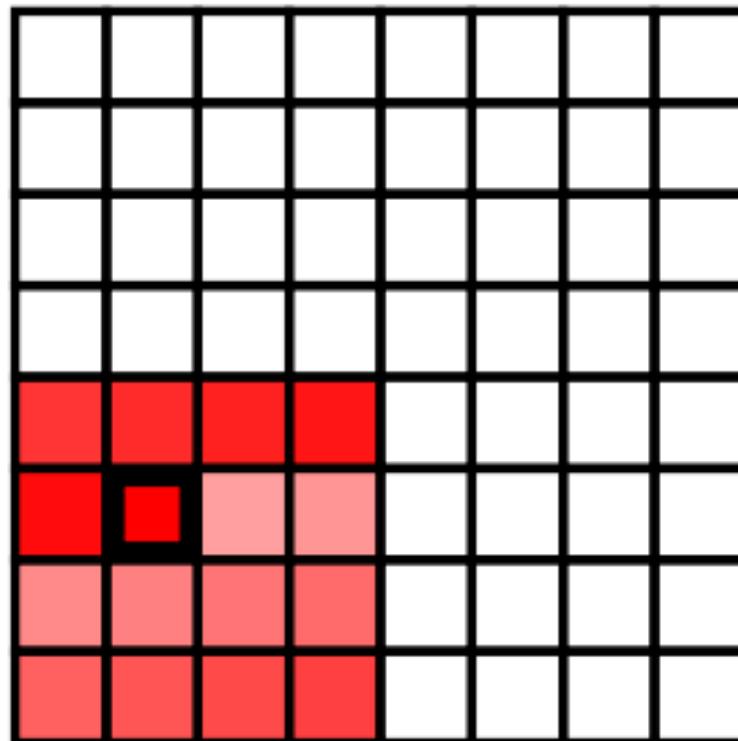
C mem:149 cache hit:111



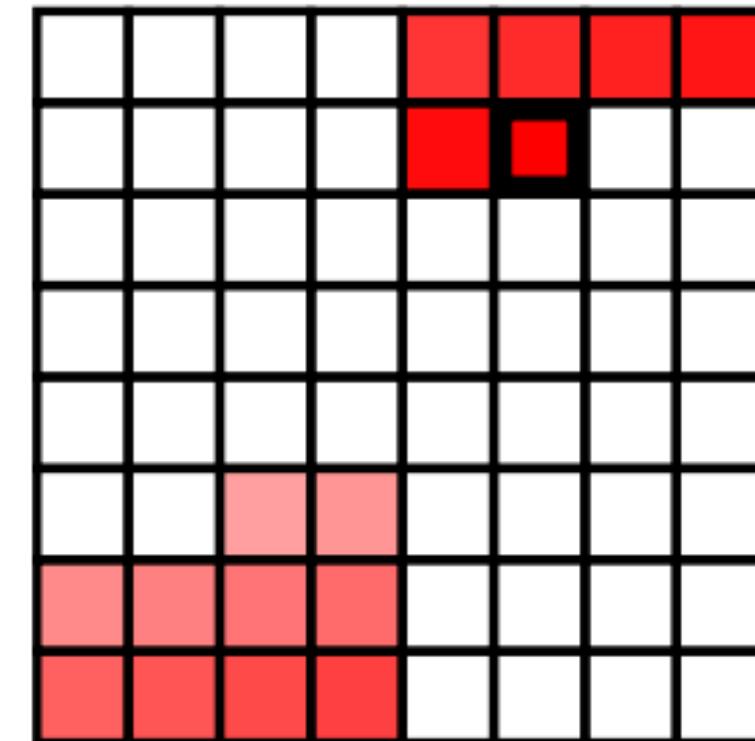
A mem:150 cache hit:110



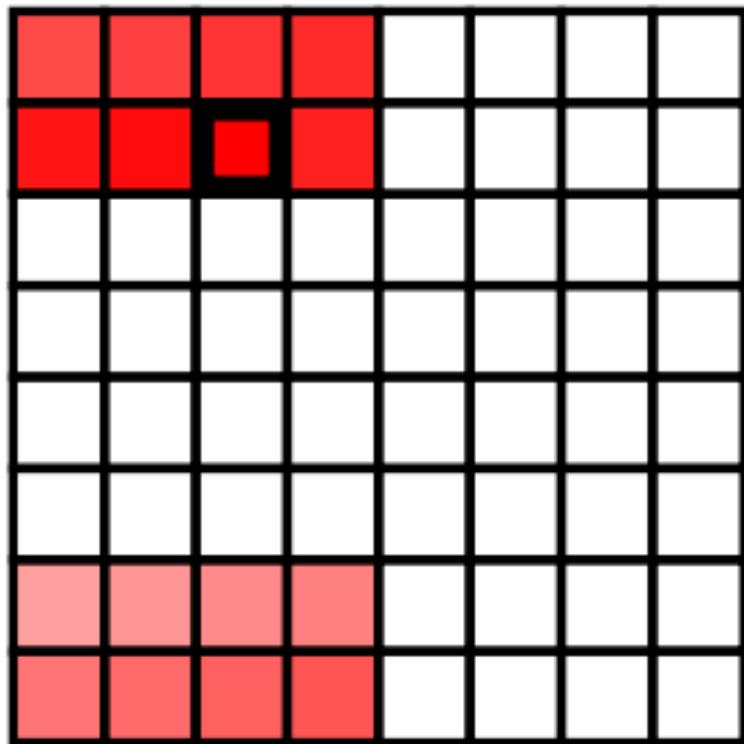
B mem:150 cache hit:118



C mem:150 cache hit:112

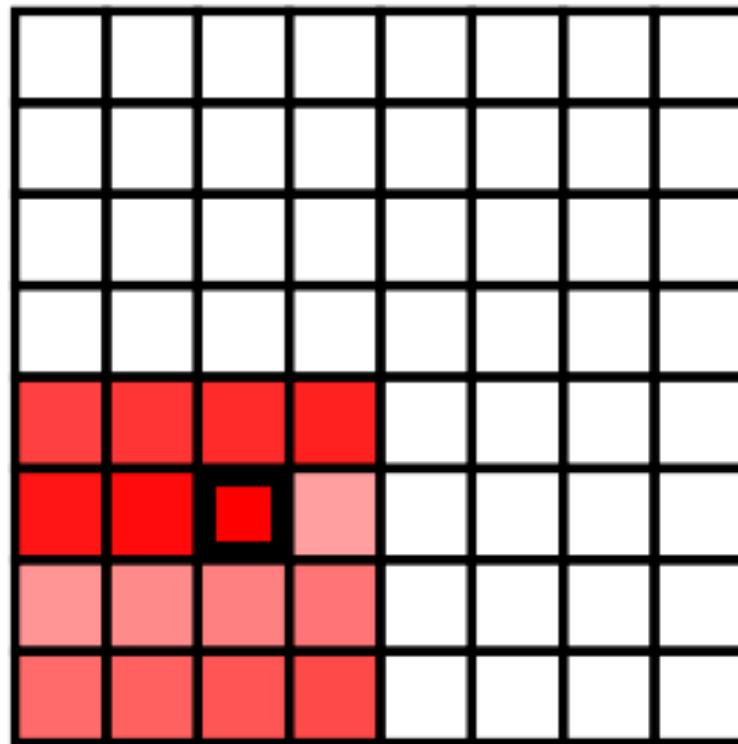


A mem:151 cache hit:111



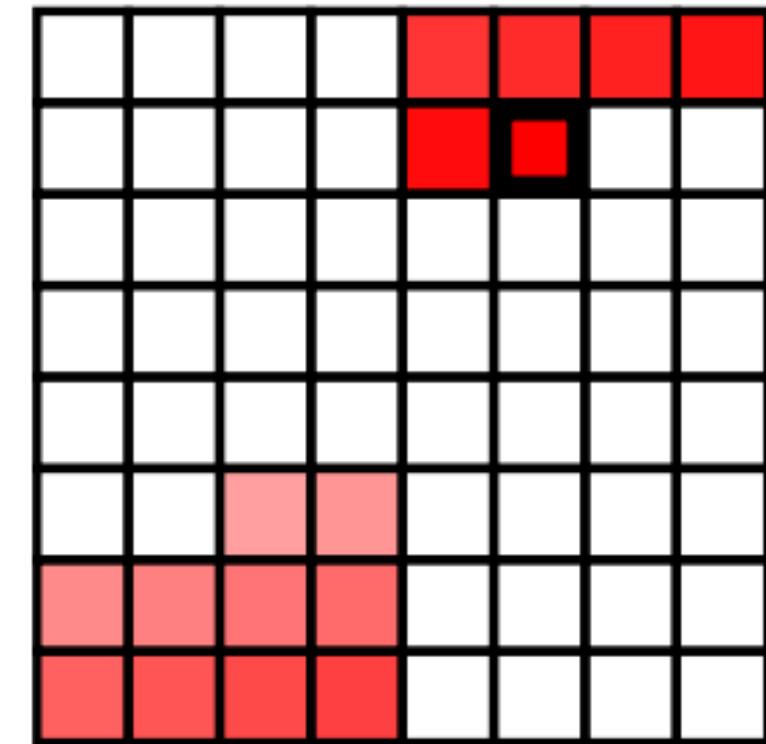
mem:453

B mem:151 cache hit:119

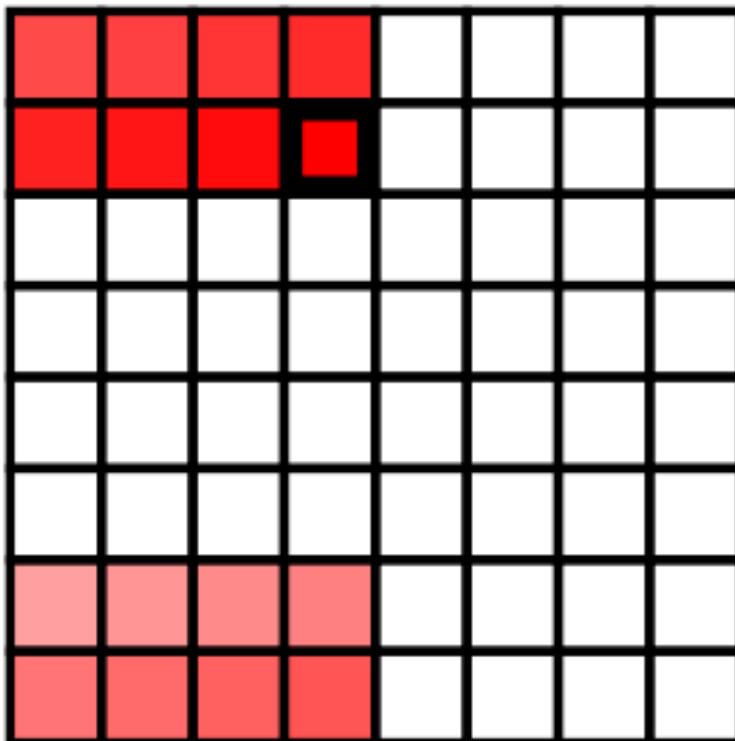


cache hits:343 ≈75%

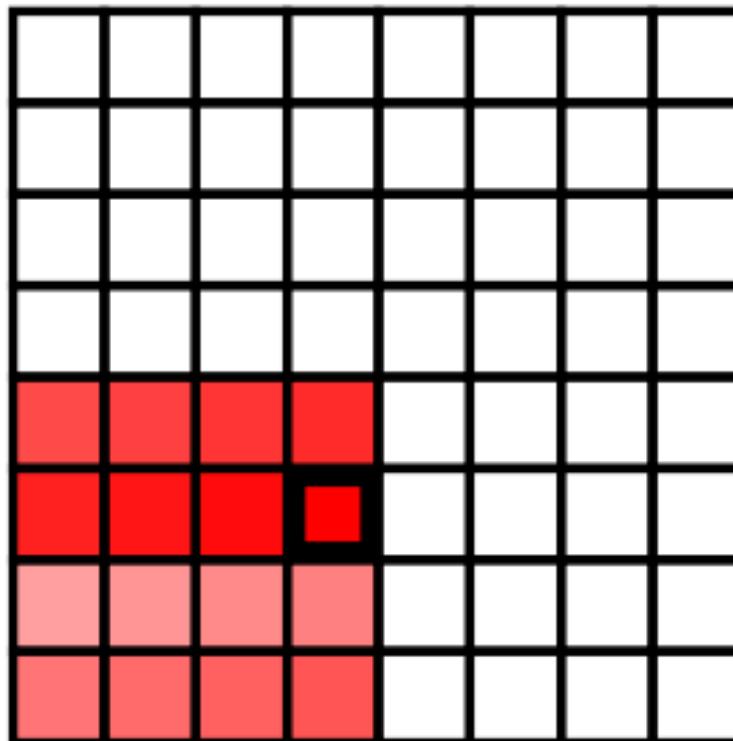
C mem:151 cache hit:113



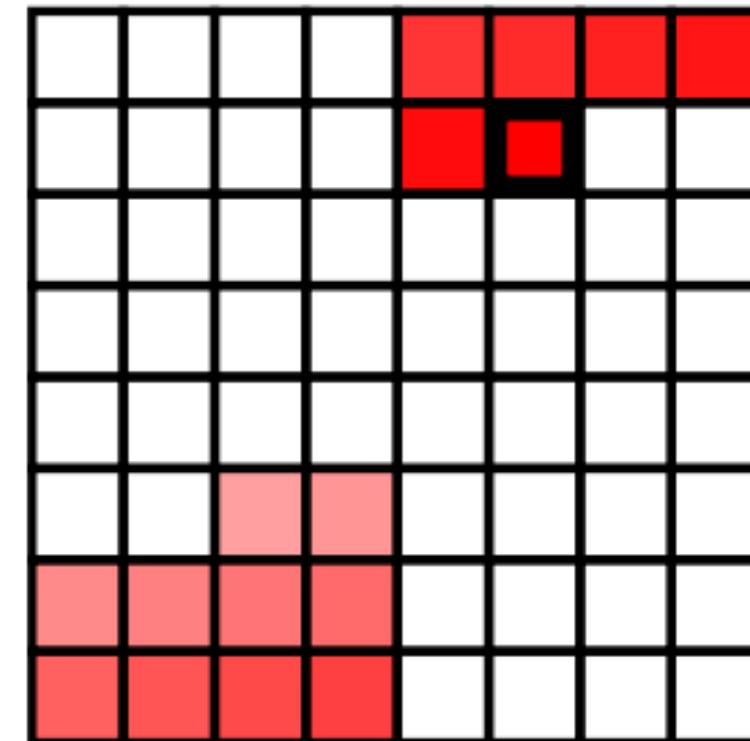
A mem:152 cache hit:112



B mem:152 cache hit:120



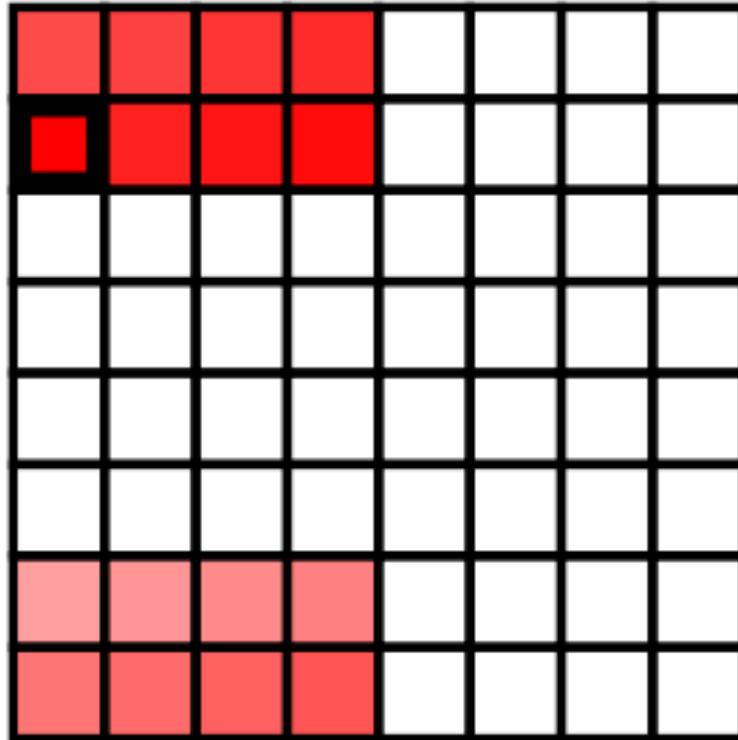
C mem:152 cache hit:114



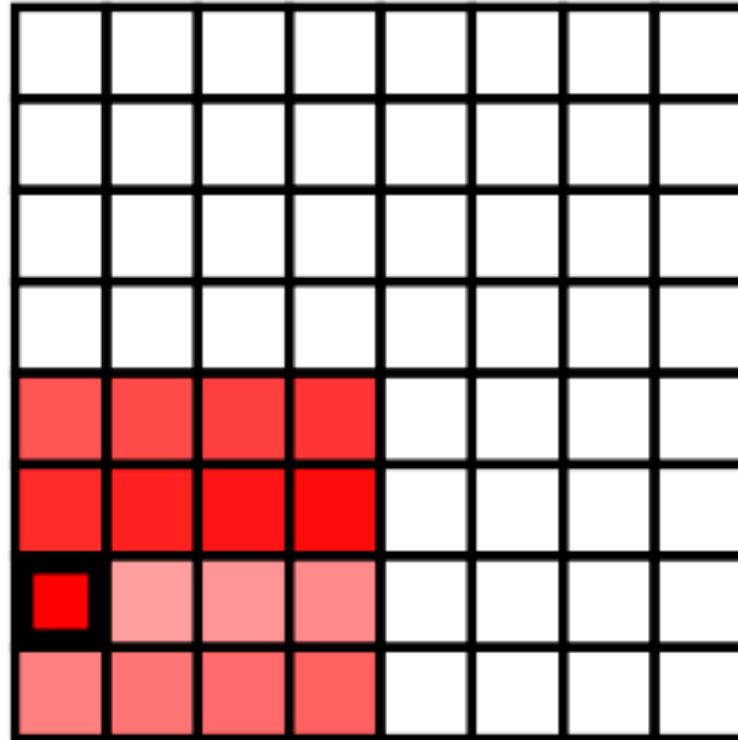
mem:456

cache hits:346 $\approx 75\%$

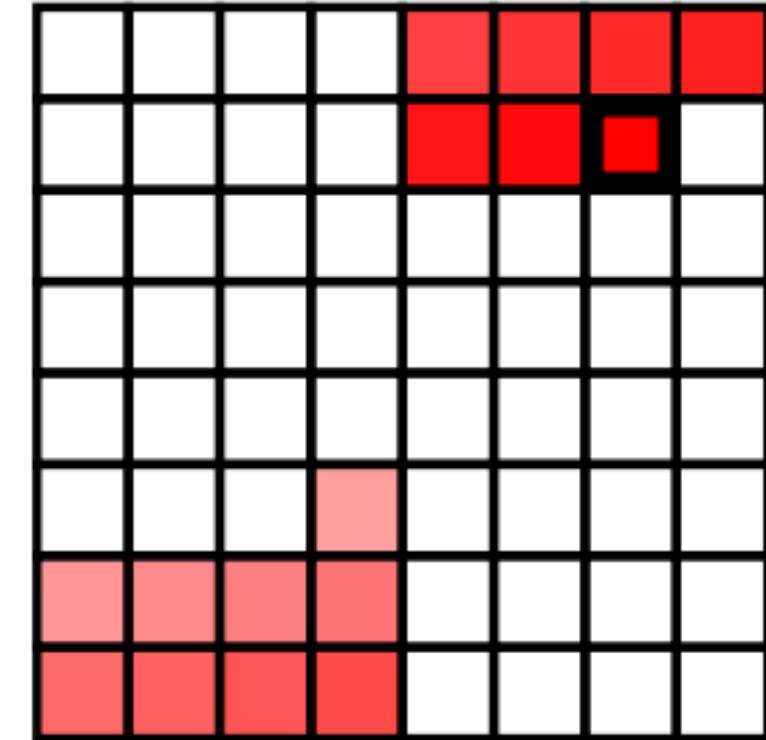
A mem:153 cache hit:113



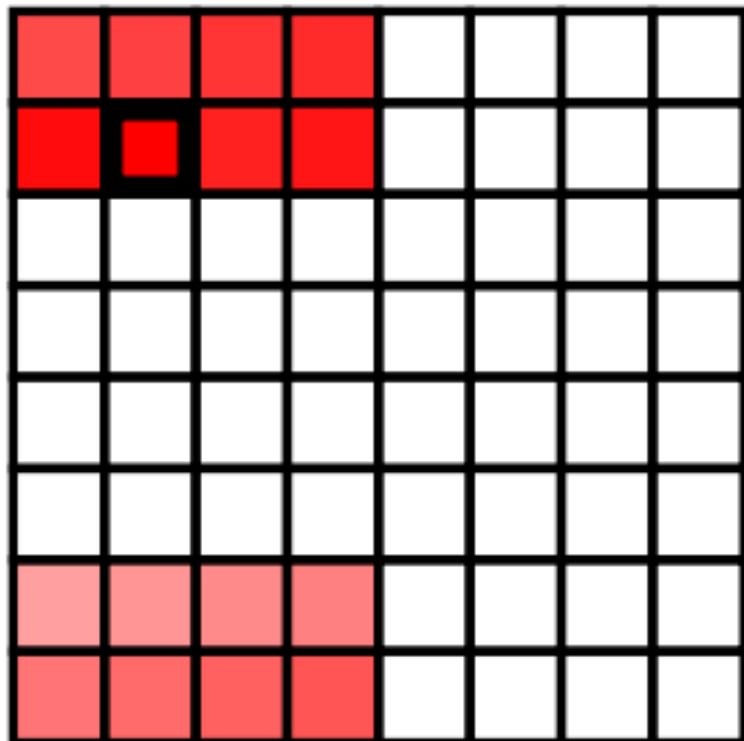
B mem:153 cache hit:121



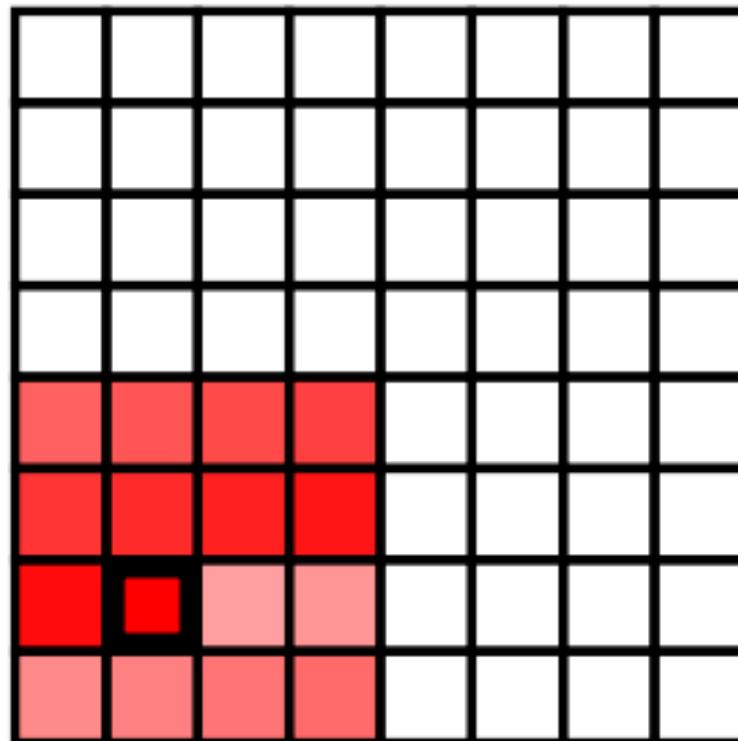
C mem:153 cache hit:114



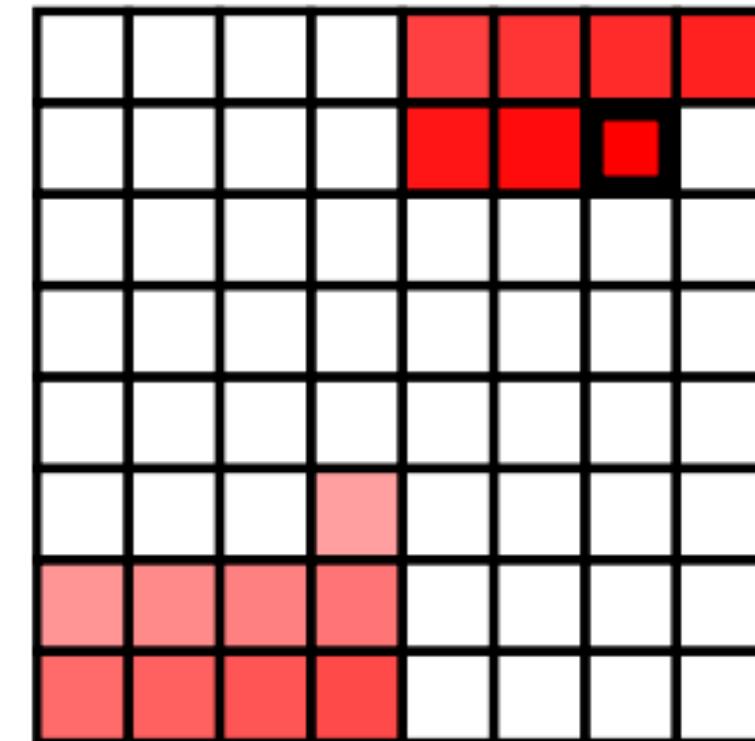
A mem:154 cache hit:114



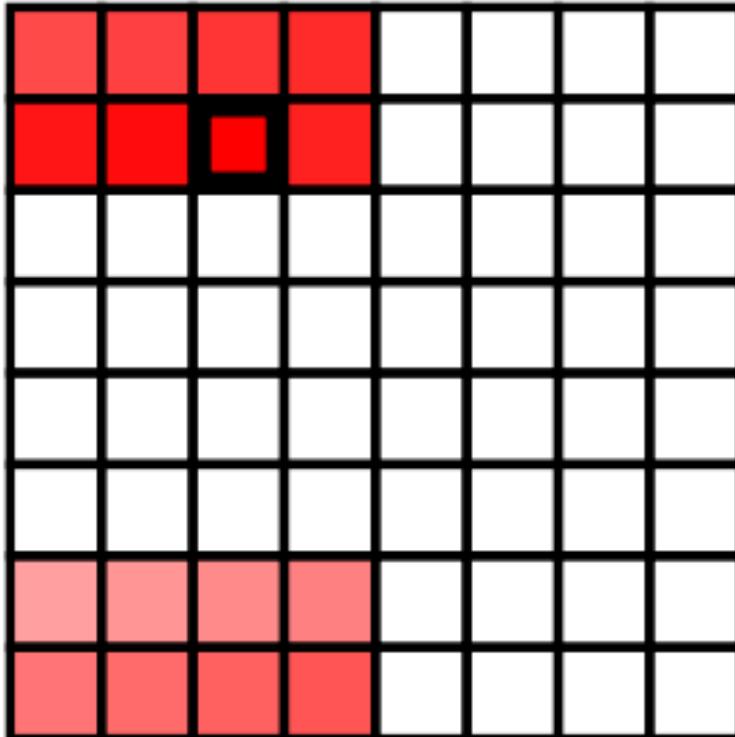
B mem:154 cache hit:122



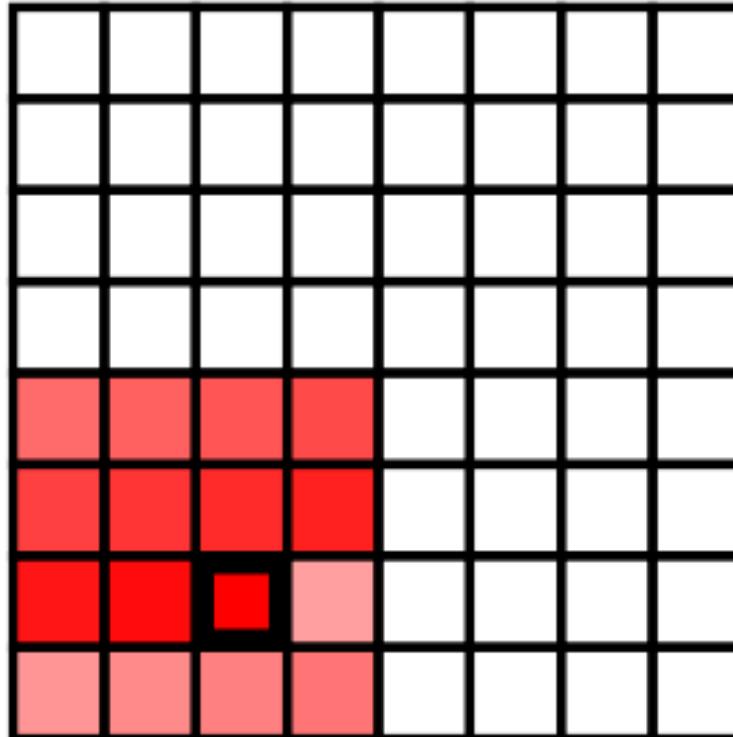
C mem:154 cache hit:115



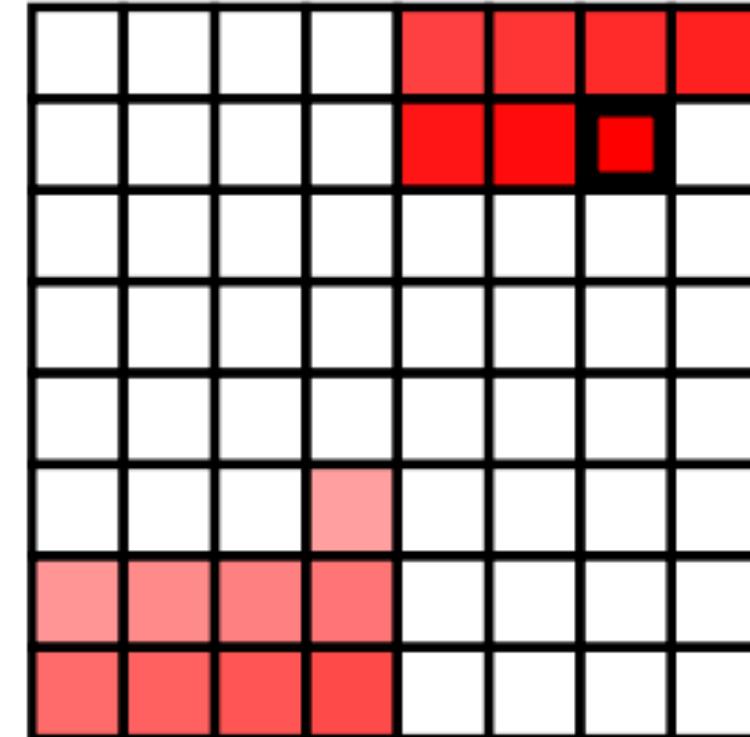
A mem:155 cache hit:115



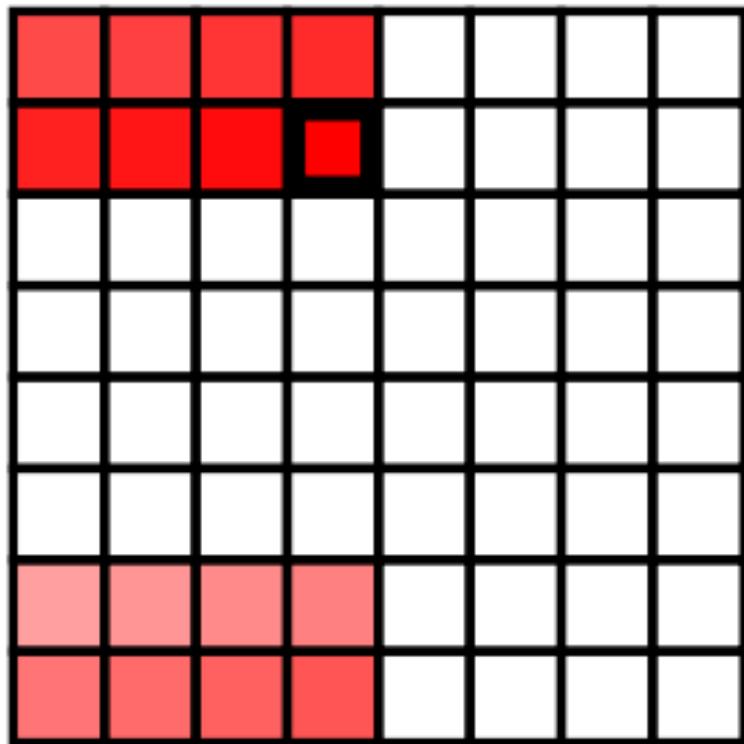
B mem:155 cache hit:123



C mem:155 cache hit:116

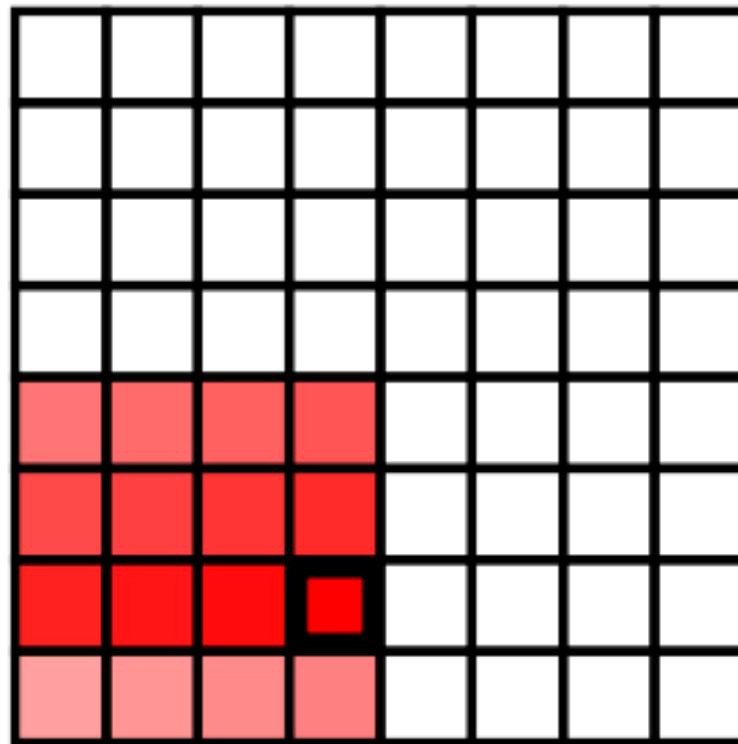


A mem:156 cache hit:116



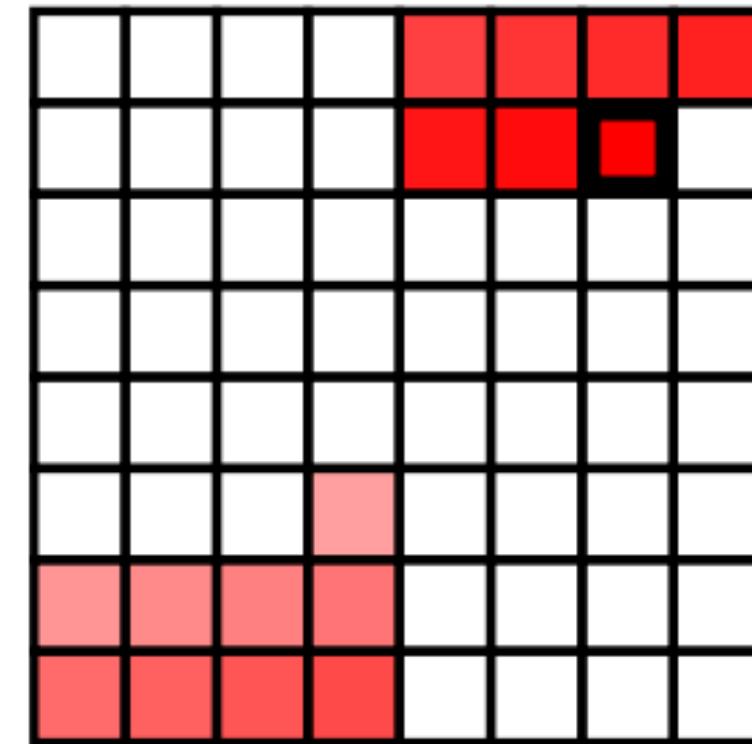
mem:468

B mem:156 cache hit:124

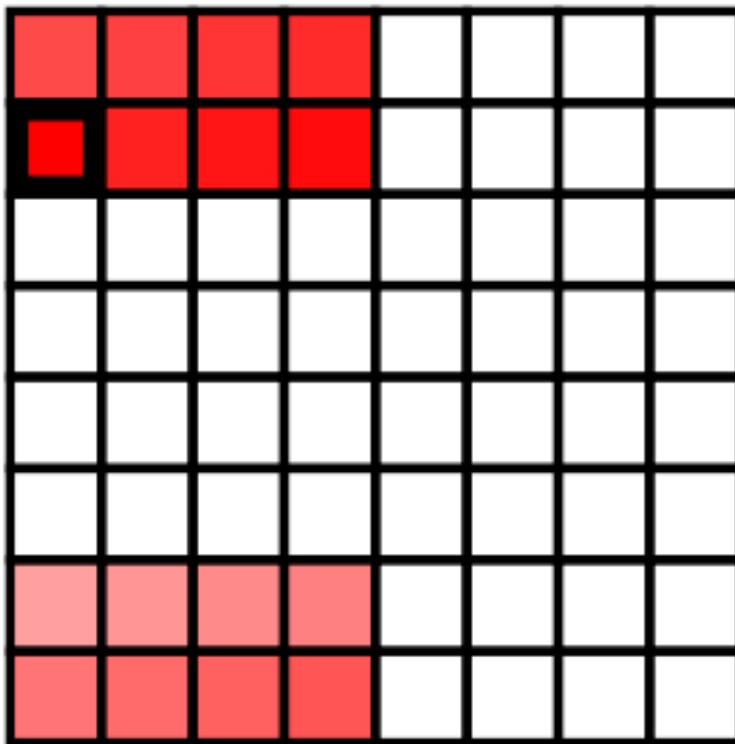


cache hits:357 ≈76%

C mem:156 cache hit:117

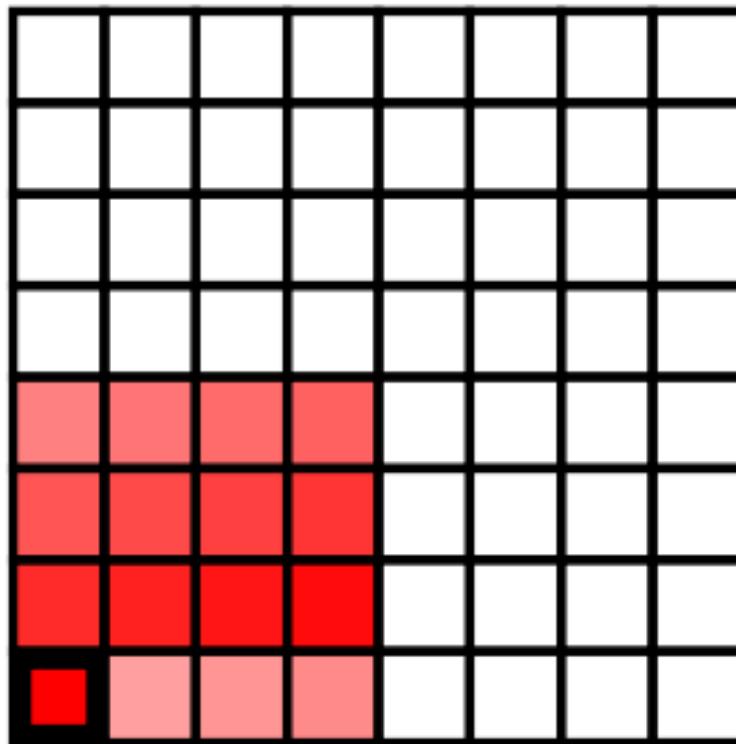


A mem:157 cache hit:117



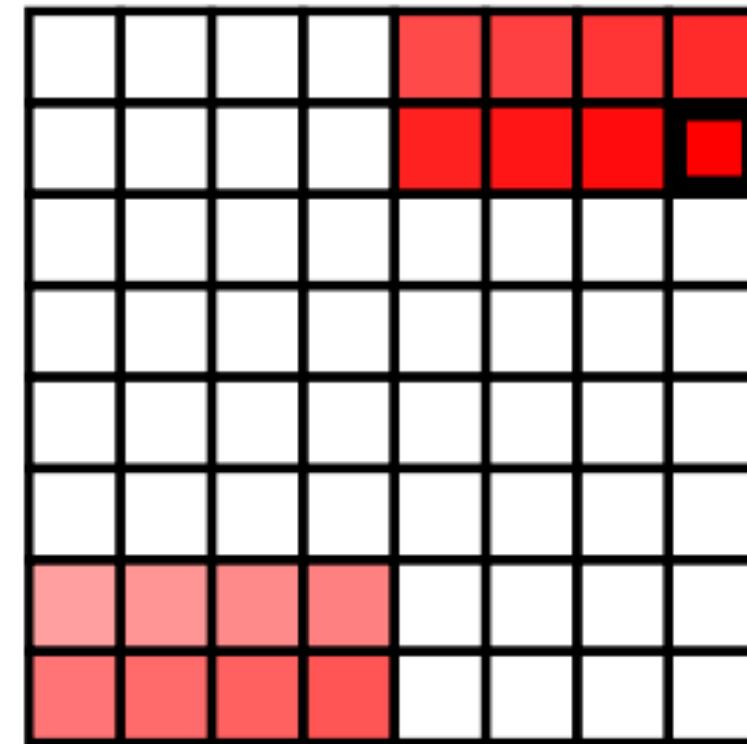
mem:471

B mem:157 cache hit:125

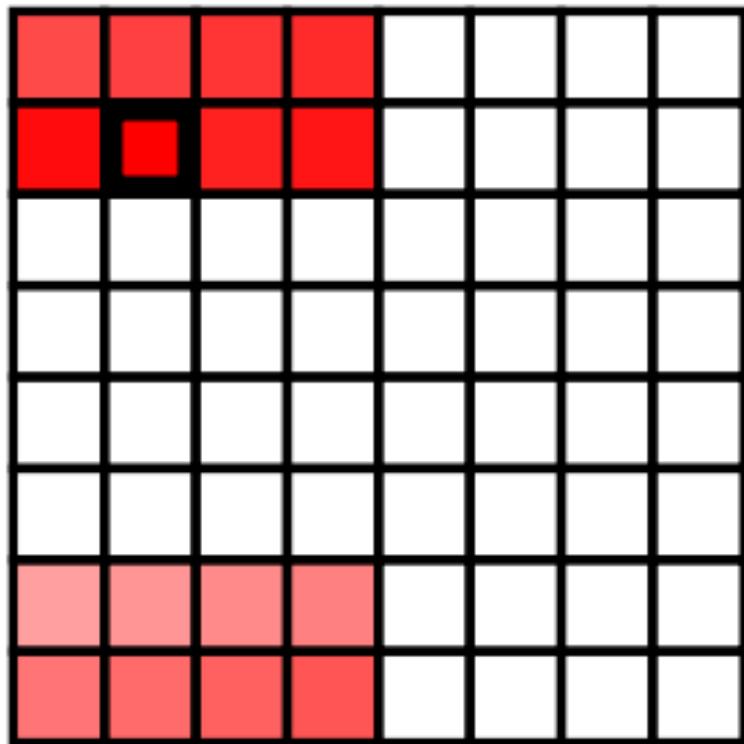


cache hits:359 ≈76%

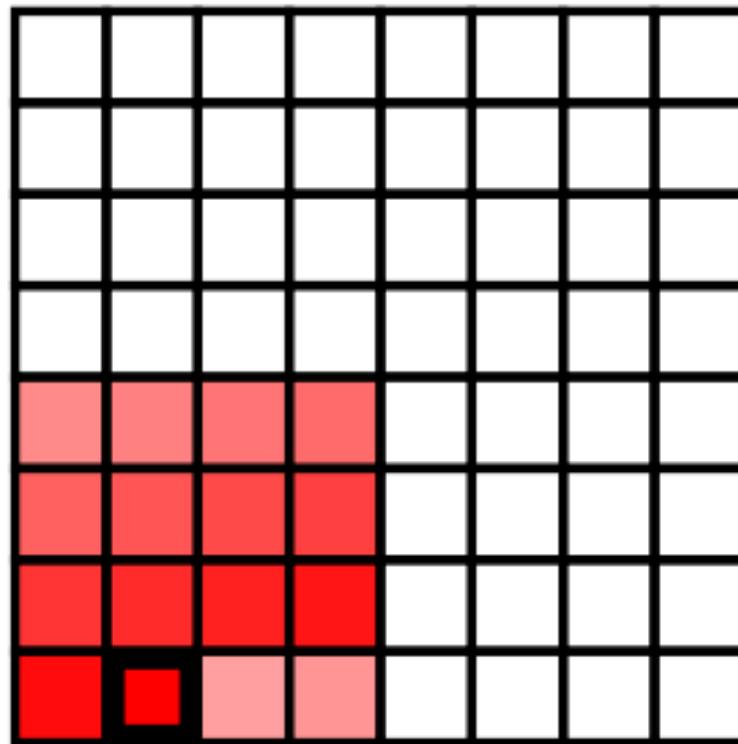
C mem:157 cache hit:117



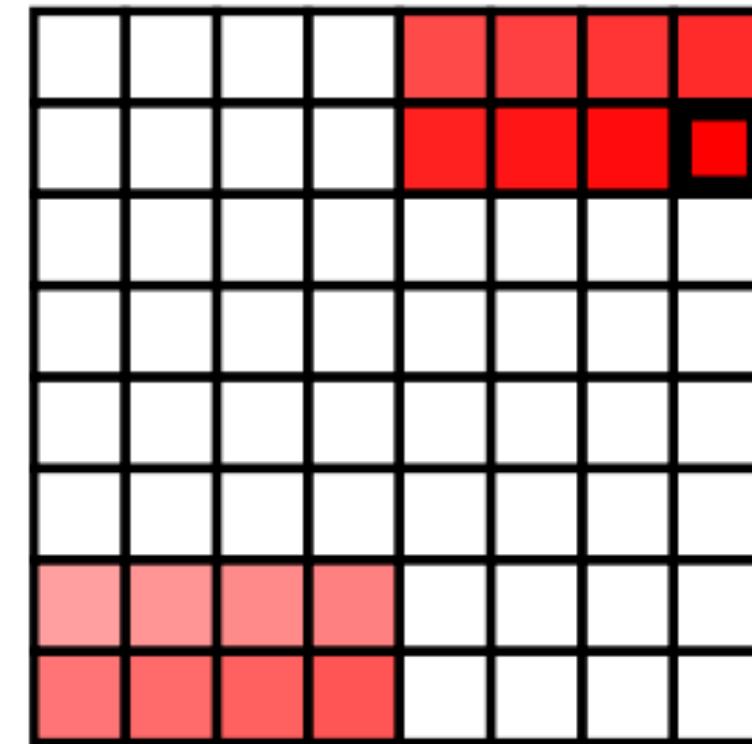
A mem:158 cache hit:118



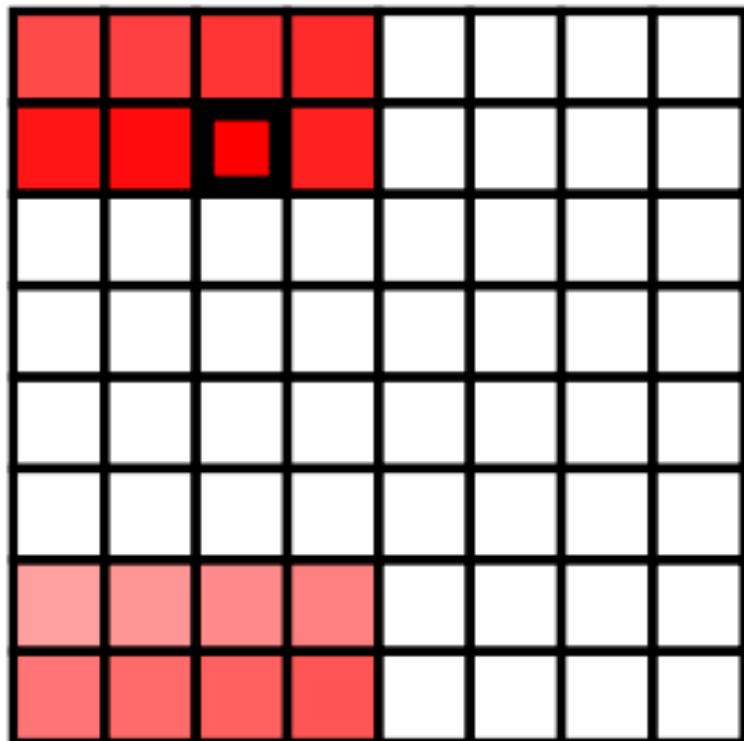
B mem:158 cache hit:126



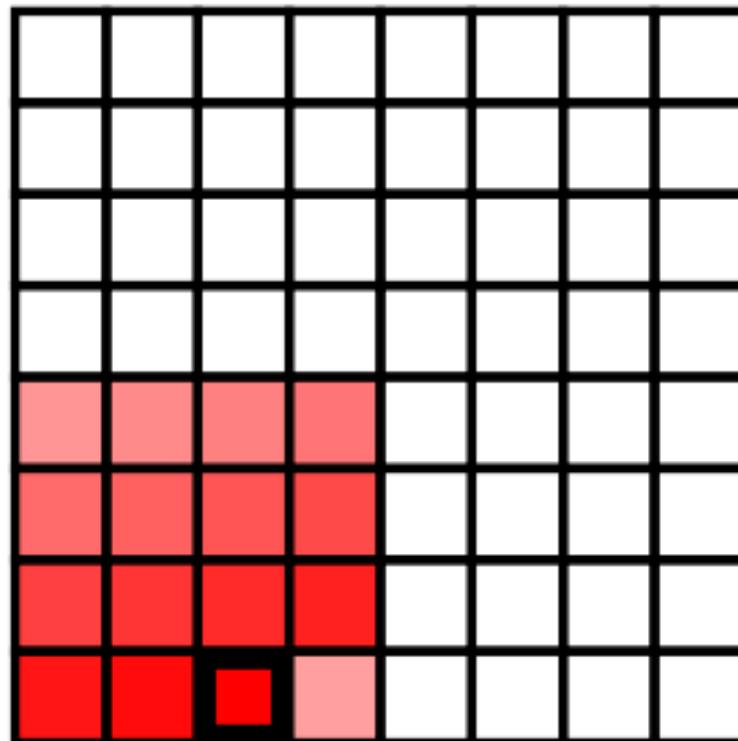
C mem:158 cache hit:118



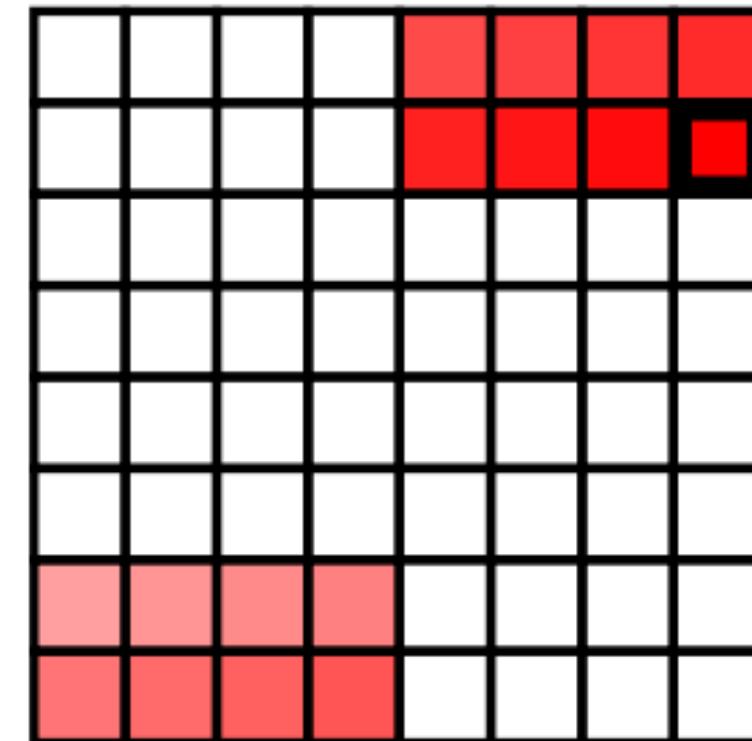
A mem:159 cache hit:119



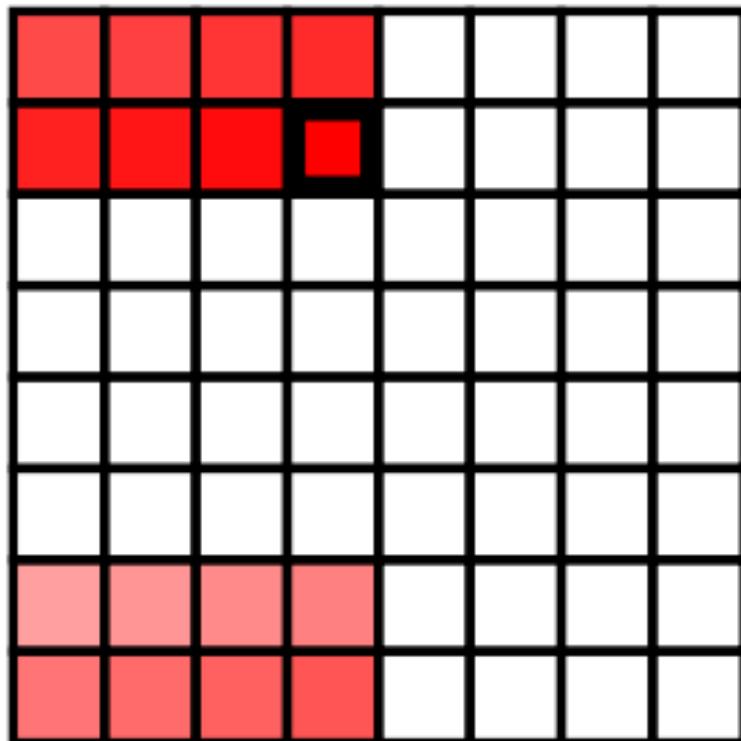
B mem:159 cache hit:127



C mem:159 cache hit:119

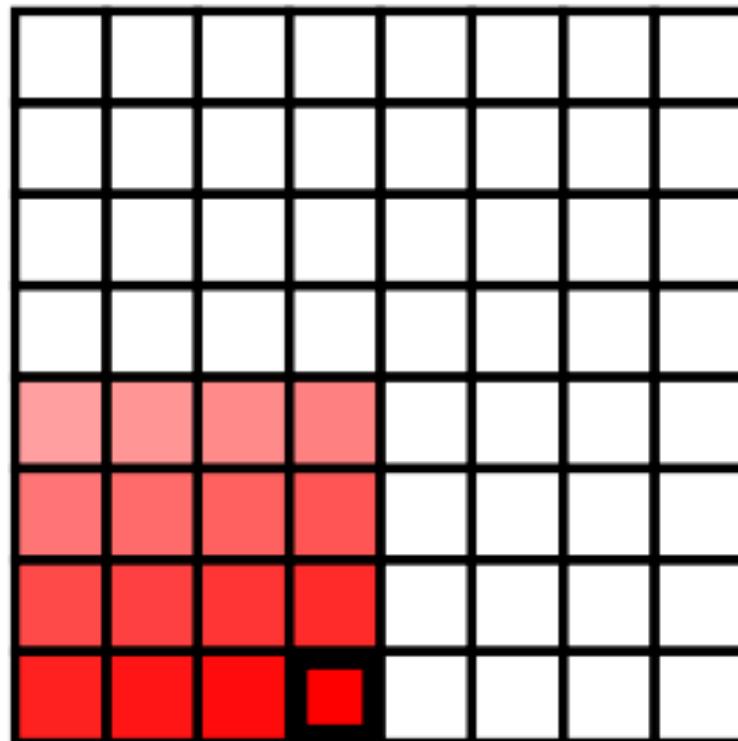


A mem:160 cache hit:120



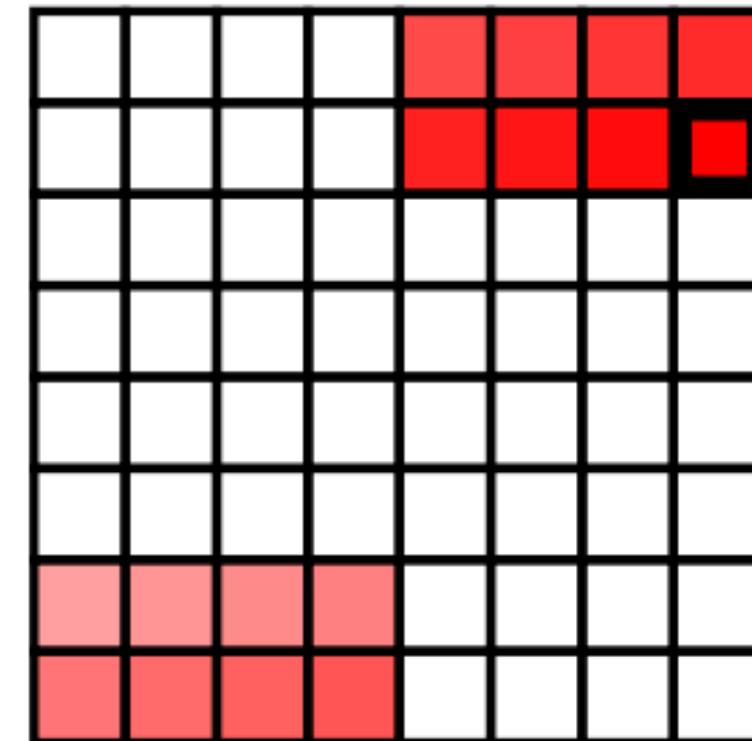
mem:480

B mem:160 cache hit:128

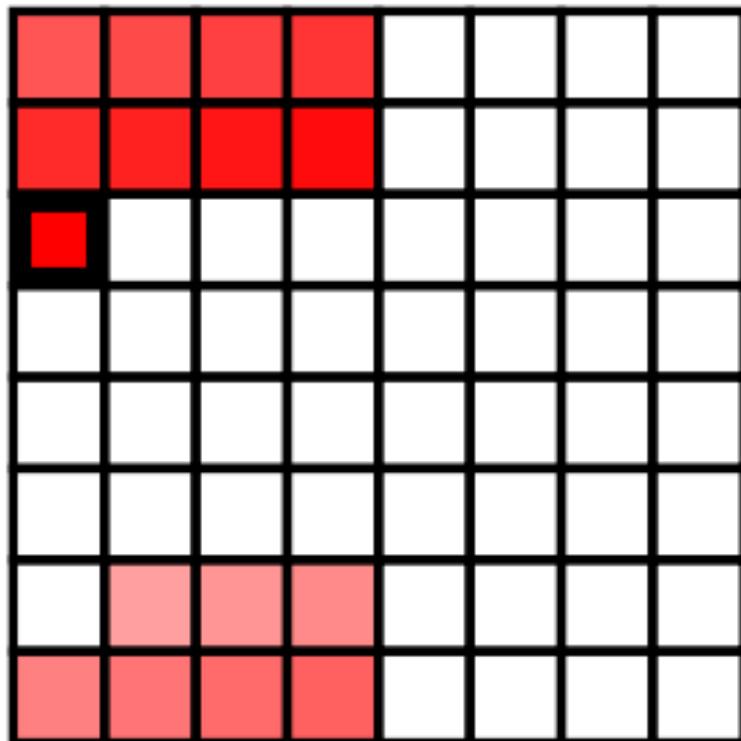


cache hits:368 ≈76%

C mem:160 cache hit:120

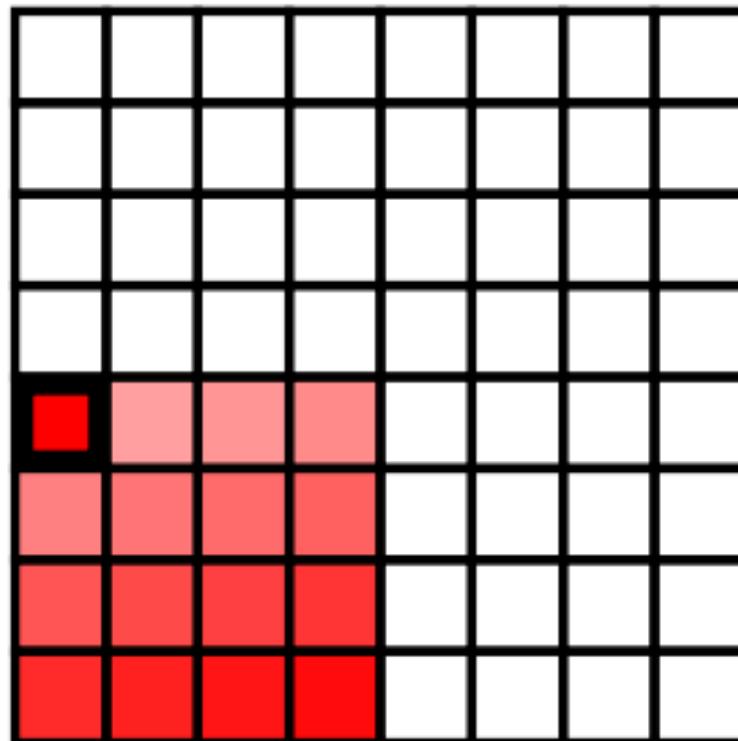


A mem:161 cache hit:120



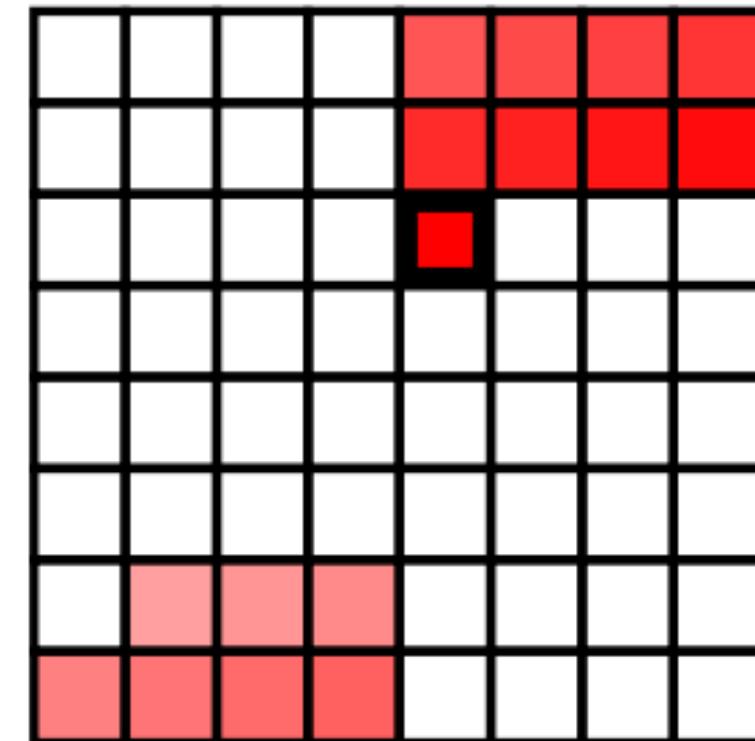
mem:483

B mem:161 cache hit:129

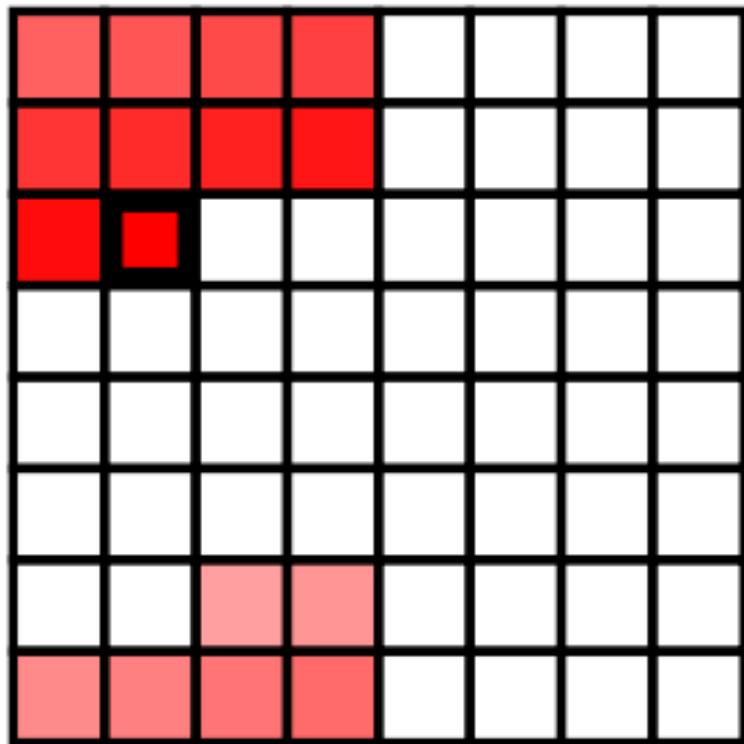


cache hits:369 ≈76%

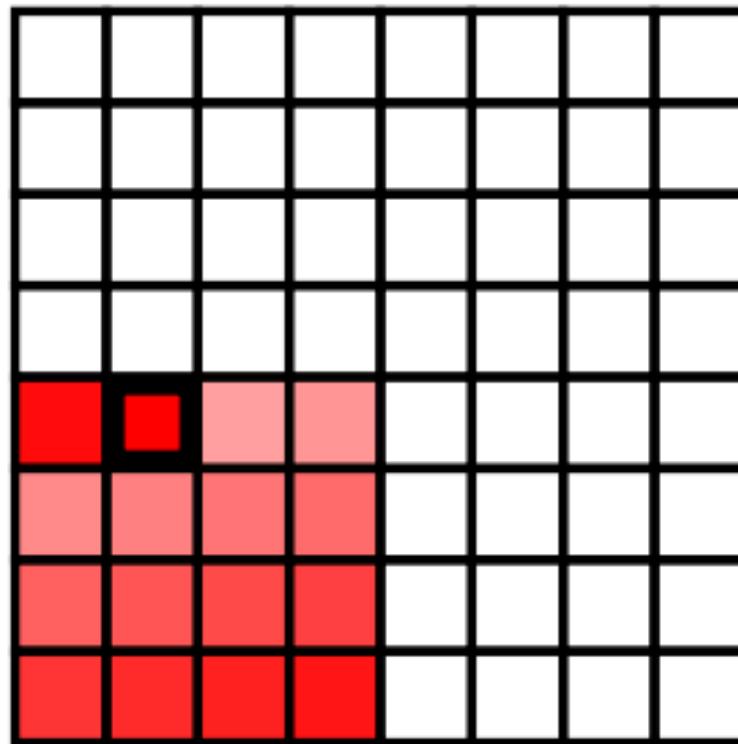
C mem:161 cache hit:120



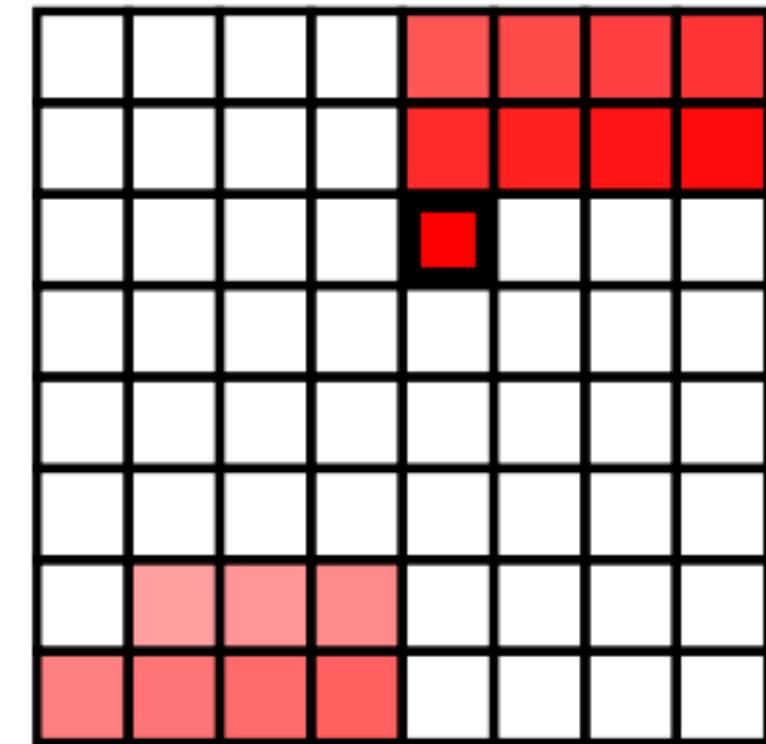
A mem:162 cache hit:120



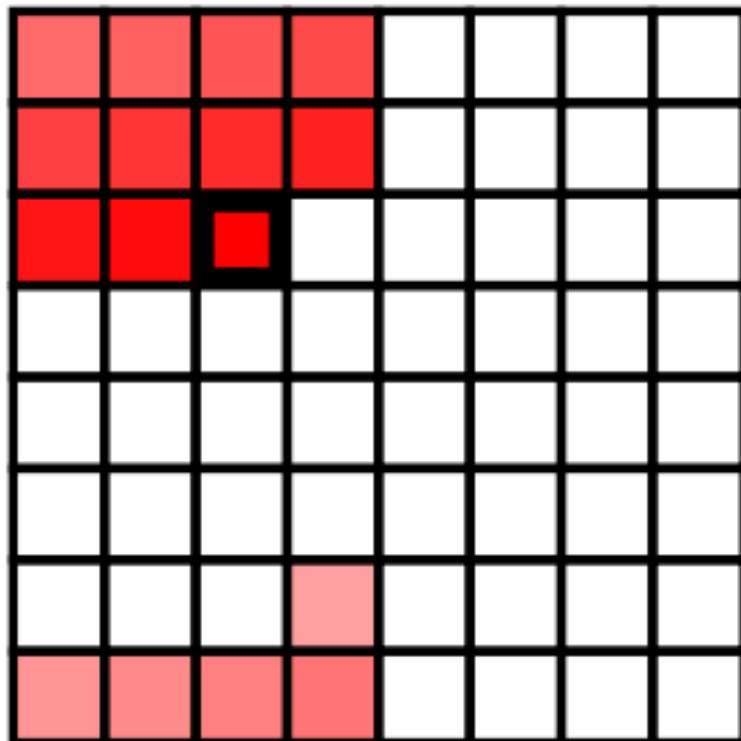
B mem:162 cache hit:130



C mem:162 cache hit:121

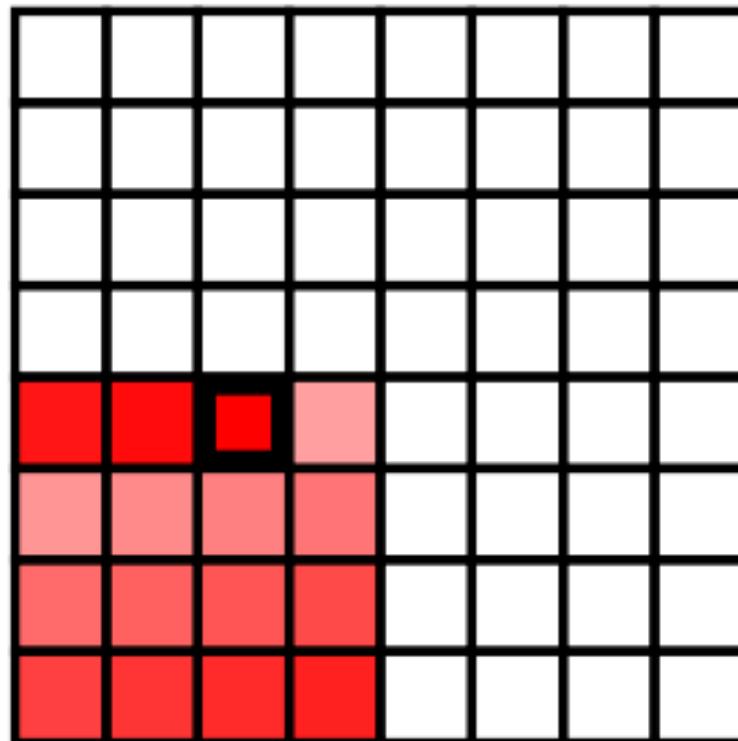


A mem:163 cache hit:120



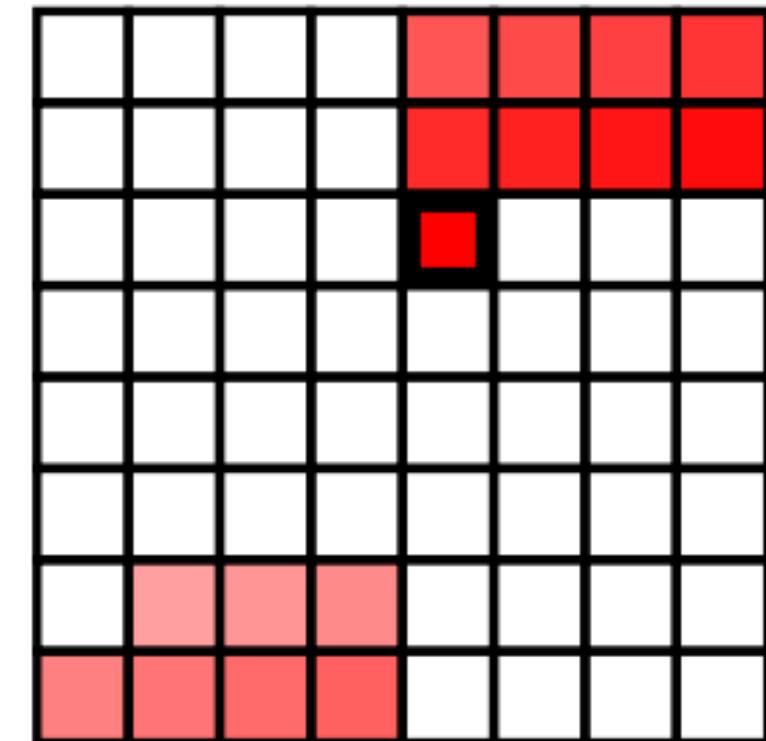
mem:489

B mem:163 cache hit:131

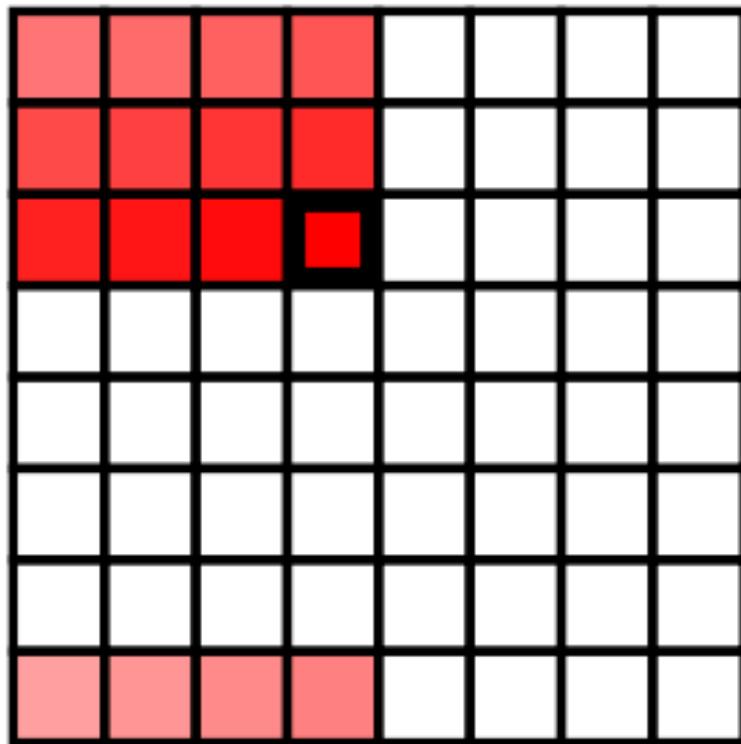


cache hits:373 ≈76%

C mem:163 cache hit:122

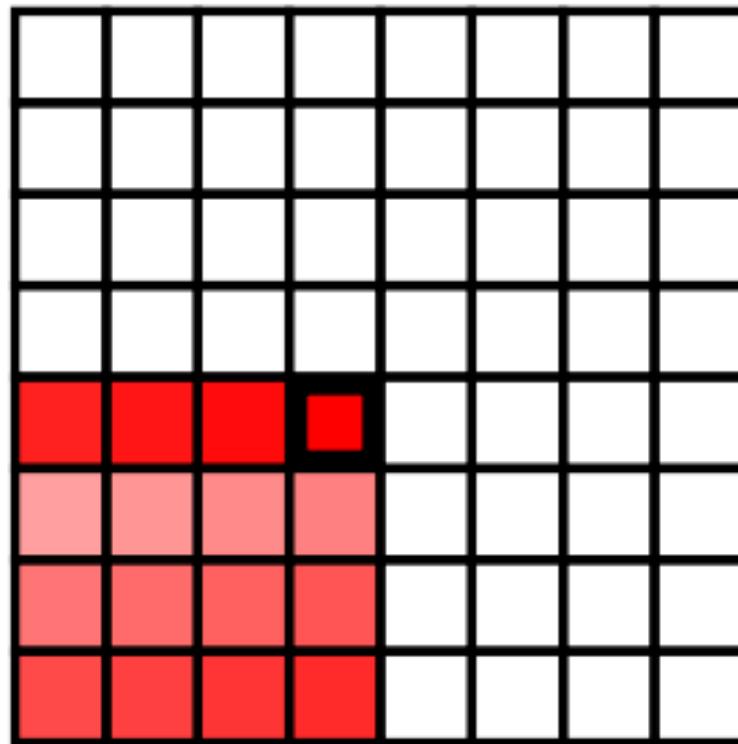


A mem:164 cache hit:120



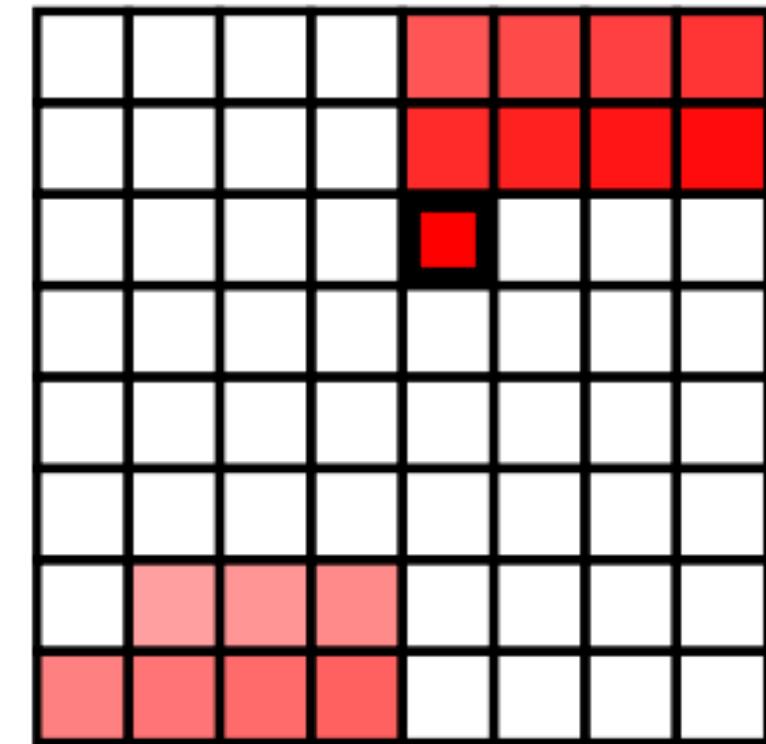
mem:492

B mem:164 cache hit:132

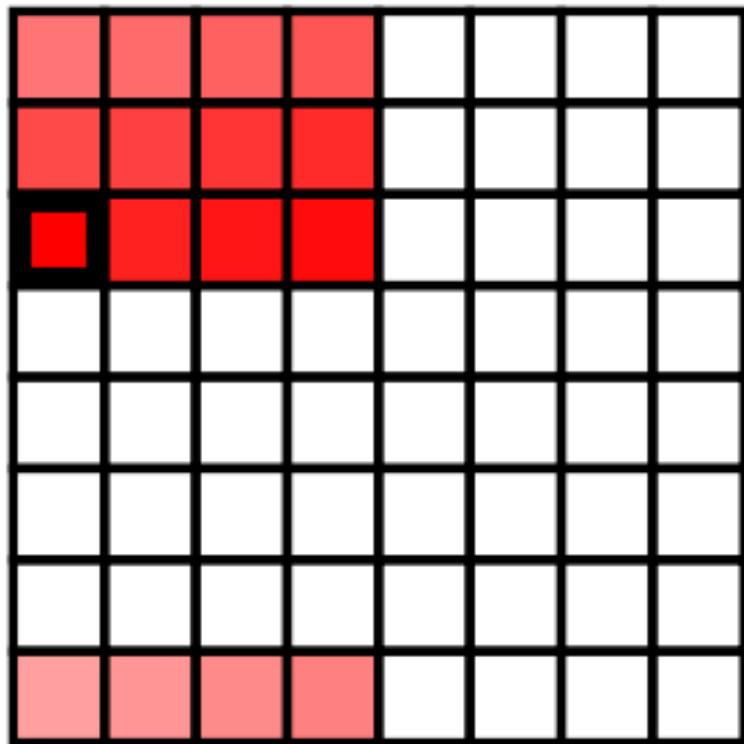


cache hits:375 ≈76%

C mem:164 cache hit:123

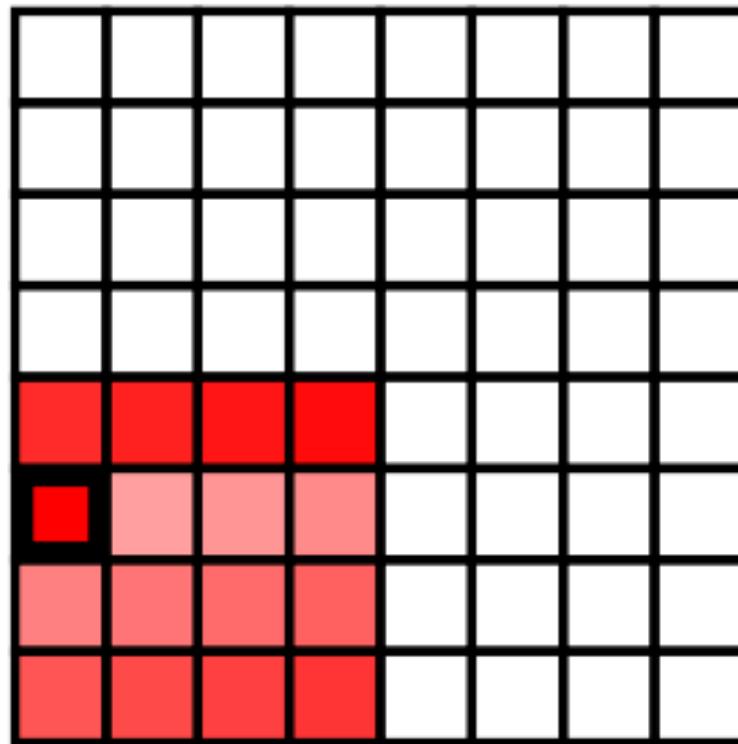


A mem:165 cache hit:121



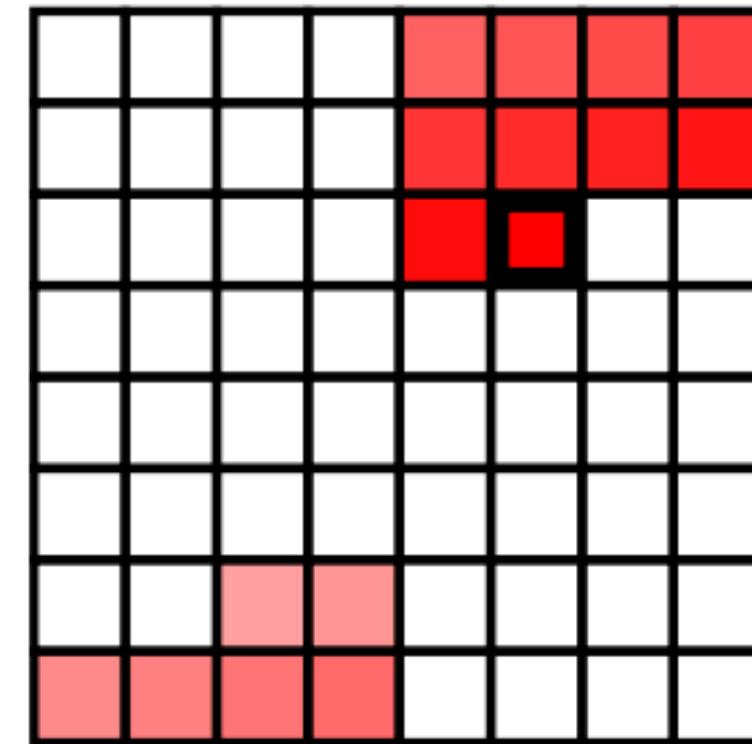
mem:495

B mem:165 cache hit:133

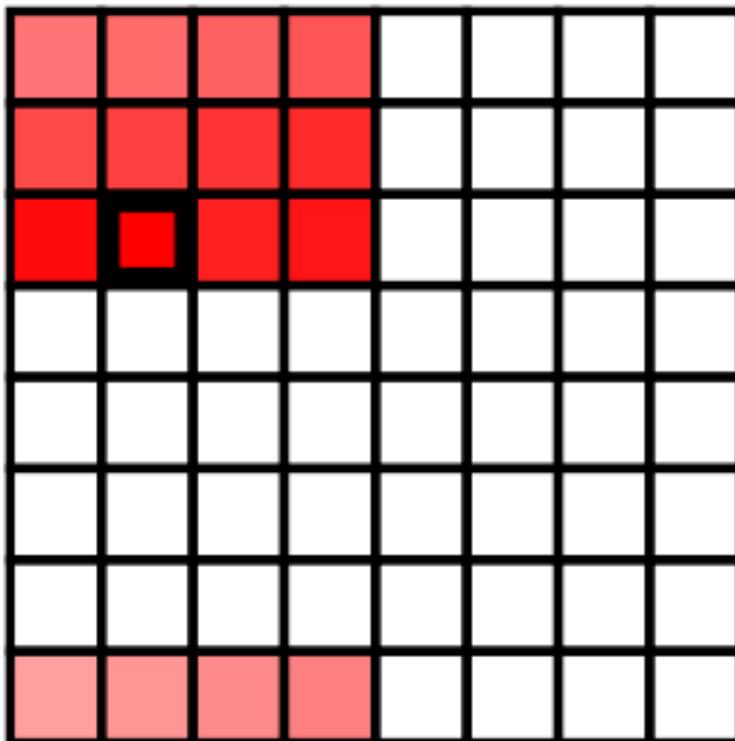


cache hits:377 ≈76%

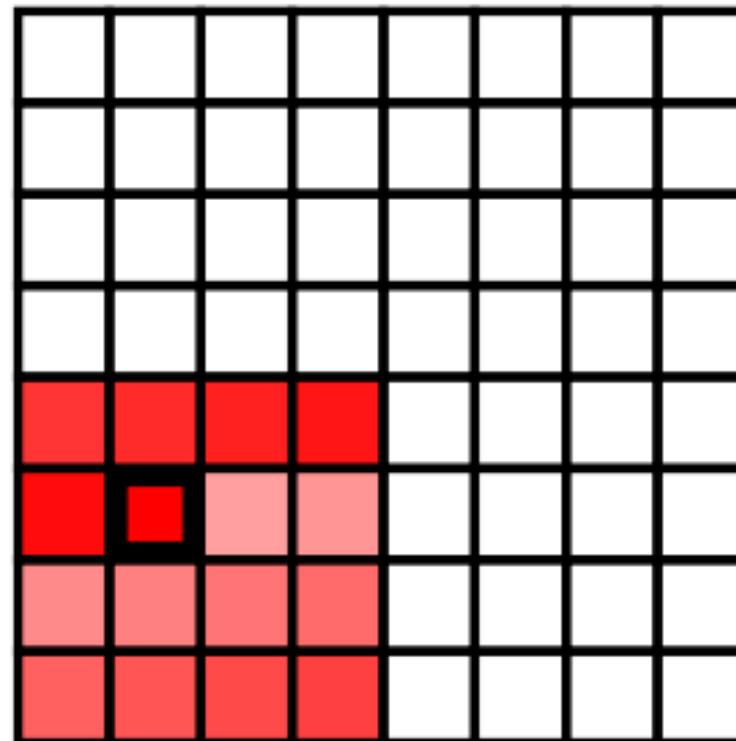
C mem:165 cache hit:123



A mem:166 cache hit:122



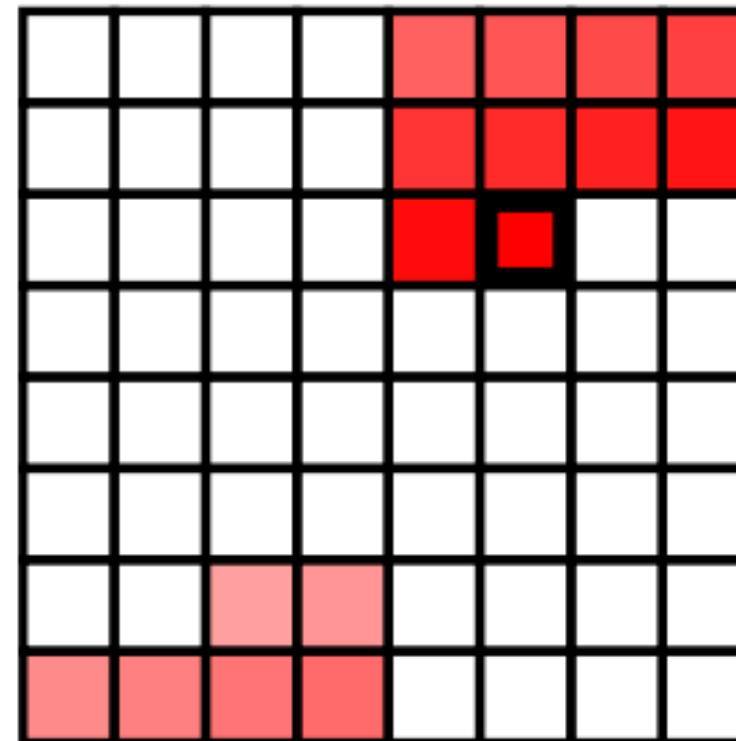
B mem:166 cache hit:134



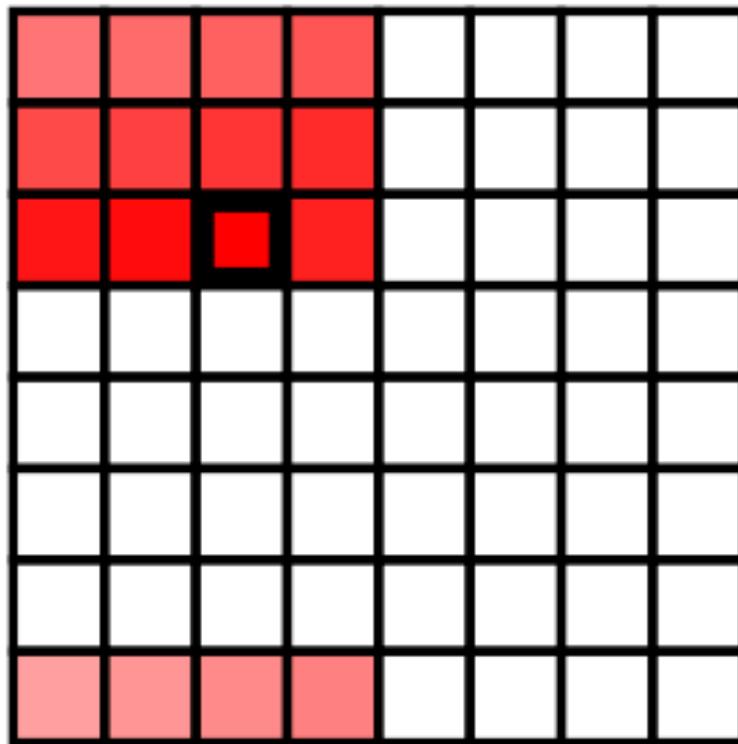
mem:498

cache hits:380 $\approx 76\%$

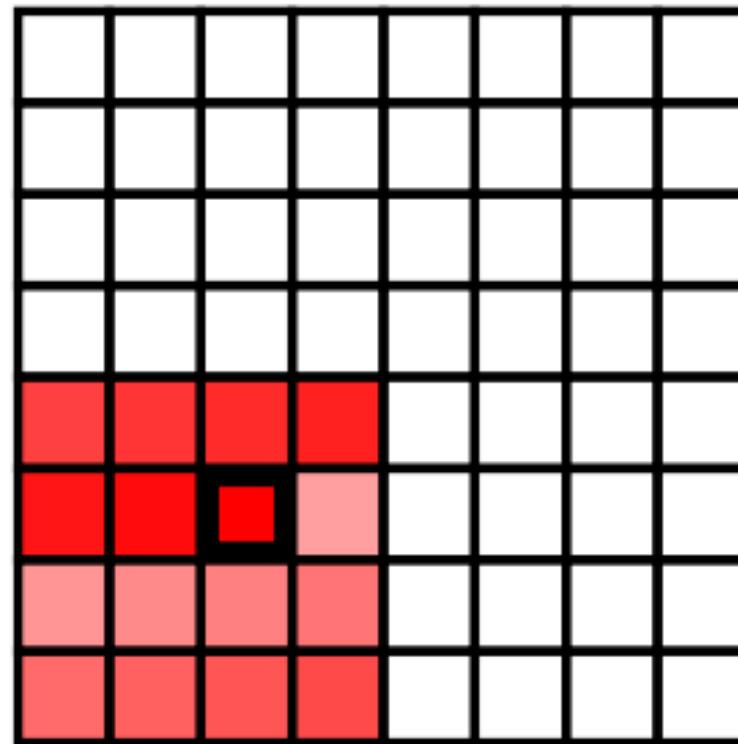
C mem:166 cache hit:124



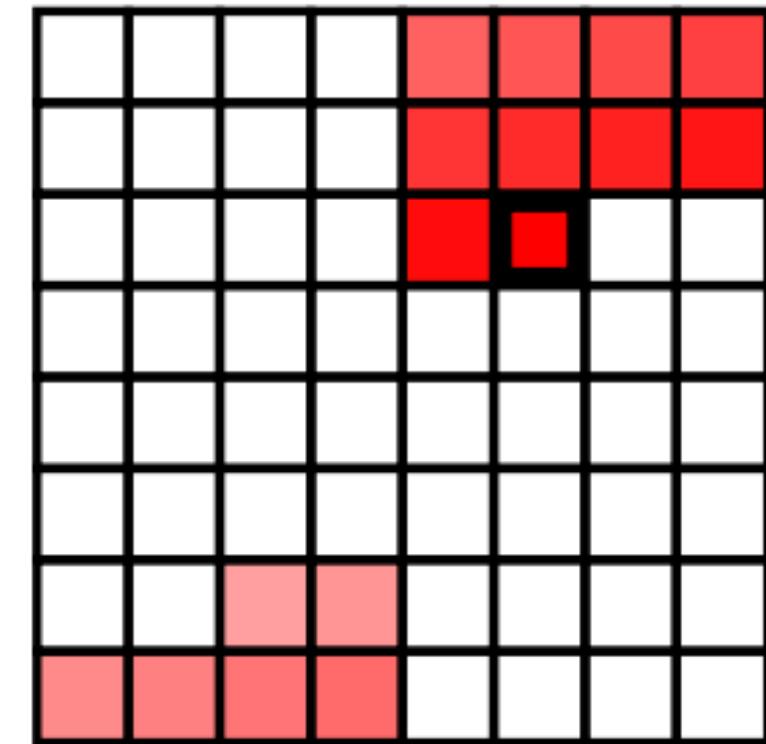
A mem:167 cache hit:123



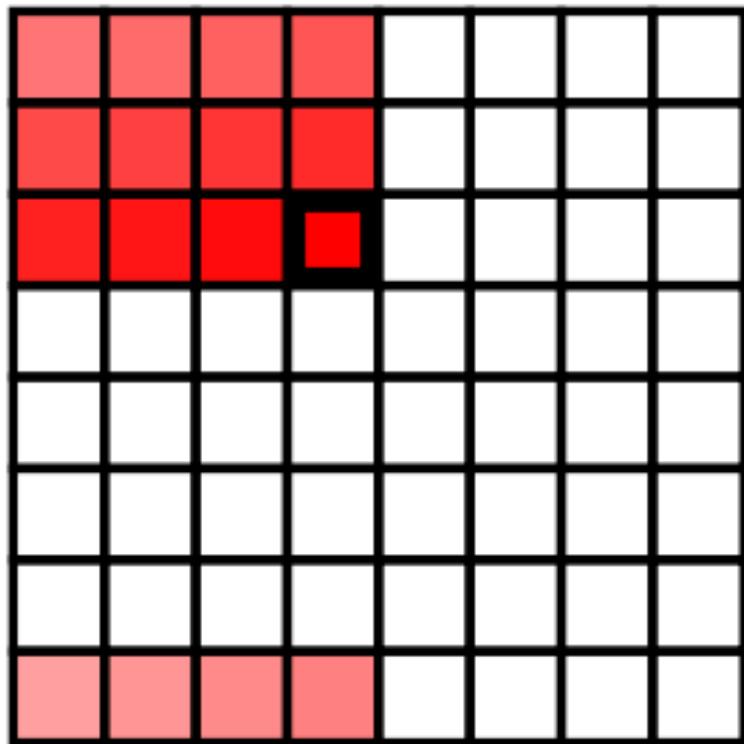
B mem:167 cache hit:135



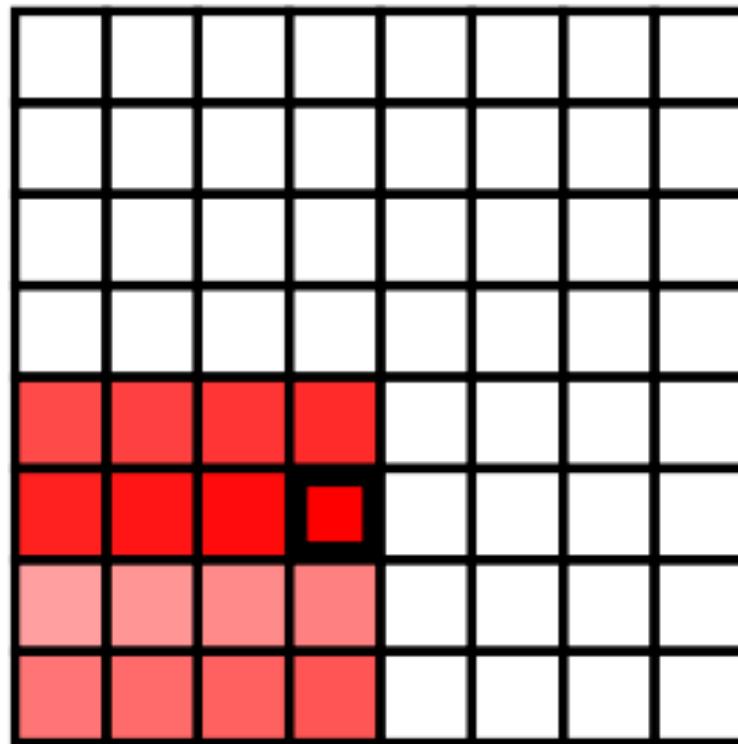
C mem:167 cache hit:125



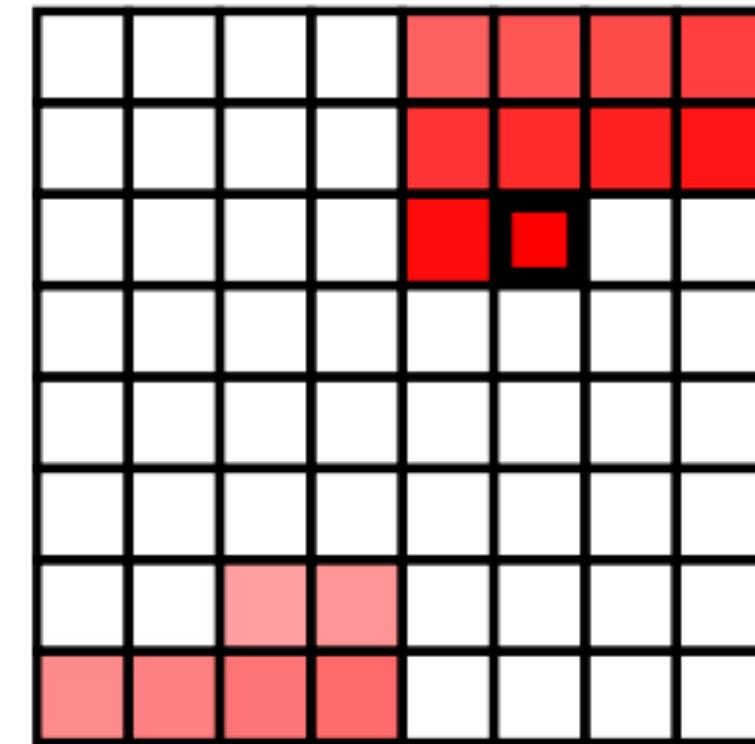
A mem:168 cache hit:124



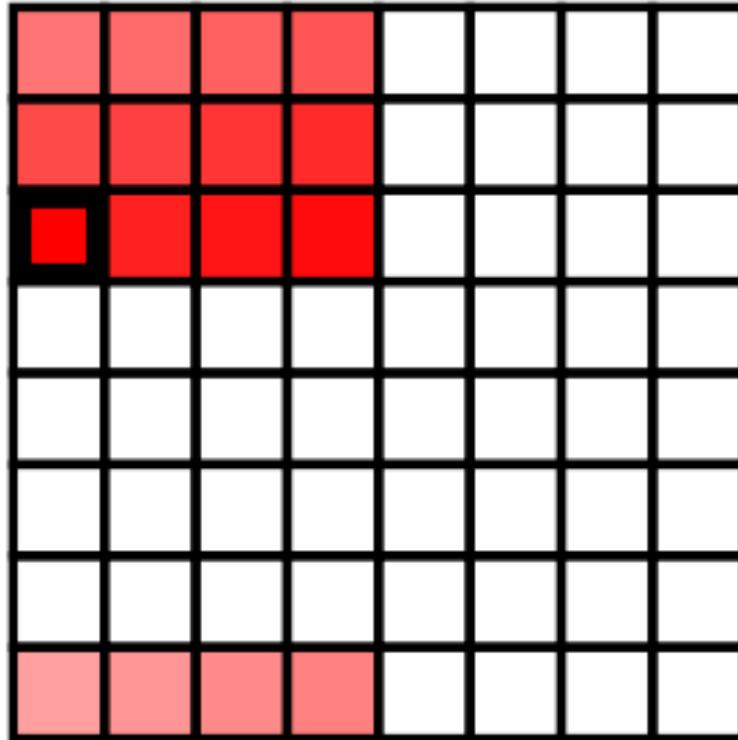
B mem:168 cache hit:136



C mem:168 cache hit:126

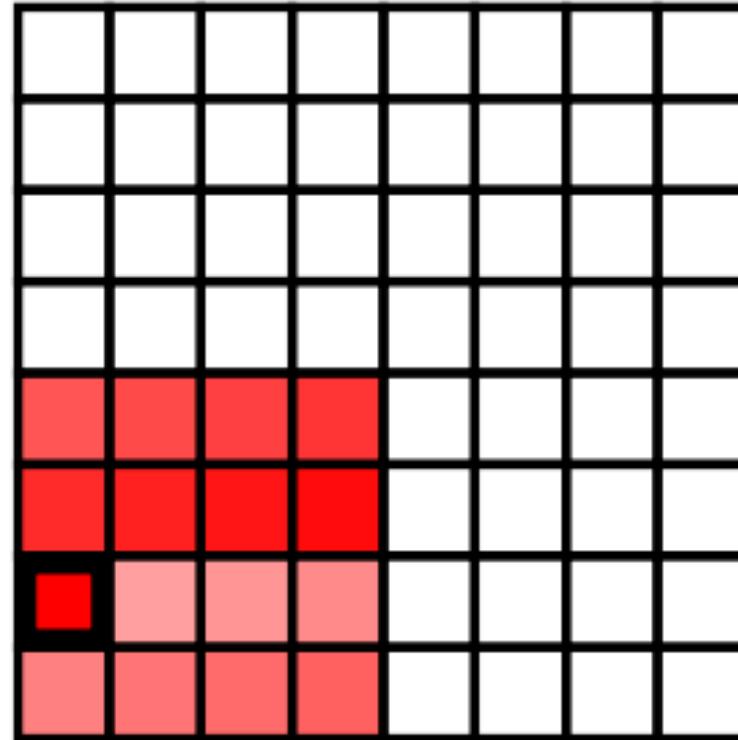


A mem:169 cache hit:125



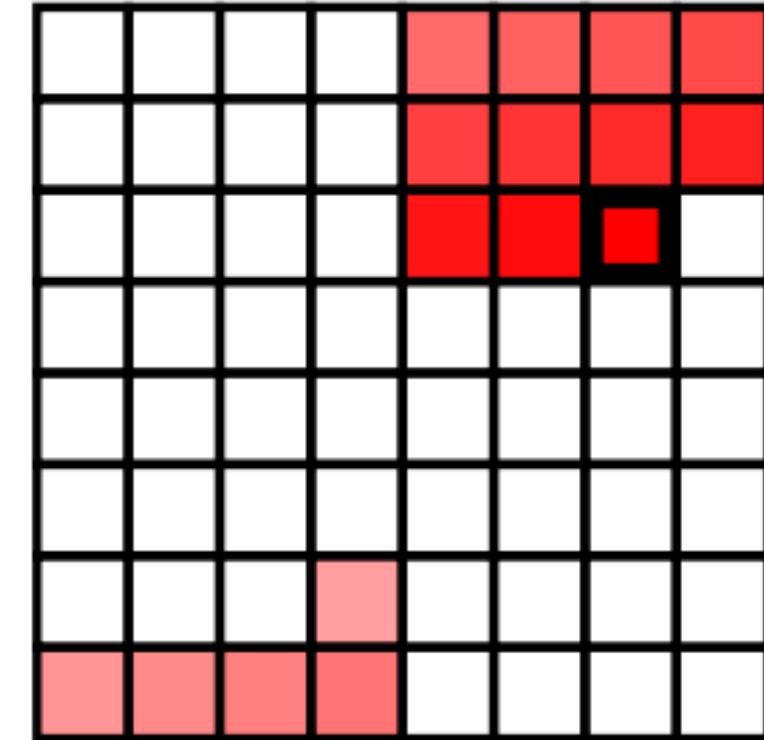
mem:507

B mem:169 cache hit:137

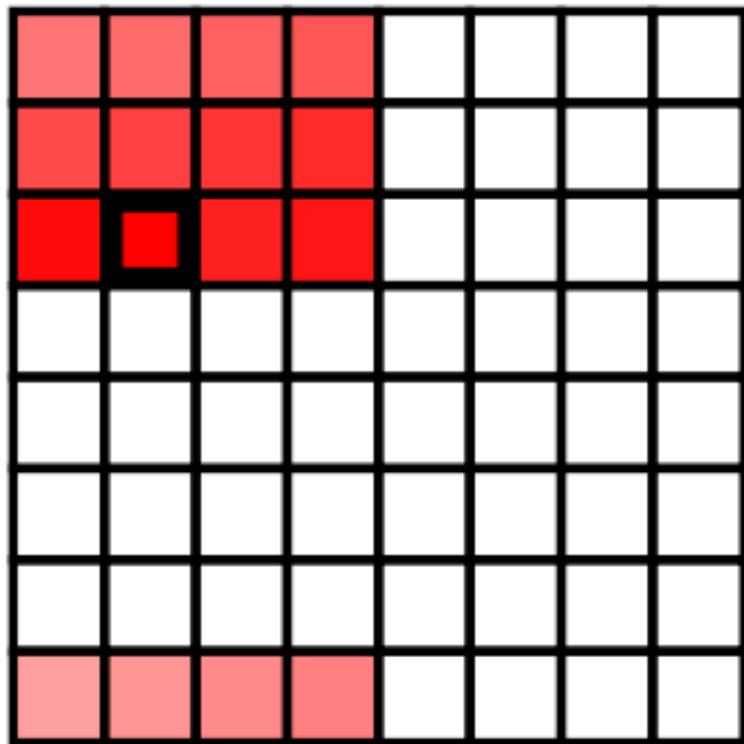


cache hits:388 ≈76%

C mem:169 cache hit:126

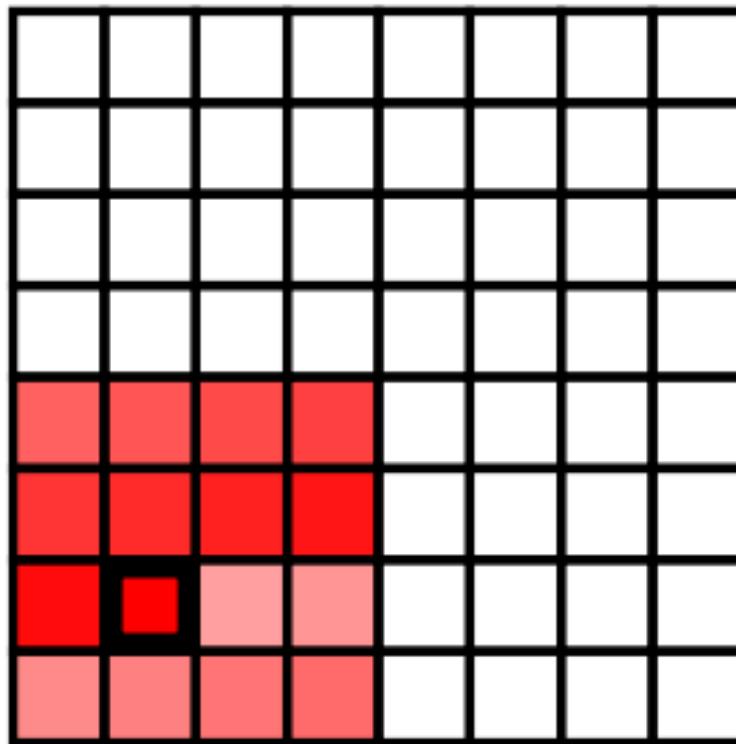


A mem:170 cache hit:126



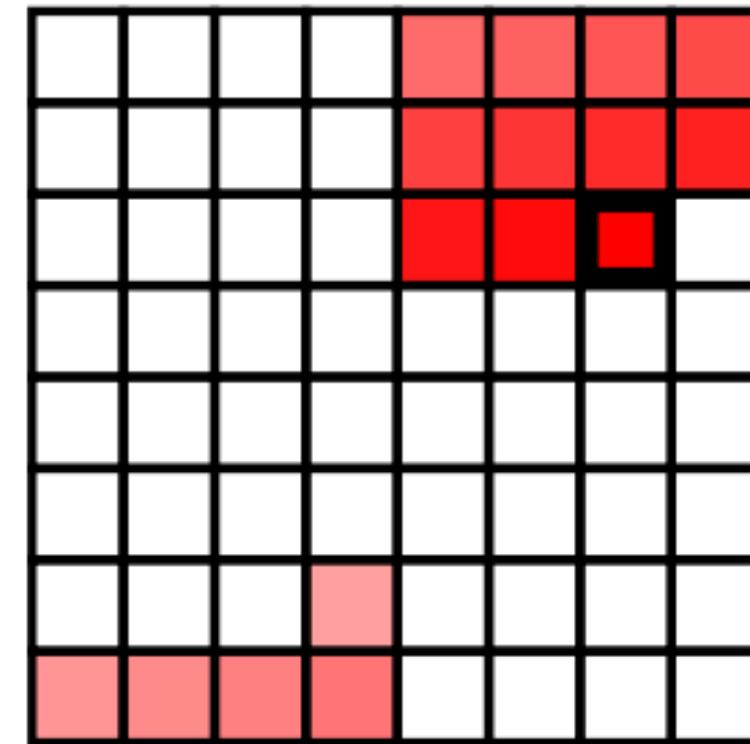
mem:510

B mem:170 cache hit:138

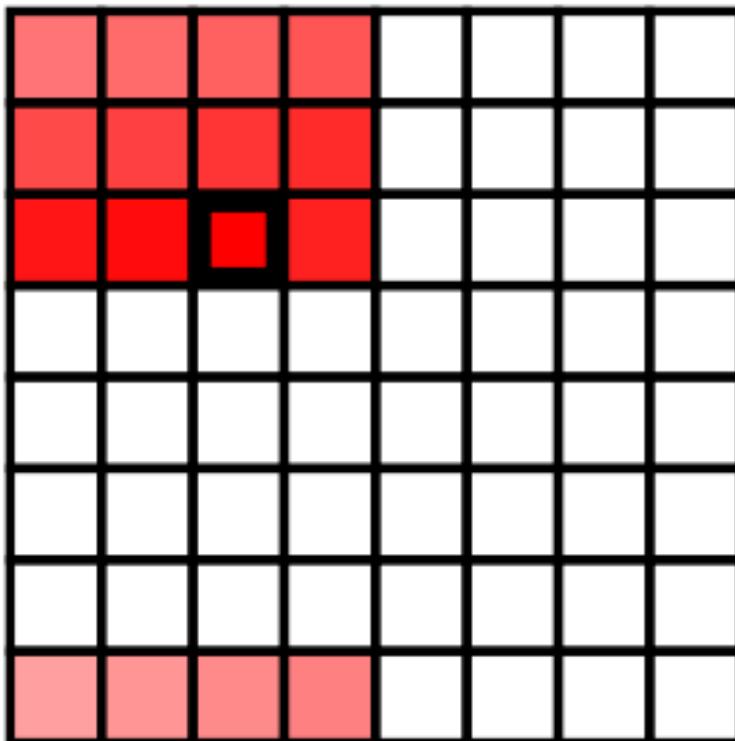


cache hits:391 $\approx 76\%$

C mem:170 cache hit:127

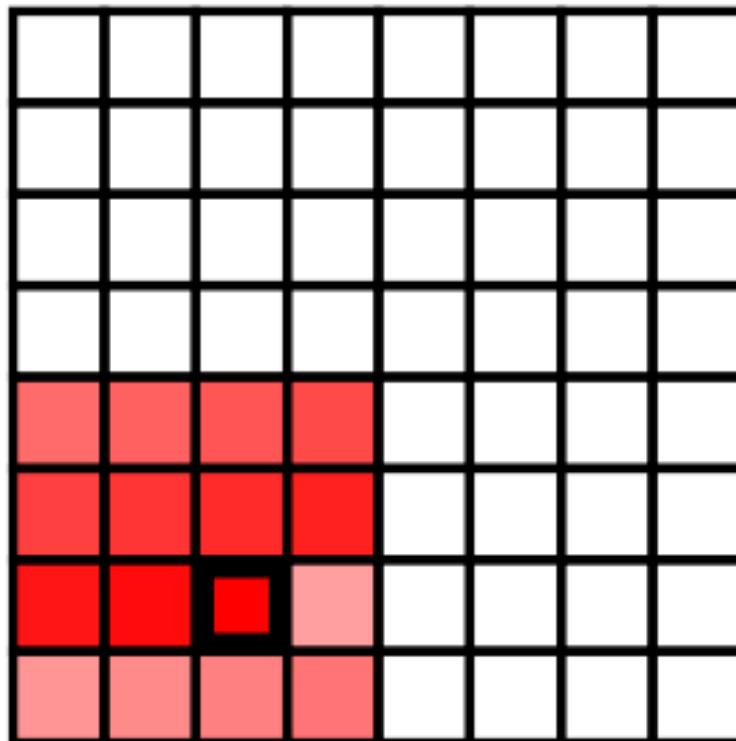


A mem:171 cache hit:127



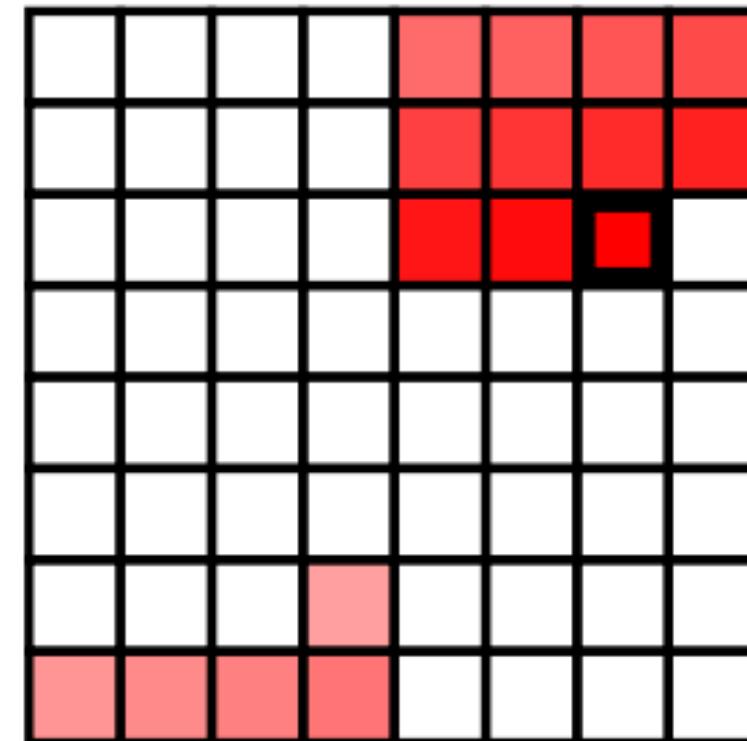
mem:513

B mem:171 cache hit:139

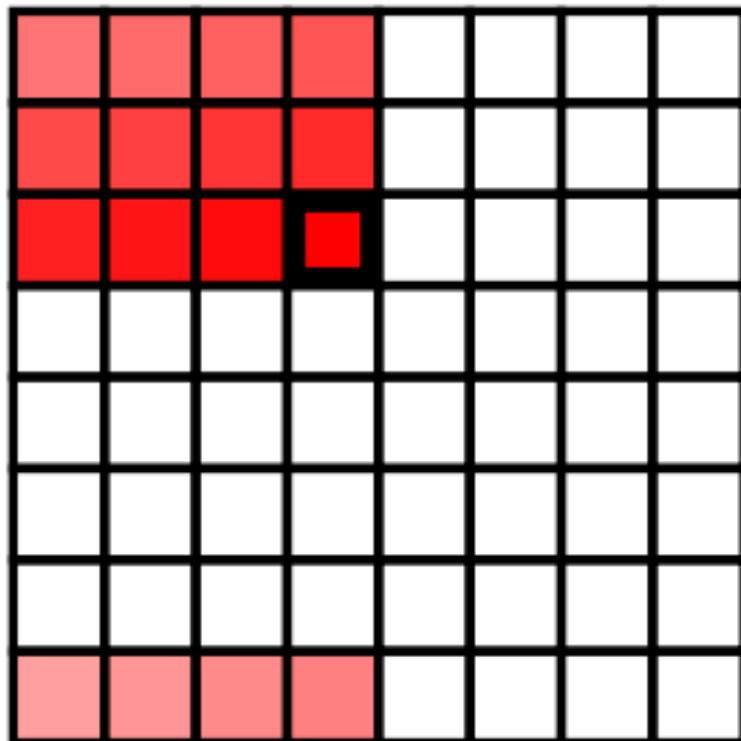


cache hits:394 ≈76%

C mem:171 cache hit:128

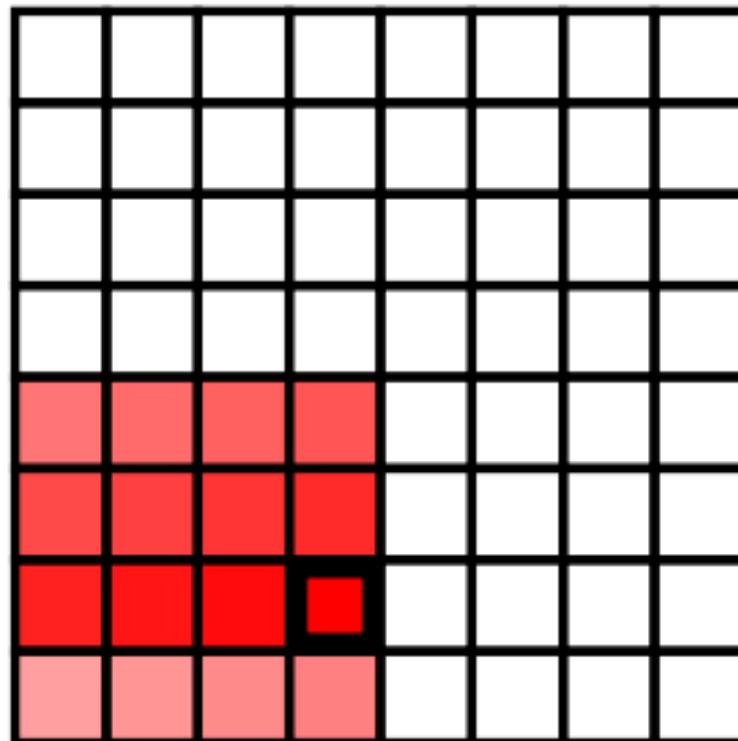


A mem:172 cache hit:128



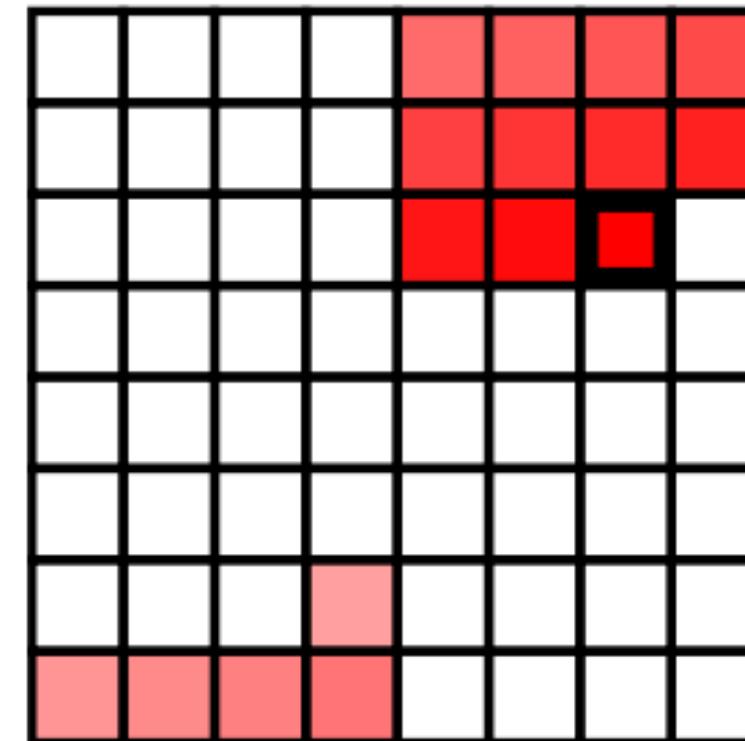
mem:516

B mem:172 cache hit:140

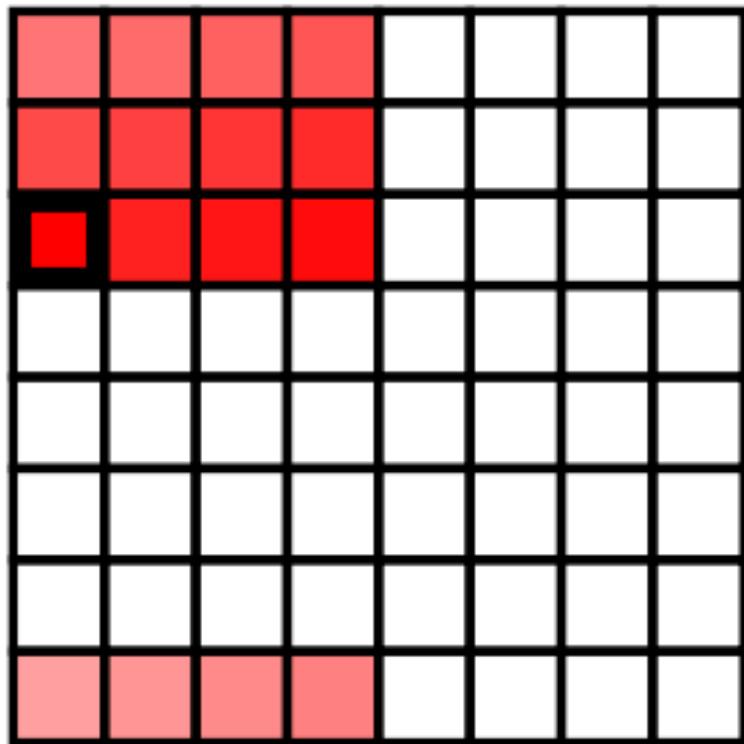


cache hits:397 $\approx 76\%$

C mem:172 cache hit:129

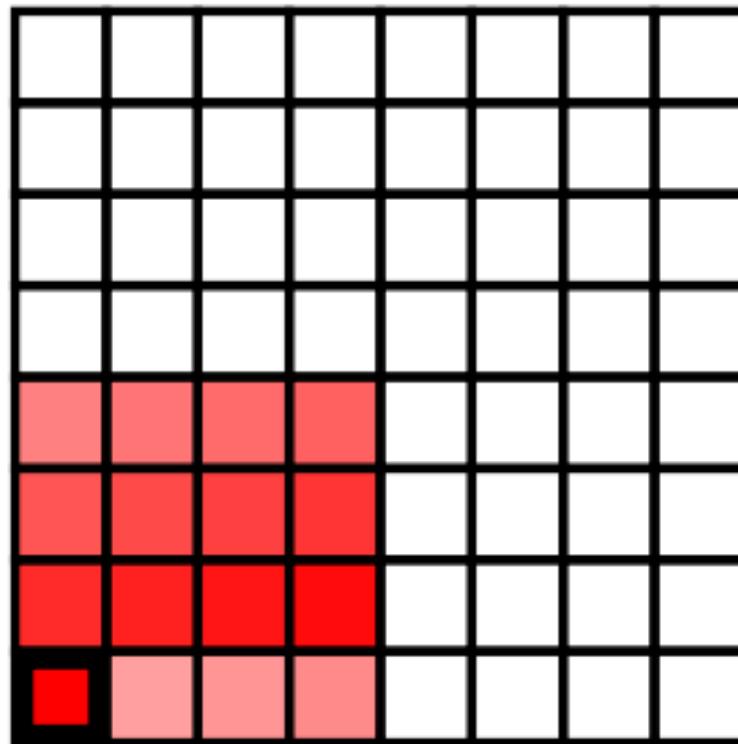


A mem:173 cache hit:129



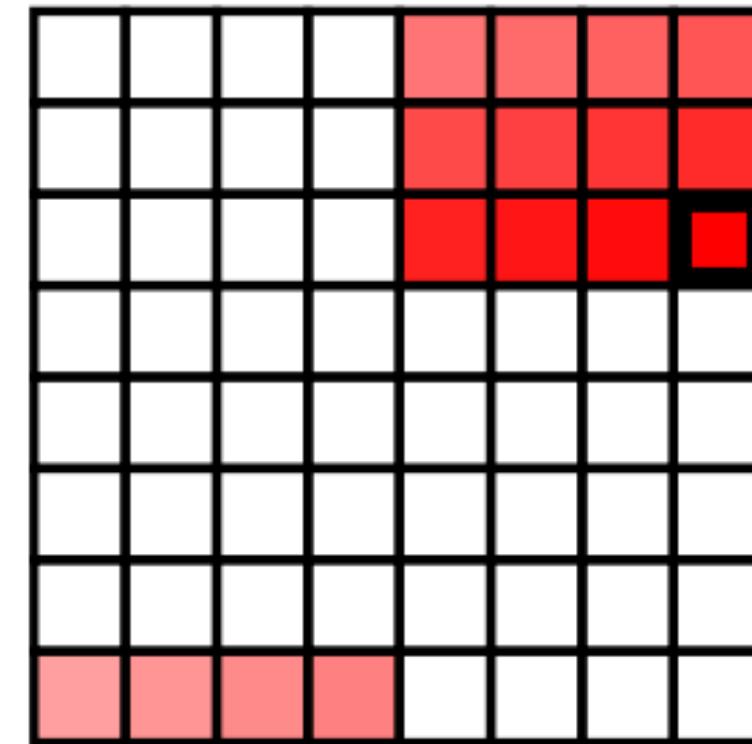
mem:519

B mem:173 cache hit:141

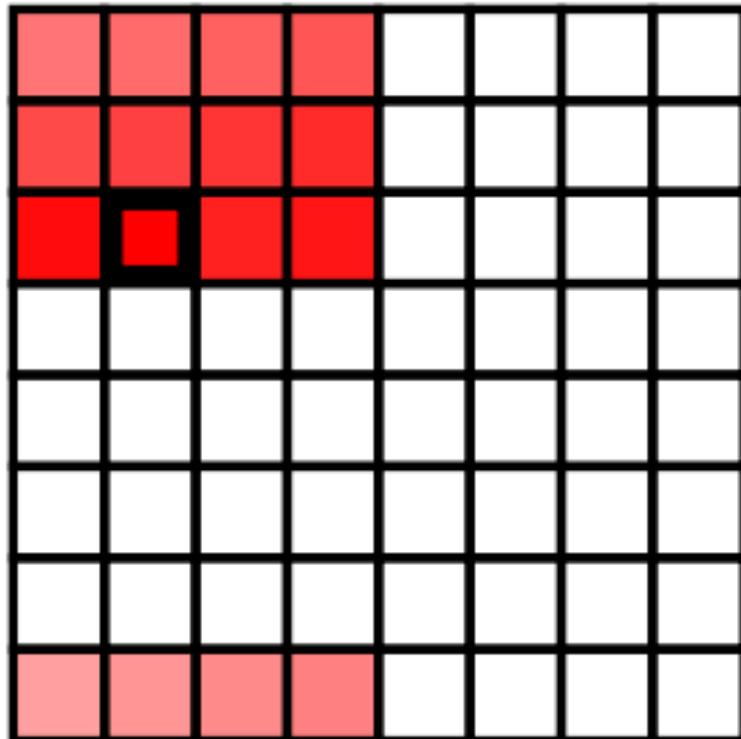


cache hits:399 ≈76%

C mem:173 cache hit:129

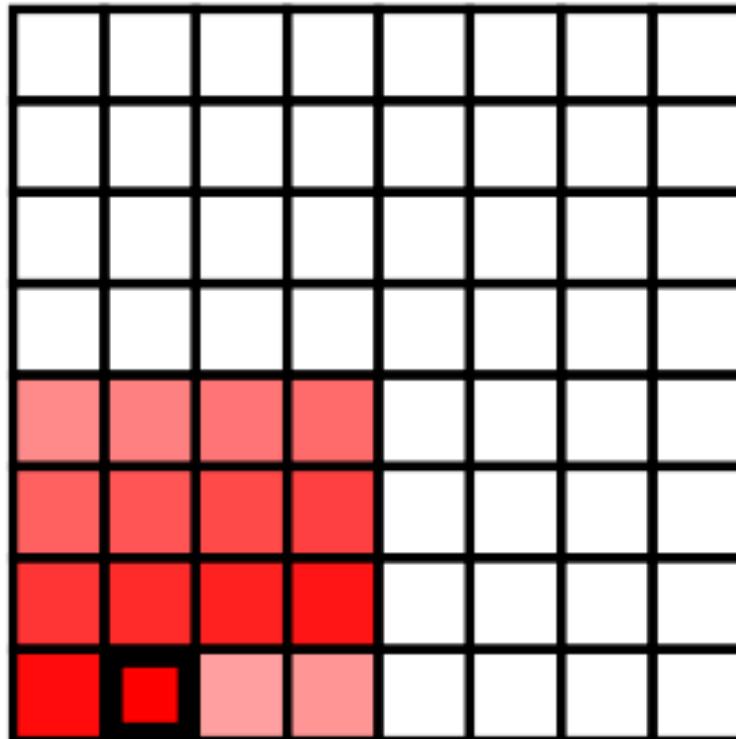


A mem:174 cache hit:130



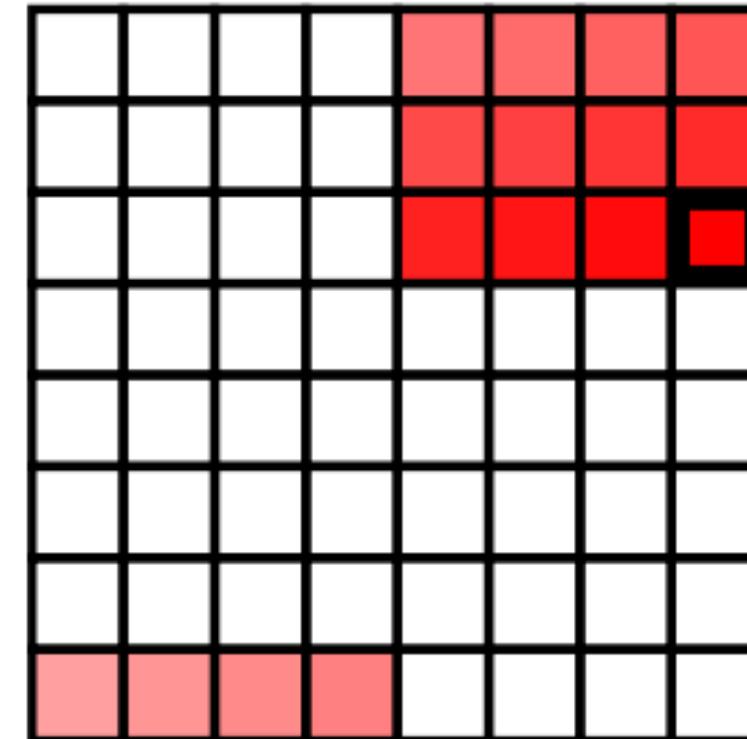
mem:522

B mem:174 cache hit:142

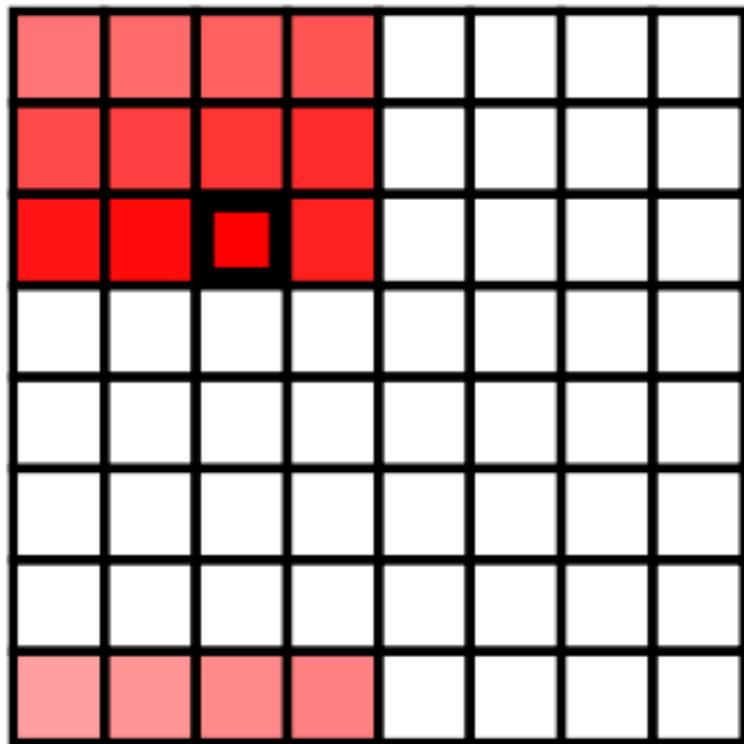


cache hits:402 ≈77%

C mem:174 cache hit:130

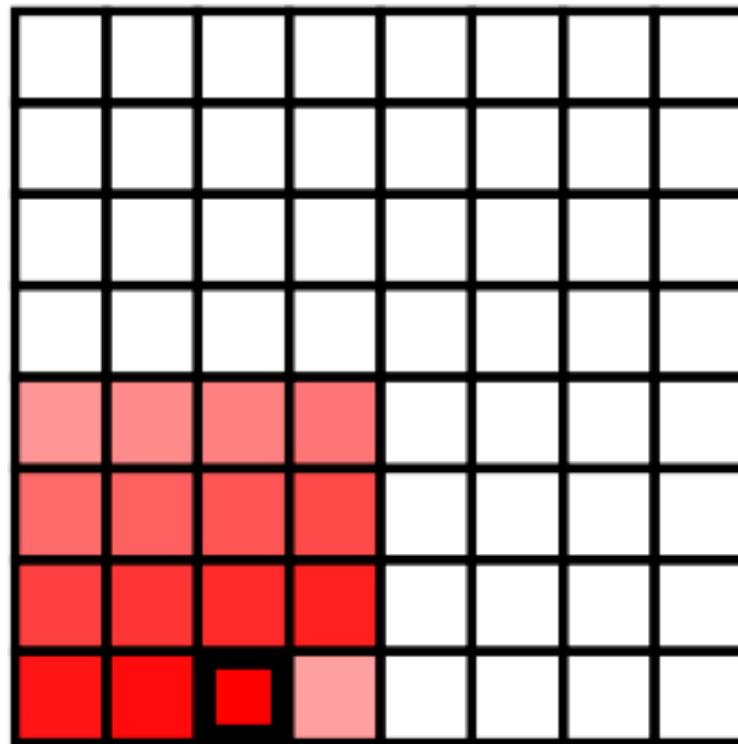


A mem:175 cache hit:131



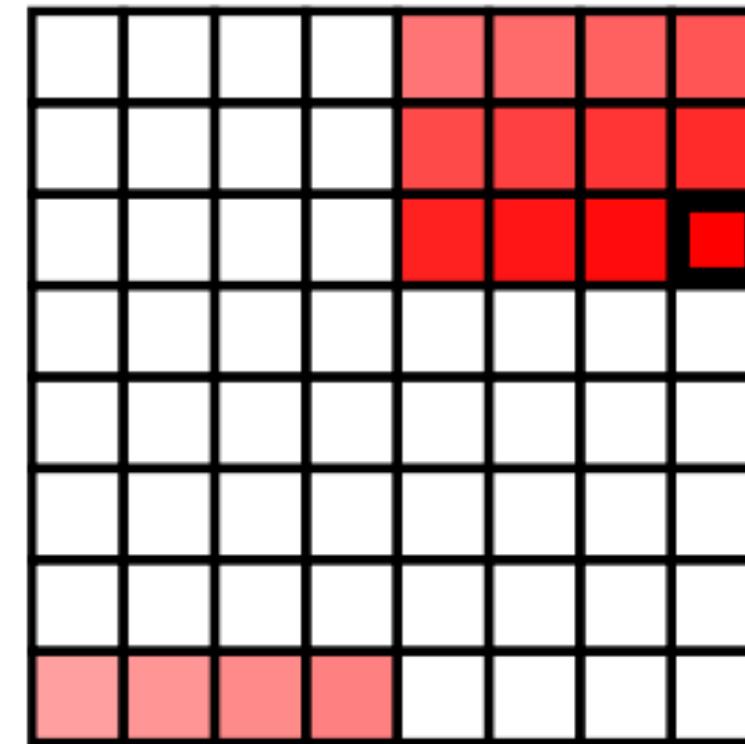
mem:525

B mem:175 cache hit:143

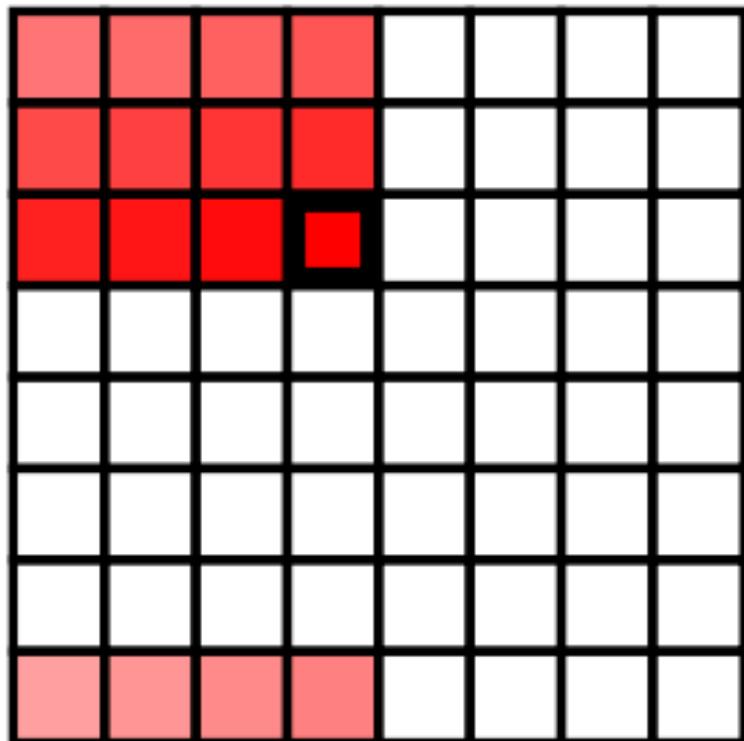


cache hits:405 ≈77%

C mem:175 cache hit:131

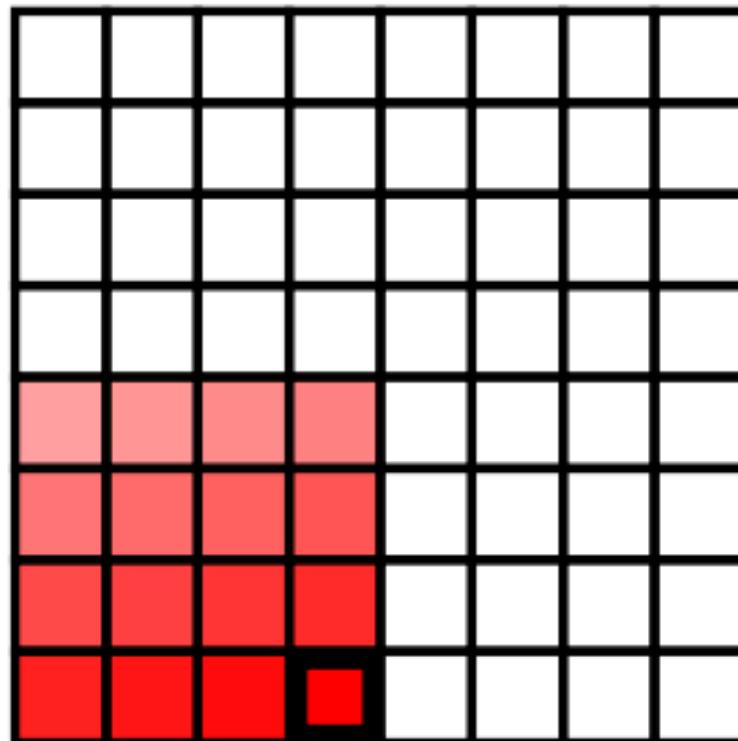


A mem:176 cache hit:132



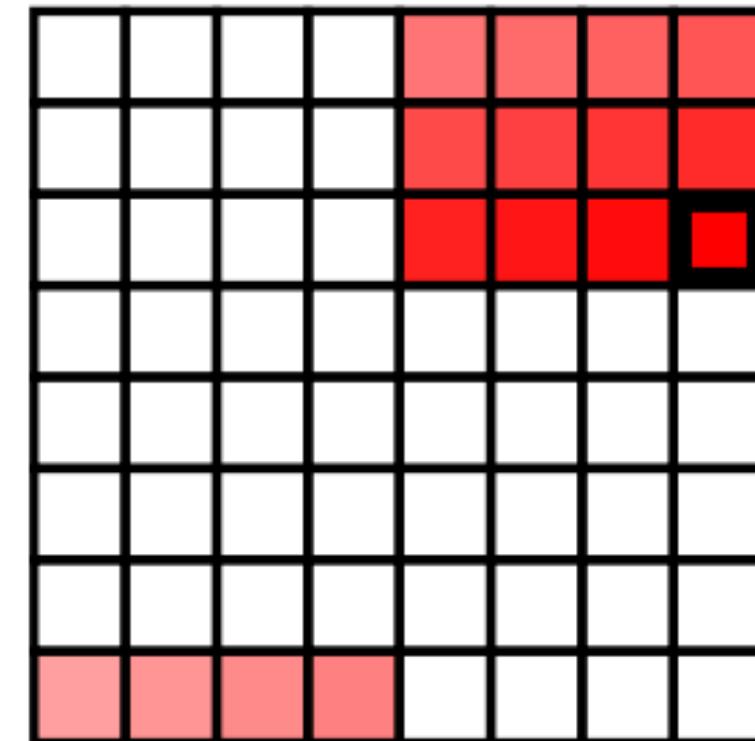
mem:528

B mem:176 cache hit:144

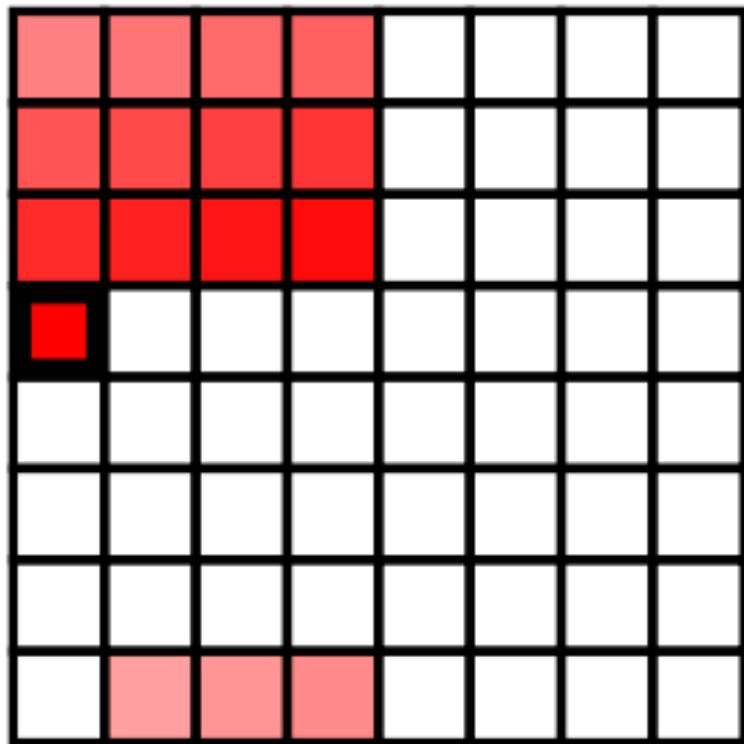


cache hits:408 ≈77%

C mem:176 cache hit:132

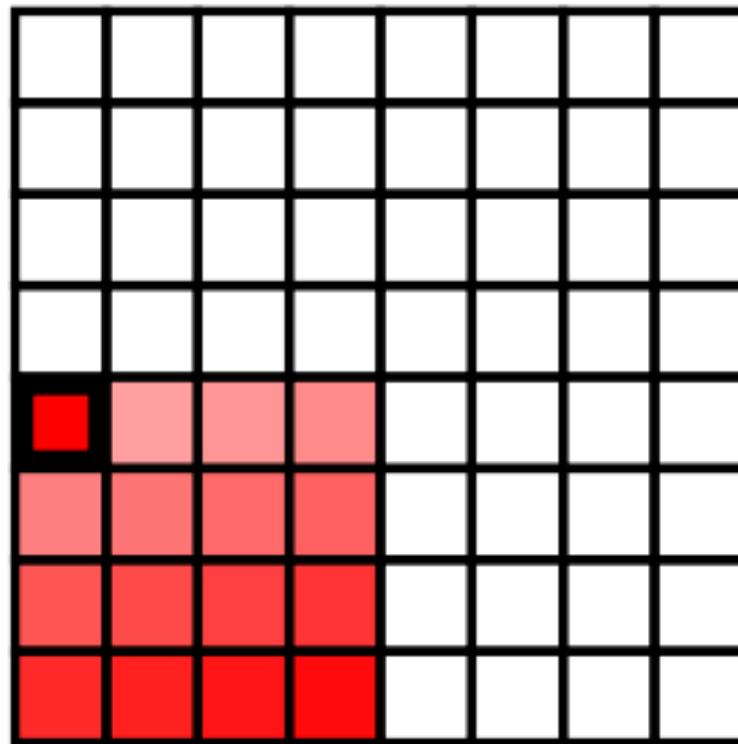


A mem:177 cache hit:132



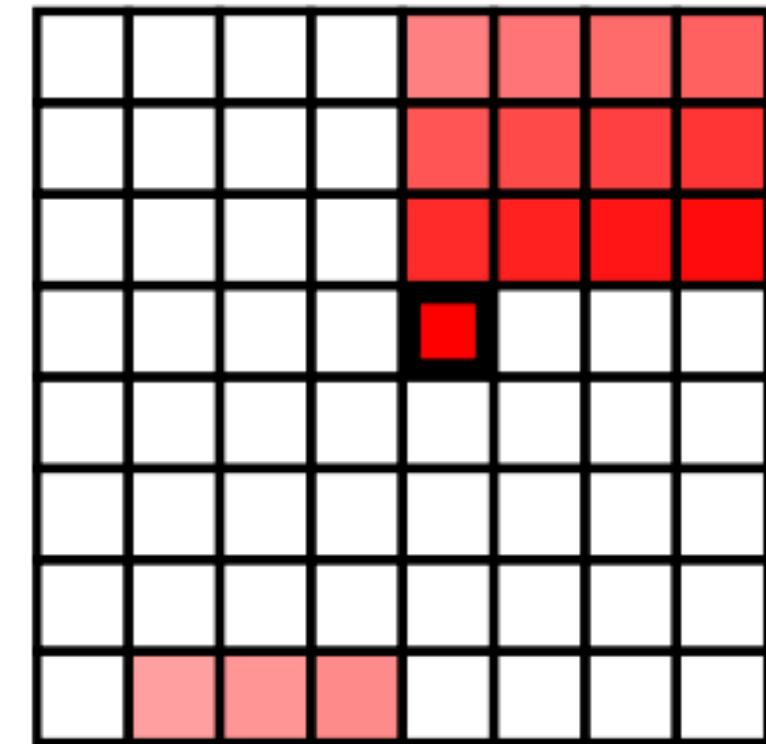
mem:531

B mem:177 cache hit:145

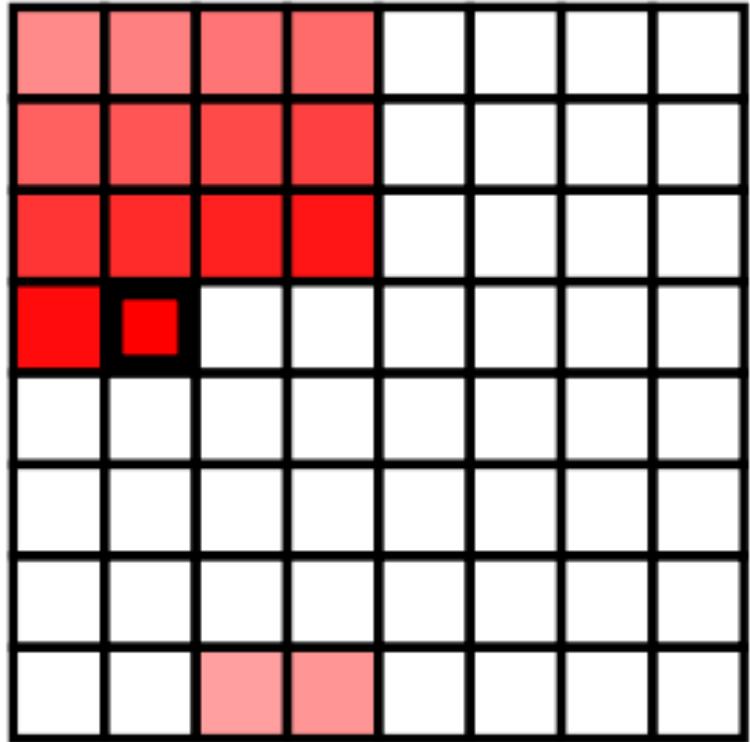


cache hits:409 ≈77%

C mem:177 cache hit:132

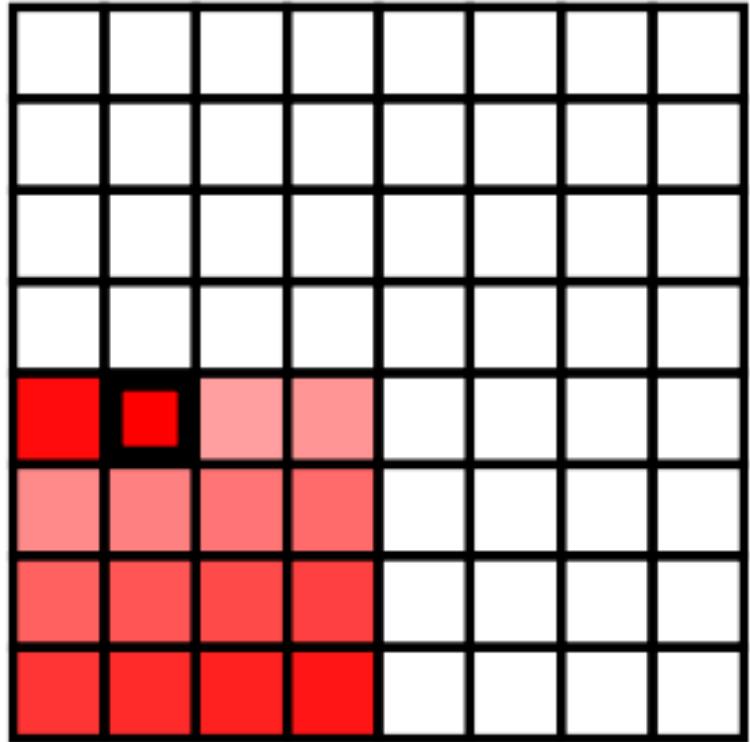


A mem:178 cache hit:132



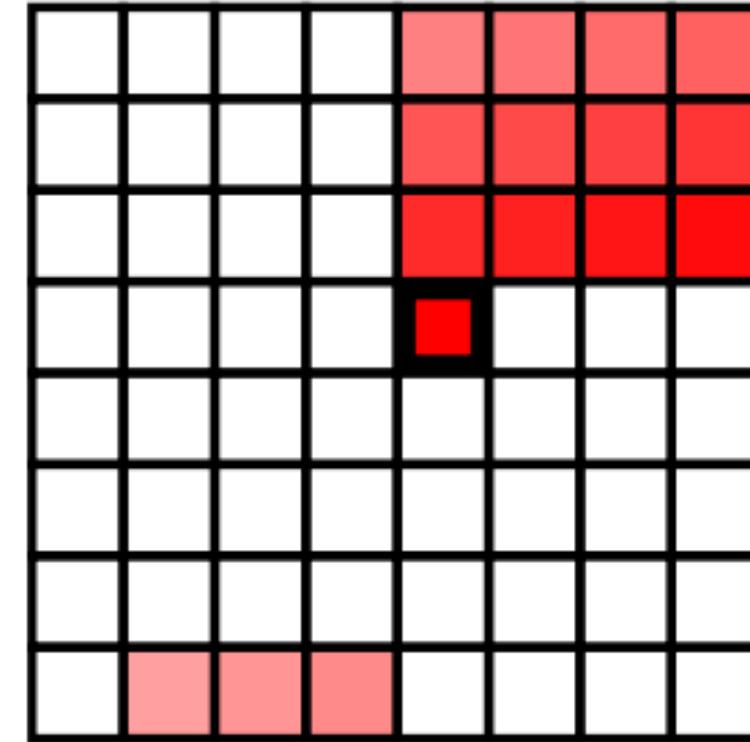
mem:534

B mem:178 cache hit:146

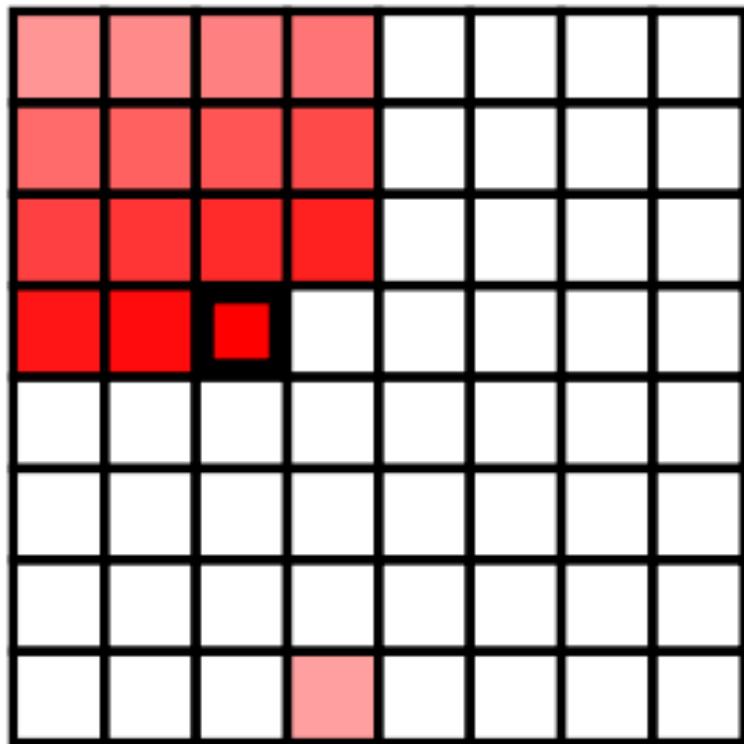


cache hits:411 ≈76%

C mem:178 cache hit:133

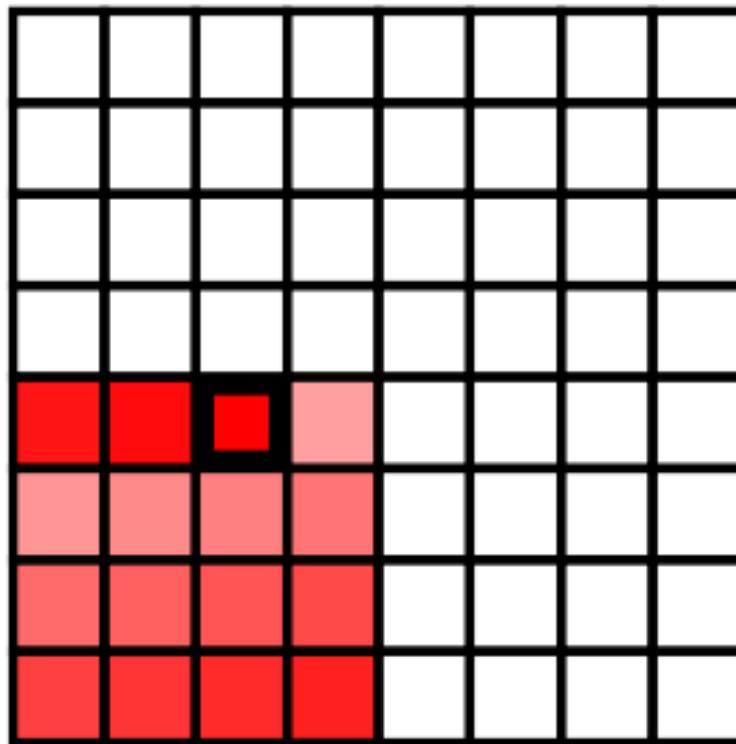


A mem:179 cache hit:132



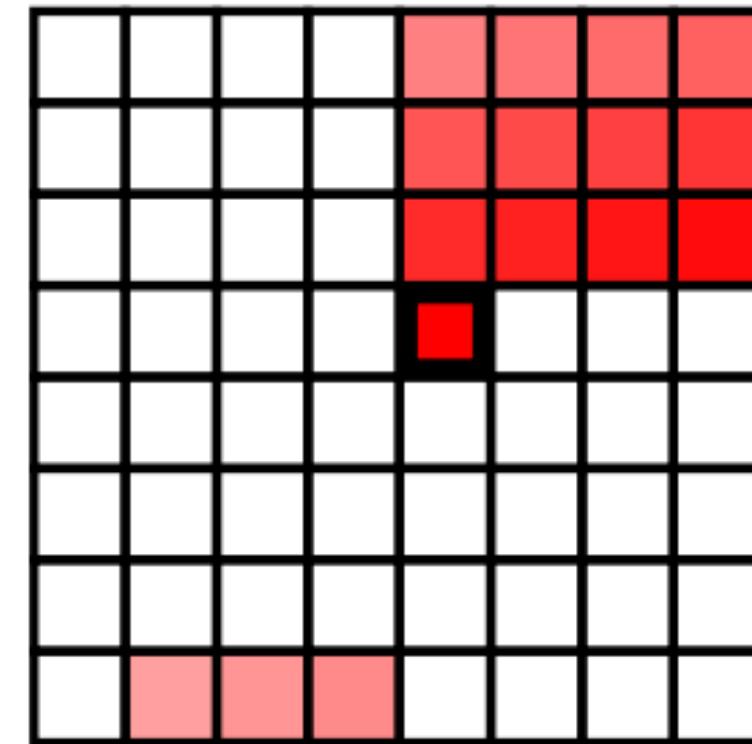
mem:537

B mem:179 cache hit:147

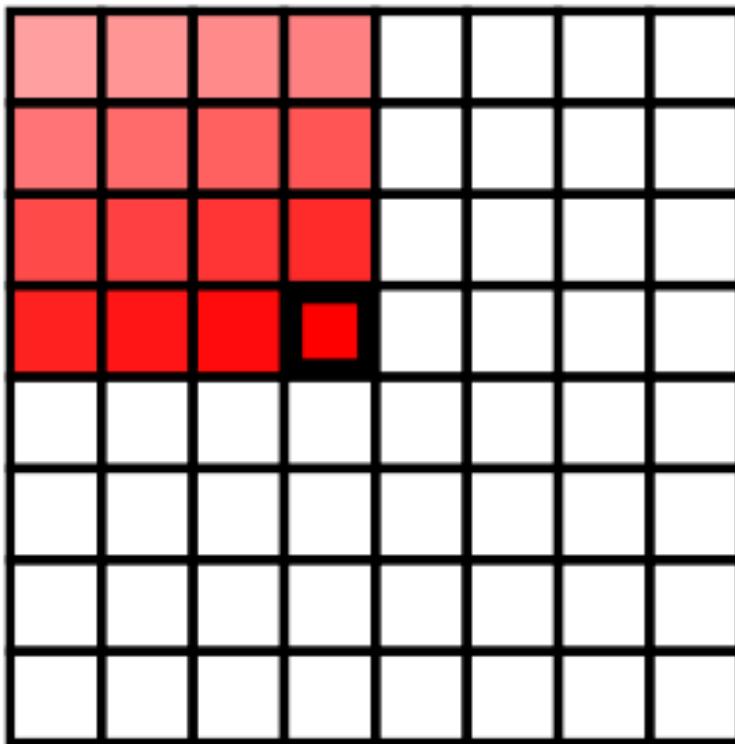


cache hits:413 $\approx 76\%$

C mem:179 cache hit:134

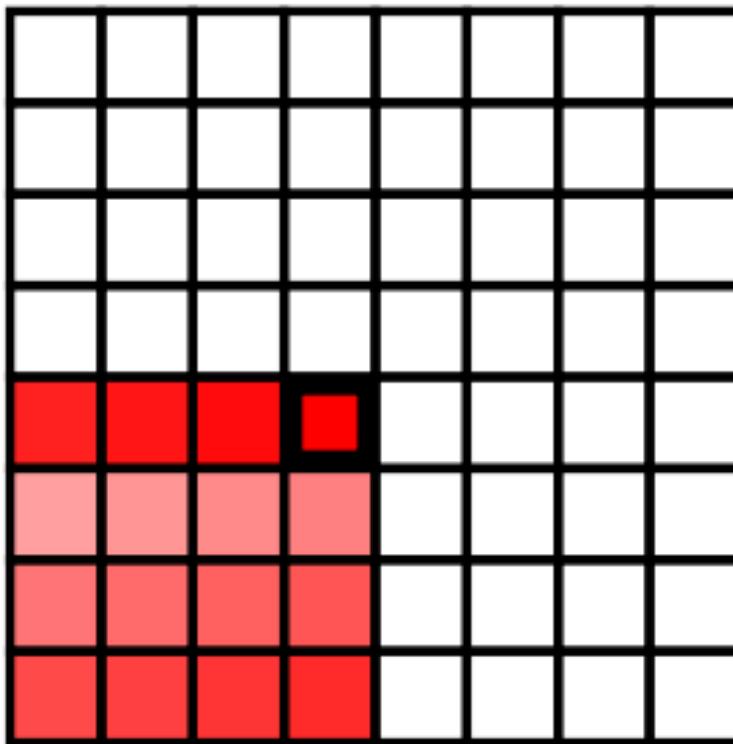


A mem:180 cache hit:132



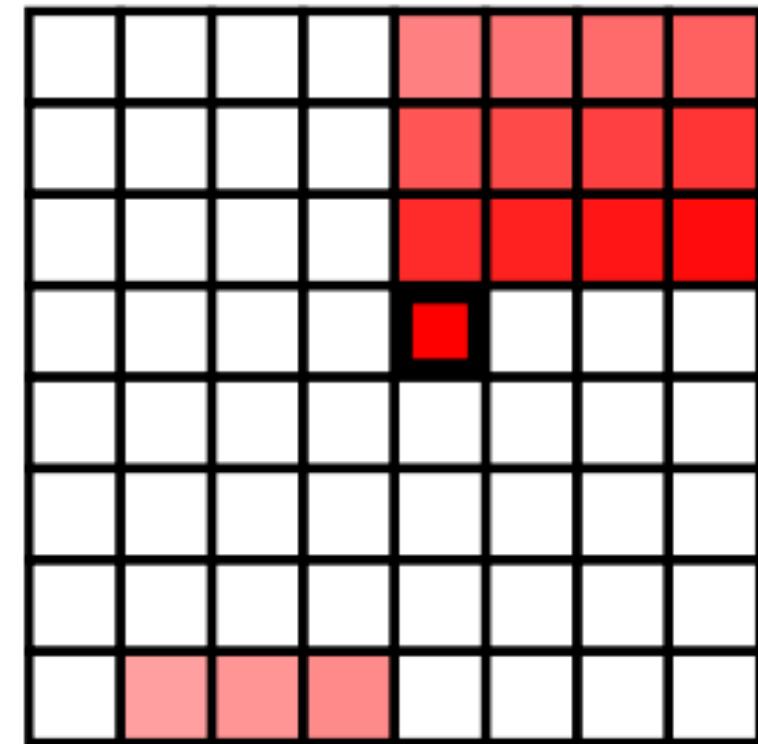
mem:540

B mem:180 cache hit:148

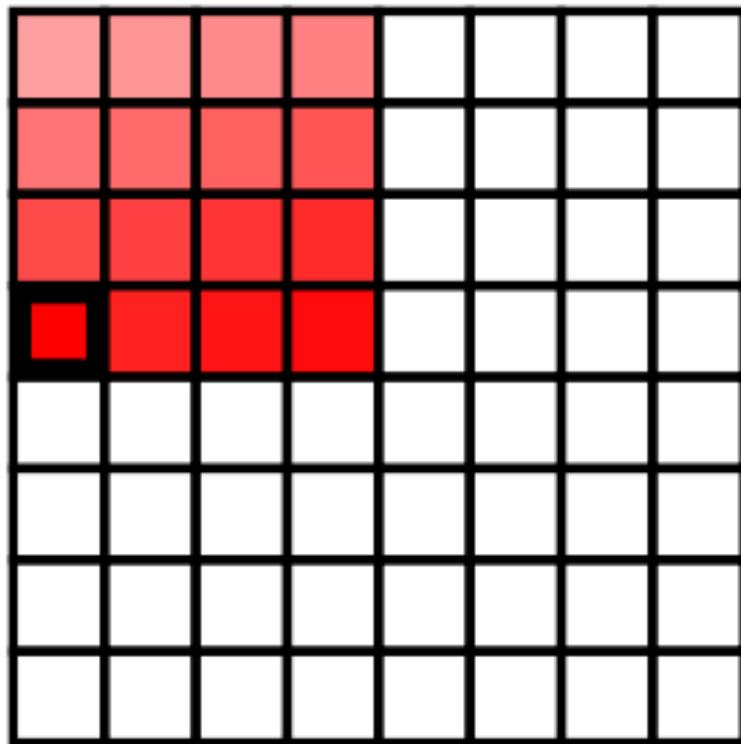


cache hits:415 $\approx 76\%$

C mem:180 cache hit:135

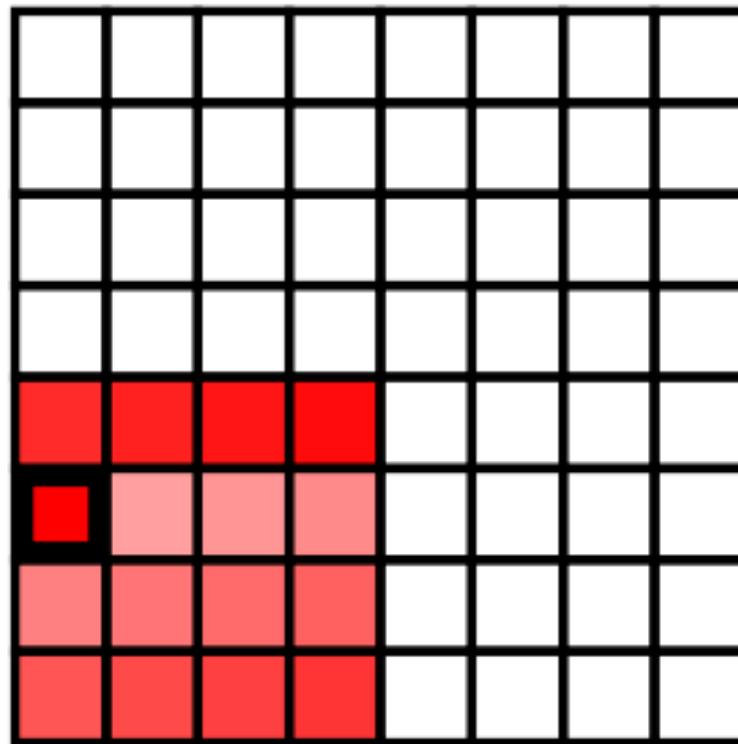


A mem:181 cache hit:133



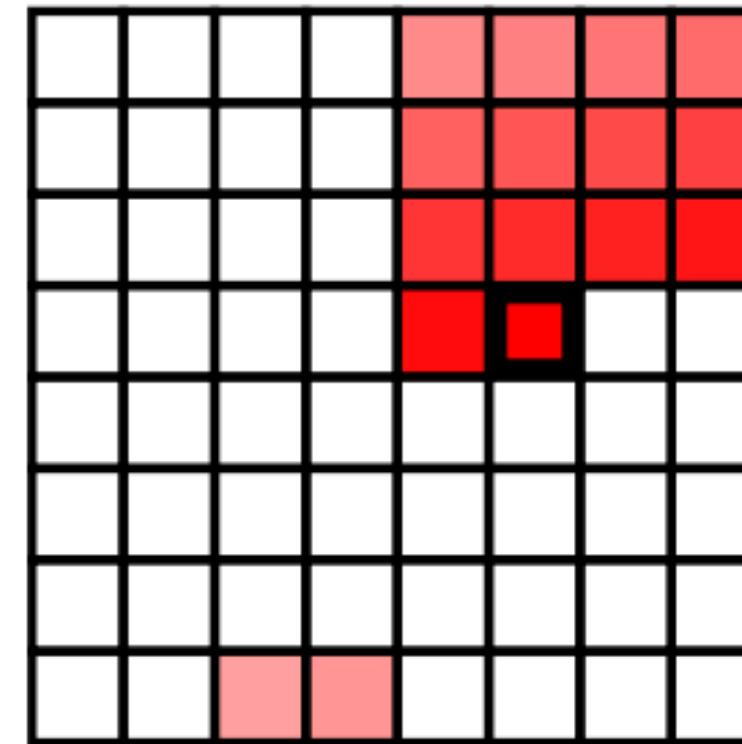
mem:543

B mem:181 cache hit:149

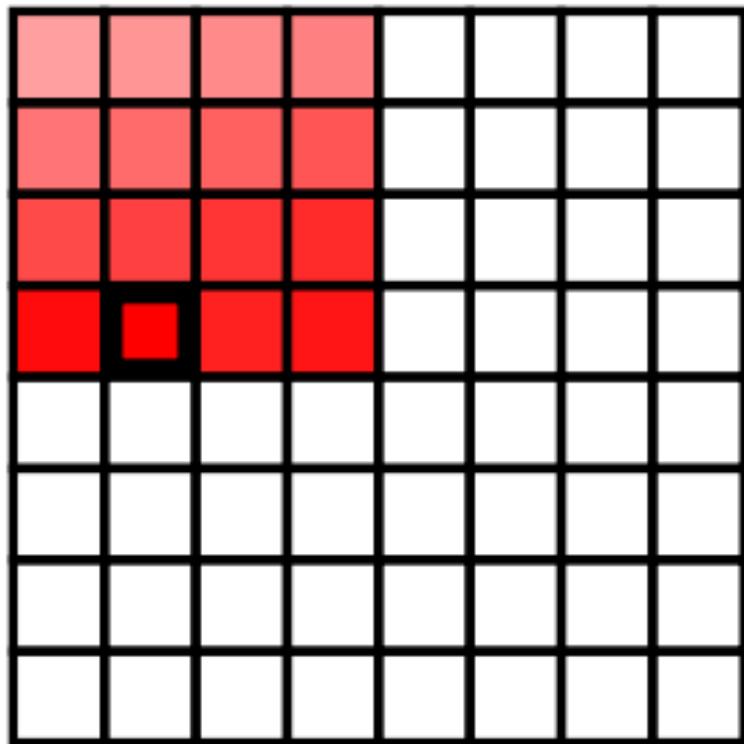


cache hits:417 $\approx 76\%$

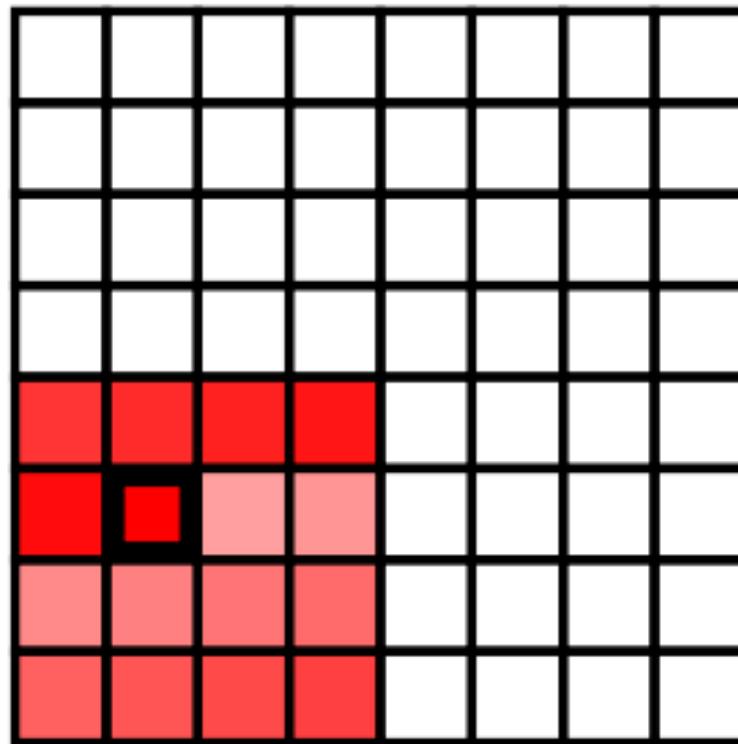
C mem:181 cache hit:135



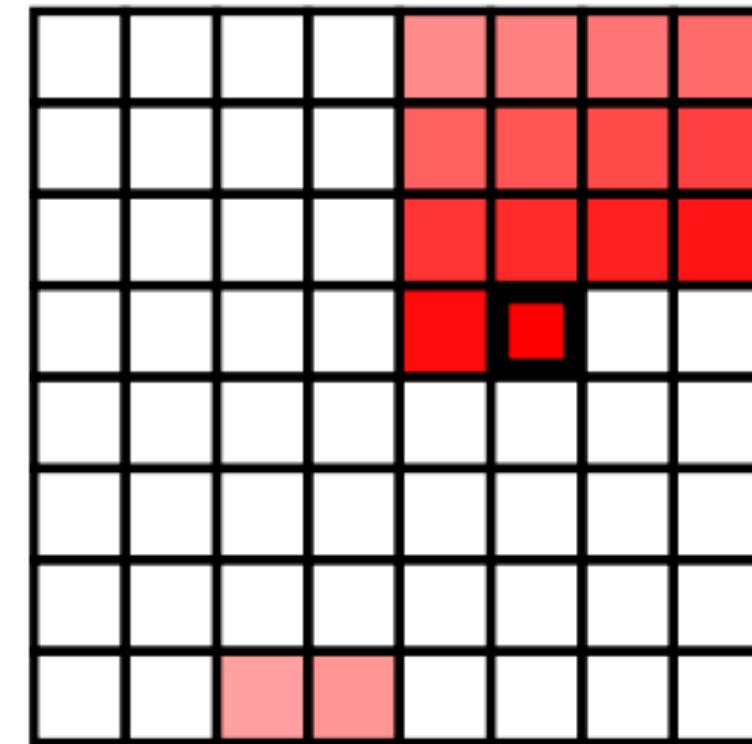
A mem:182 cache hit:134



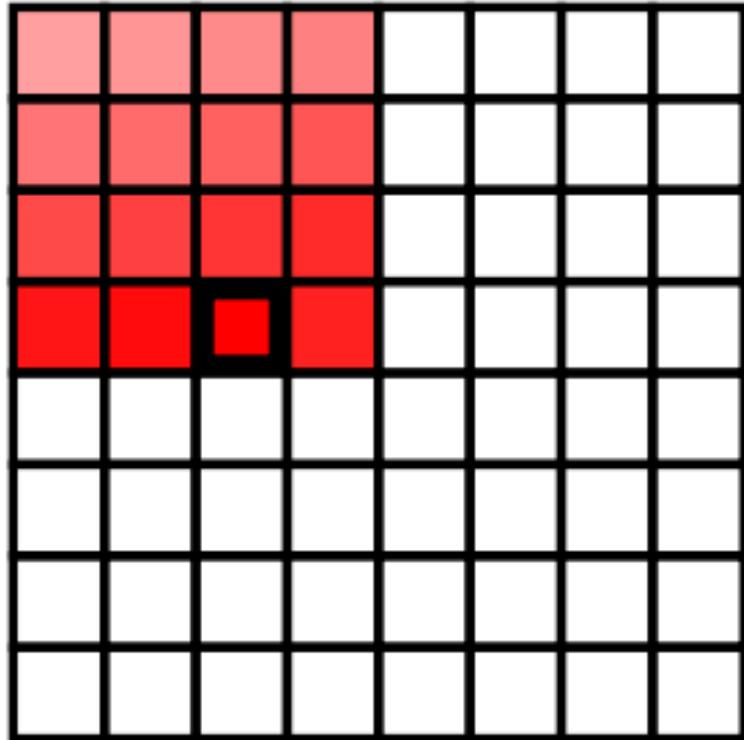
B mem:182 cache hit:150



C mem:182 cache hit:136

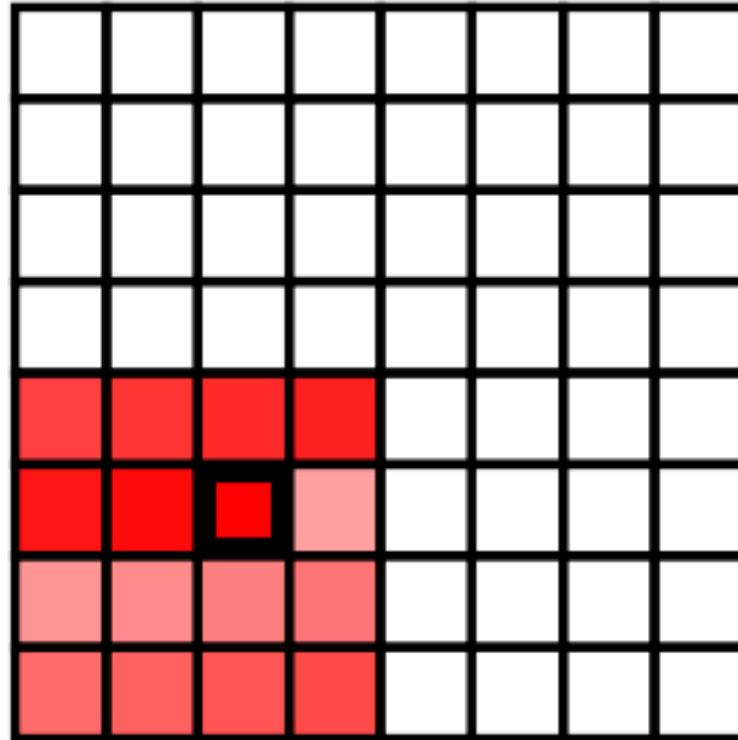


A mem:183 cache hit:135



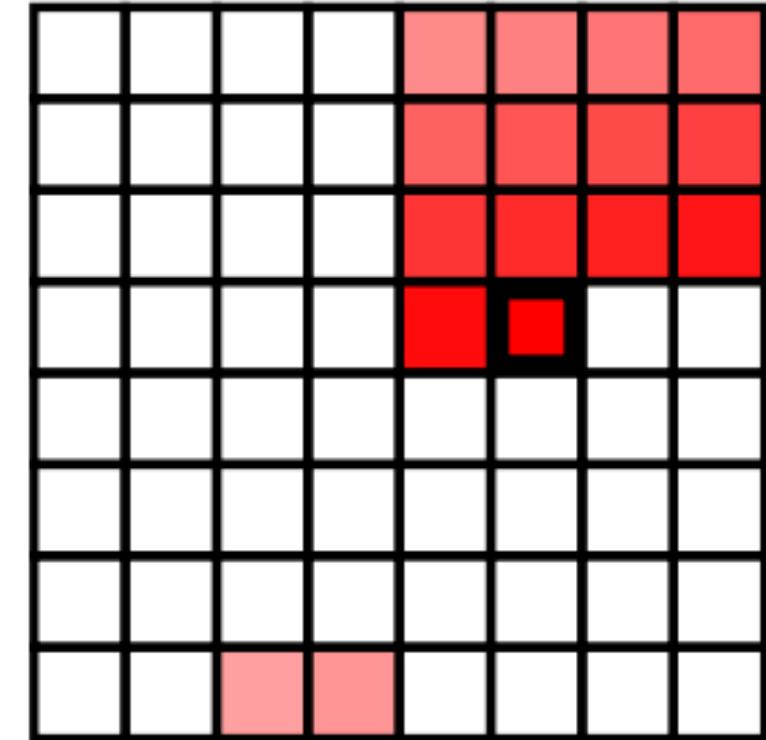
mem:549

B mem:183 cache hit:151

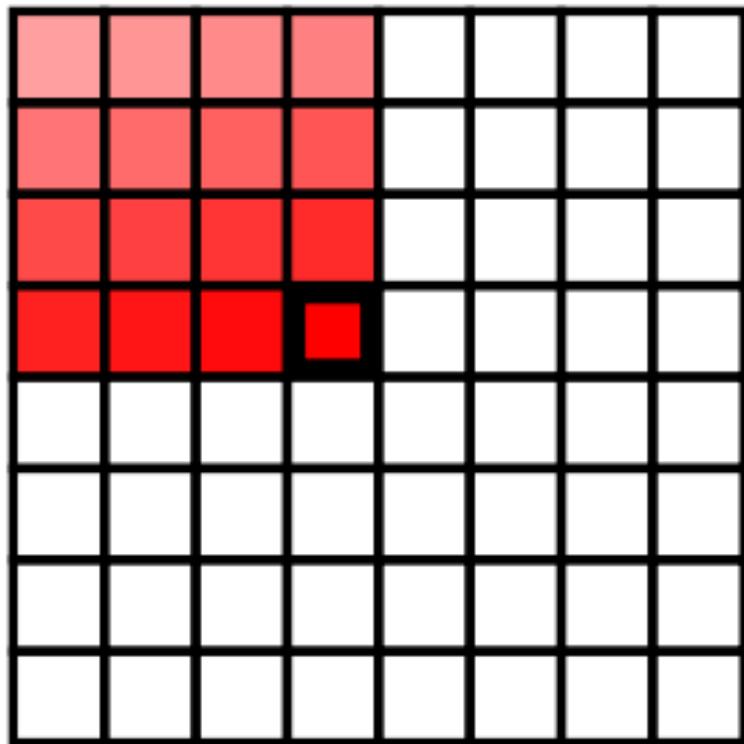


cache hits:423 ≈77%

C mem:183 cache hit:137

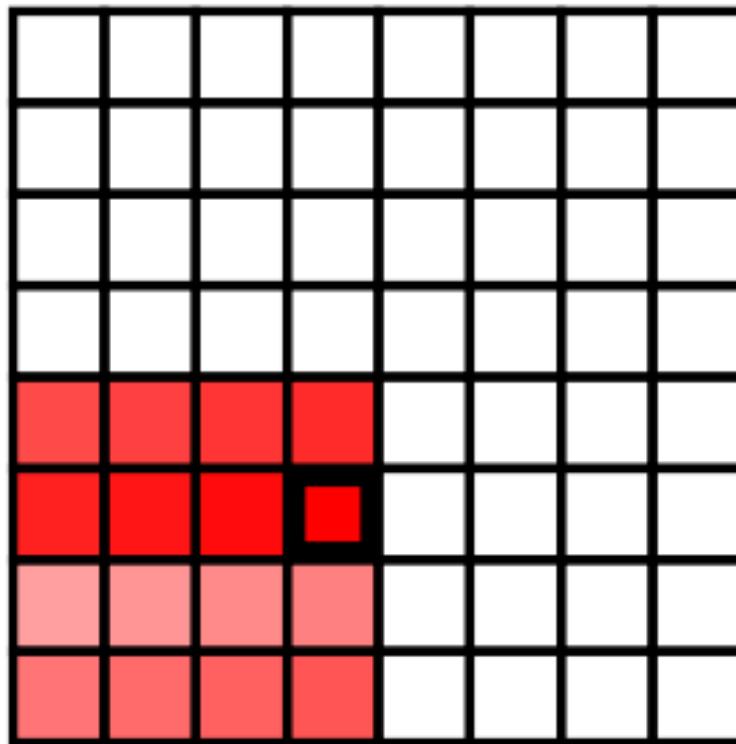


A mem:184 cache hit:136



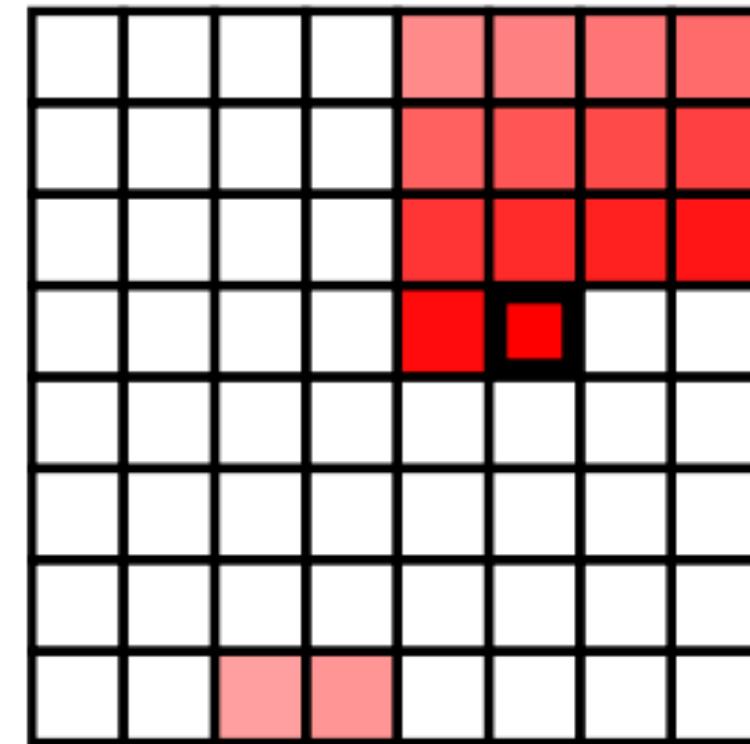
mem:552

B mem:184 cache hit:152

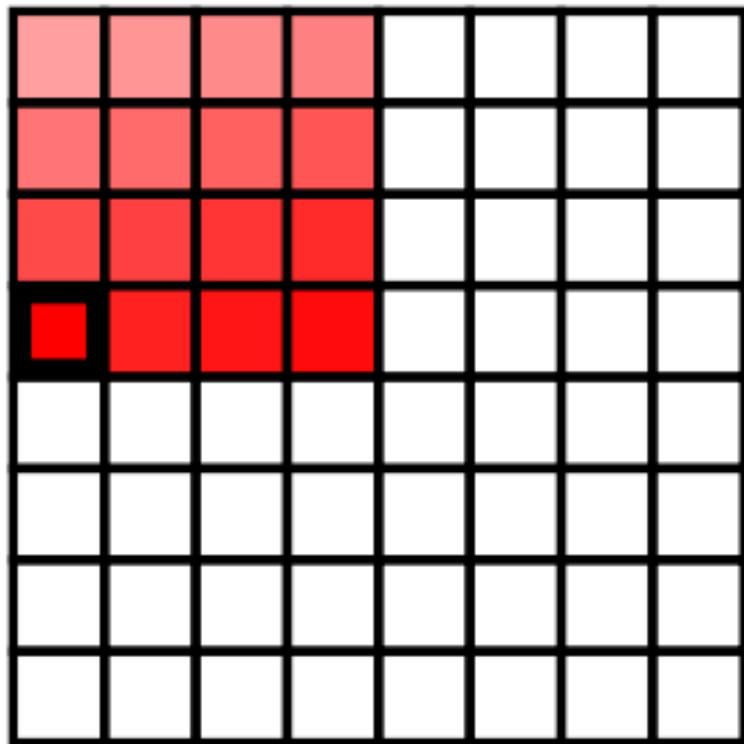


cache hits:426 ≈77%

C mem:184 cache hit:138

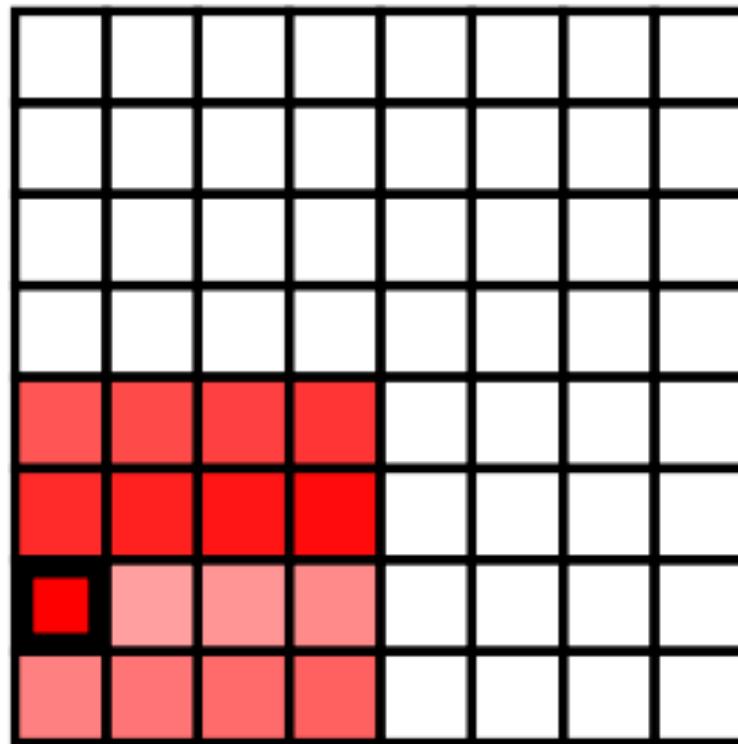


A mem:185 cache hit:137



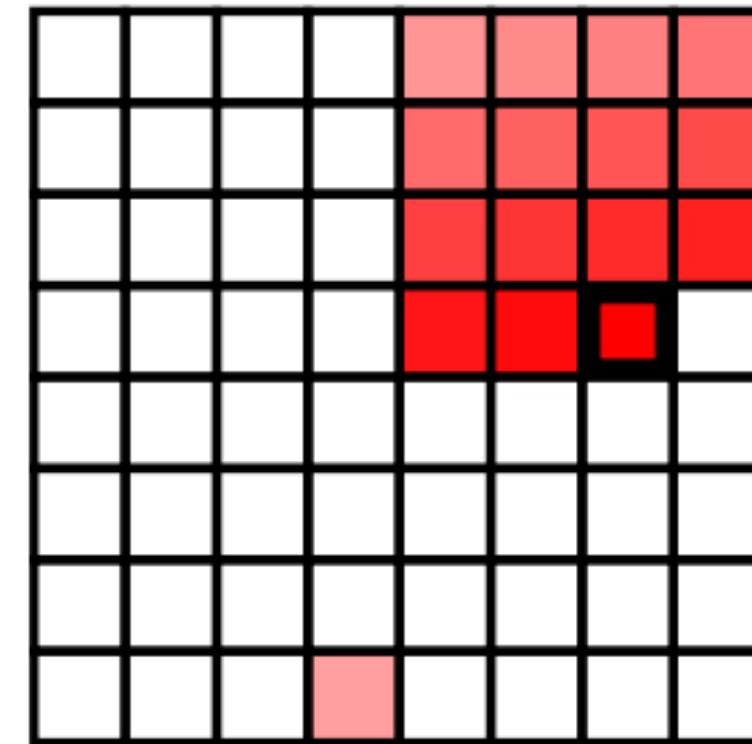
mem:555

B mem:185 cache hit:153

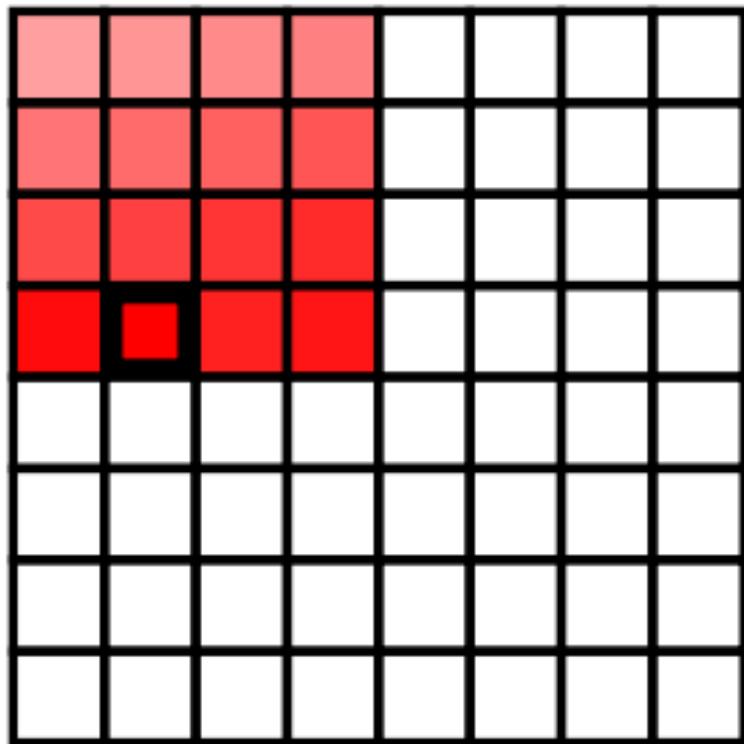


cache hits:428 ≈77%

C mem:185 cache hit:138

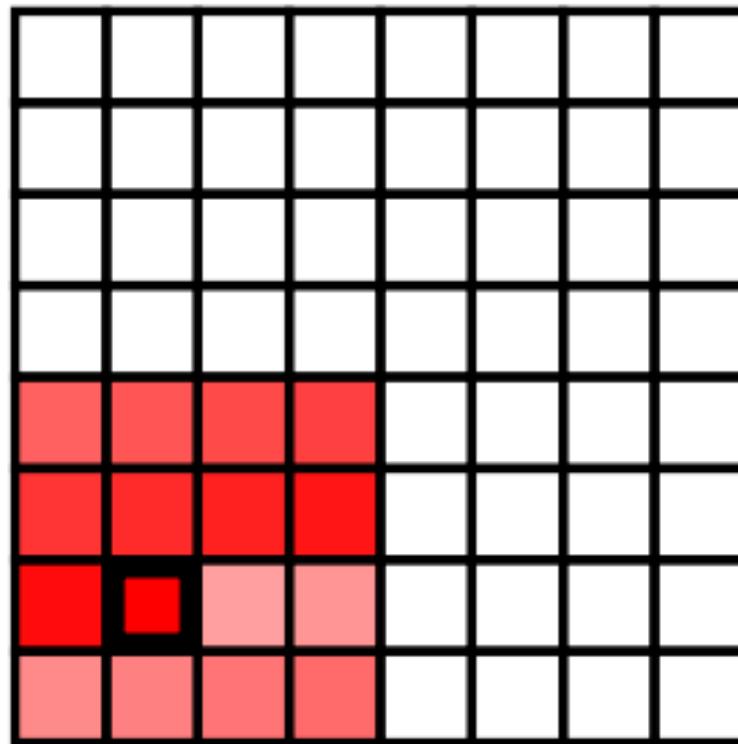


A mem:186 cache hit:138



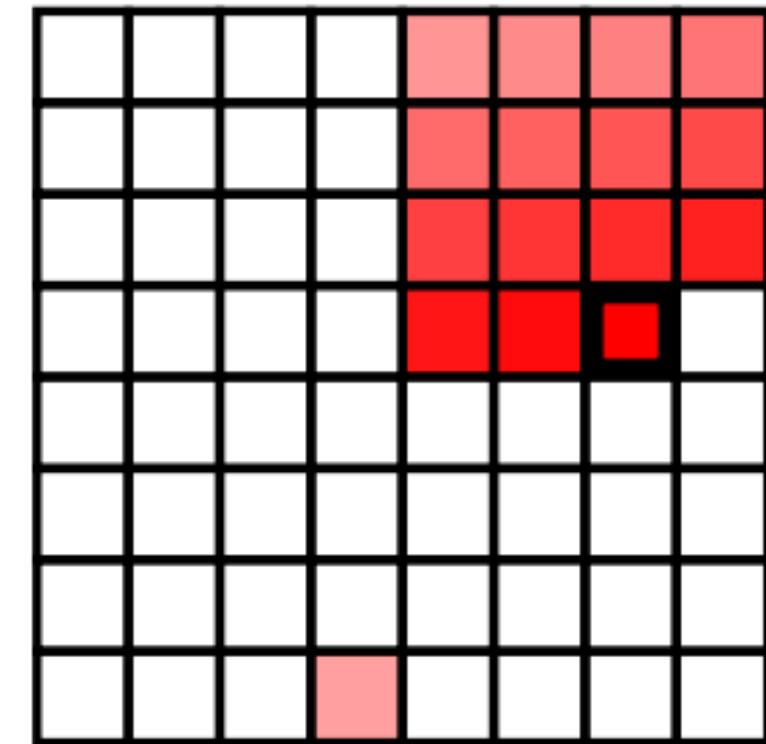
mem:558

B mem:186 cache hit:154

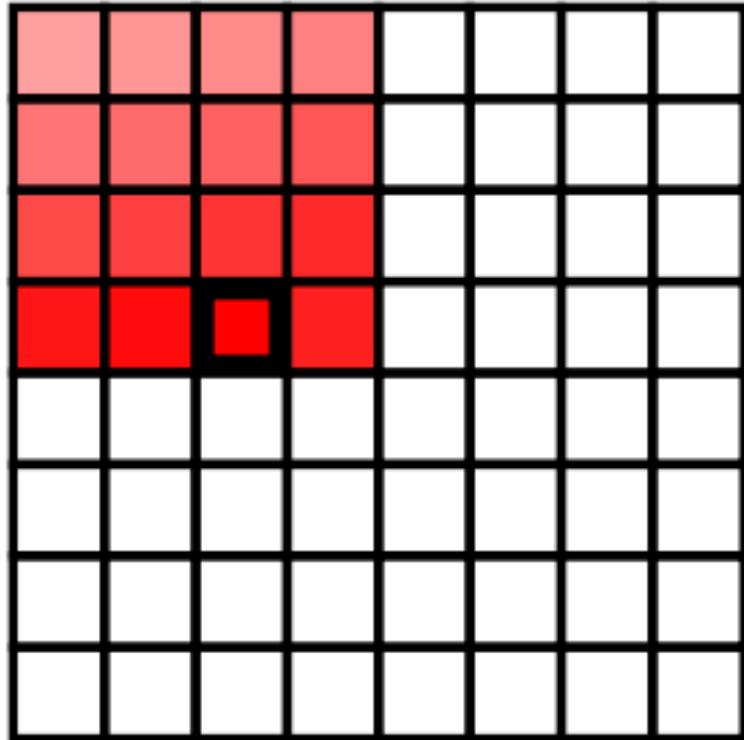


cache hits:431 ≈77%

C mem:186 cache hit:139

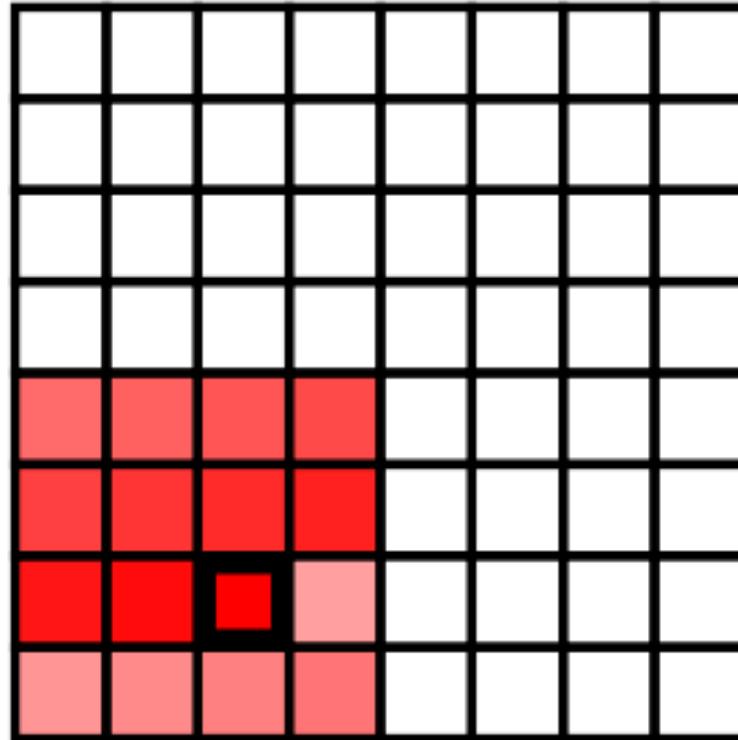


A mem:187 cache hit:139



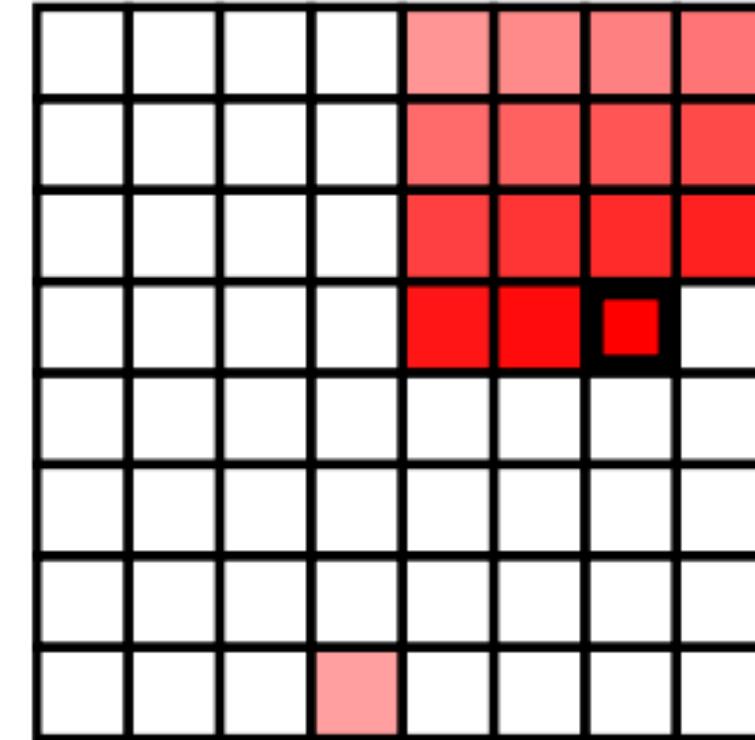
mem:561

B mem:187 cache hit:155

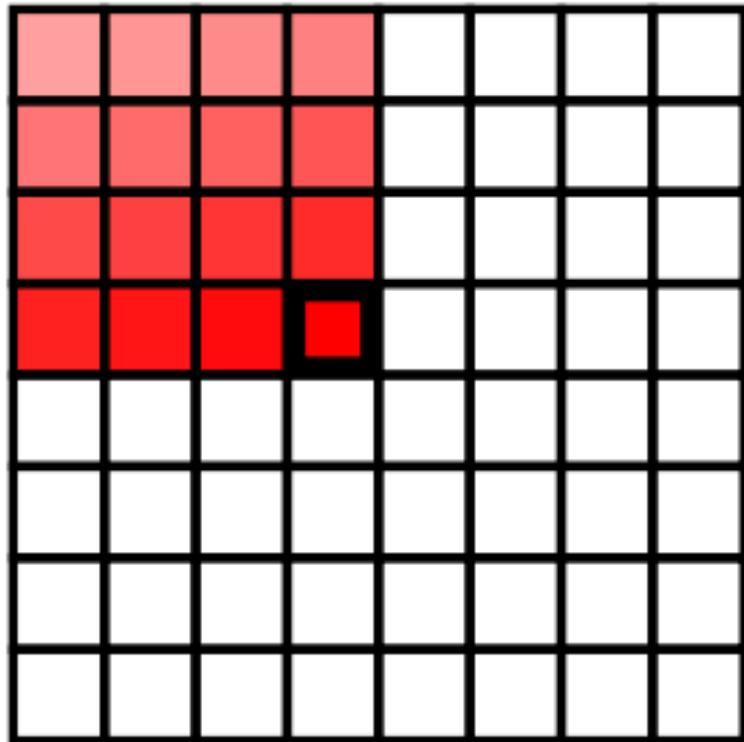


cache hits:434 ≈77%

C mem:187 cache hit:140

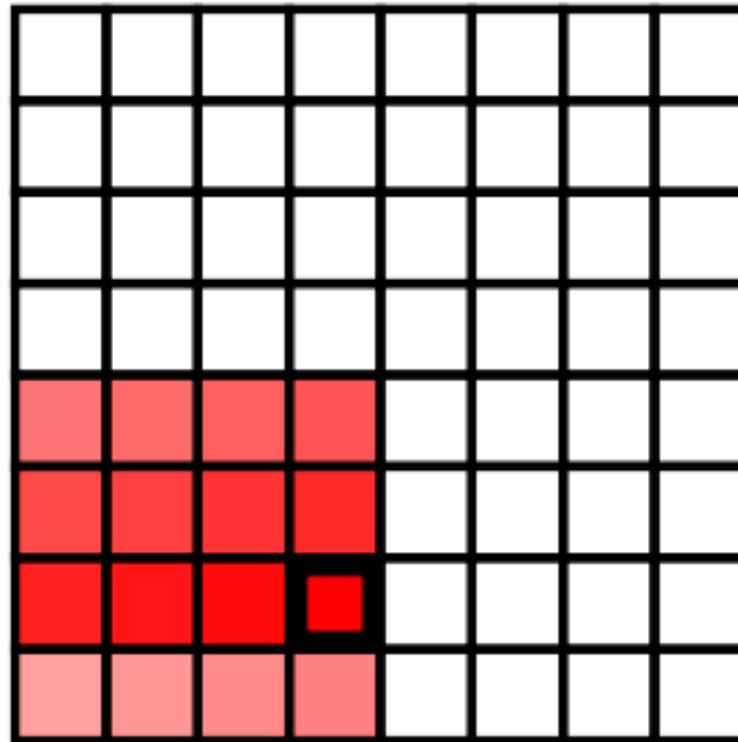


A mem:188 cache hit:140



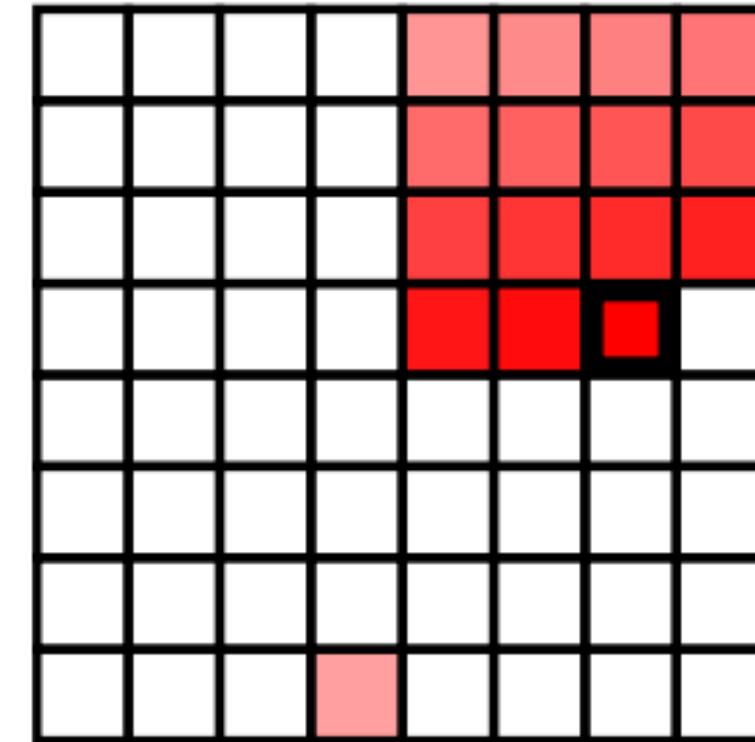
mem:564

B mem:188 cache hit:156

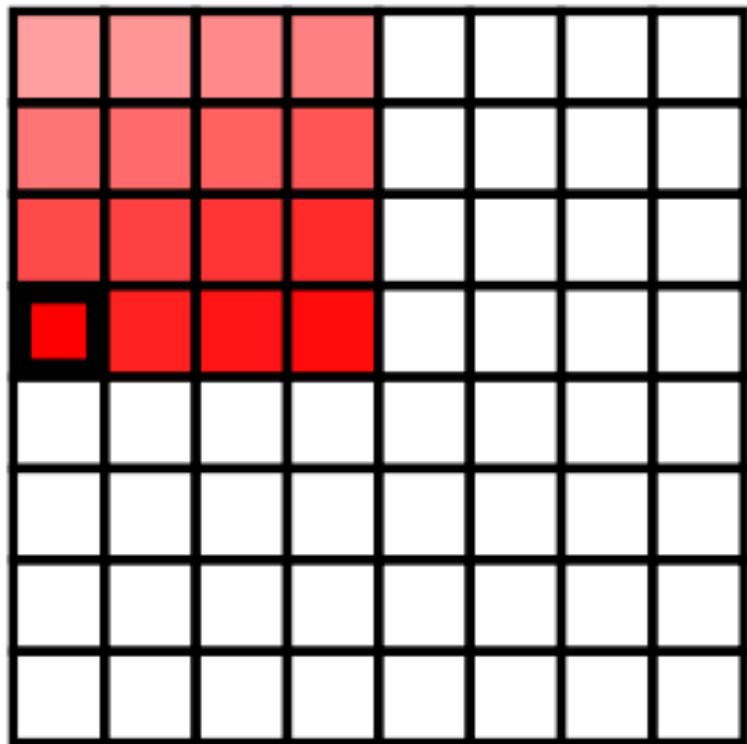


cache hits:437 ≈77%

C mem:188 cache hit:141

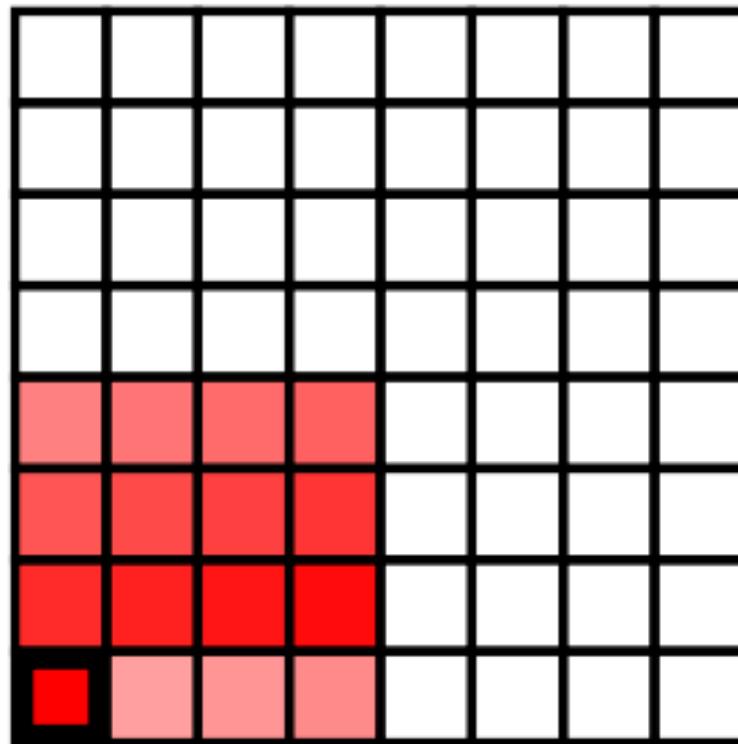


A mem:189 cache hit:141



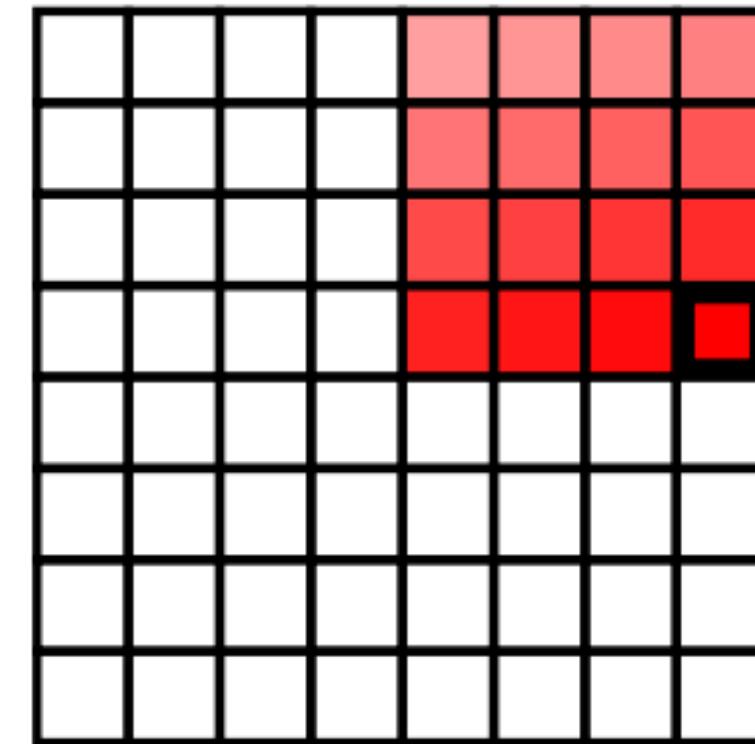
mem:567

B mem:189 cache hit:157

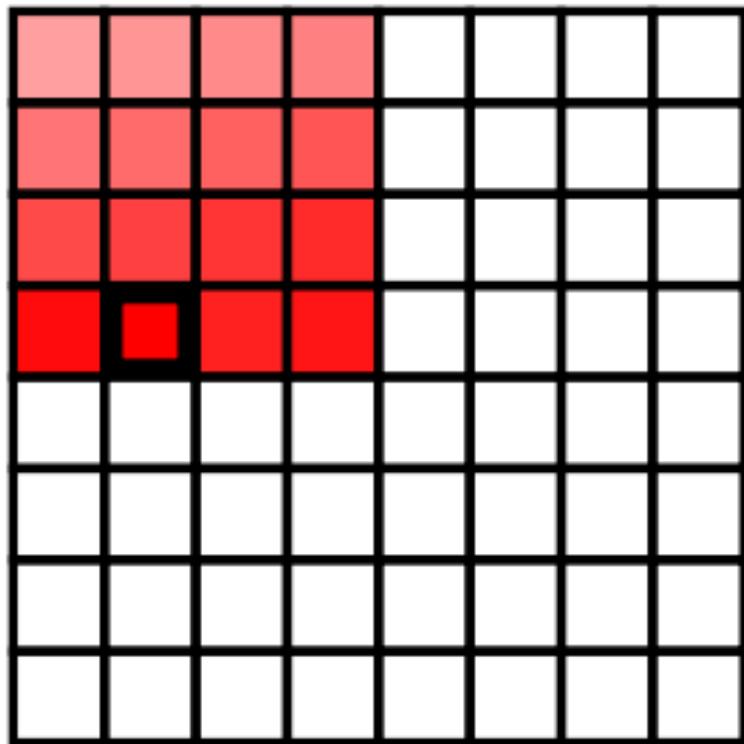


cache hits:439 ≈77%

C mem:189 cache hit:141

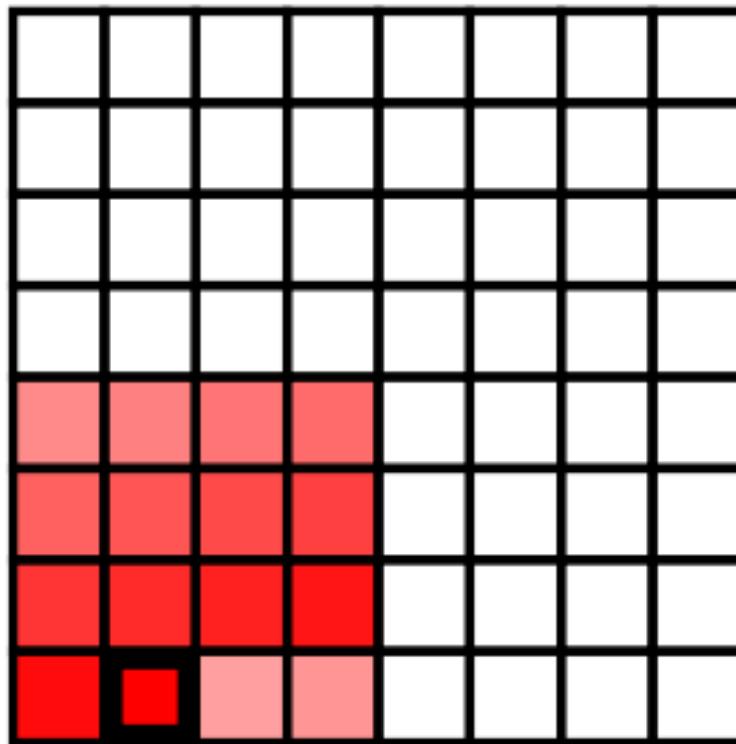


A mem:190 cache hit:142



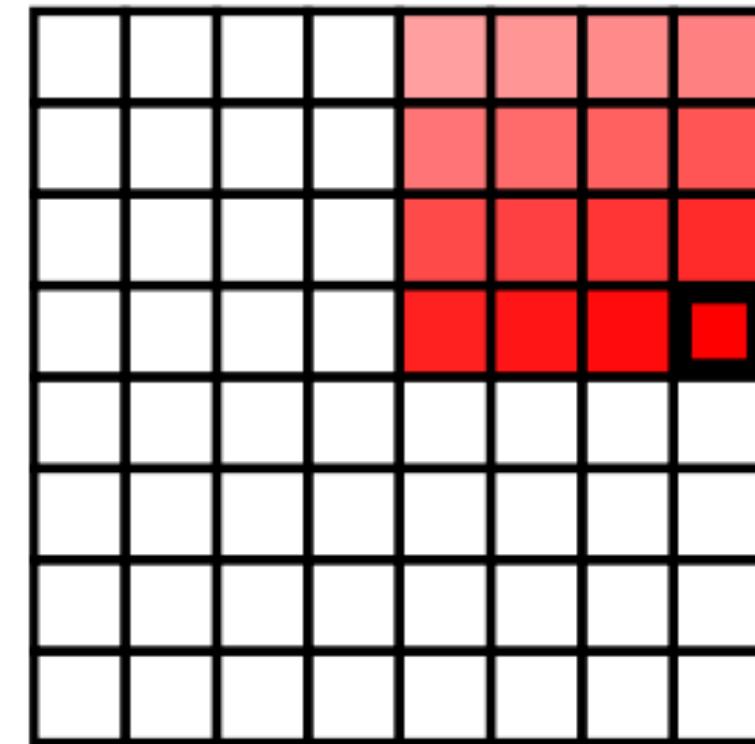
mem:570

B mem:190 cache hit:158

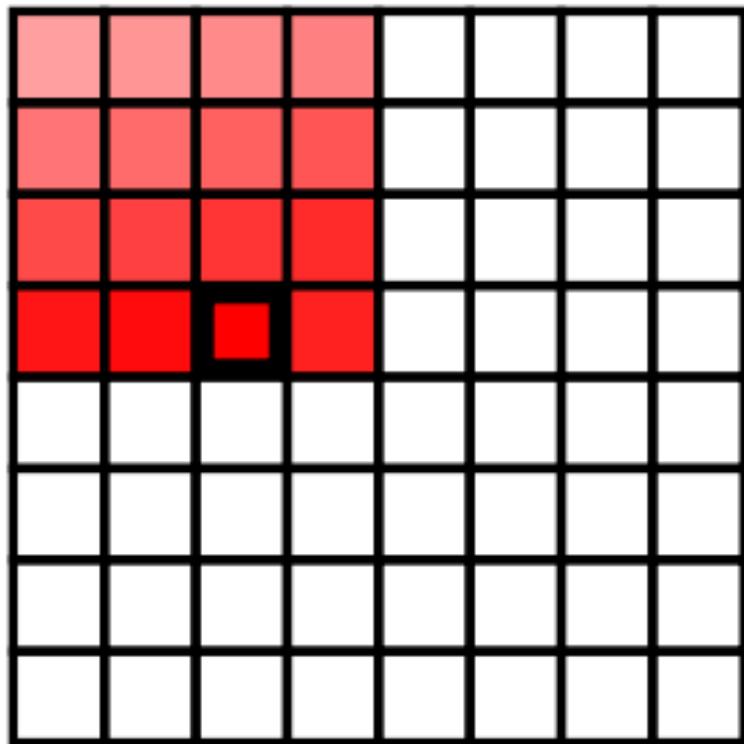


cache hits:442 $\approx 77\%$

C mem:190 cache hit:142

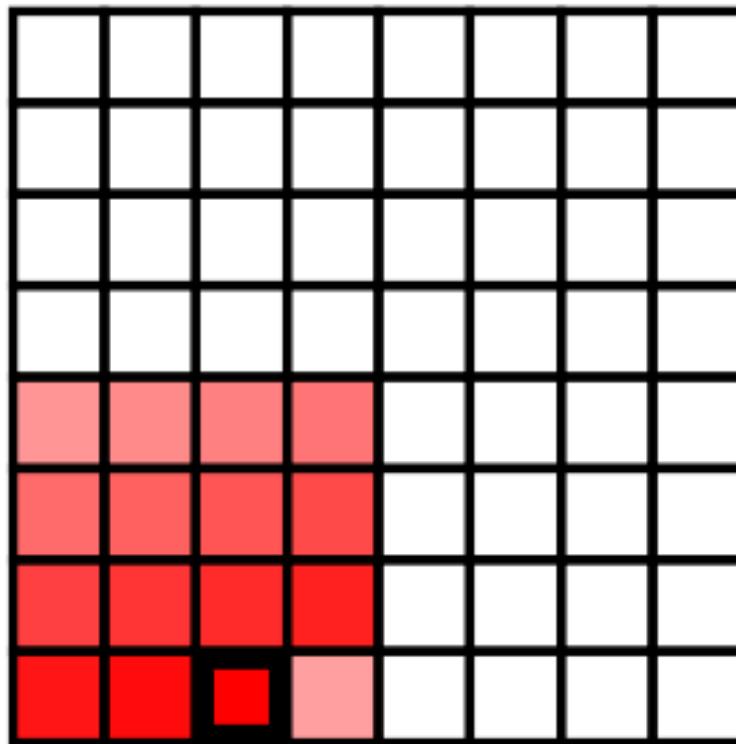


A mem:191 cache hit:143



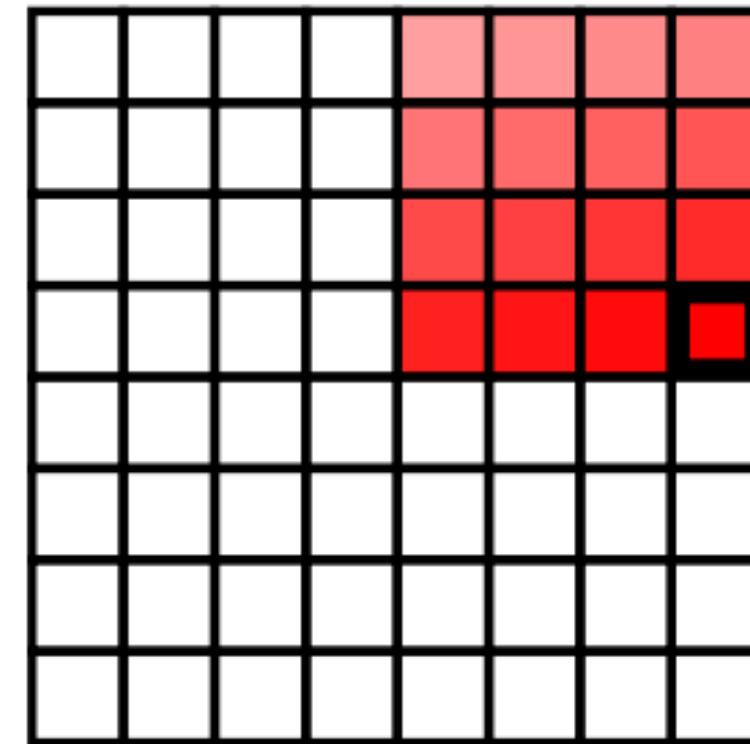
mem:573

B mem:191 cache hit:159

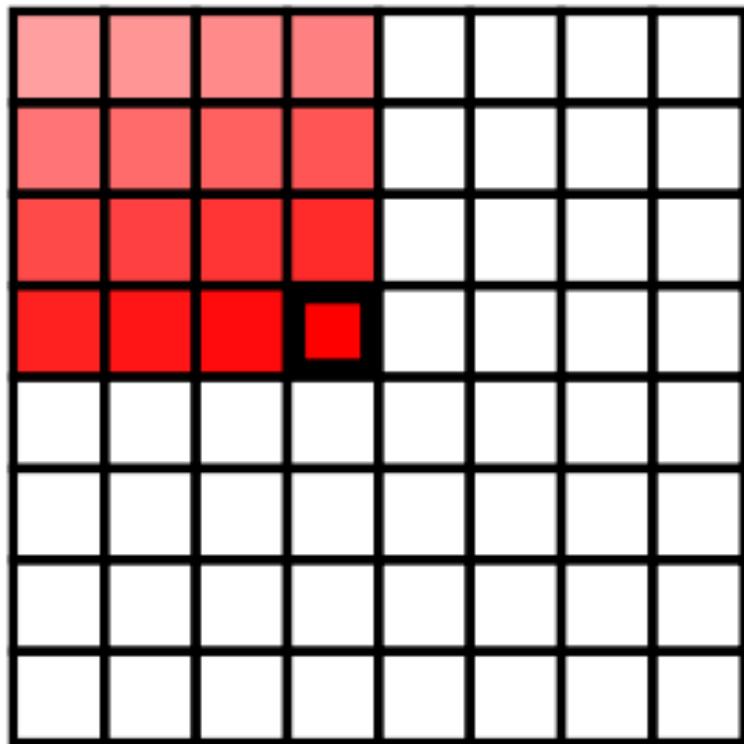


cache hits:445 ≈77%

C mem:191 cache hit:143

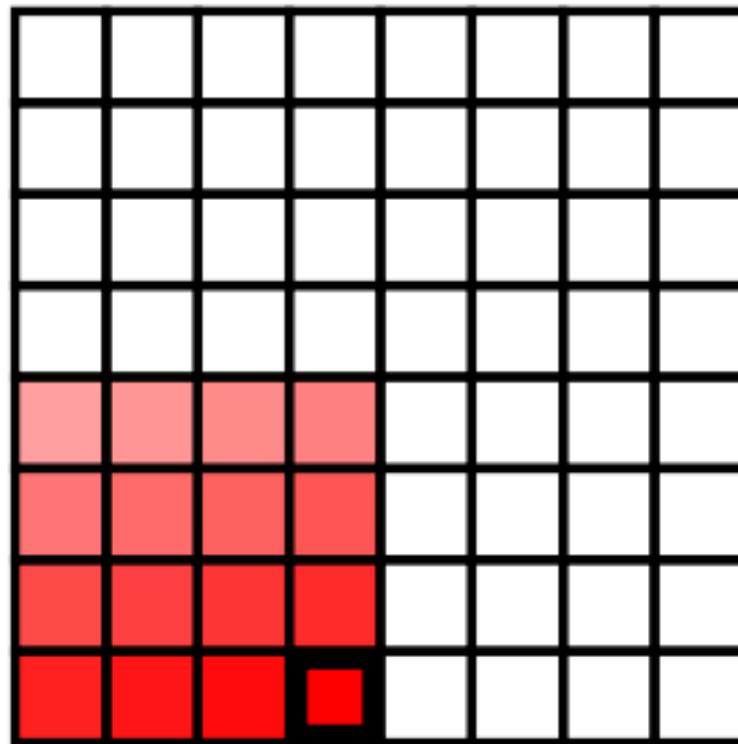


A mem:192 cache hit:144



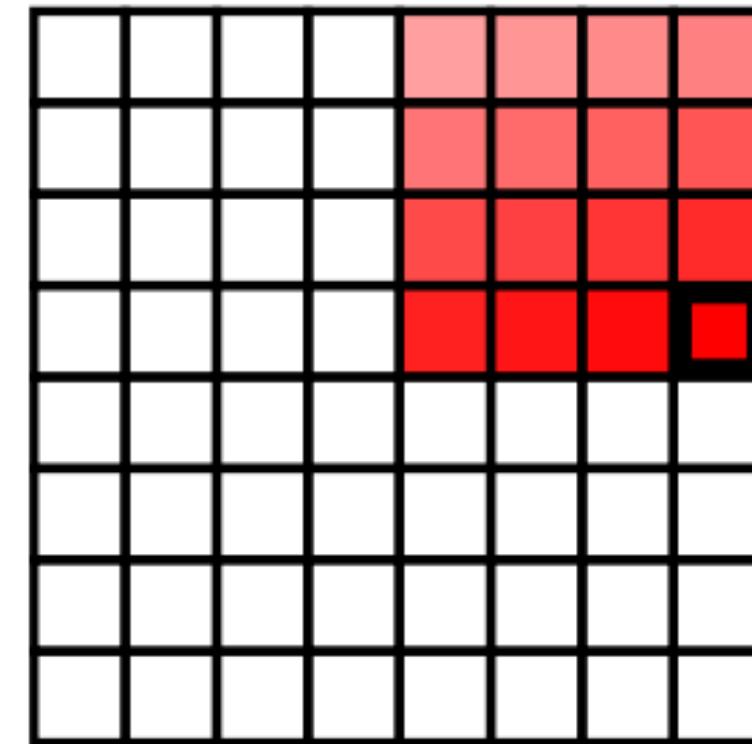
mem:576

B mem:192 cache hit:160

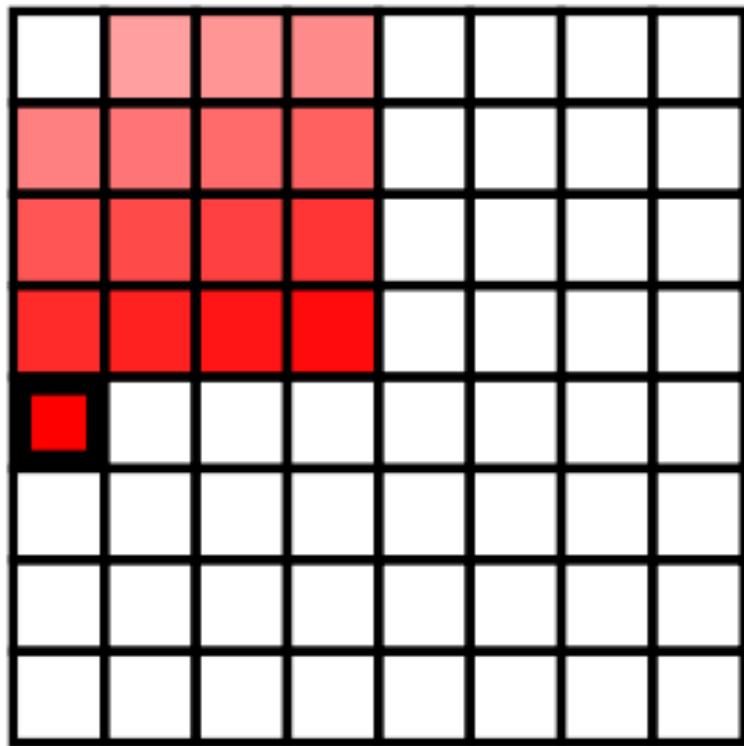


cache hits:448 $\approx 77\%$

C mem:192 cache hit:144

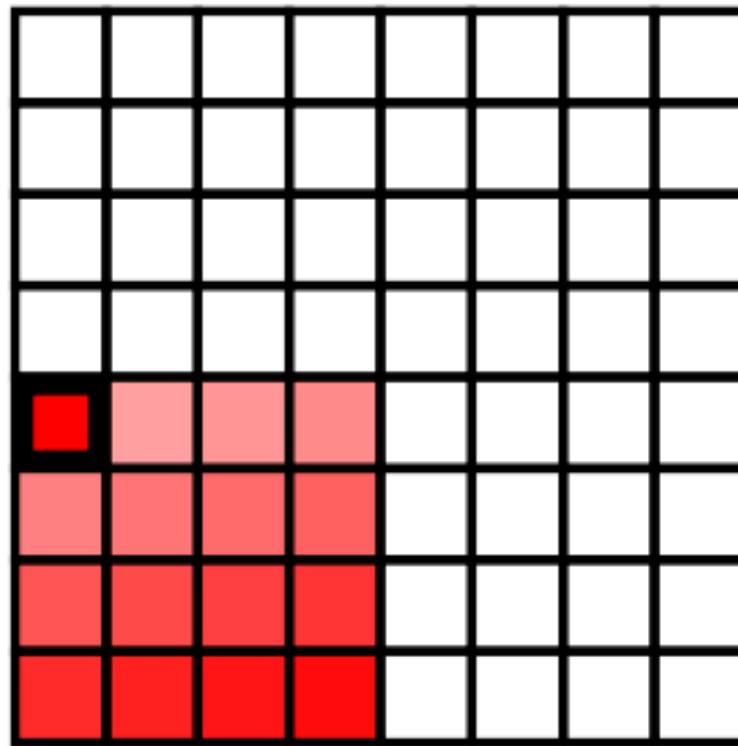


A mem:193 cache hit:144



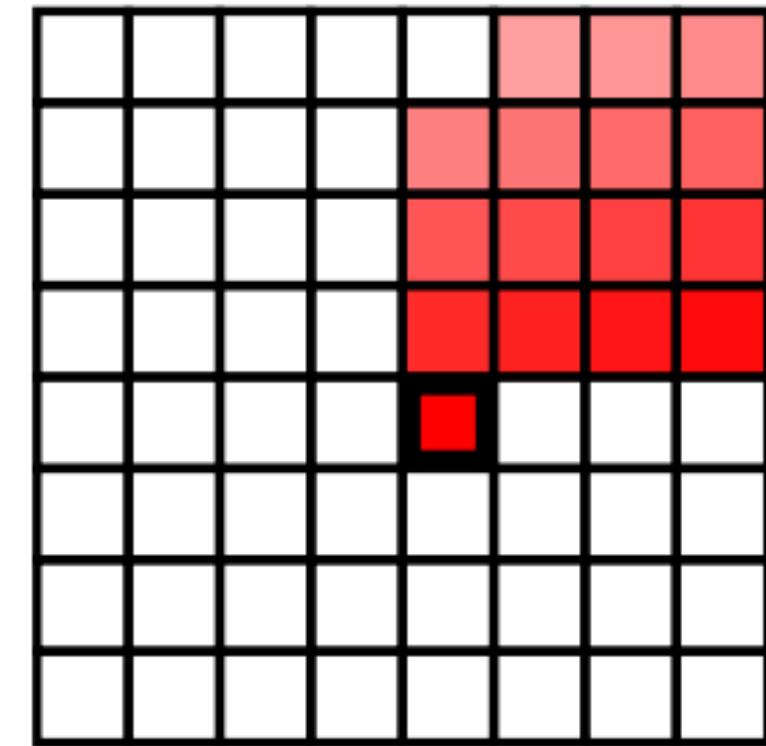
mem:579

B mem:193 cache hit:161

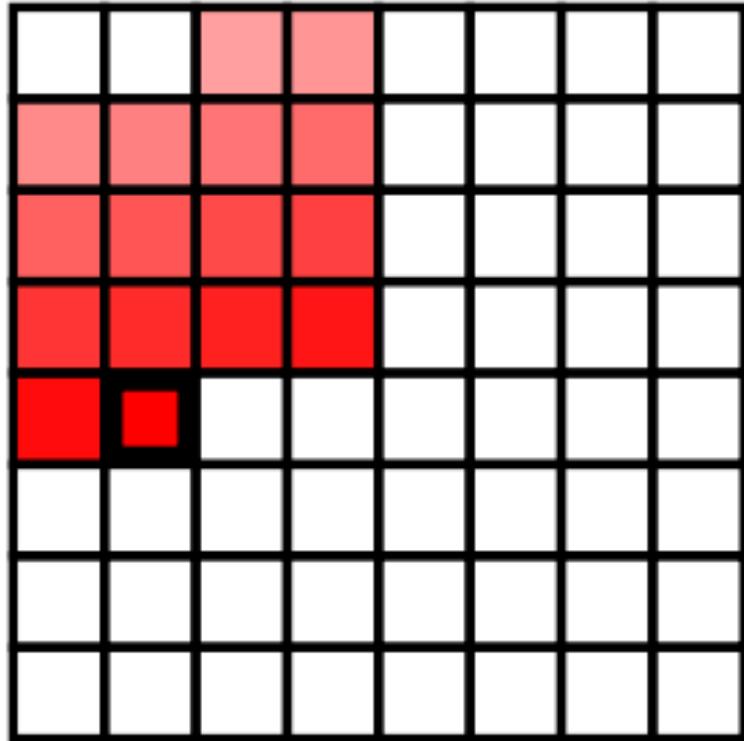


cache hits:449 ≈77%

C mem:193 cache hit:144

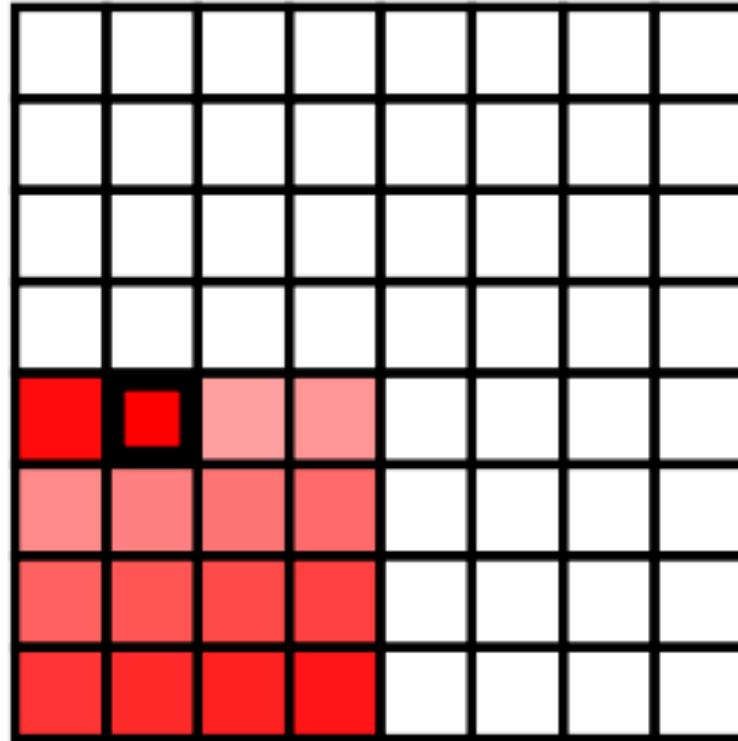


A mem:194 cache hit:144



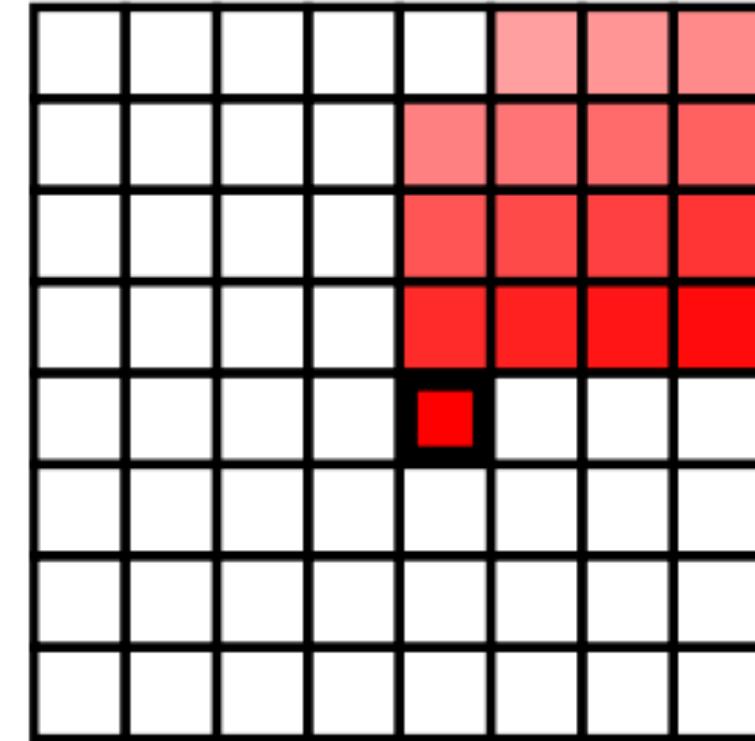
mem:582

B mem:194 cache hit:162

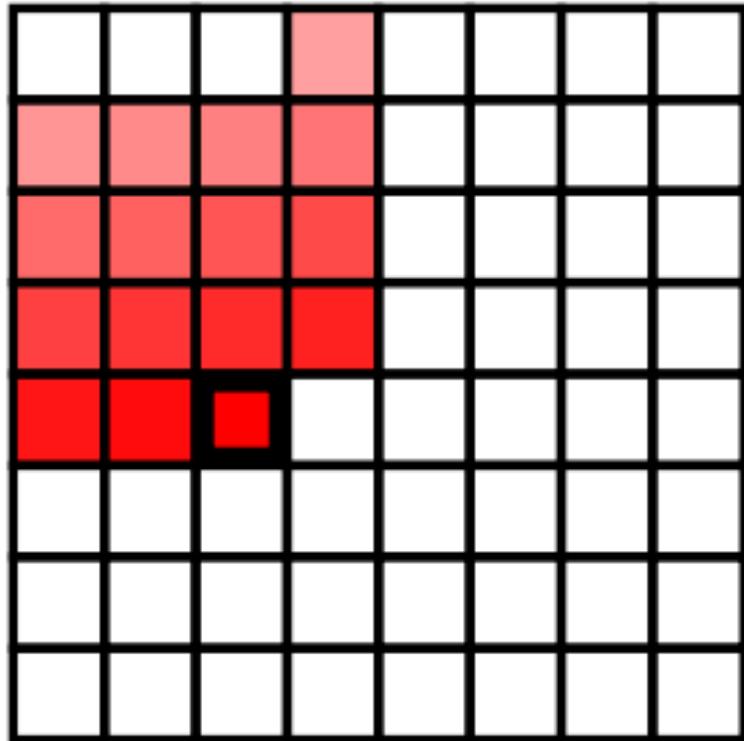


cache hits:451 ≈77%

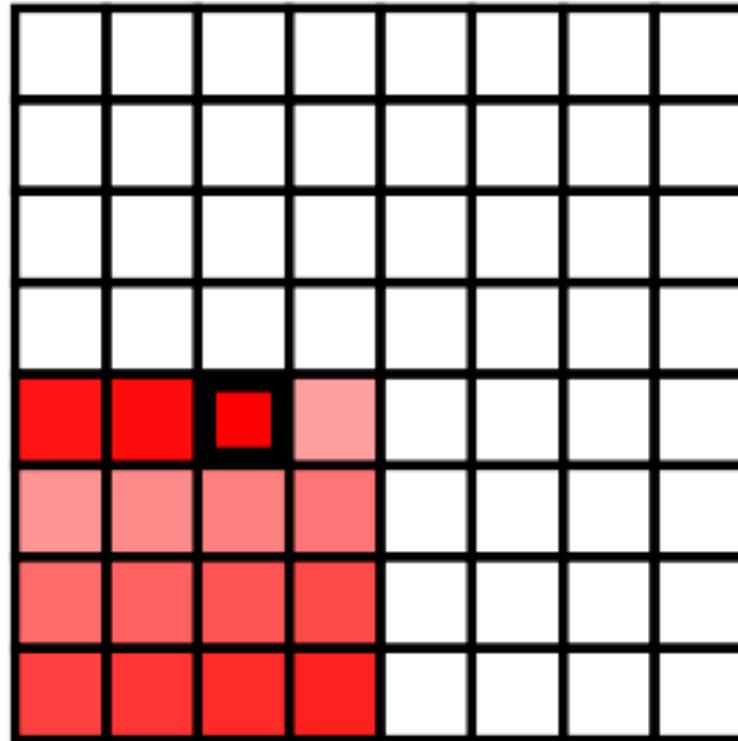
C mem:194 cache hit:145



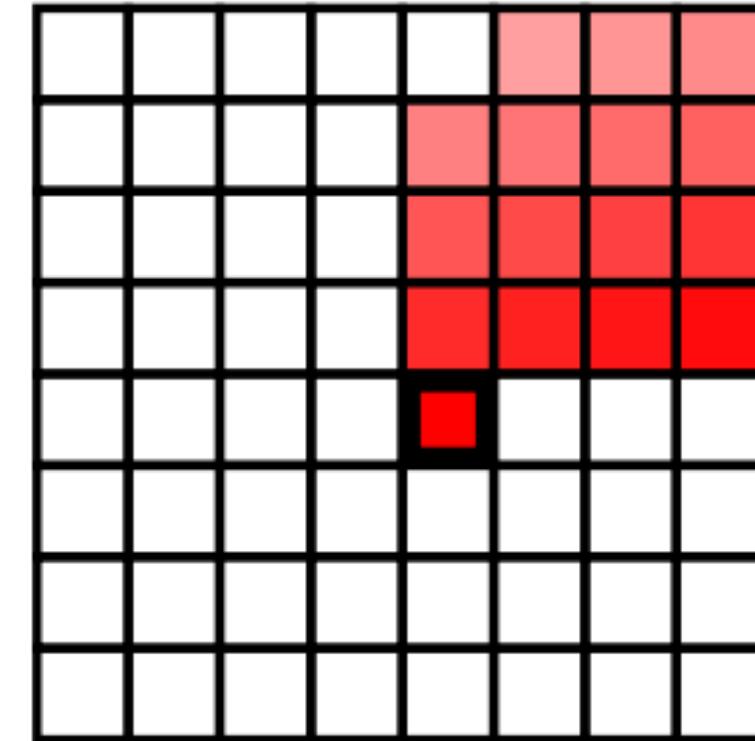
A mem:195 cache hit:144



B mem:195 cache hit:163



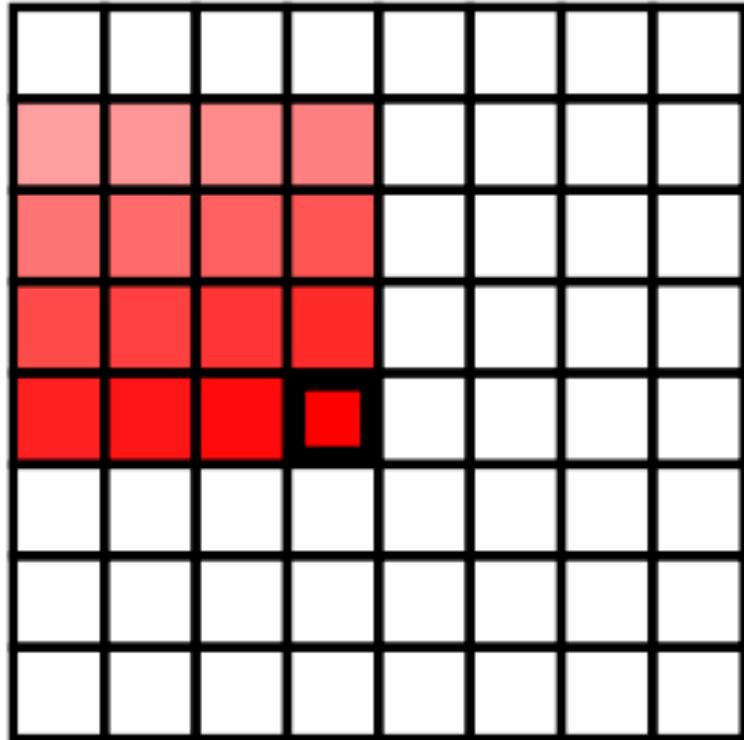
C mem:195 cache hit:146



mem:585

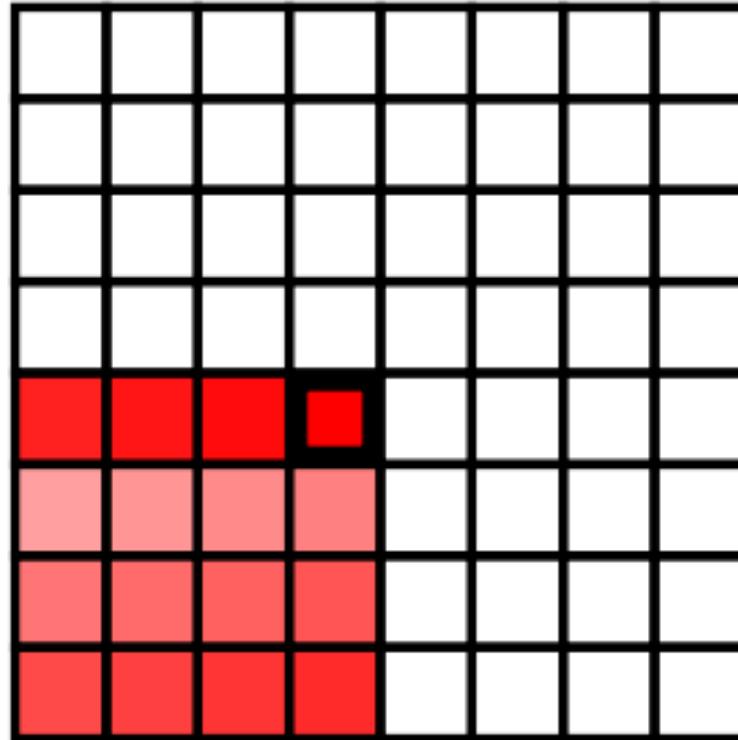
cache hits:453 $\approx 77\%$

A mem:196 cache hit:144



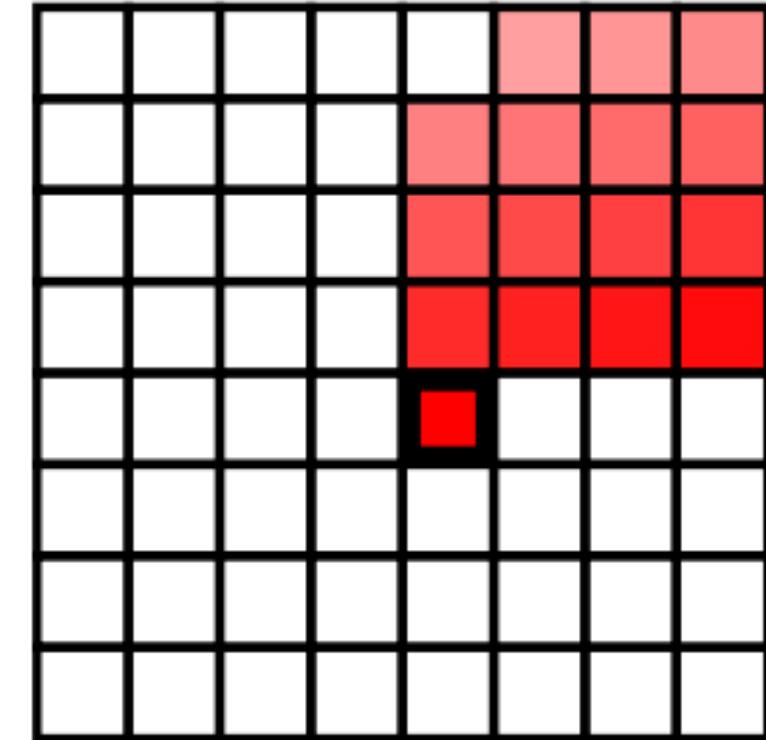
mem:588

B mem:196 cache hit:164

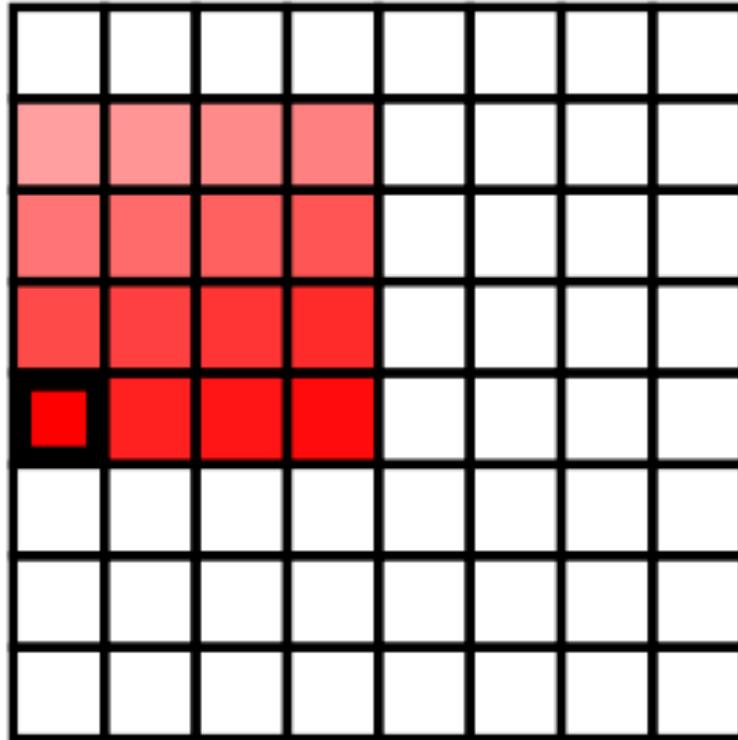


cache hits:455 ≈77%

C mem:196 cache hit:147

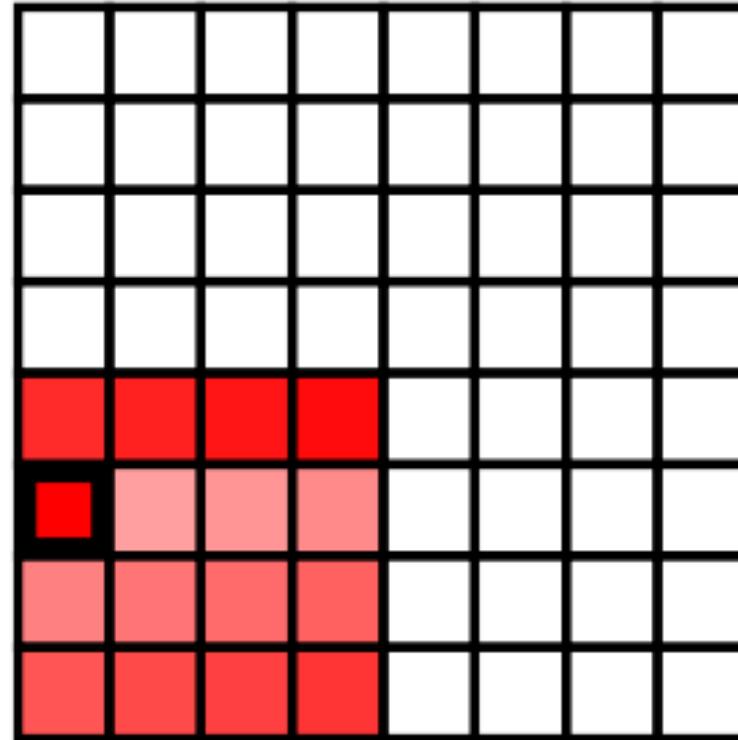


A mem:197 cache hit:145



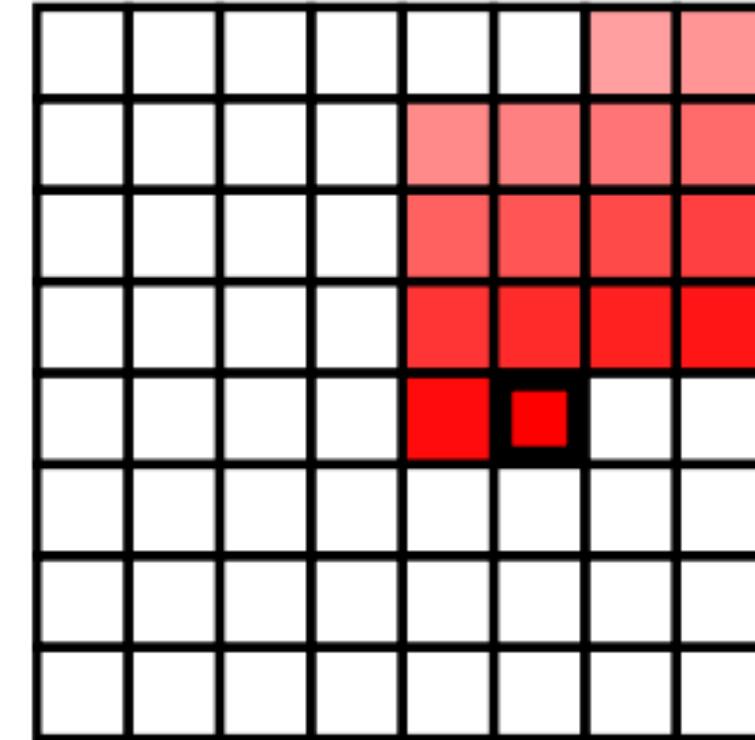
mem:591

B mem:197 cache hit:165

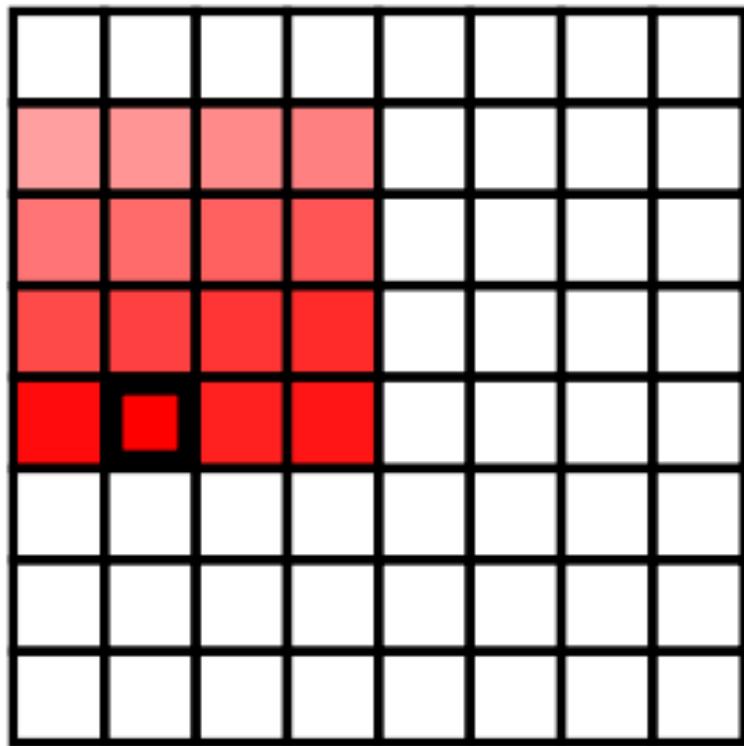


cache hits:457 ≈77%

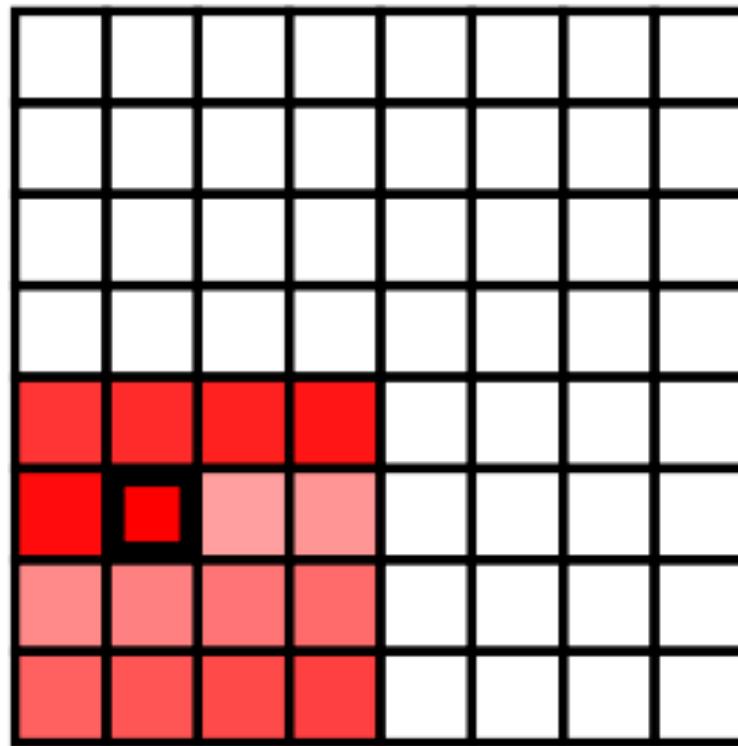
C mem:197 cache hit:147



A mem:198 cache hit:146



B mem:198 cache hit:166

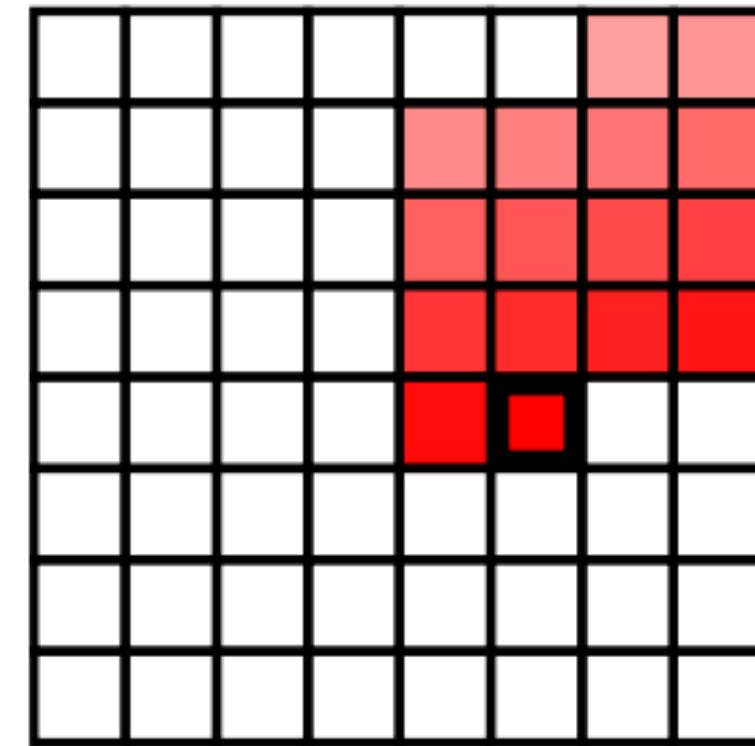


mem:594

cache hits:460 $\approx 77\%$

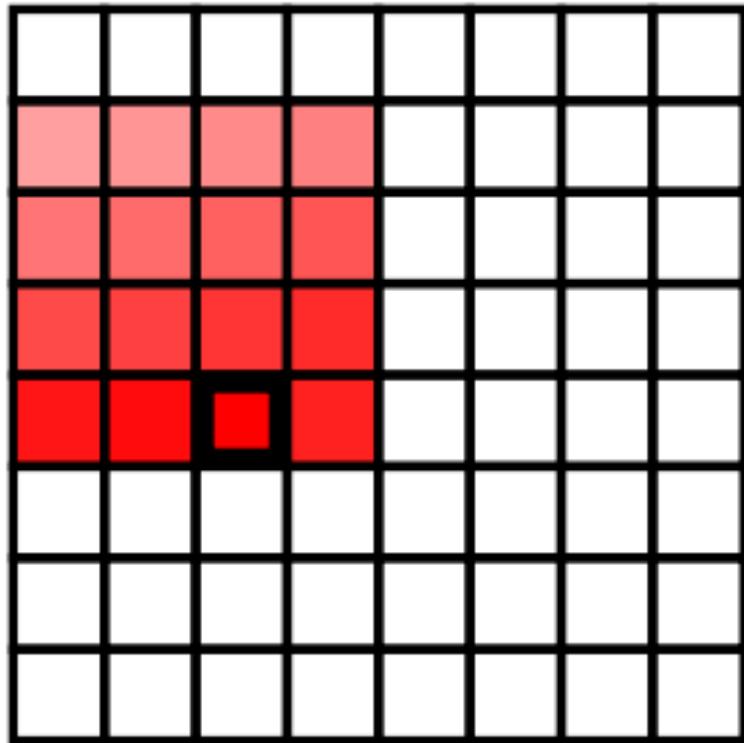
\times

C mem:198 cache hit:148

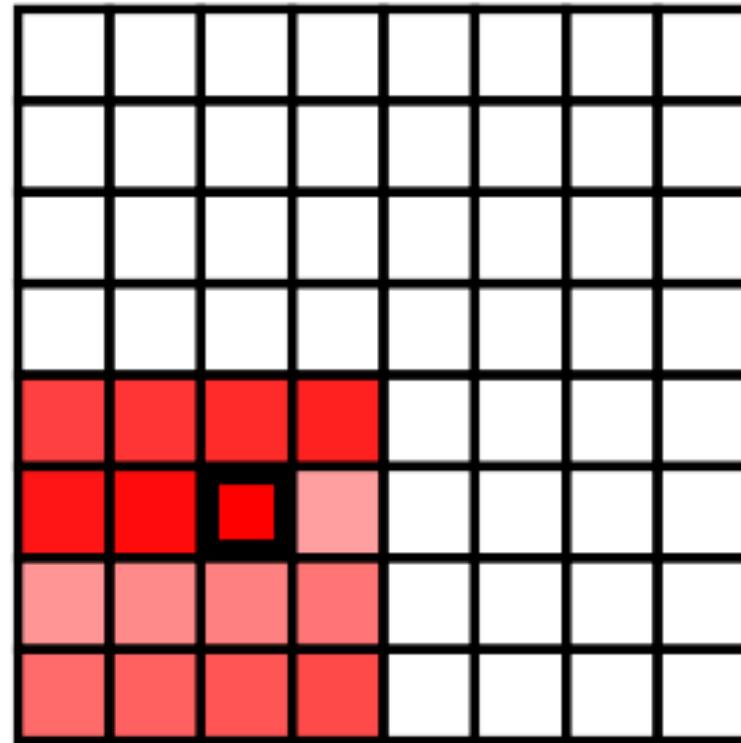


=

A mem:199 cache hit:147

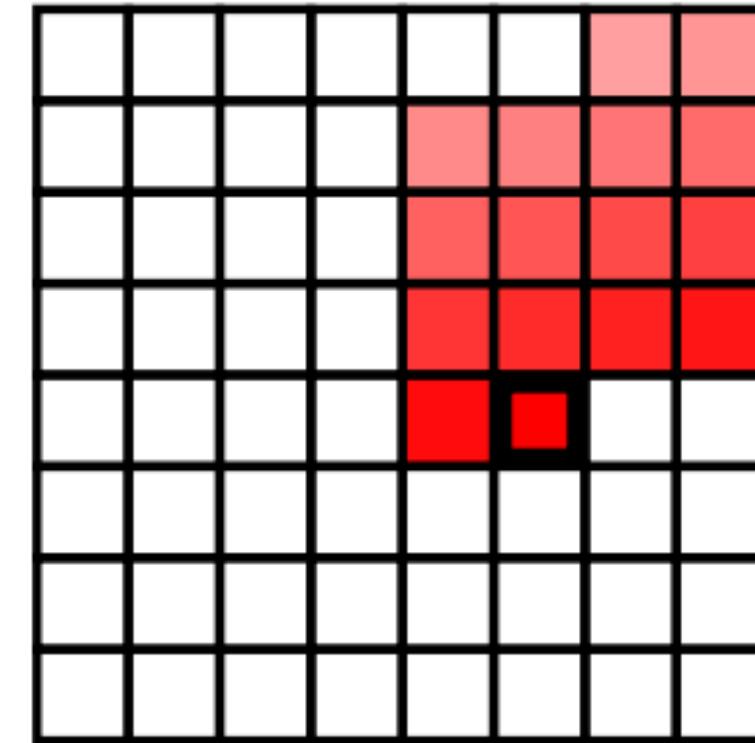


B mem:199 cache hit:167



x

C mem:199 cache hit:149

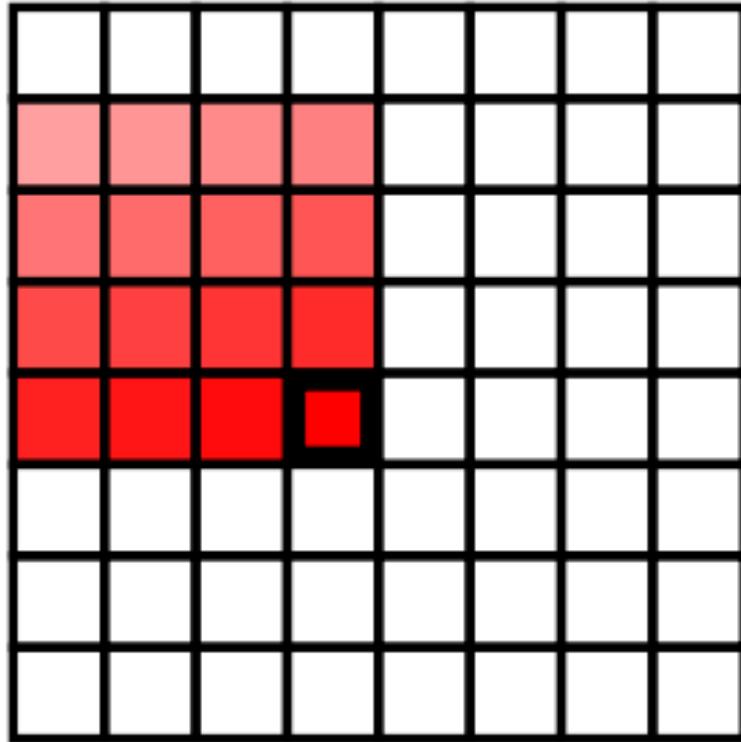


=

mem:597

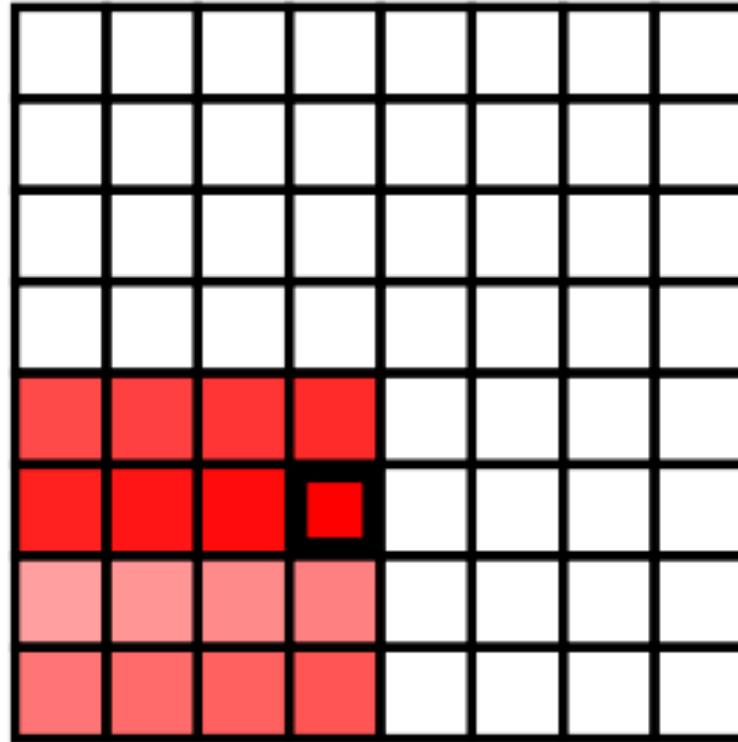
cache hits:463 \approx 77%

A mem:200 cache hit:148



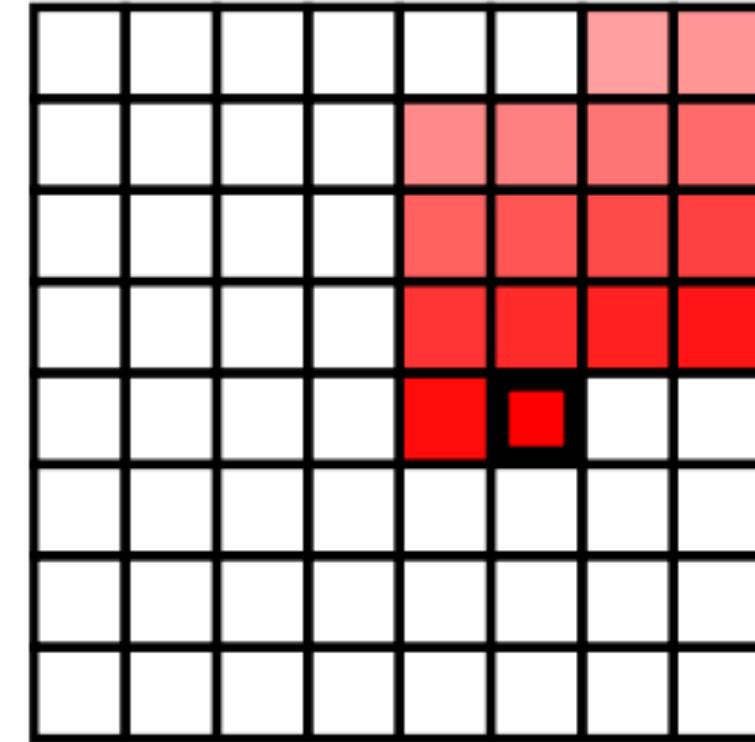
mem:600

B mem:200 cache hit:168

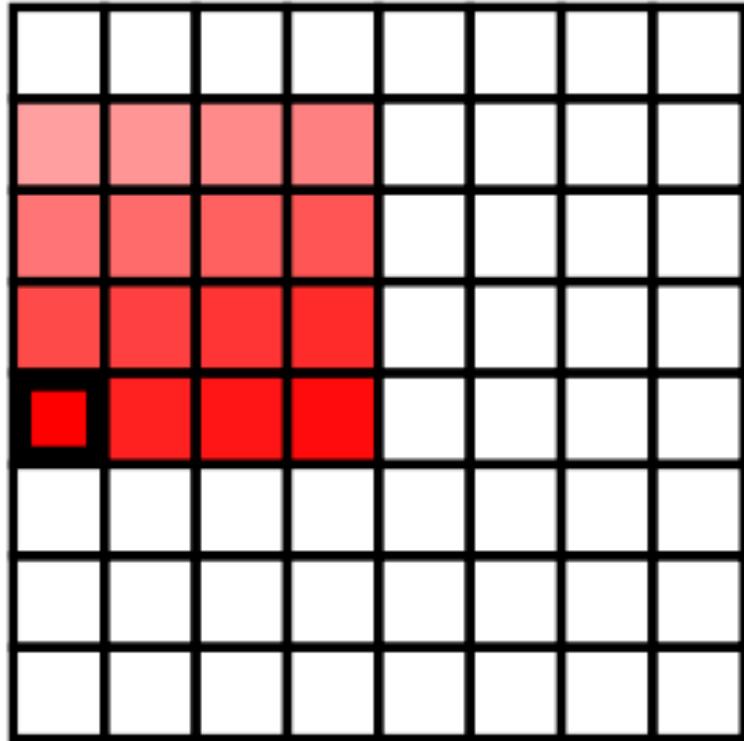


cache hits:466 ≈77%

C mem:200 cache hit:150

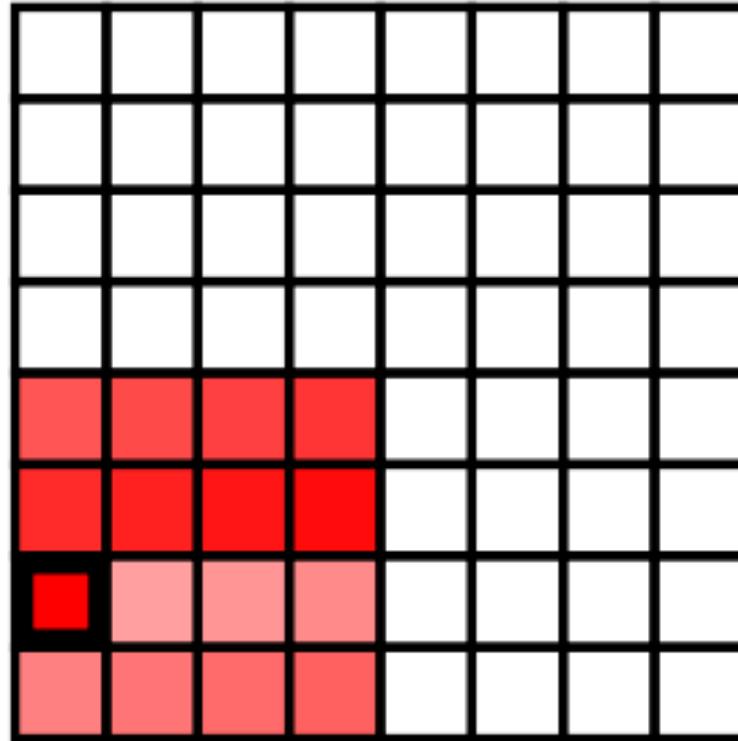


A mem:201 cache hit:149



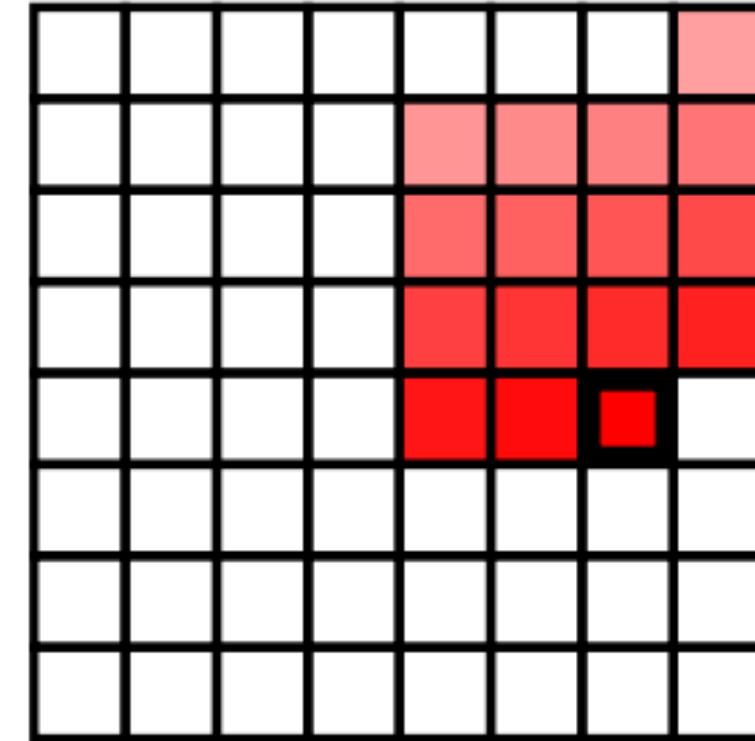
mem:603

B mem:201 cache hit:169

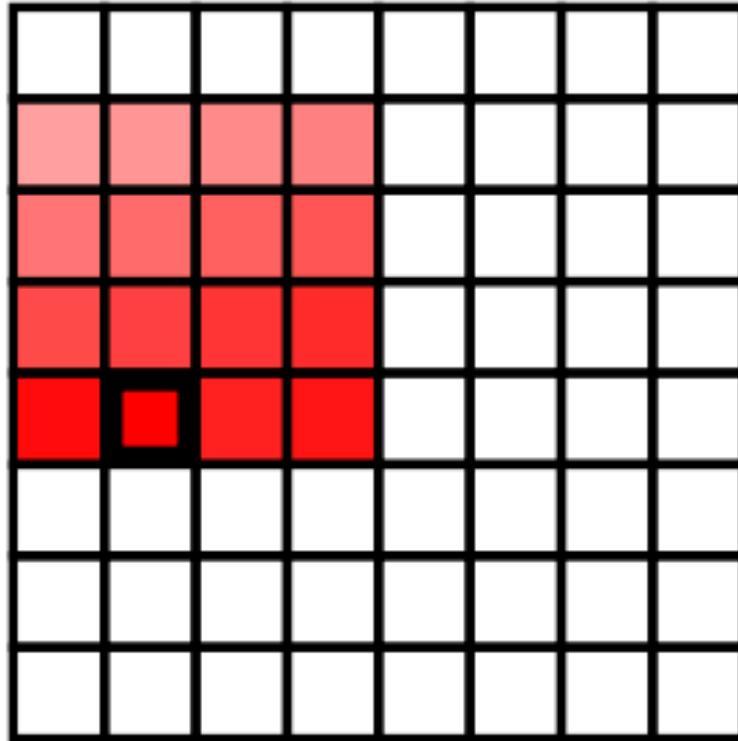


cache hits:468 ≈77%

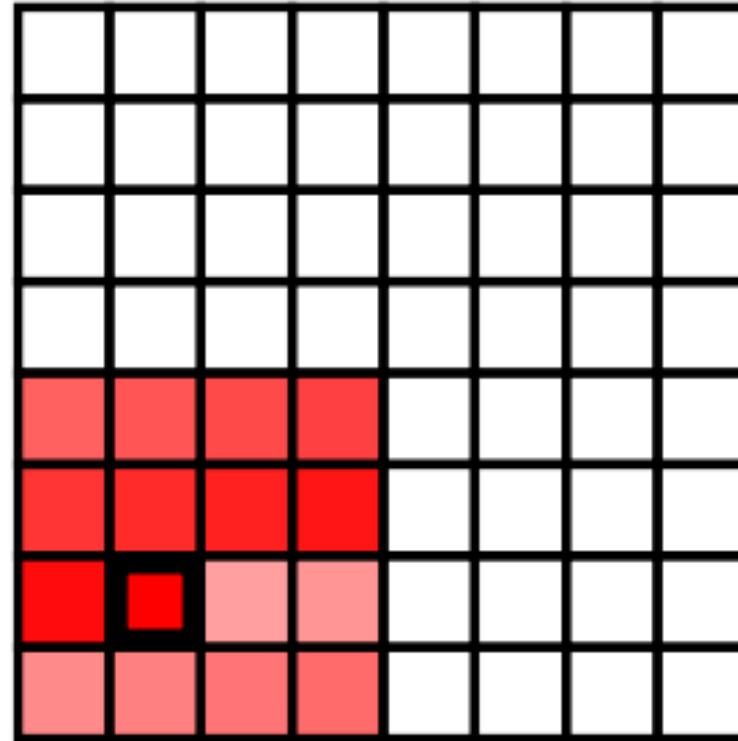
C mem:201 cache hit:150



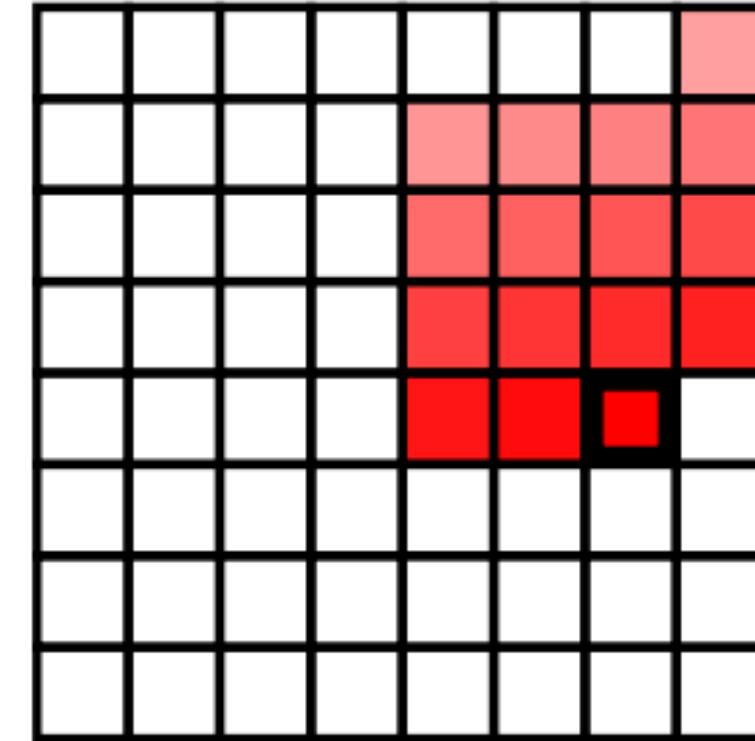
A mem:202 cache hit:150



B mem:202 cache hit:170



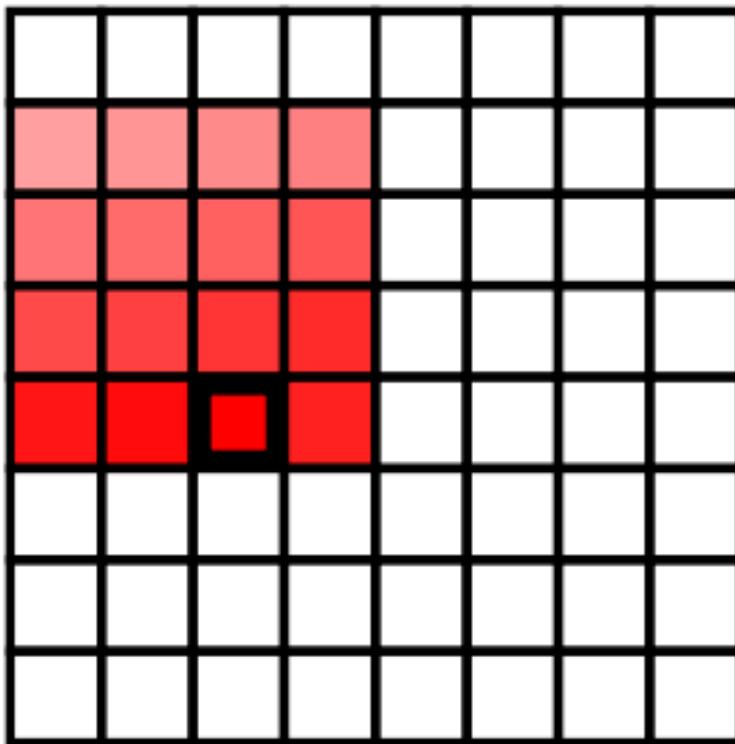
C mem:202 cache hit:151



mem:606

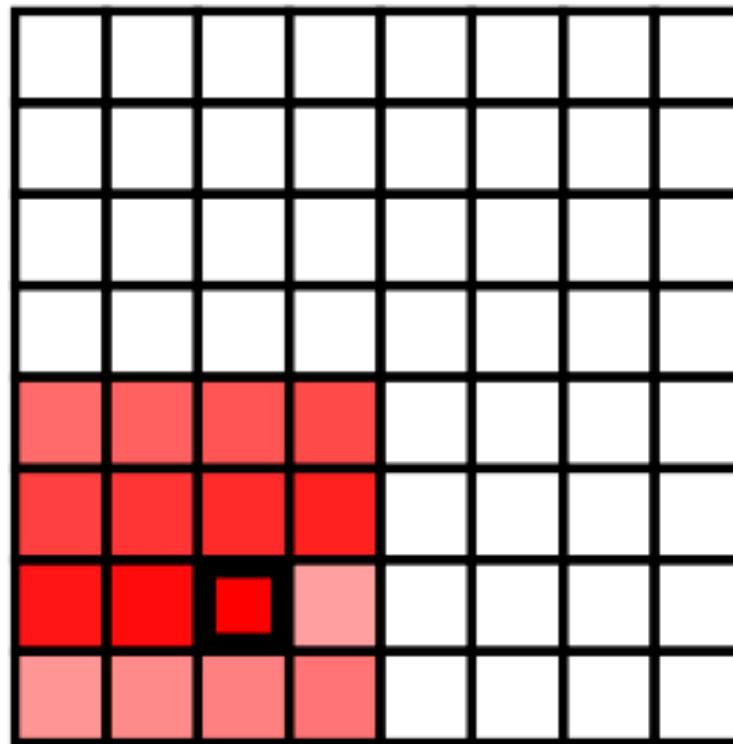
cache hits:471 $\approx 77\%$

A mem:203 cache hit:151



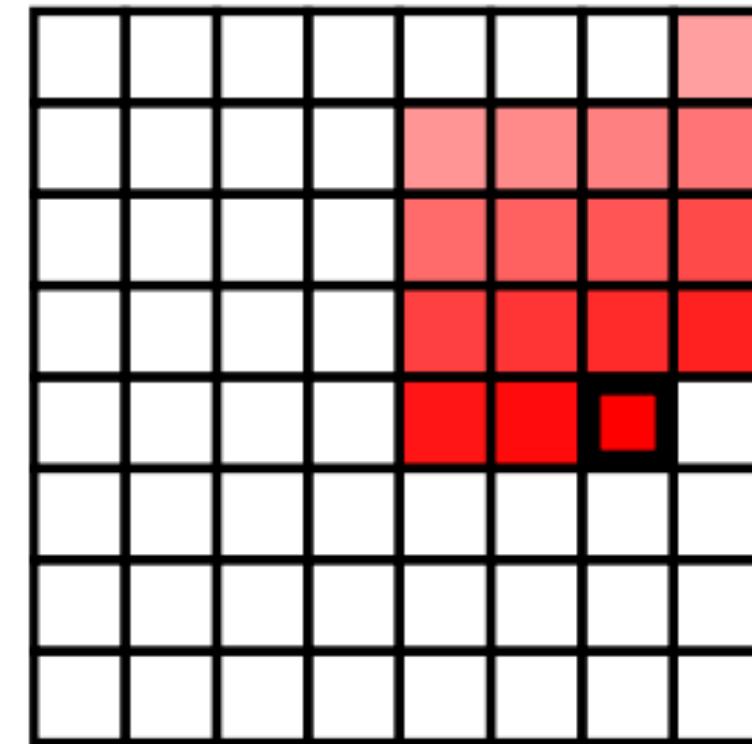
mem:609

B mem:203 cache hit:171

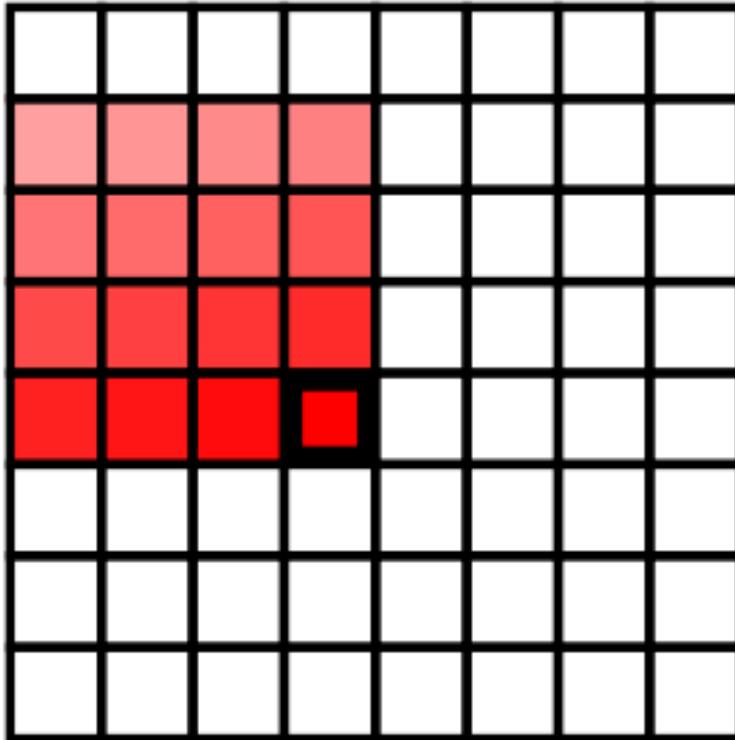


cache hits:474 ≈77%

C mem:203 cache hit:152

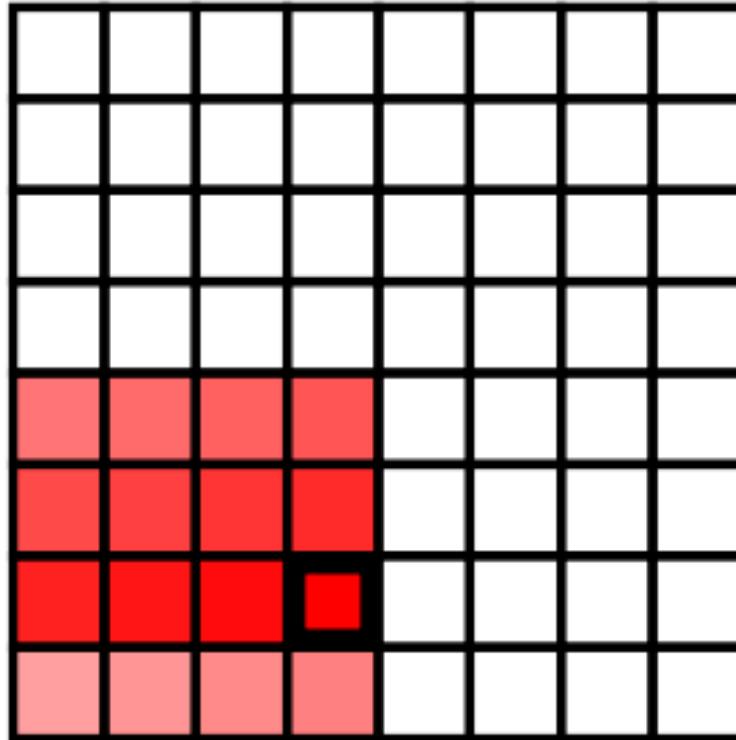


A mem:204 cache hit:152



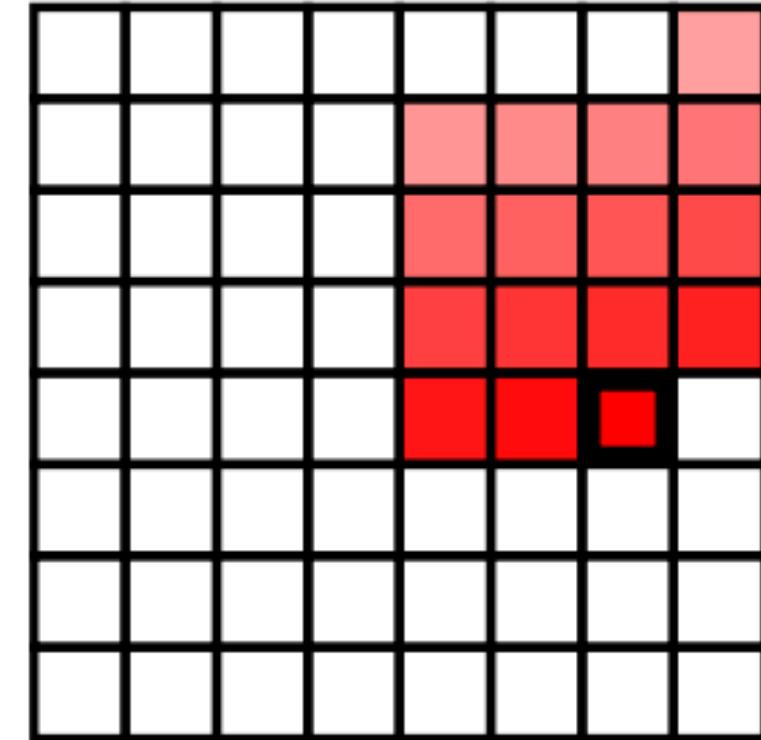
mem:612

B mem:204 cache hit:172

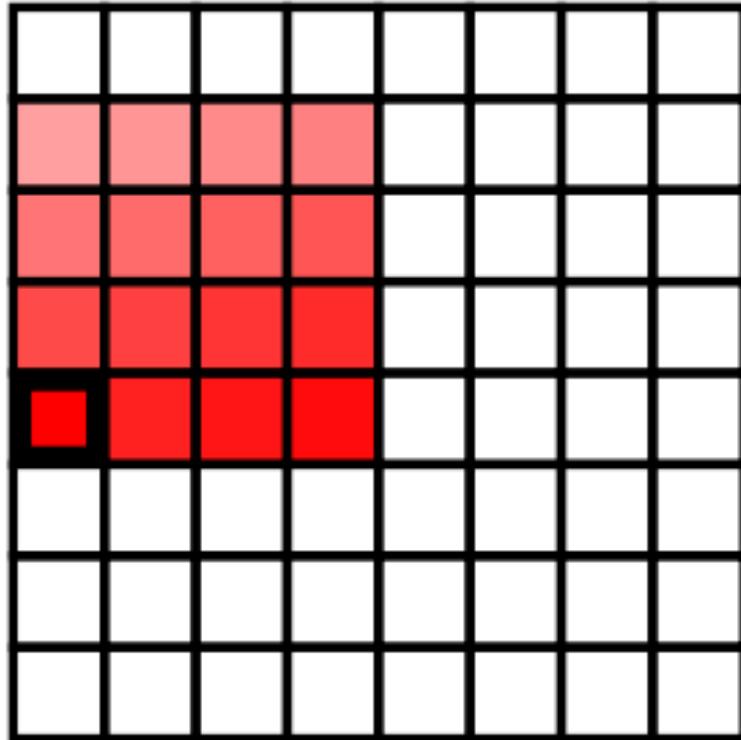


cache hits:477 ≈77%

C mem:204 cache hit:153

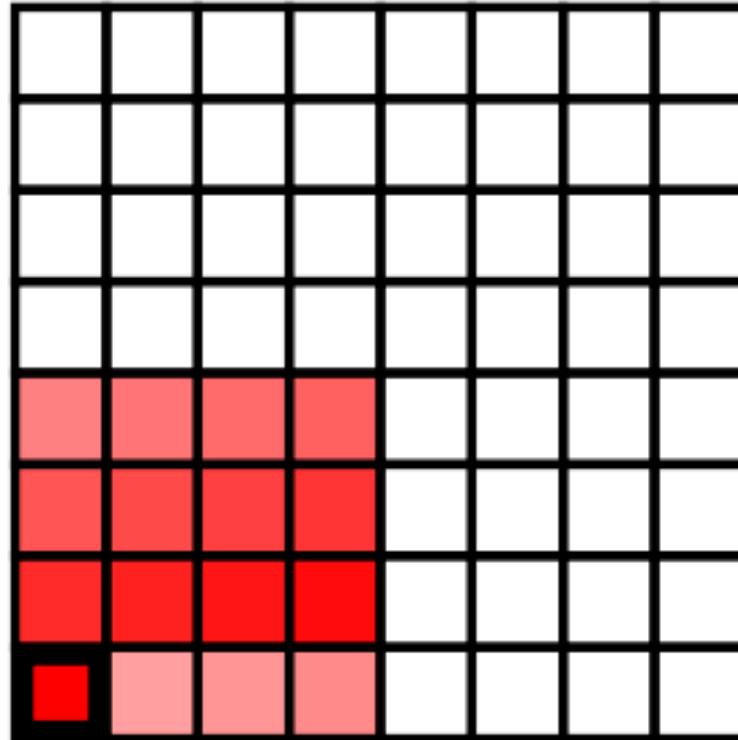


A mem:205 cache hit:153



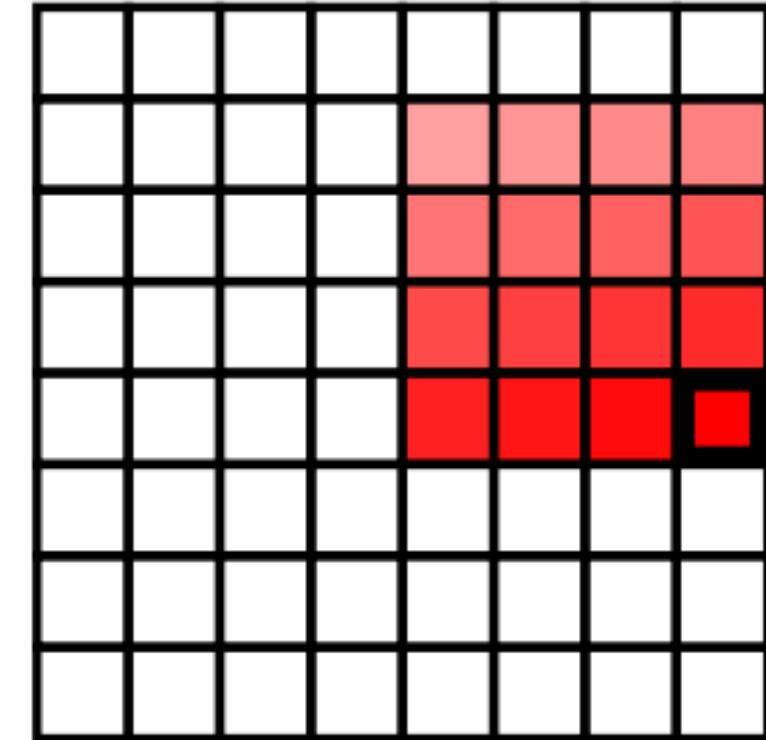
mem:615

B mem:205 cache hit:173

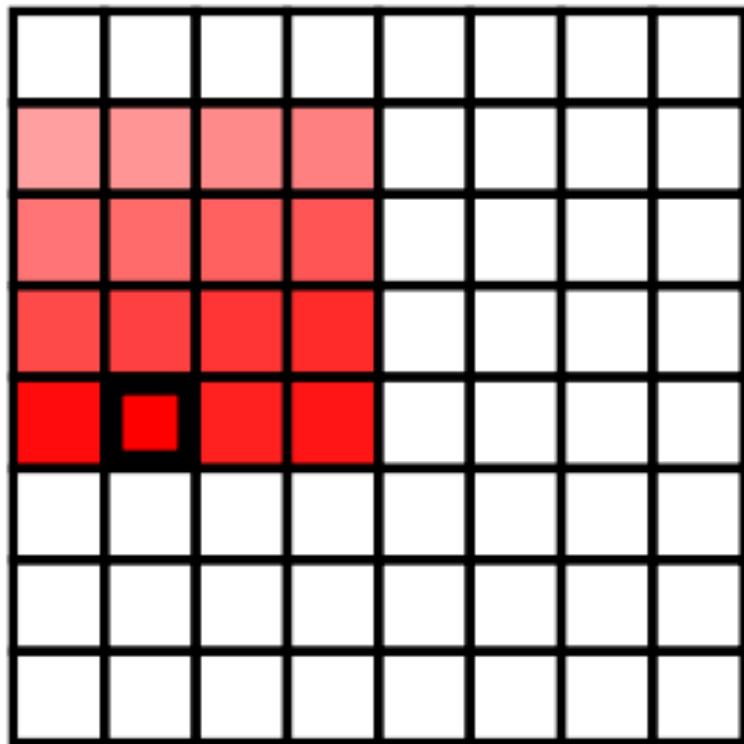


cache hits:479 ≈77%

C mem:205 cache hit:153

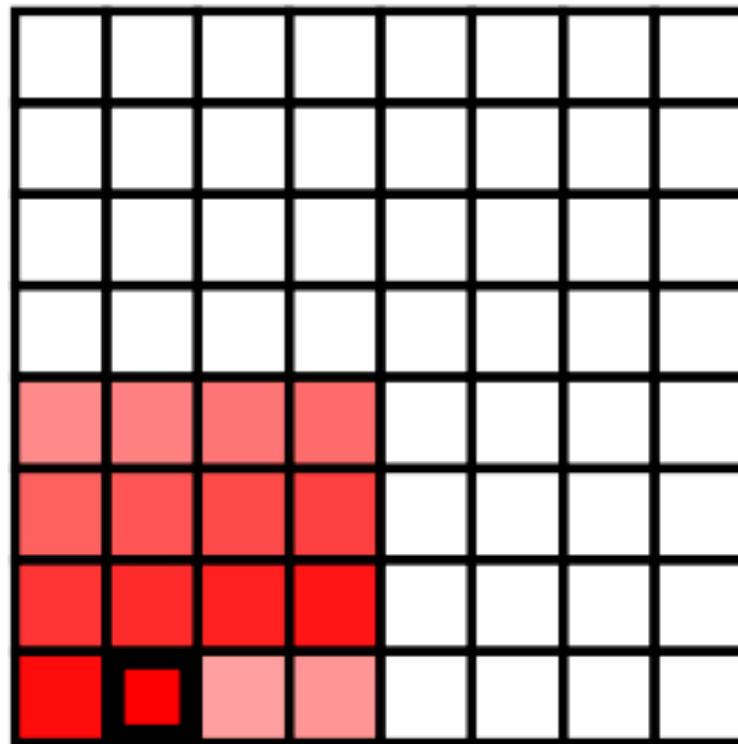


A mem:206 cache hit:154



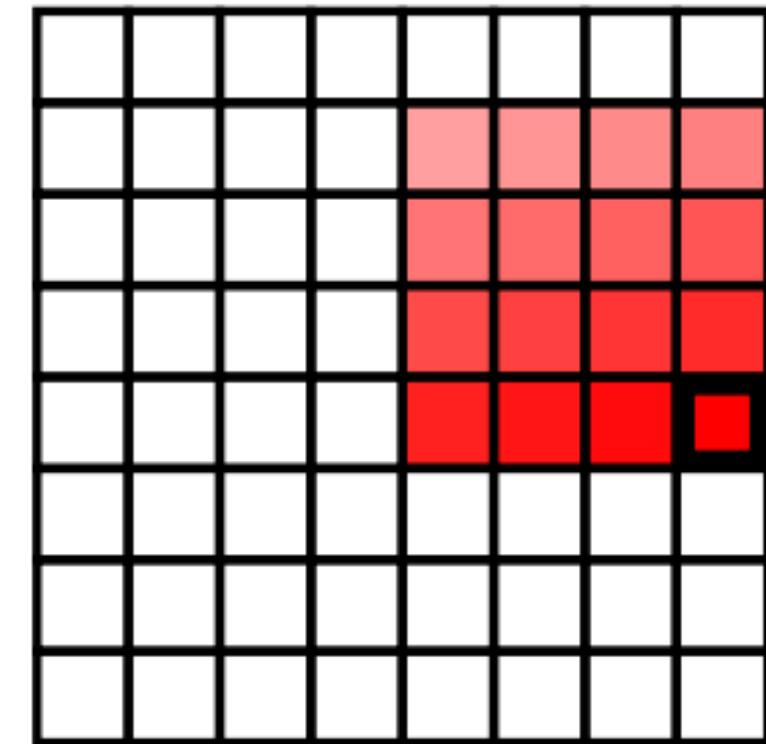
mem:618

B mem:206 cache hit:174

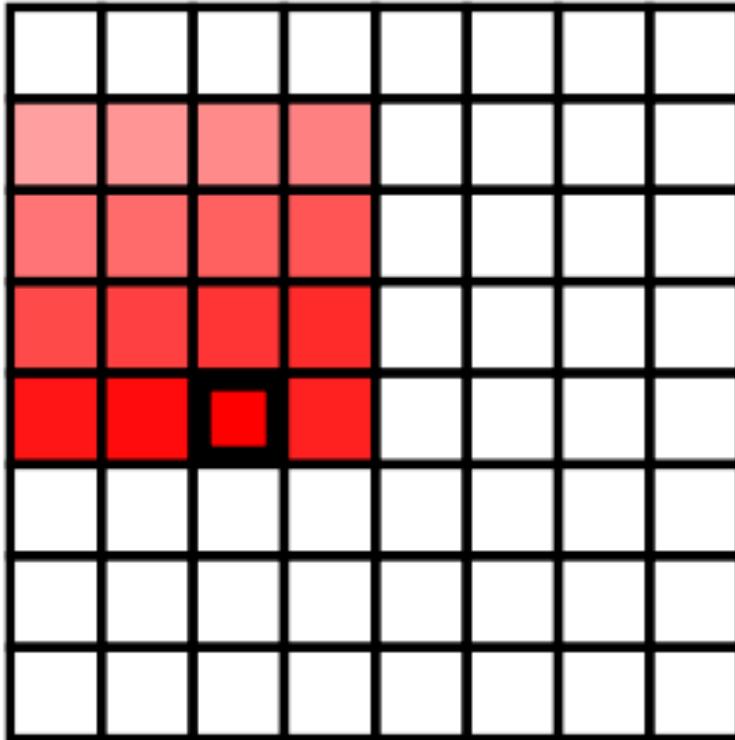


cache hits:482 ≈77%

C mem:206 cache hit:154

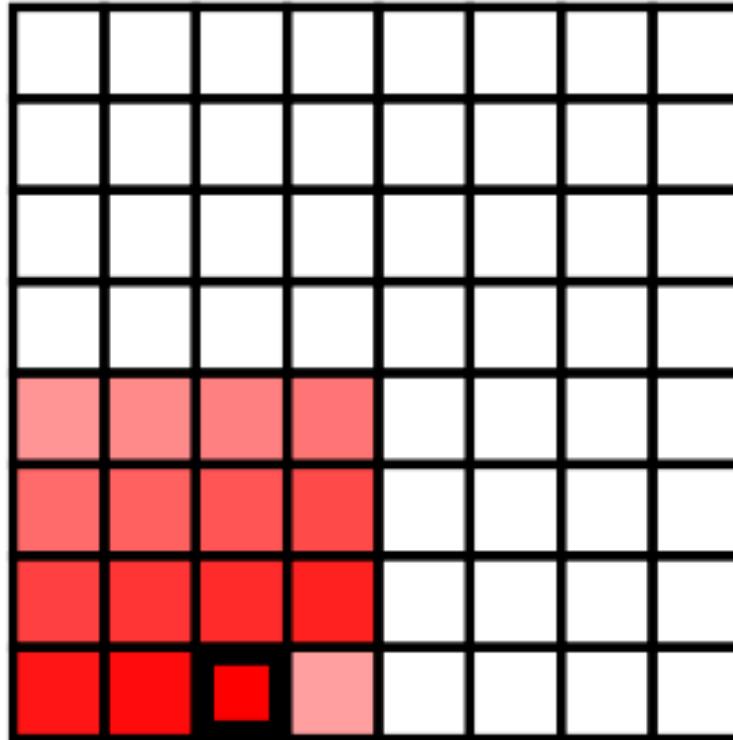


A mem:207 cache hit:155



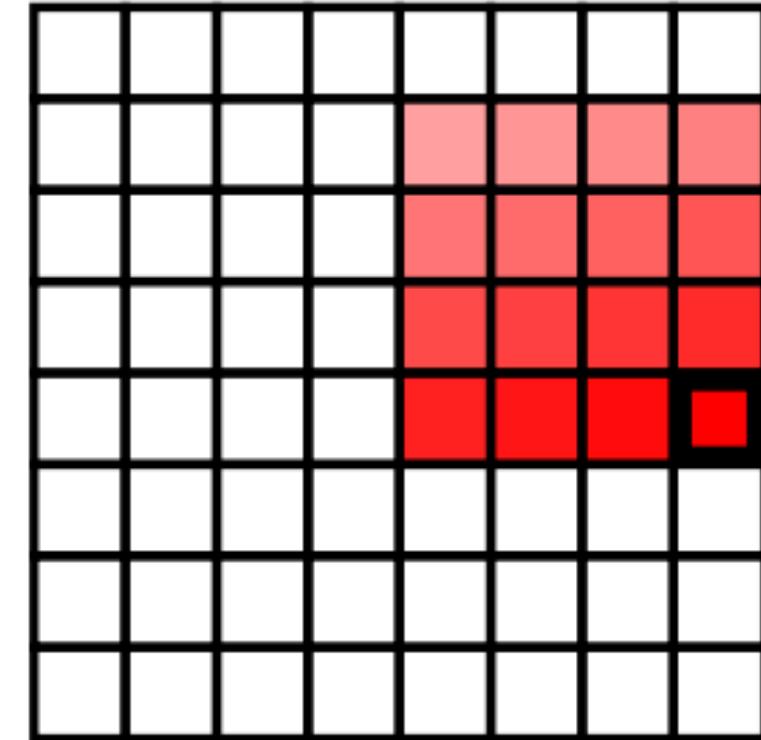
mem:621

B mem:207 cache hit:175

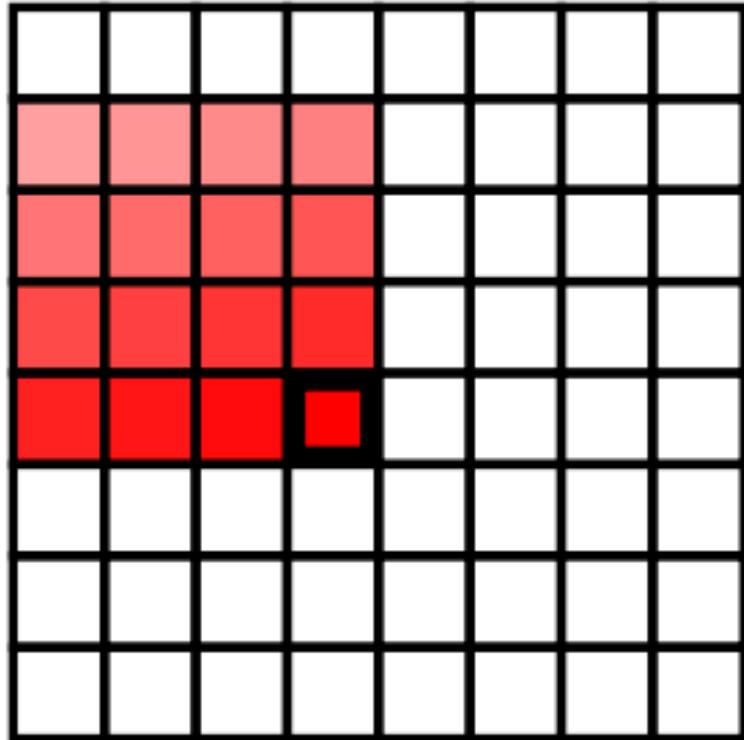


cache hits:485 ≈78%

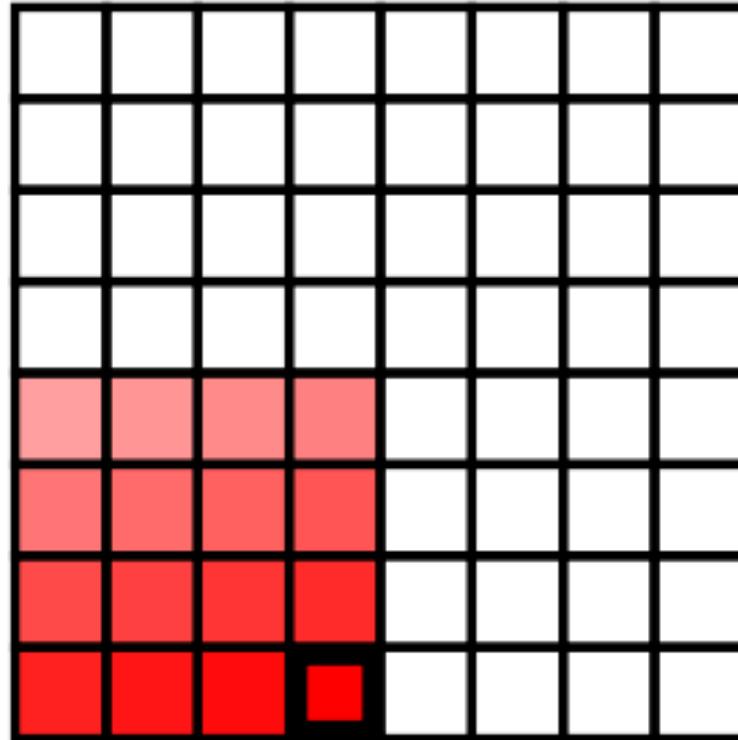
C mem:207 cache hit:155



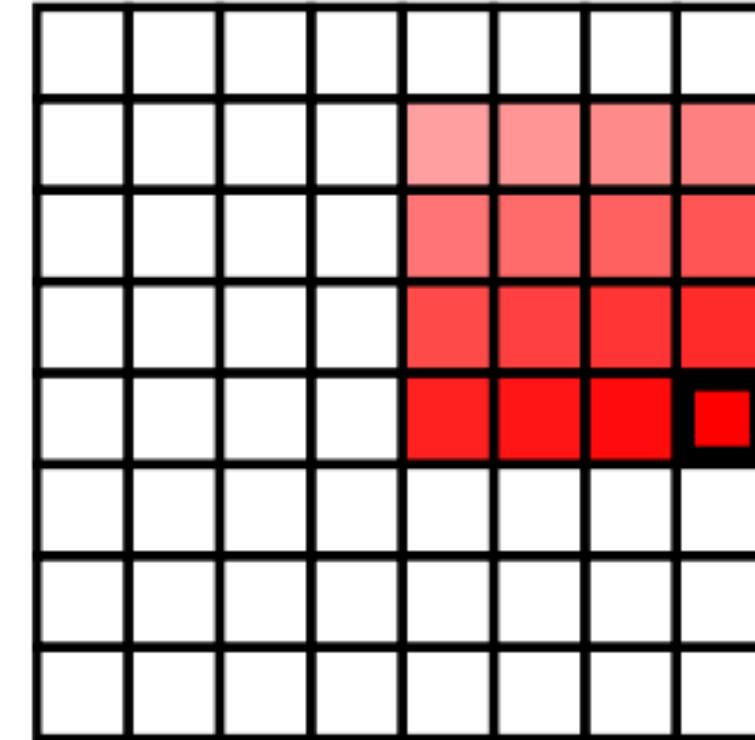
A mem:208 cache hit:156



B mem:208 cache hit:176



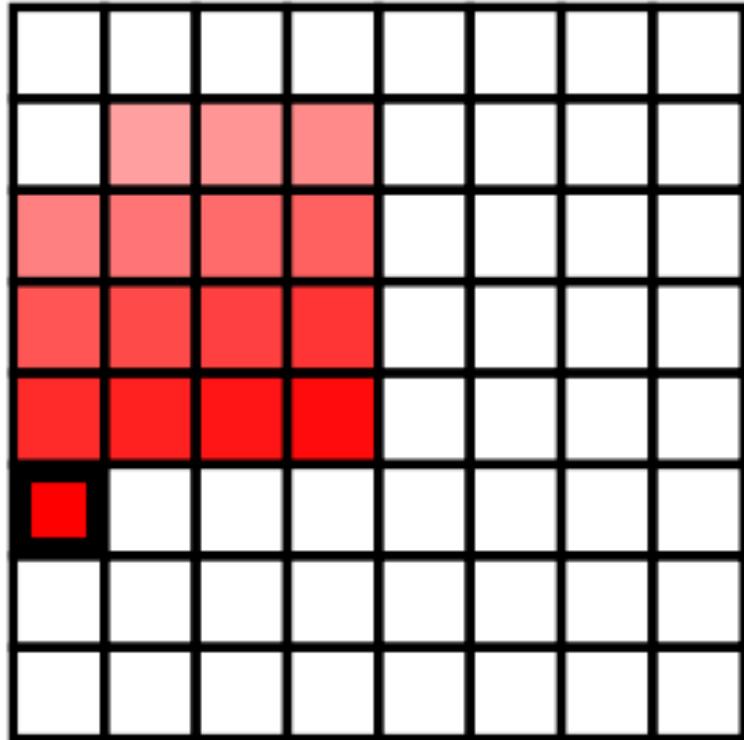
C mem:208 cache hit:156



mem:624

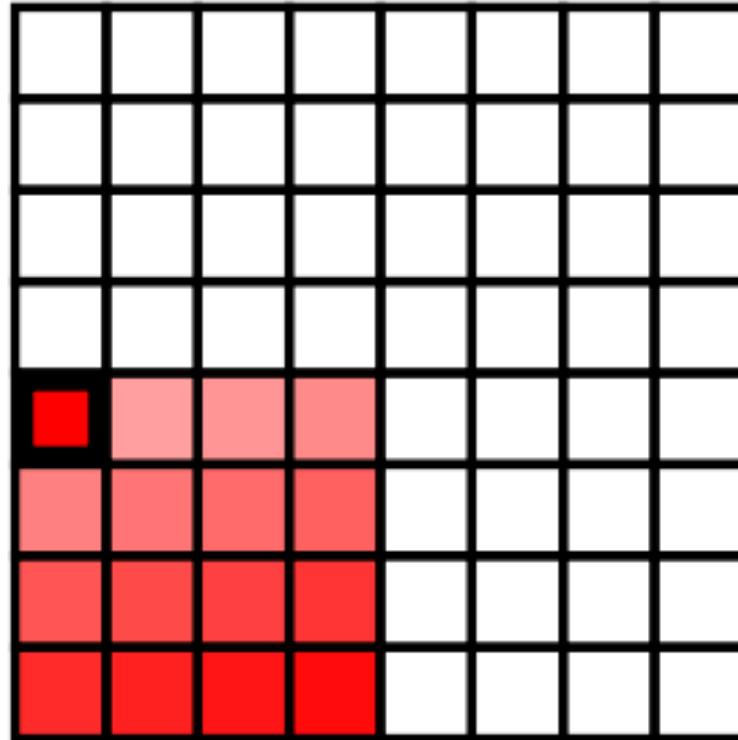
cache hits:488 \approx 78%

A mem:209 cache hit:156



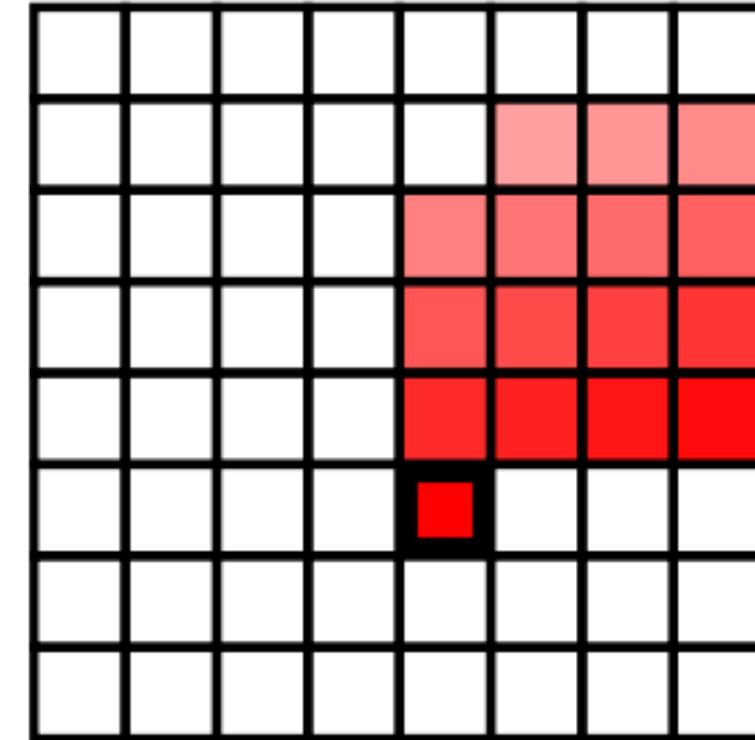
mem:627

B mem:209 cache hit:177

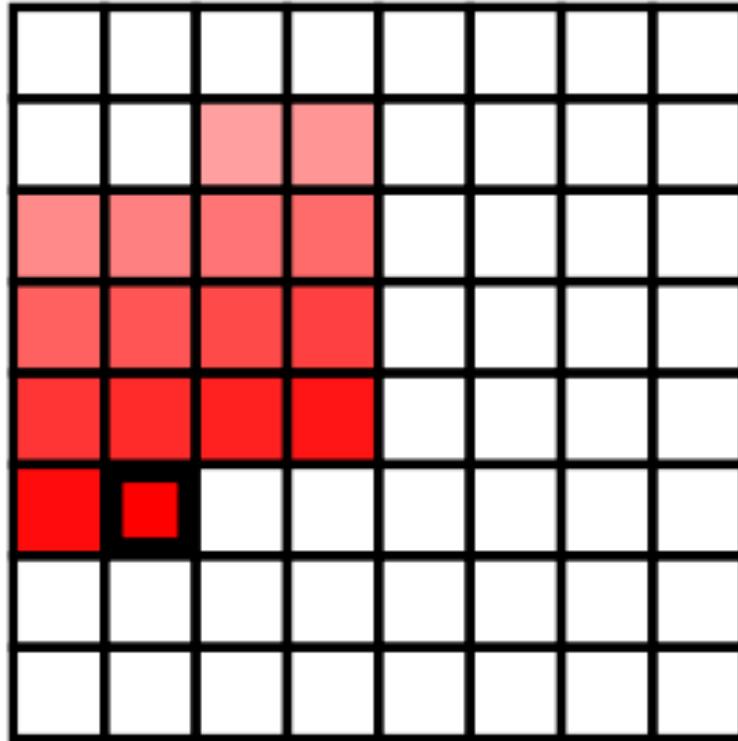


cache hits:489 ≈77%

C mem:209 cache hit:156

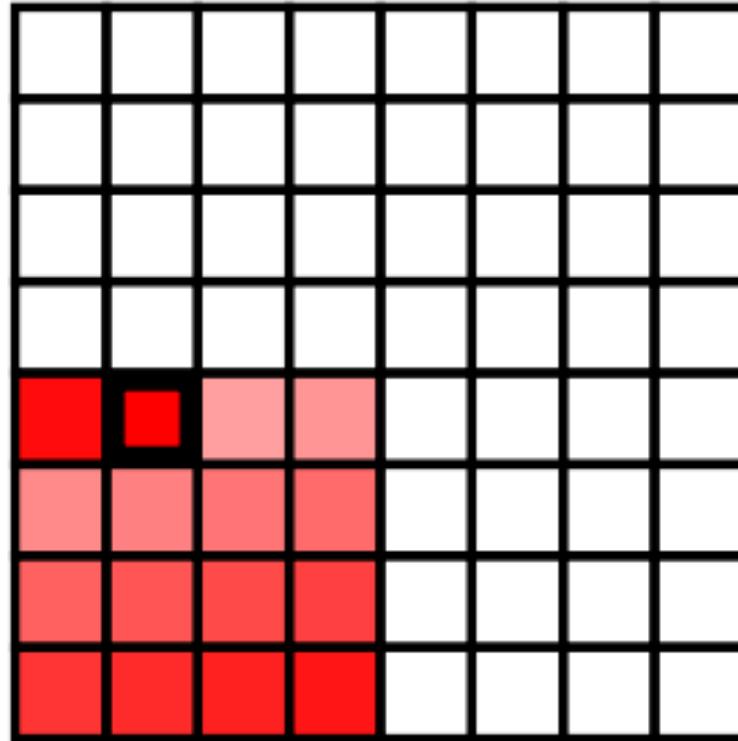


A mem:210 cache hit:156



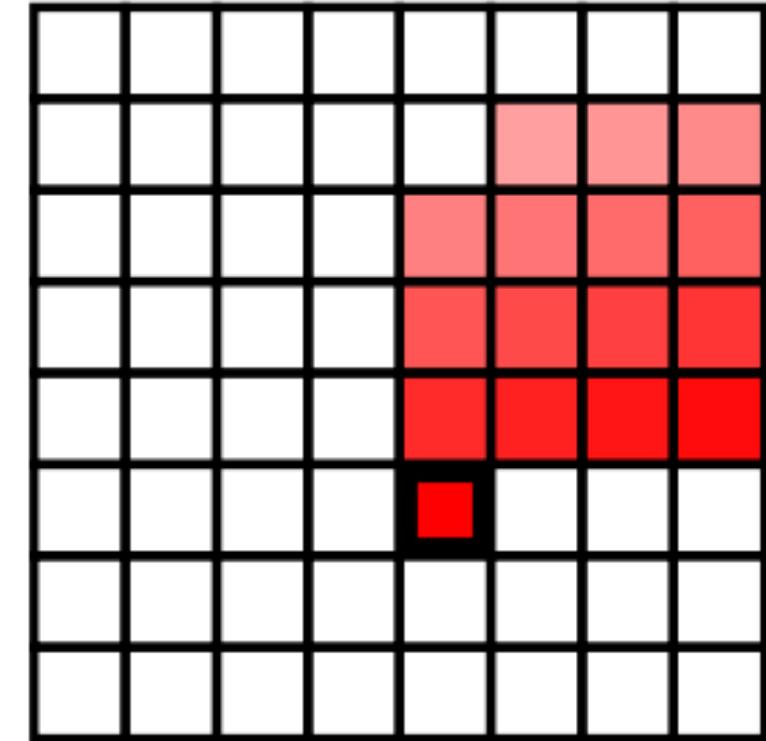
mem:630

B mem:210 cache hit:178

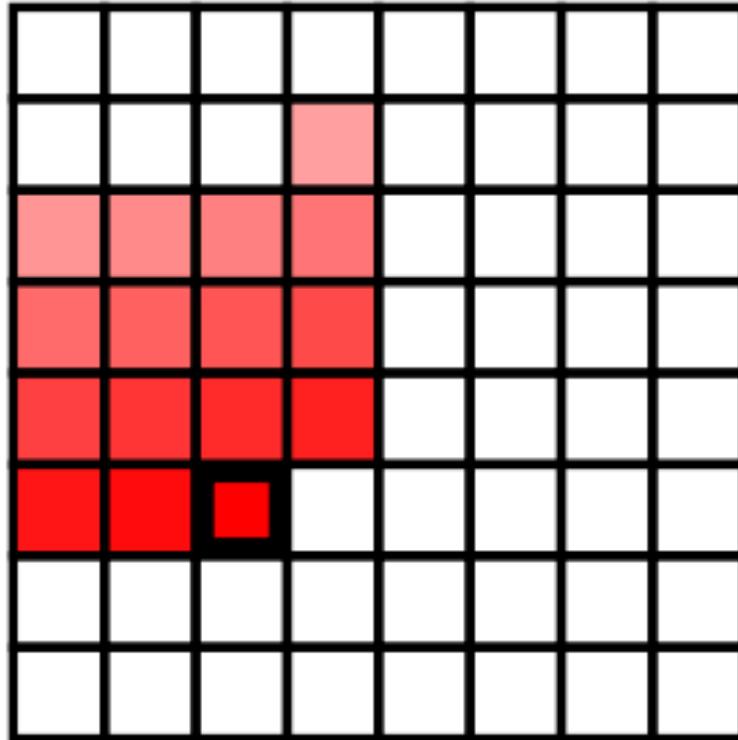


cache hits:491 $\approx 77\%$

C mem:210 cache hit:157

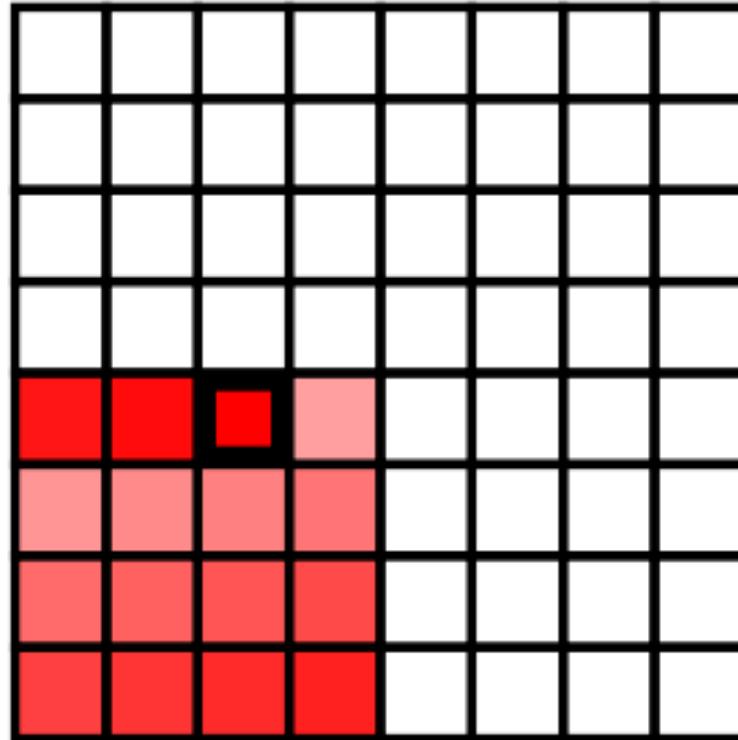


A mem:211 cache hit:156



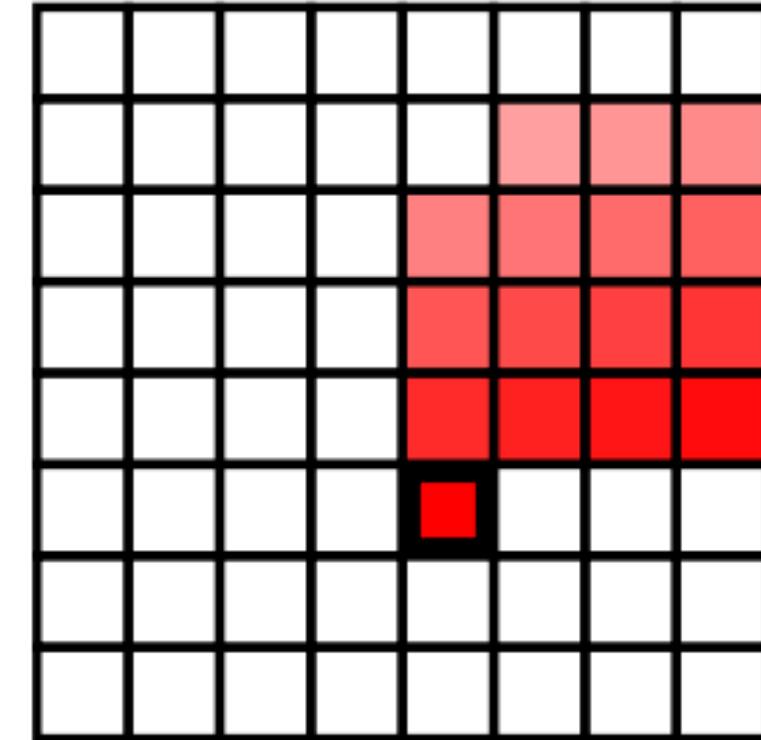
mem:633

B mem:211 cache hit:179

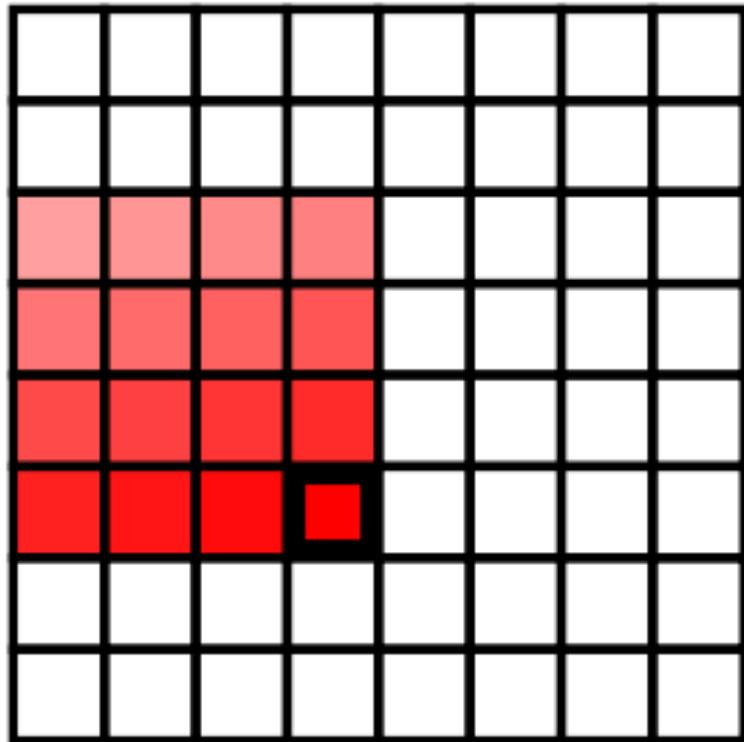


cache hits:493 ≈77%

C mem:211 cache hit:158

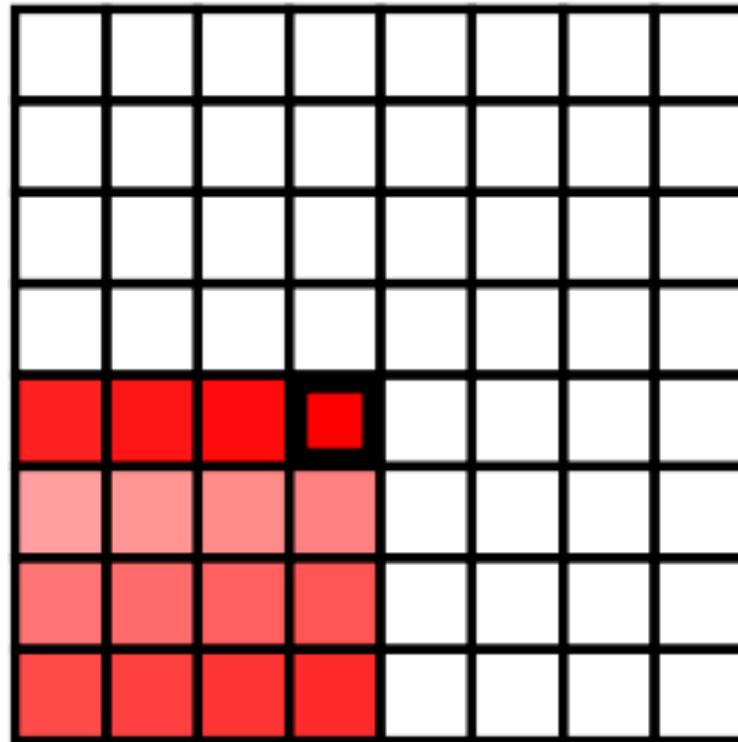


A mem:212 cache hit:156



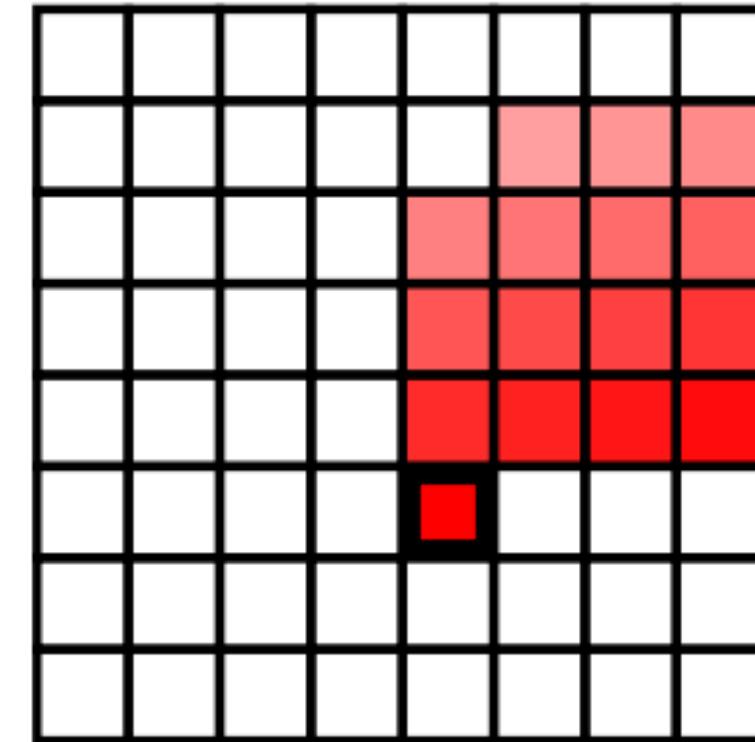
mem:636

B mem:212 cache hit:180

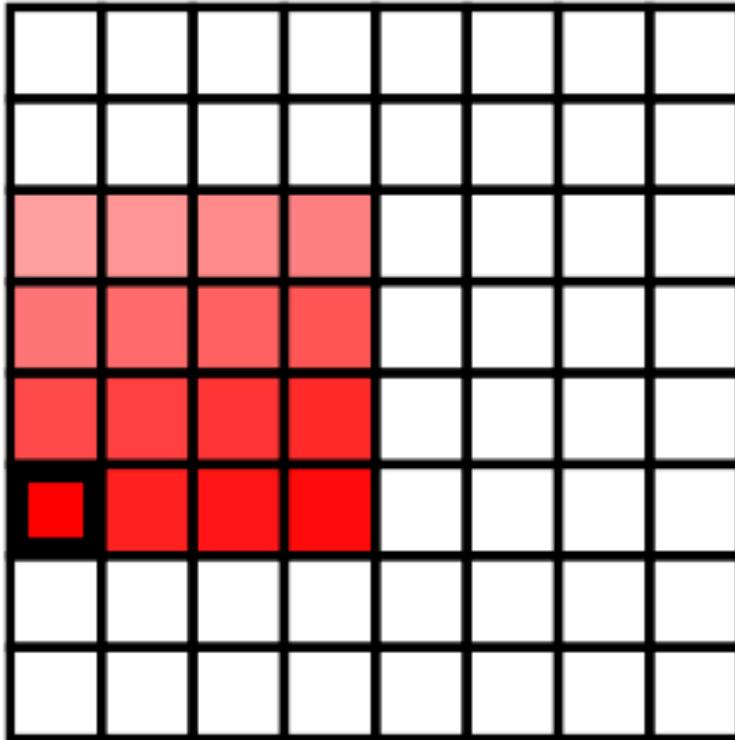


cache hits:495 ≈77%

C mem:212 cache hit:159

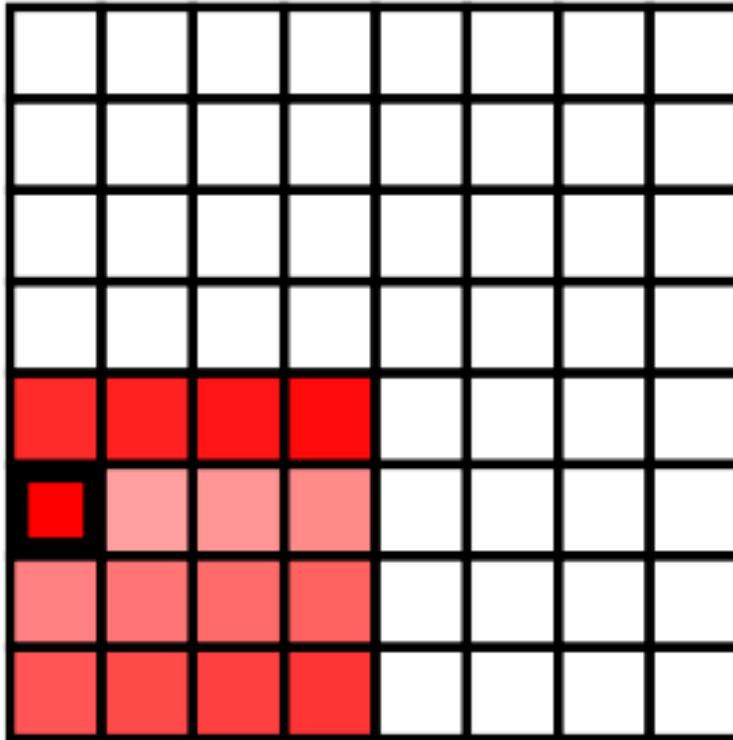


A mem:213 cache hit:157



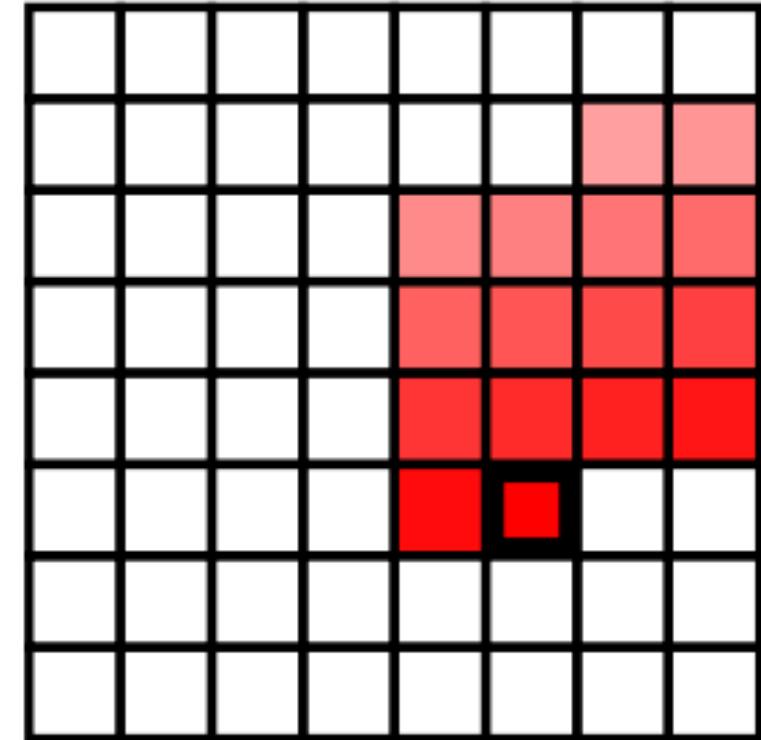
mem:639

B mem:213 cache hit:181

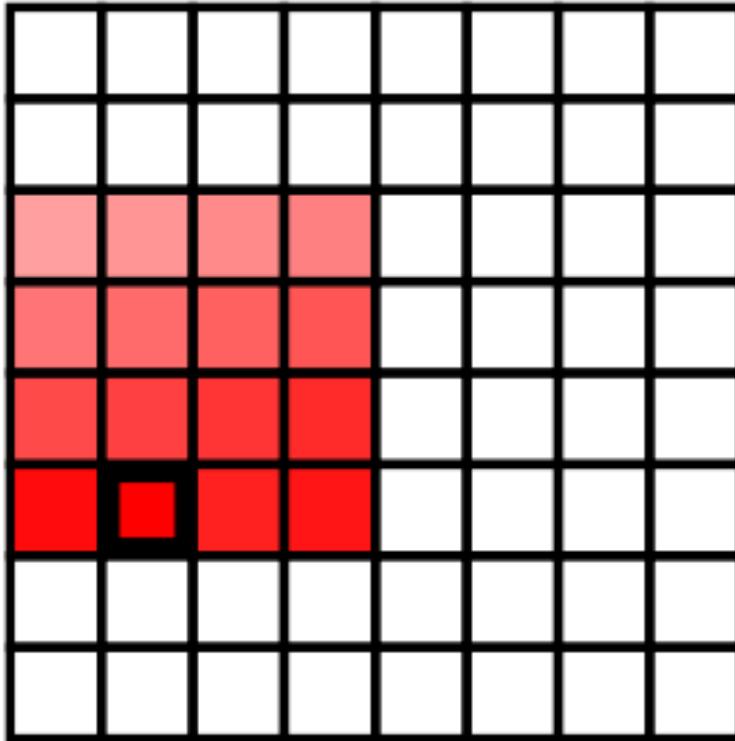


cache hits:497 ≈77%

C mem:213 cache hit:159

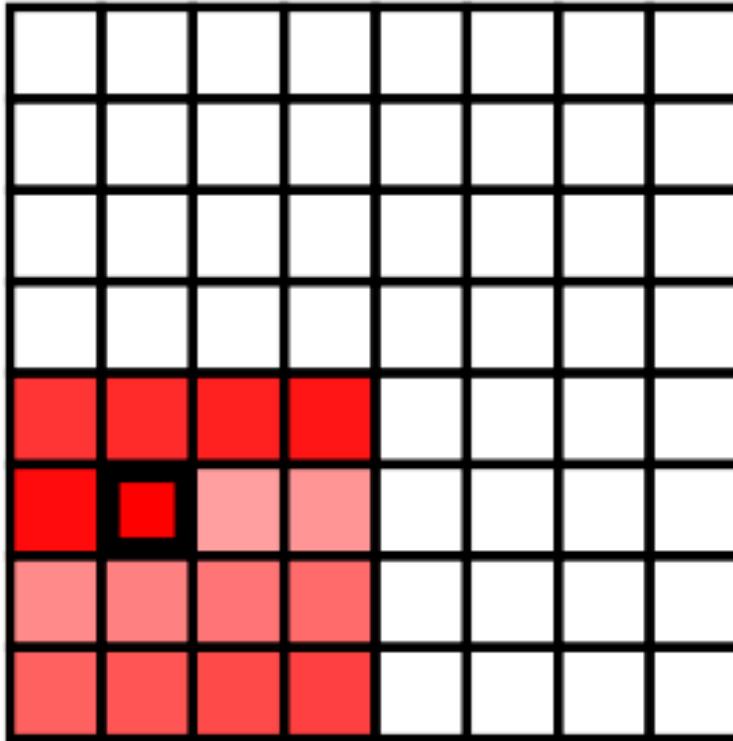


A mem:214 cache hit:158



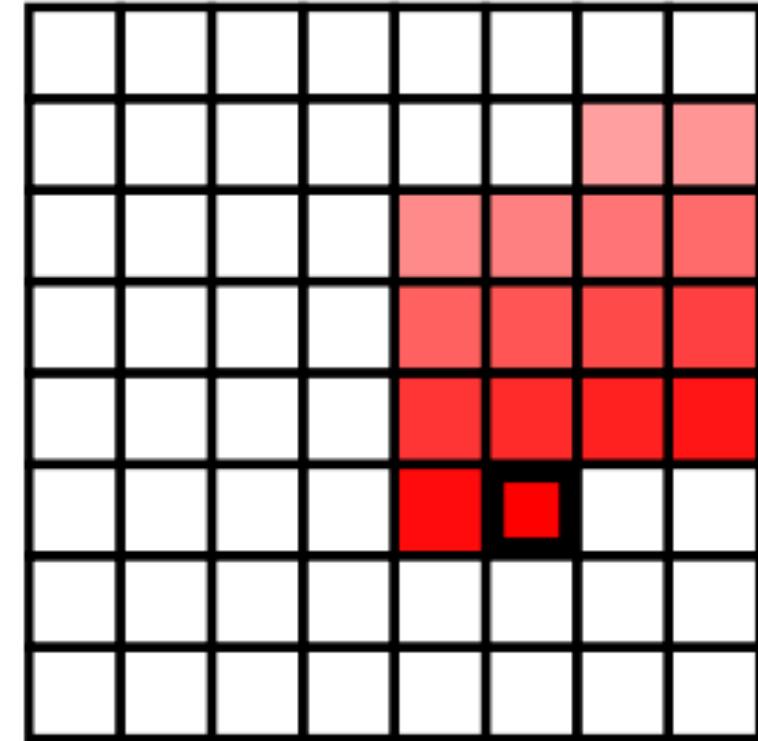
mem:642

B mem:214 cache hit:182

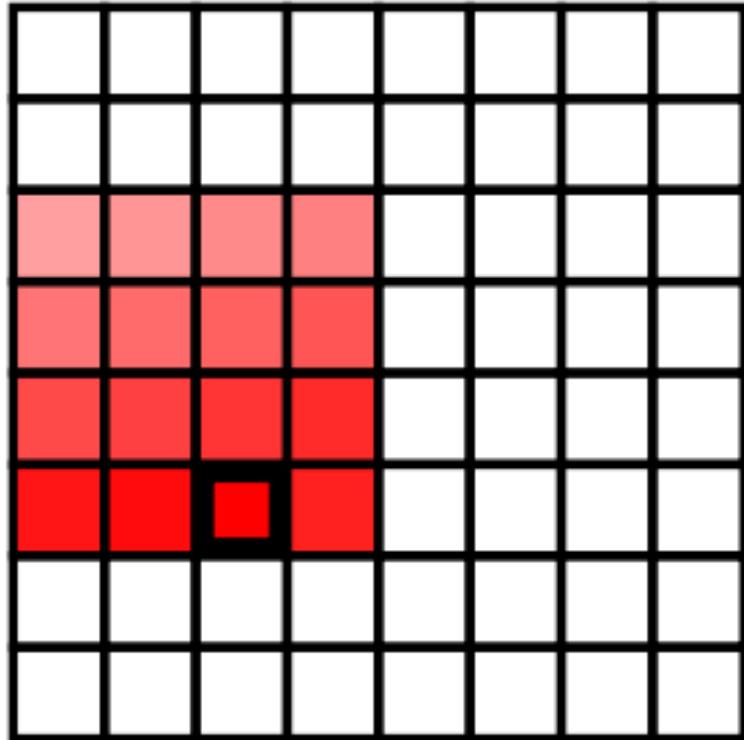


cache hits:500 ≈77%

C mem:214 cache hit:160

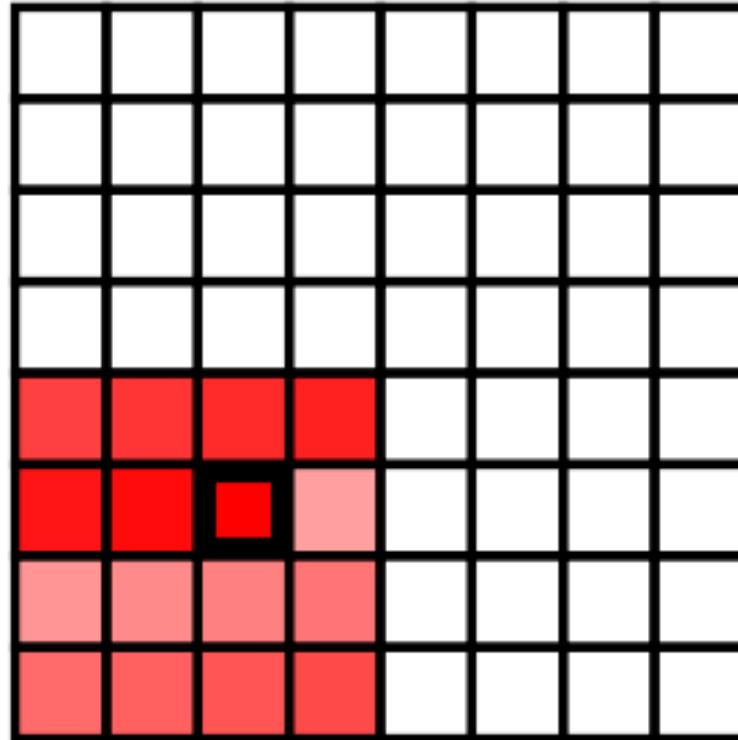


A mem:215 cache hit:159



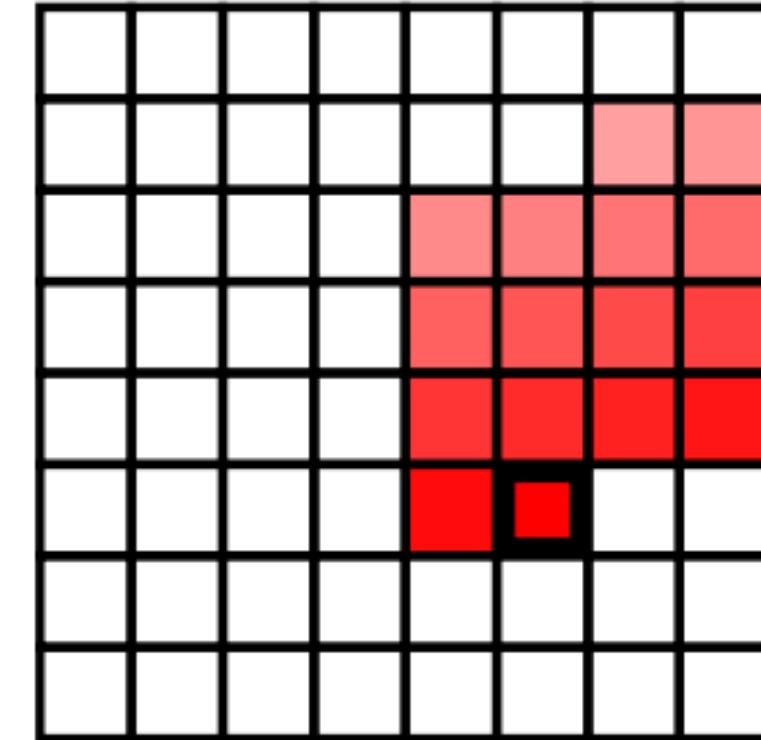
mem:645

B mem:215 cache hit:183

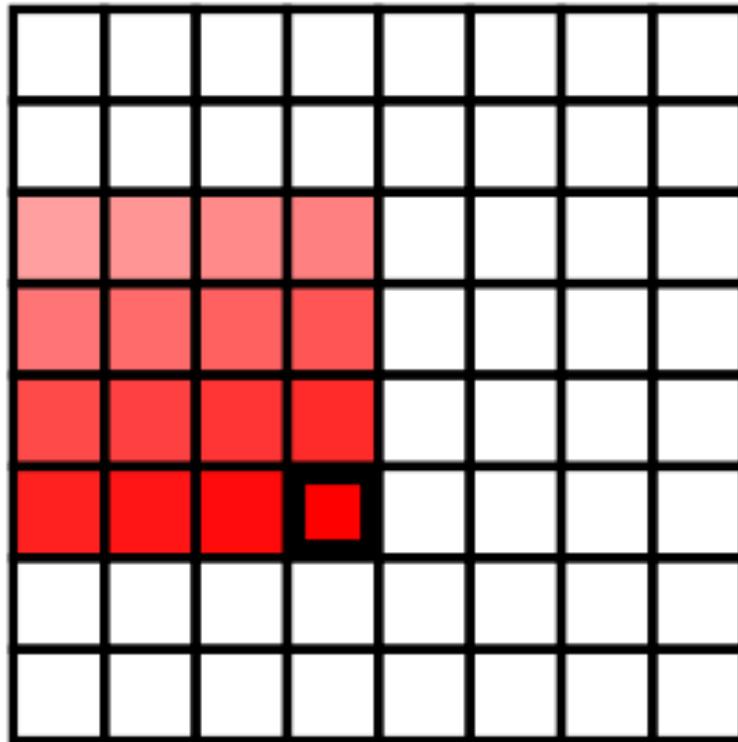


cache hits:503 ≈77%

C mem:215 cache hit:161

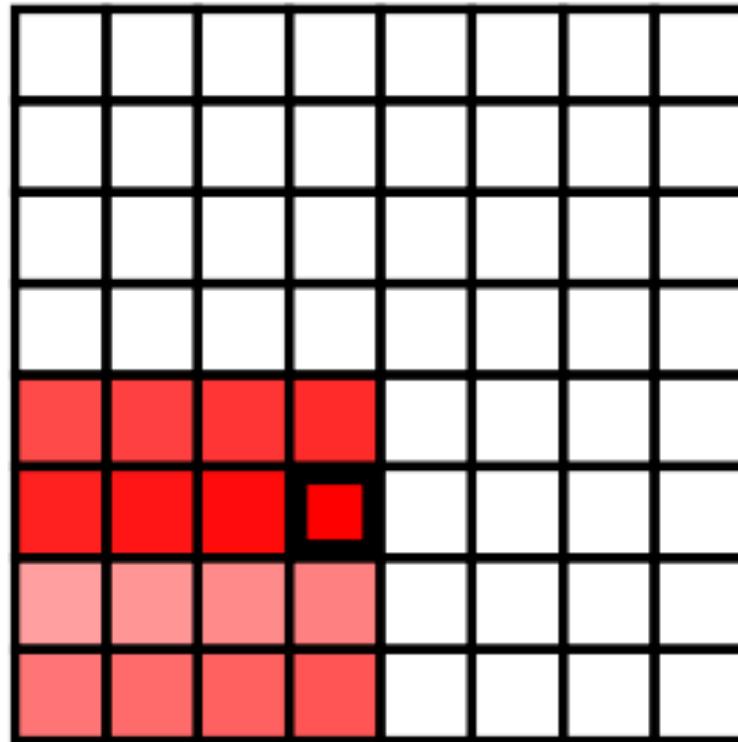


A mem:216 cache hit:160



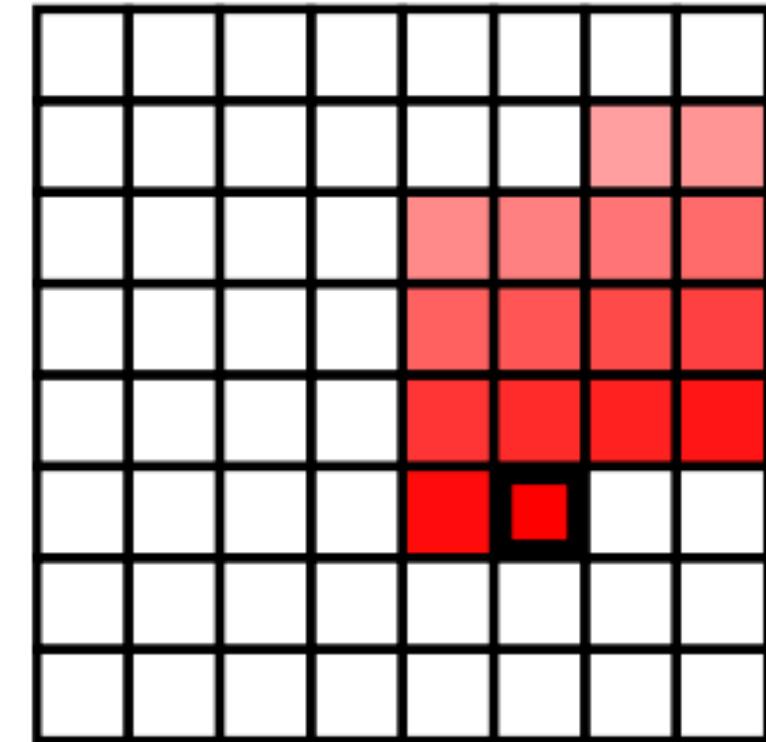
mem:648

B mem:216 cache hit:184

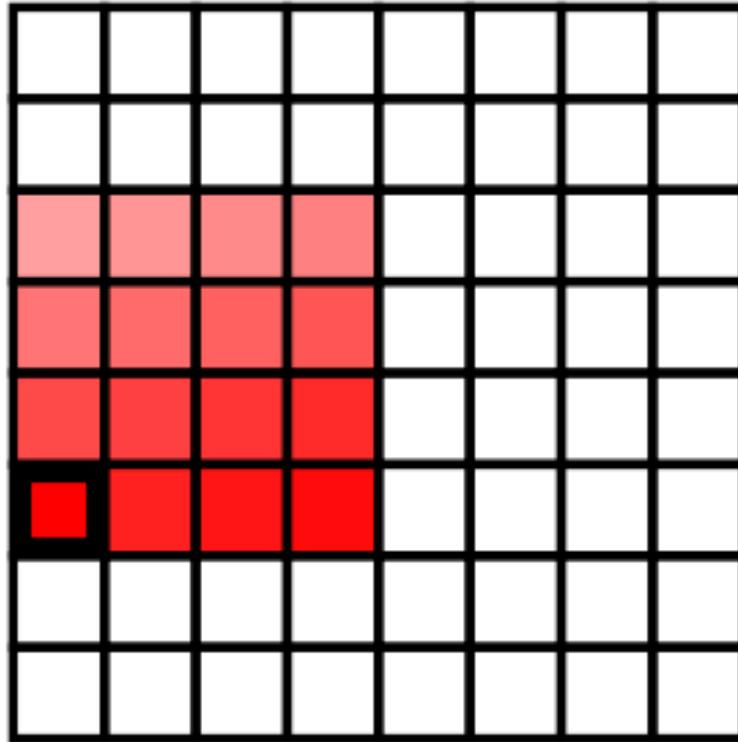


cache hits:506 ≈78%

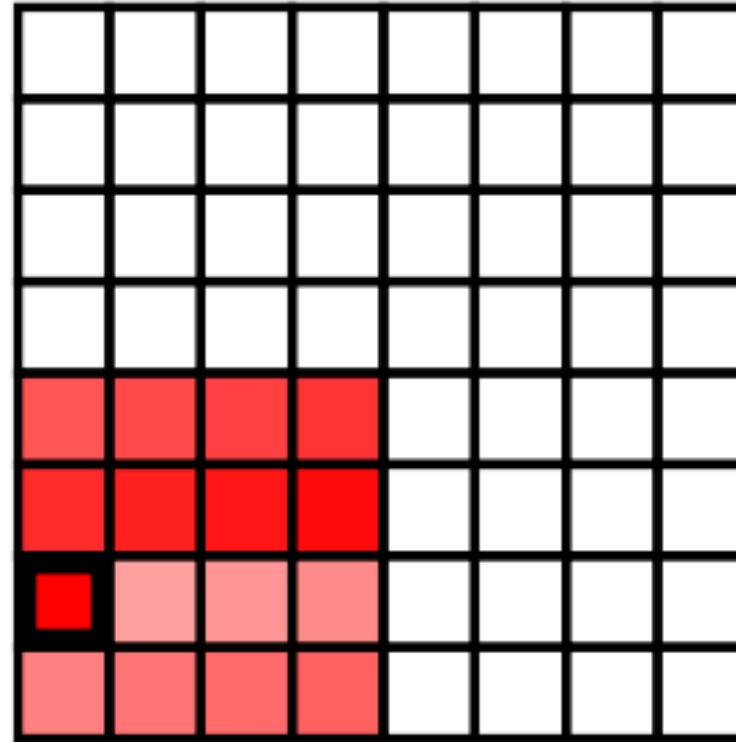
C mem:216 cache hit:162



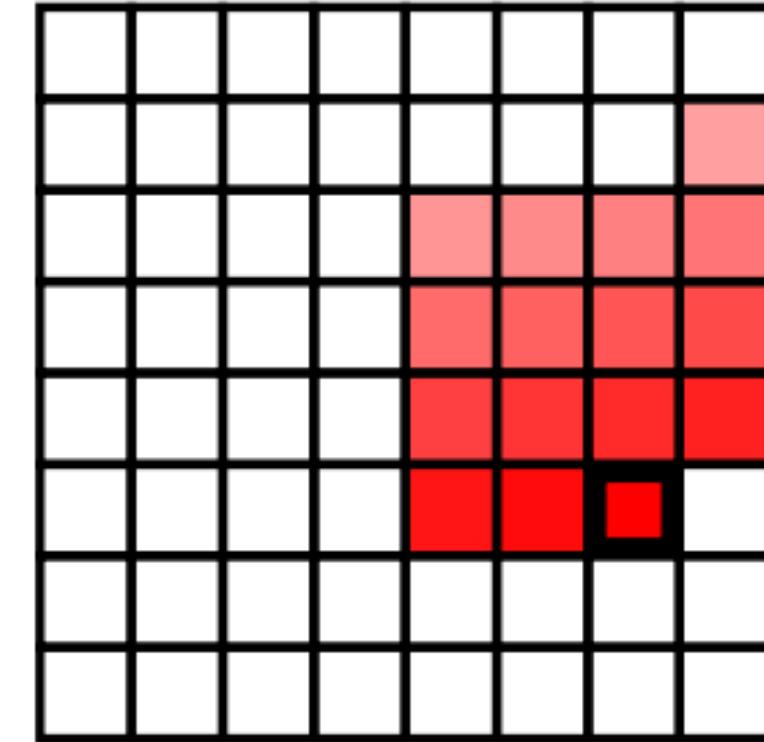
A mem:217 cache hit:161



B mem:217 cache hit:185



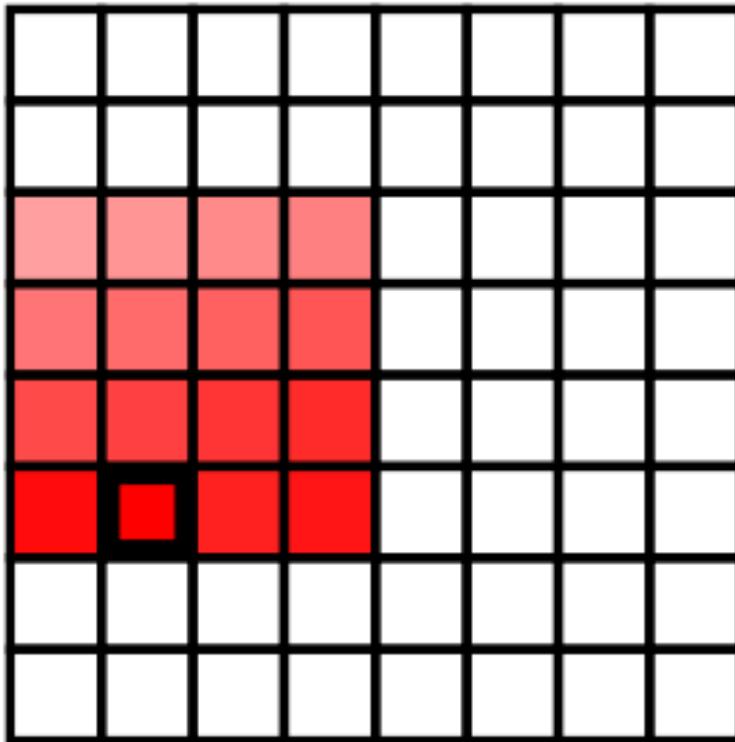
C mem:217 cache hit:162



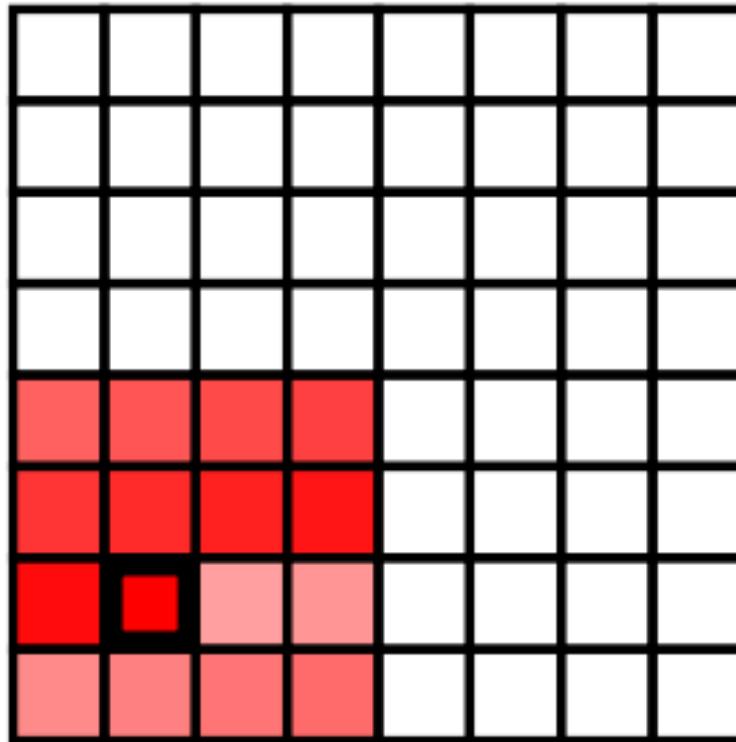
mem:651

cache hits:508 $\approx 78\%$

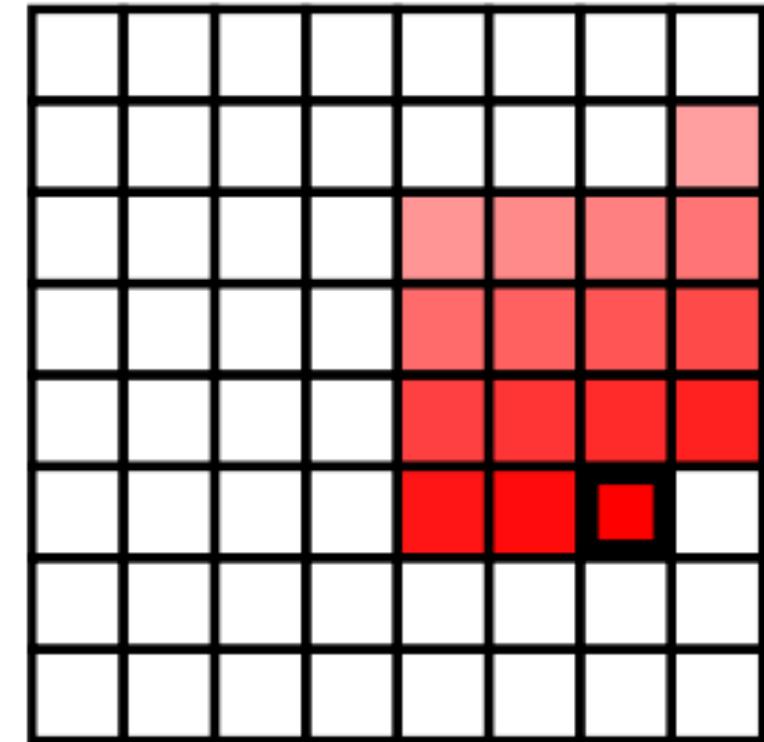
A mem:218 cache hit:162



B mem:218 cache hit:186



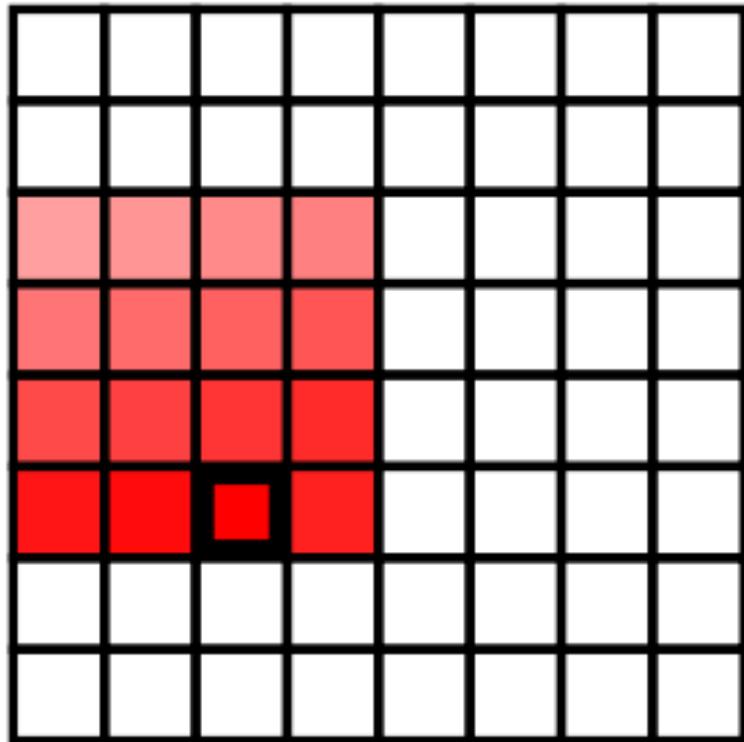
C mem:218 cache hit:163



mem:654

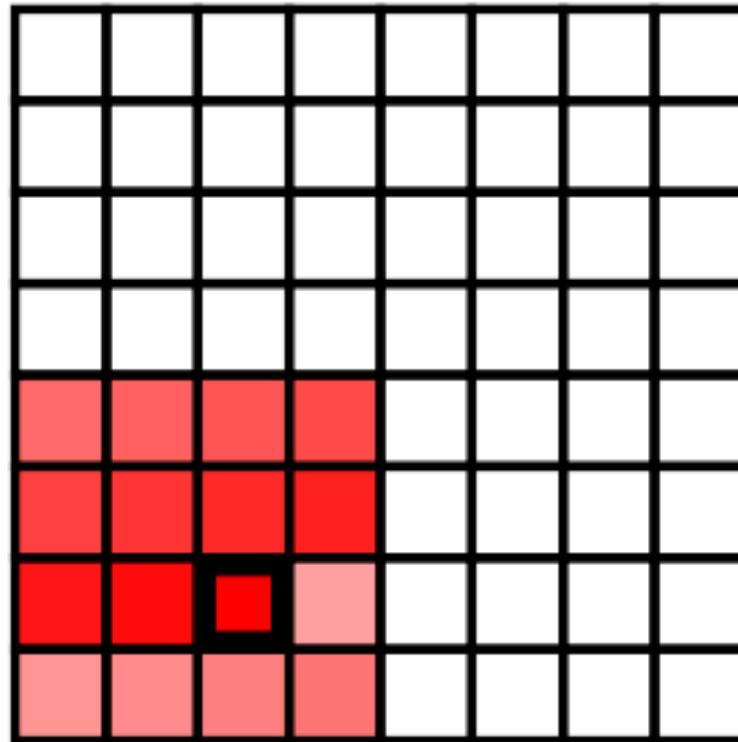
cache hits:511 $\approx 78\%$

A mem:219 cache hit:163



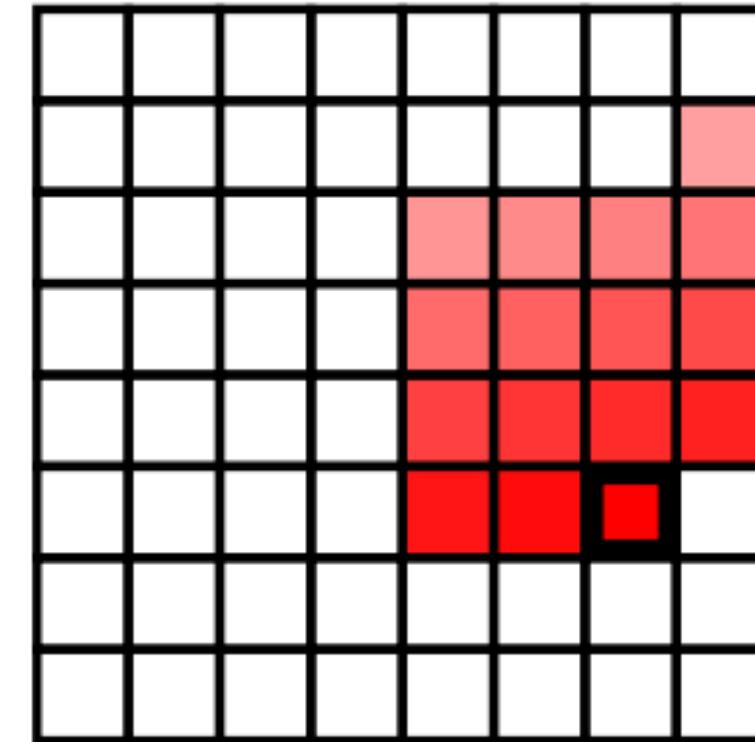
mem:657

B mem:219 cache hit:187

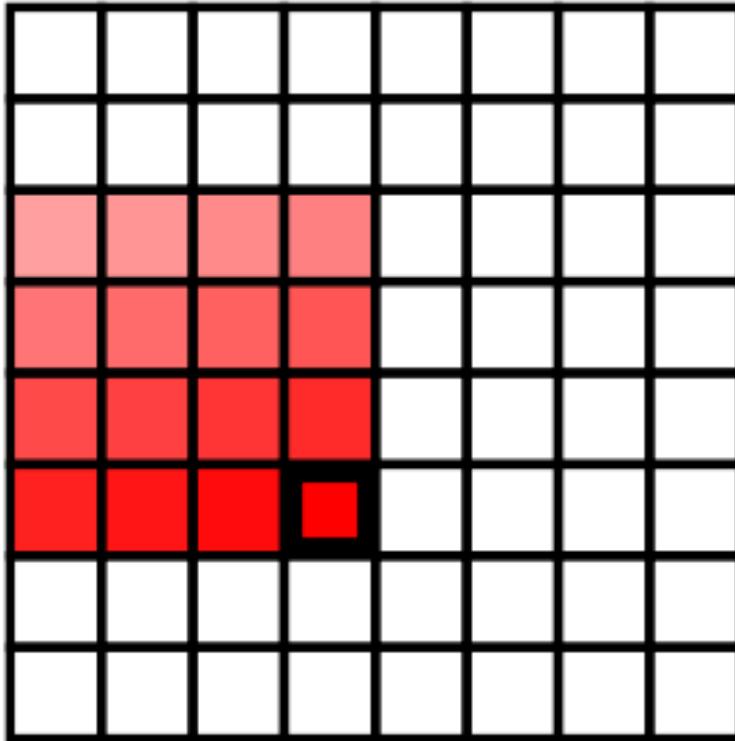


cache hits:514 ≈78%

C mem:219 cache hit:164

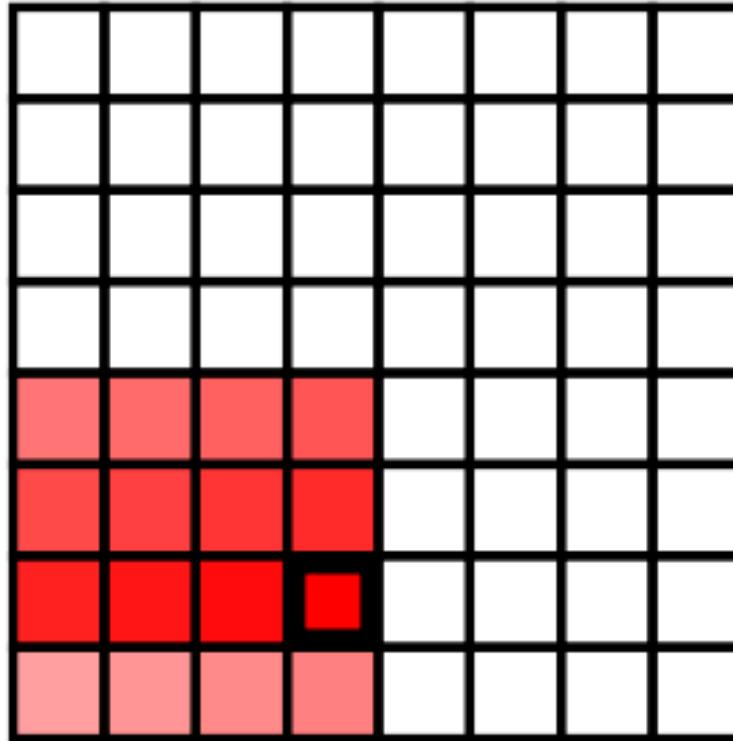


A mem:220 cache hit:164



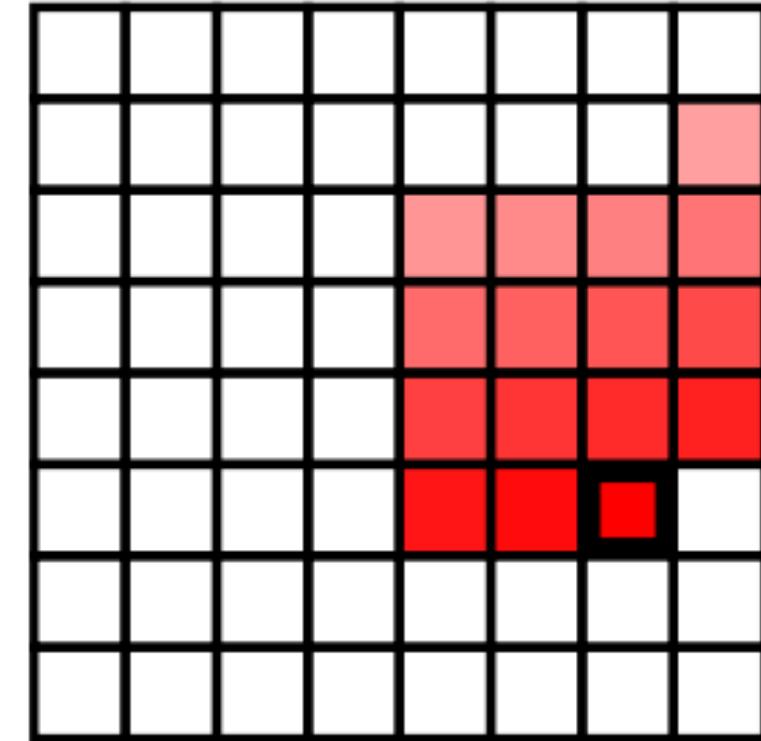
mem:660

B mem:220 cache hit:188

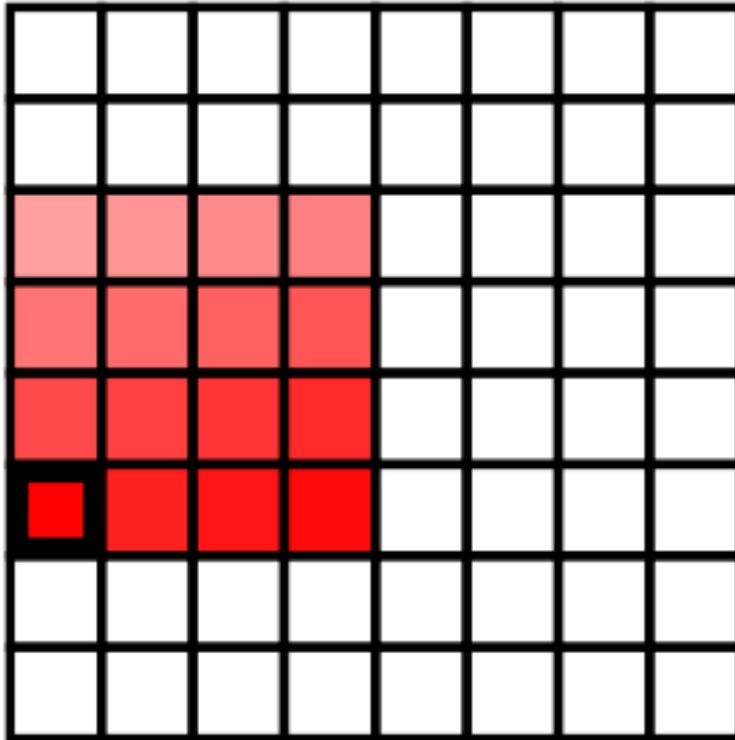


cache hits:517 ≈78%

C mem:220 cache hit:165

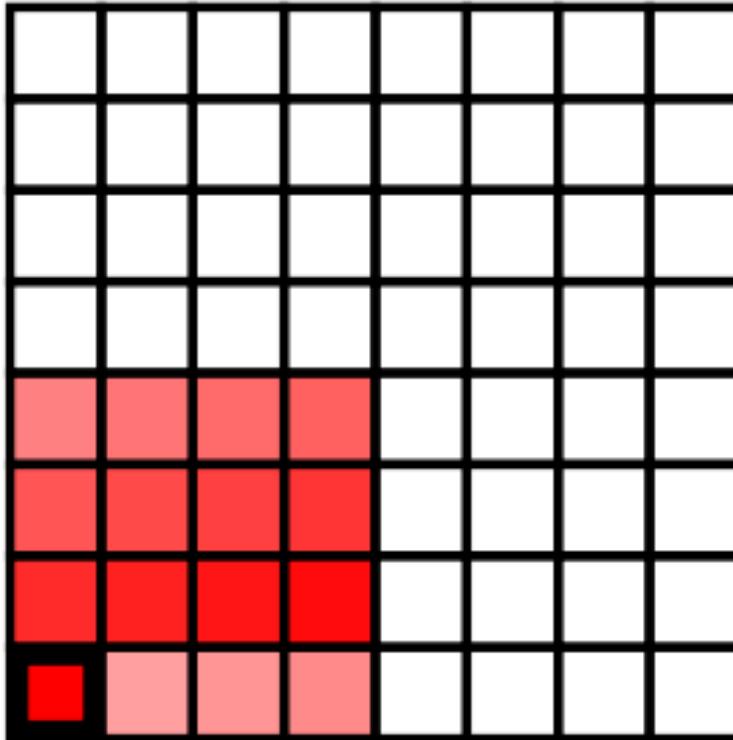


A mem:221 cache hit:165



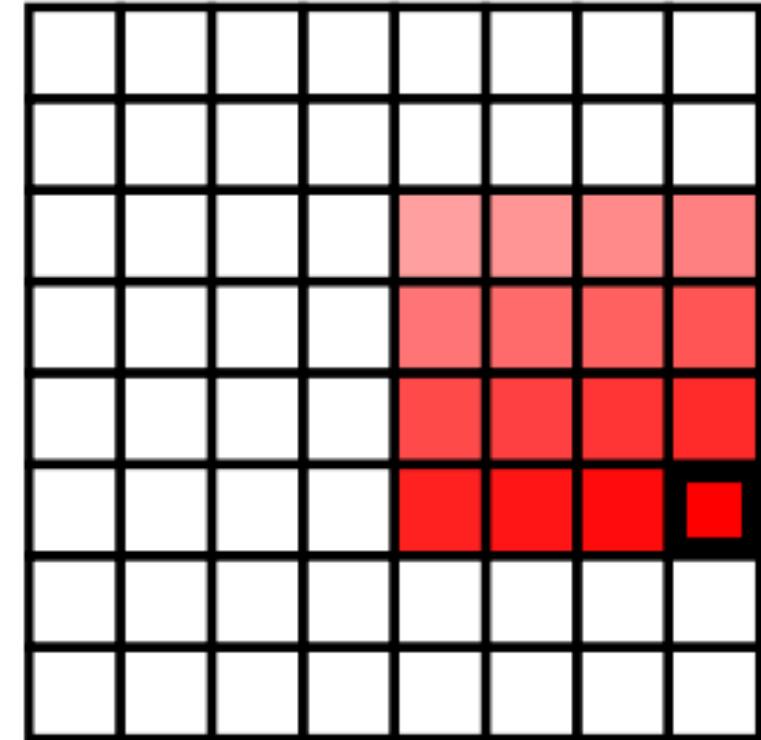
mem:663

B mem:221 cache hit:189

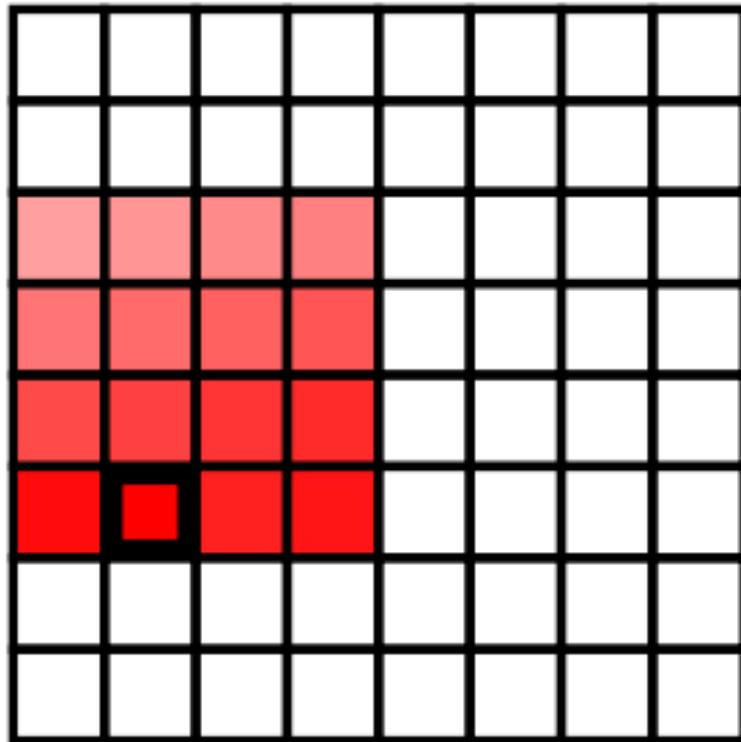


cache hits:519 ≈78%

C mem:221 cache hit:165

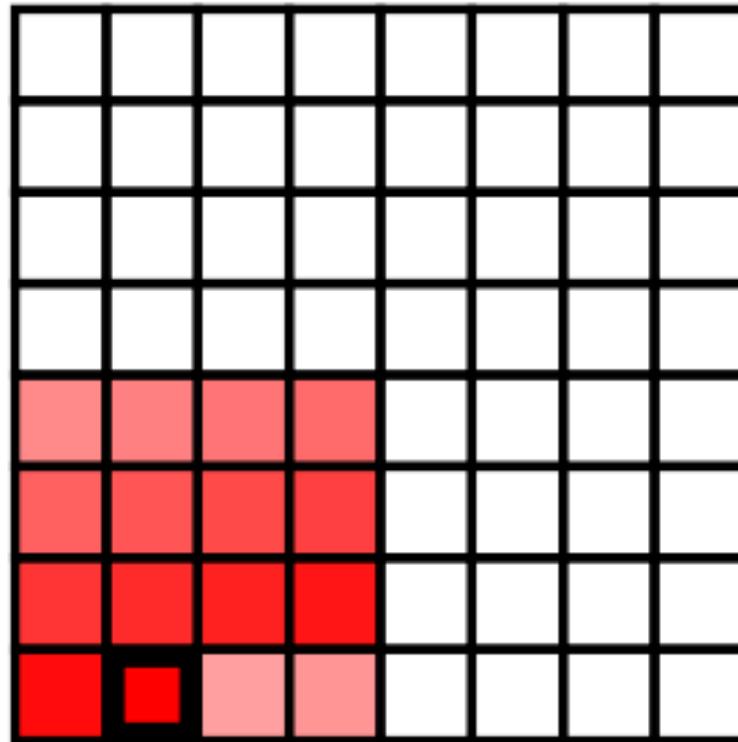


A mem:222 cache hit:166



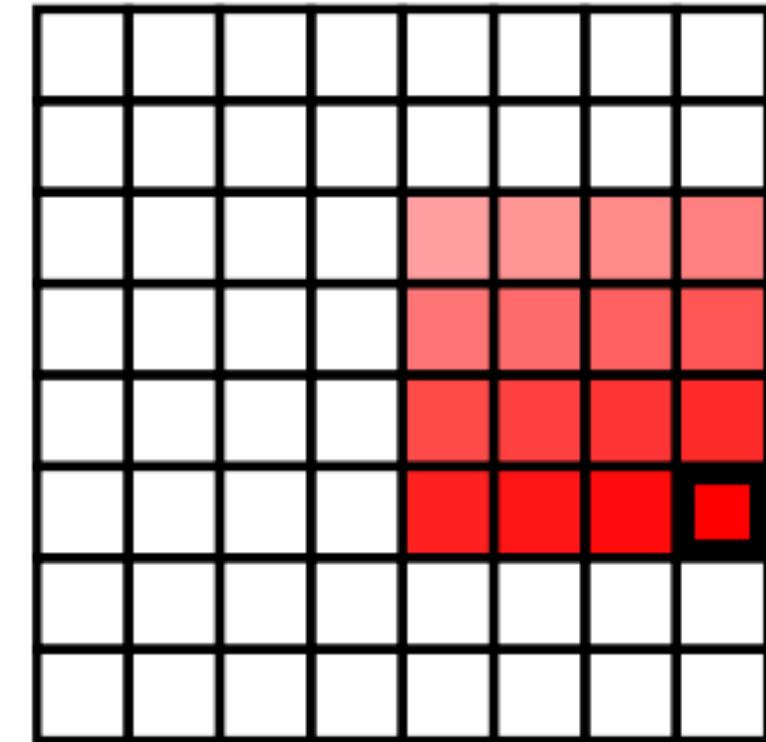
mem:666

B mem:222 cache hit:190

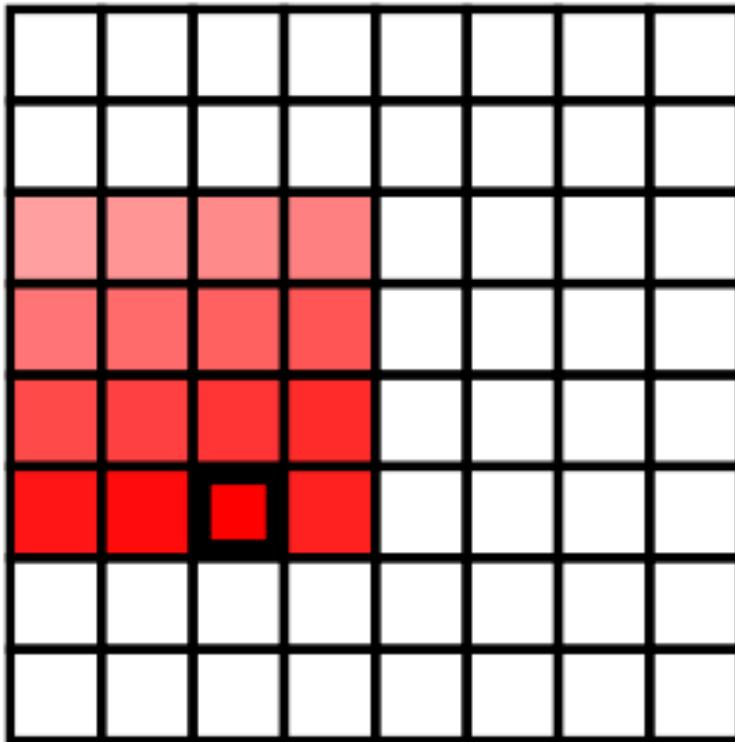


cache hits:522 ≈78%

C mem:222 cache hit:166

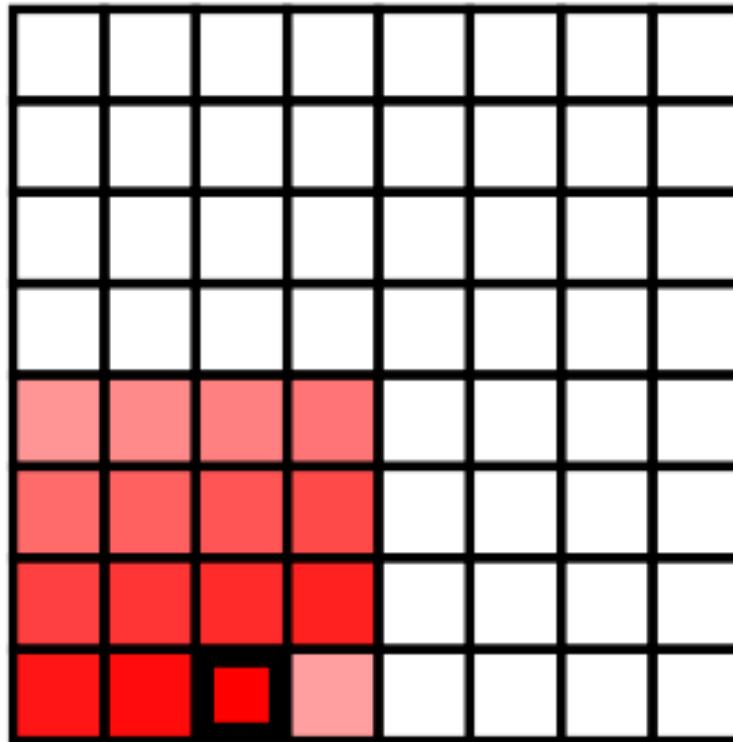


A mem:223 cache hit:167



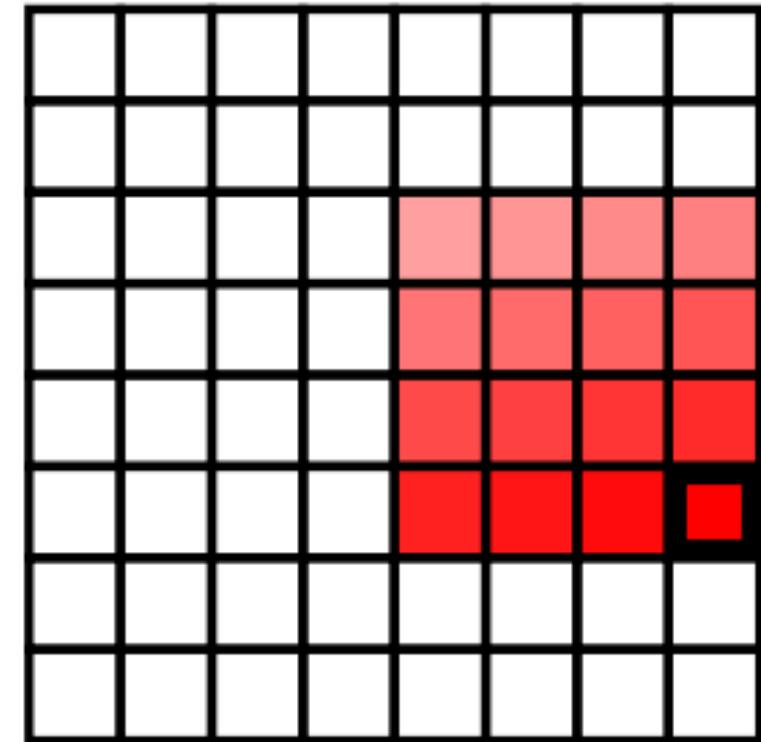
mem:669

B mem:223 cache hit:191

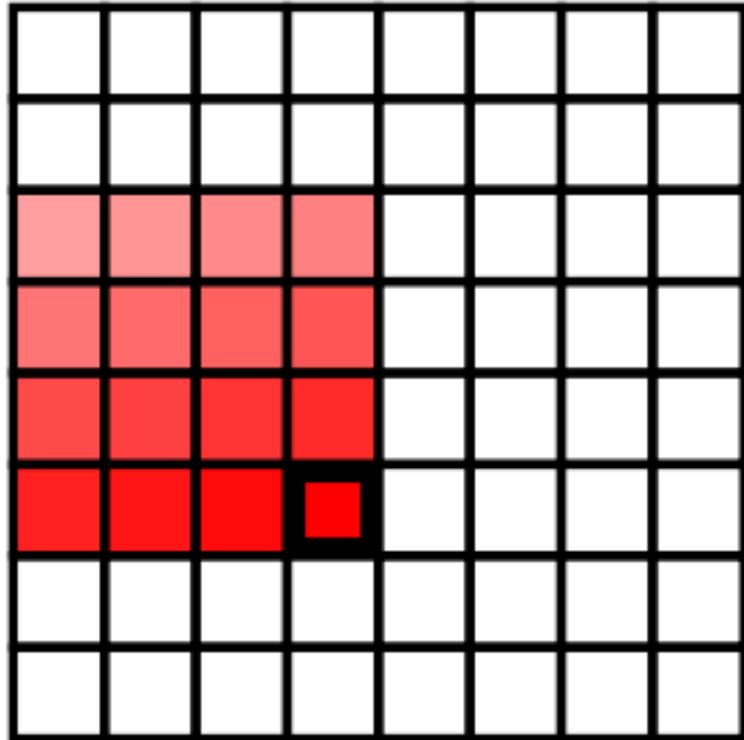


cache hits:525 ≈78%

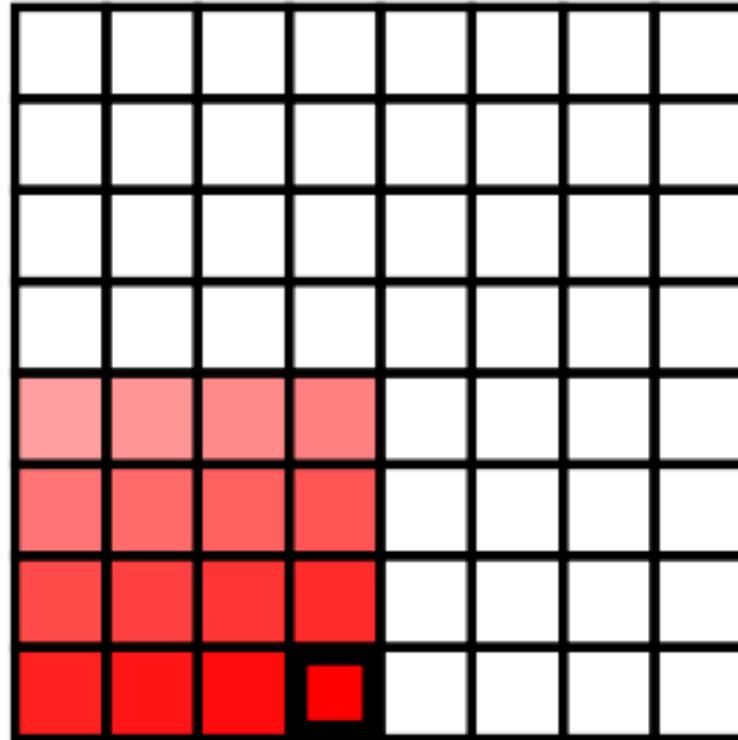
C mem:223 cache hit:167



A mem:224 cache hit:168



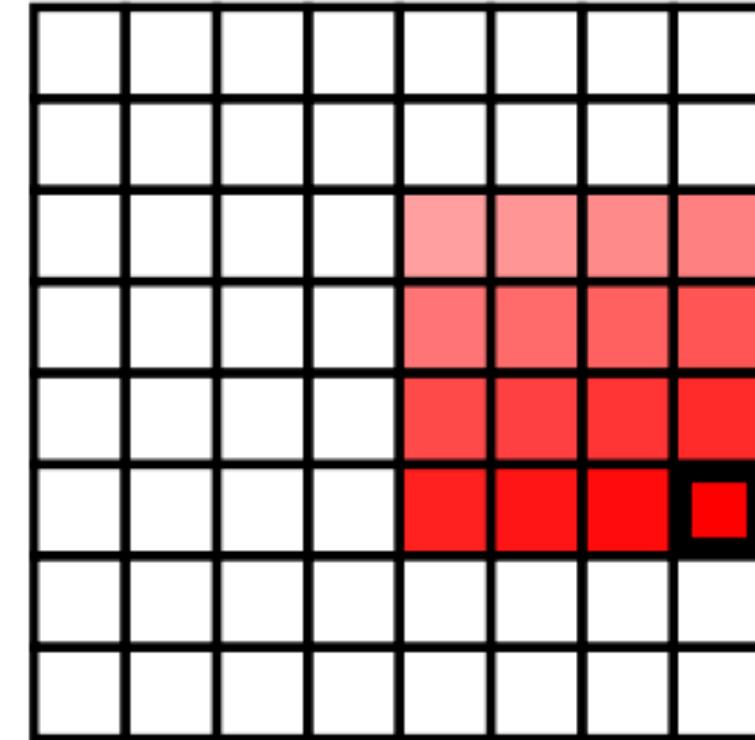
B mem:224 cache hit:192



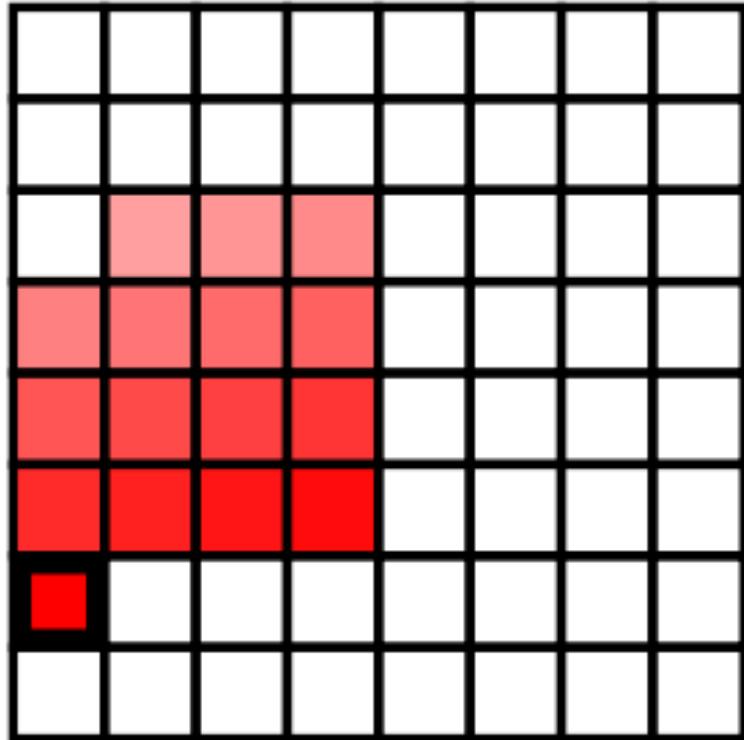
mem:672

cache hits:528 ≈78%

C mem:224 cache hit:168

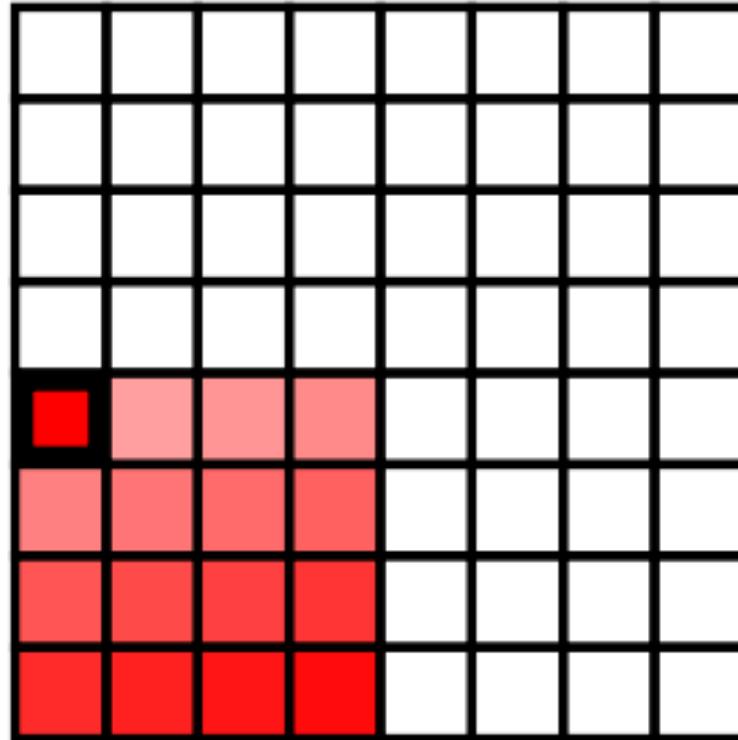


A mem:225 cache hit:168



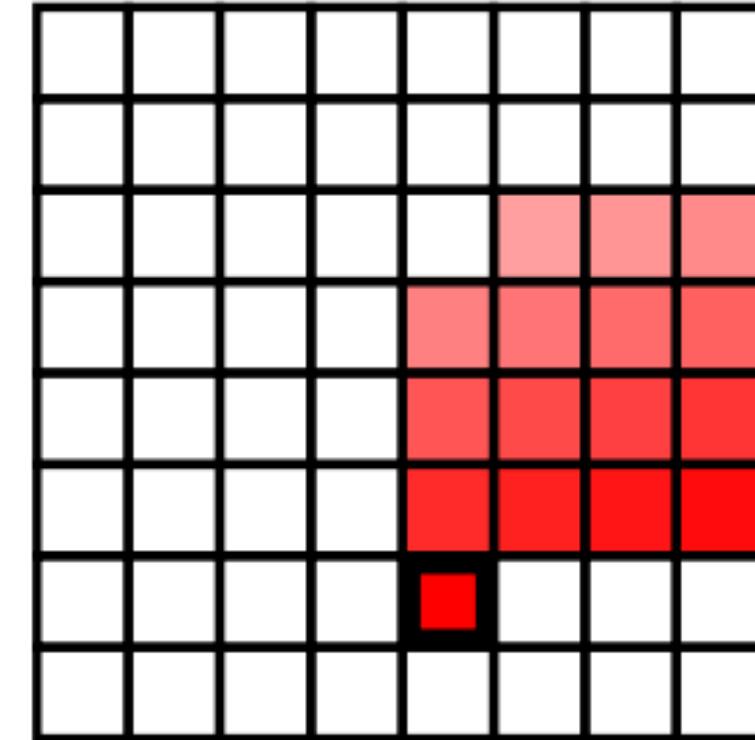
mem:675

B mem:225 cache hit:193

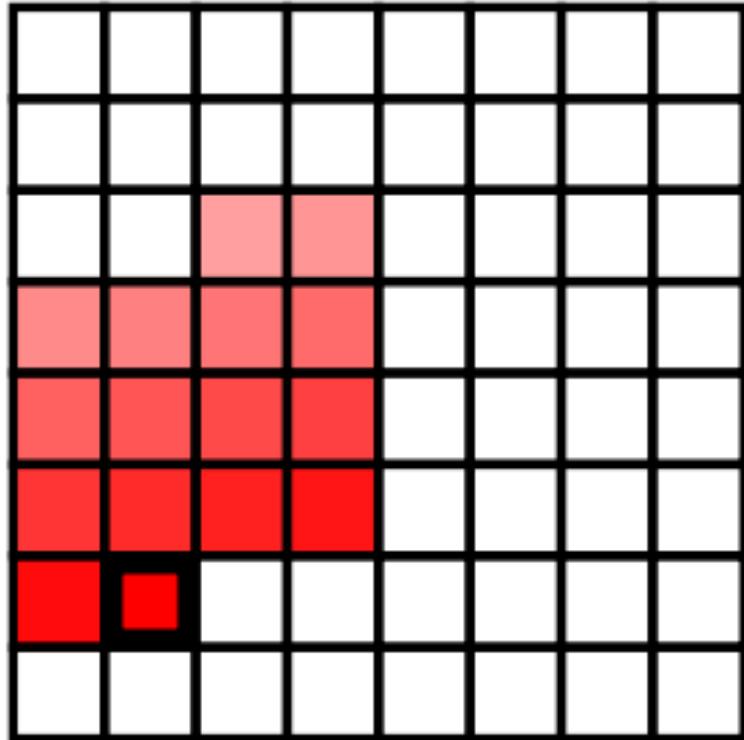


cache hits:529 ≈78%

C mem:225 cache hit:168

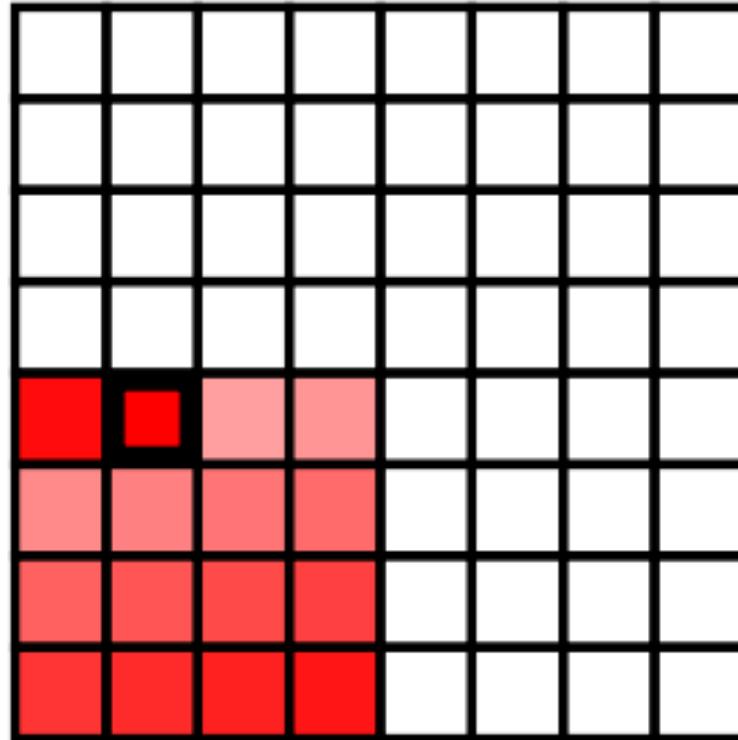


A mem:226 cache hit:168



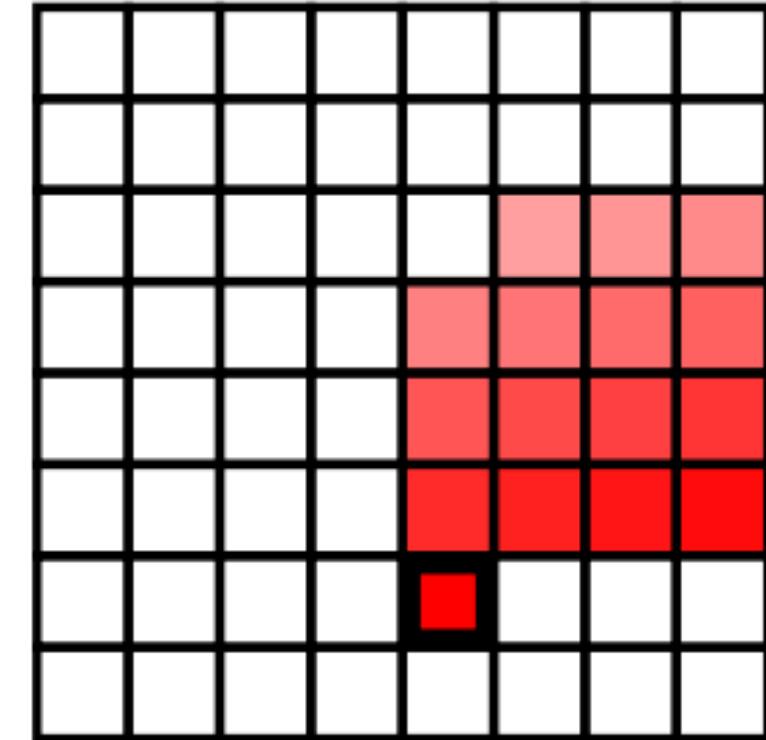
mem:678

B mem:226 cache hit:194

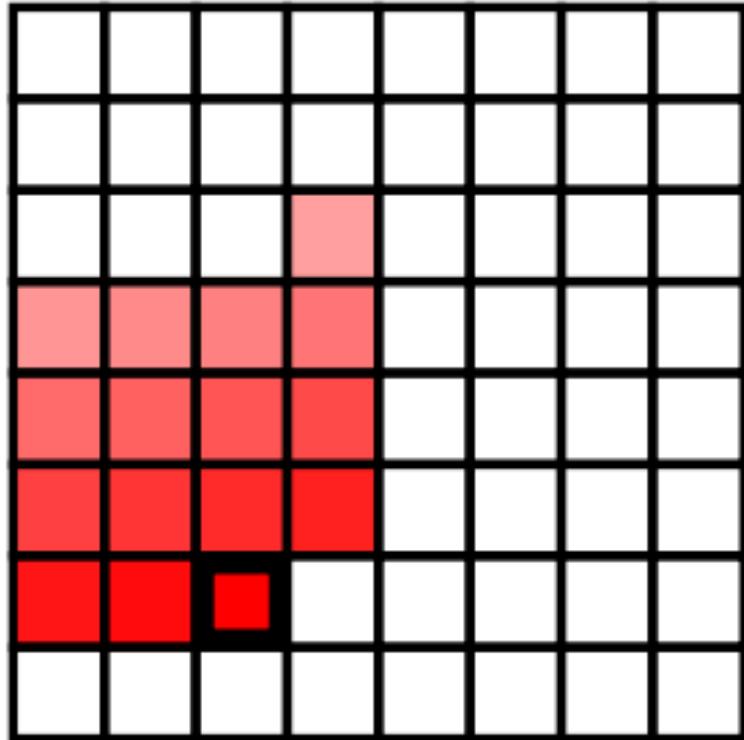


cache hits:531 ≈78%

C mem:226 cache hit:169

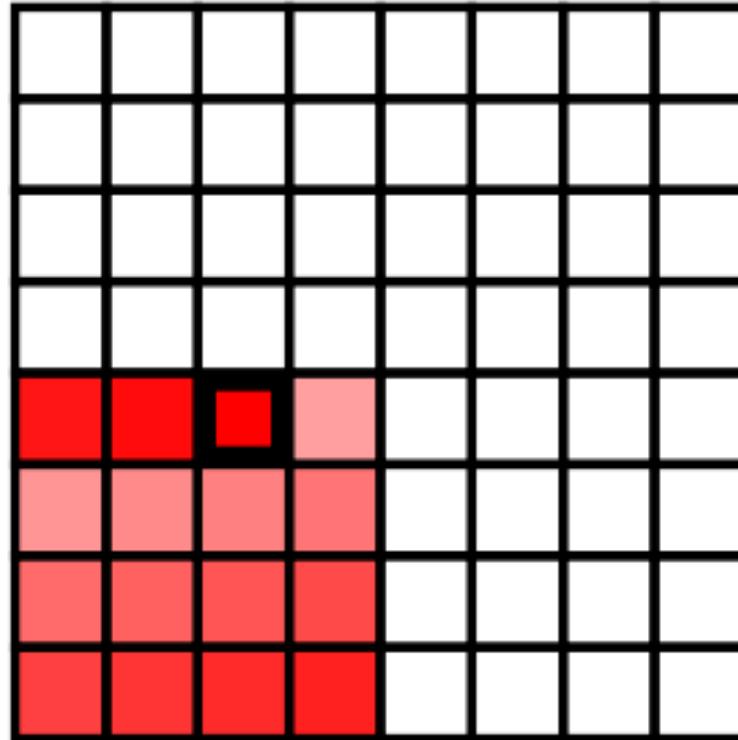


A mem:227 cache hit:168



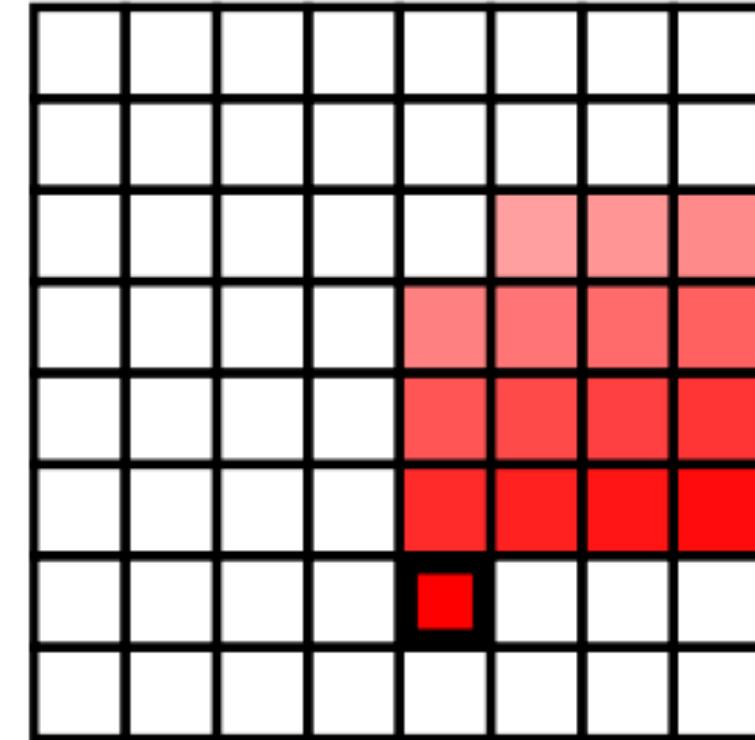
mem:681

B mem:227 cache hit:195

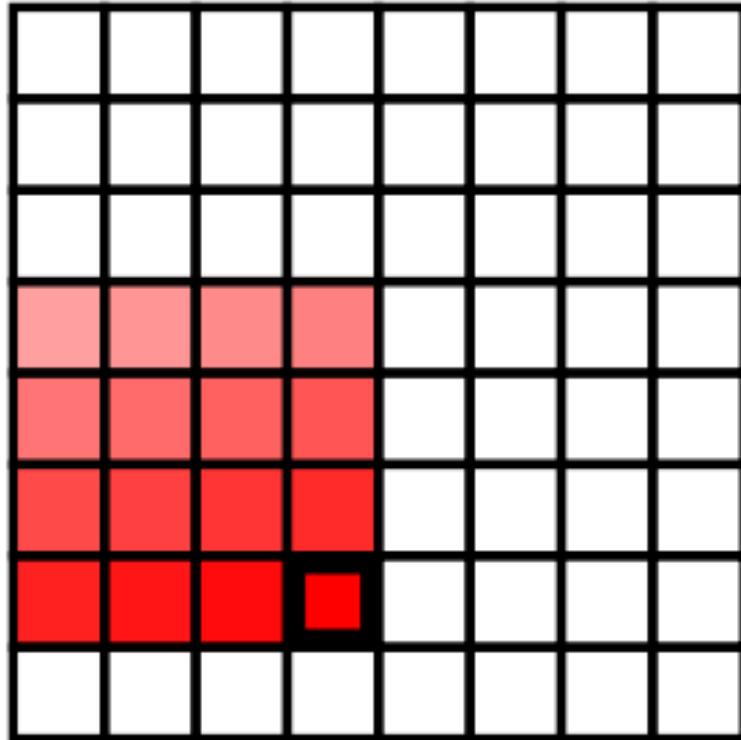


cache hits:533 ≈78%

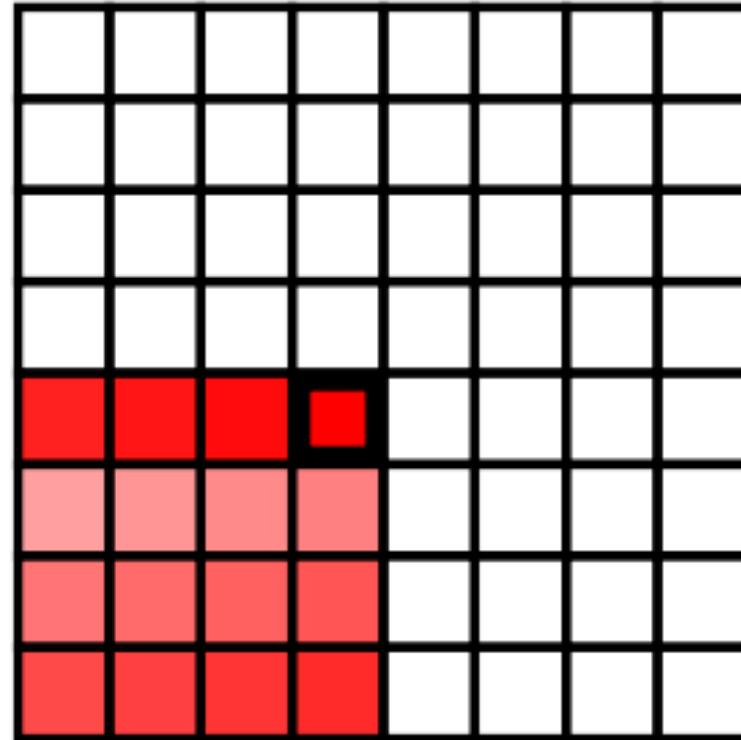
C mem:227 cache hit:170



A mem:228 cache hit:168



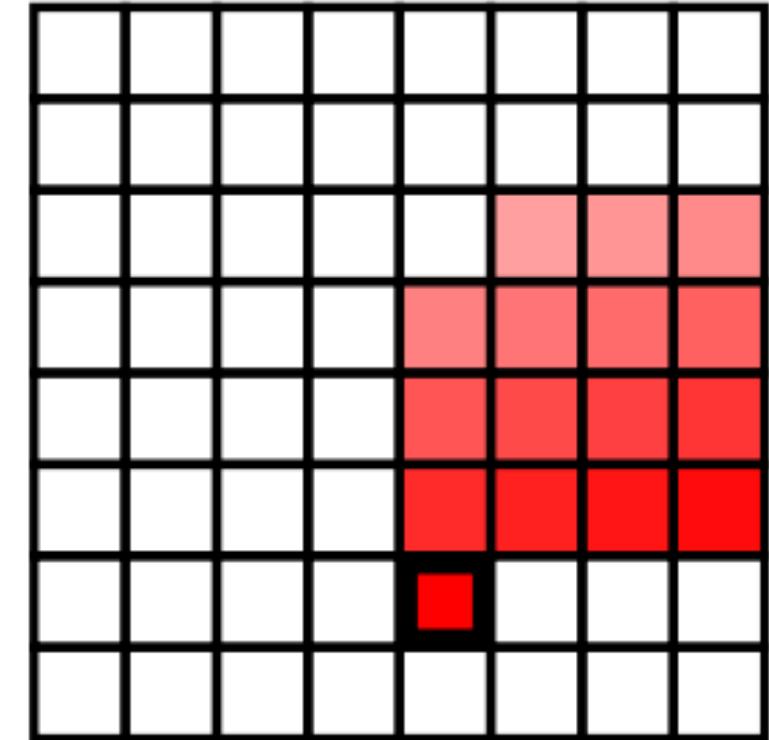
B mem:228 cache hit:196



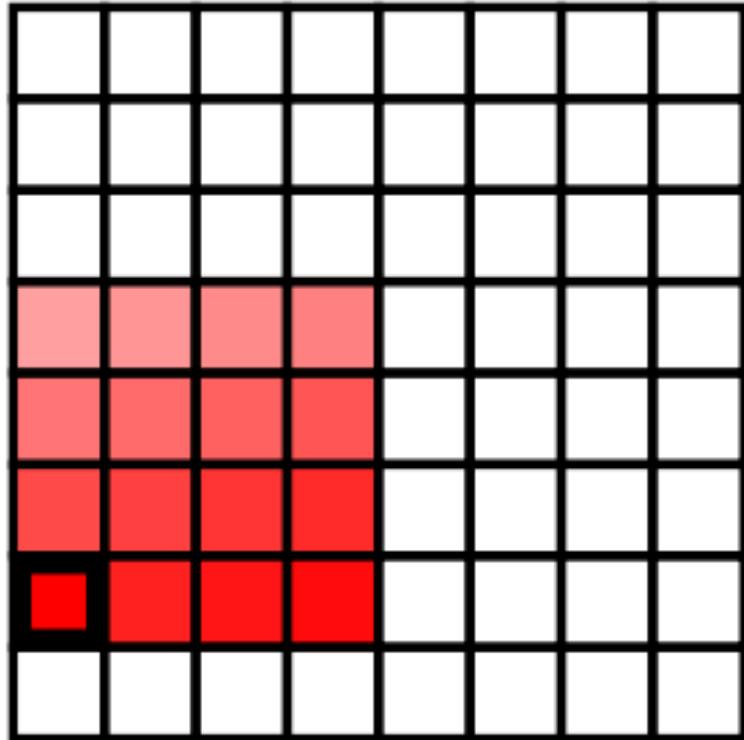
mem:684

cache hits:535 ≈78%

C mem:228 cache hit:171

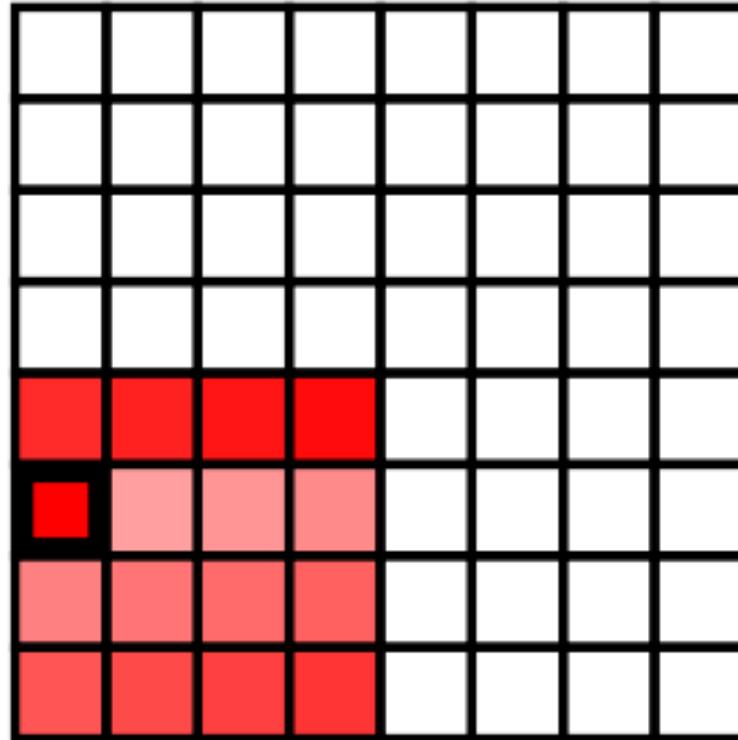


A mem:229 cache hit:169



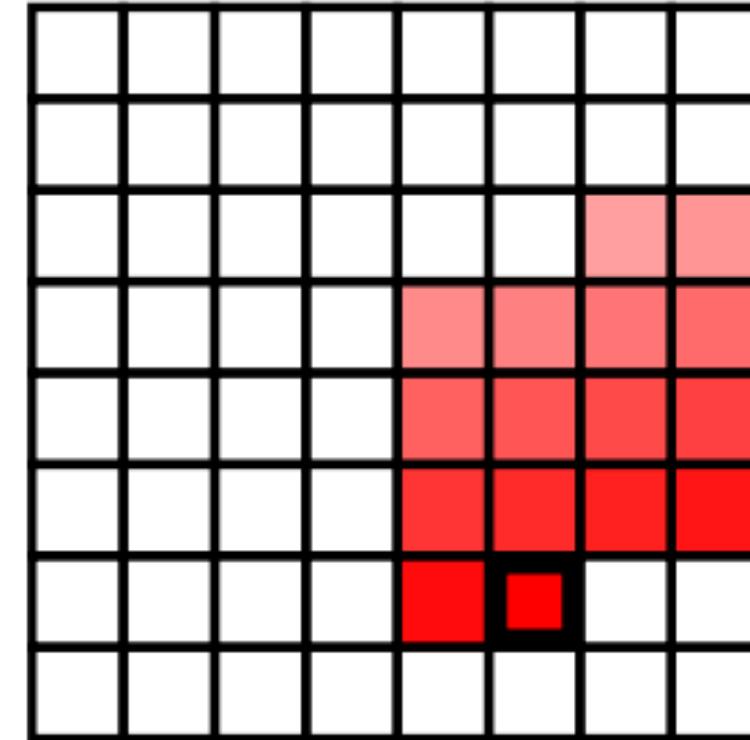
mem:687

B mem:229 cache hit:197

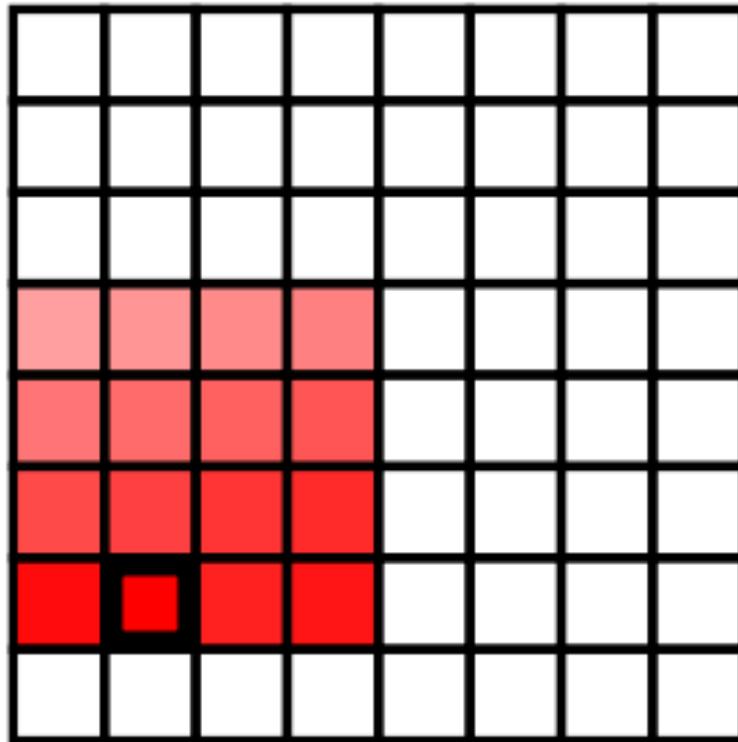


cache hits:537 $\approx 78\%$

C mem:229 cache hit:171

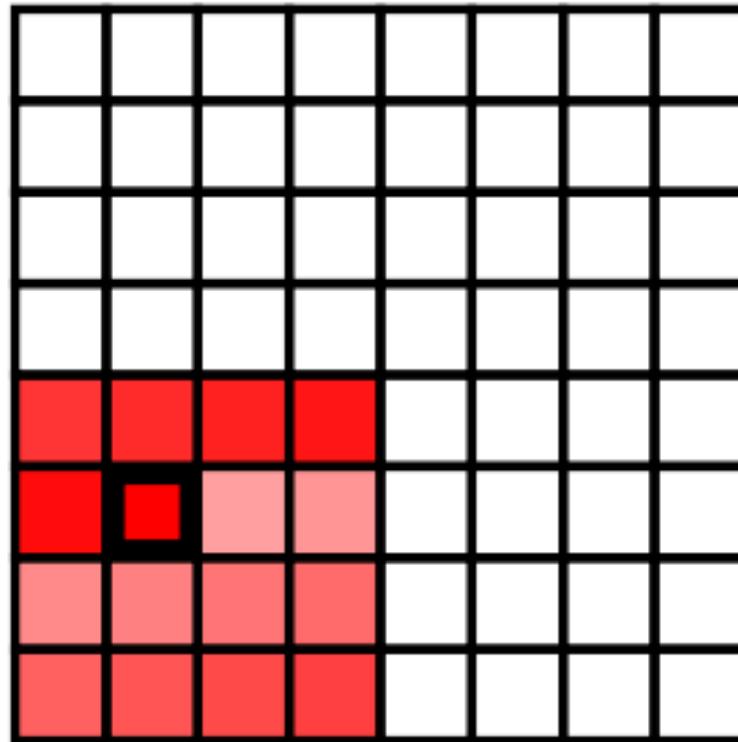


A mem:230 cache hit:170



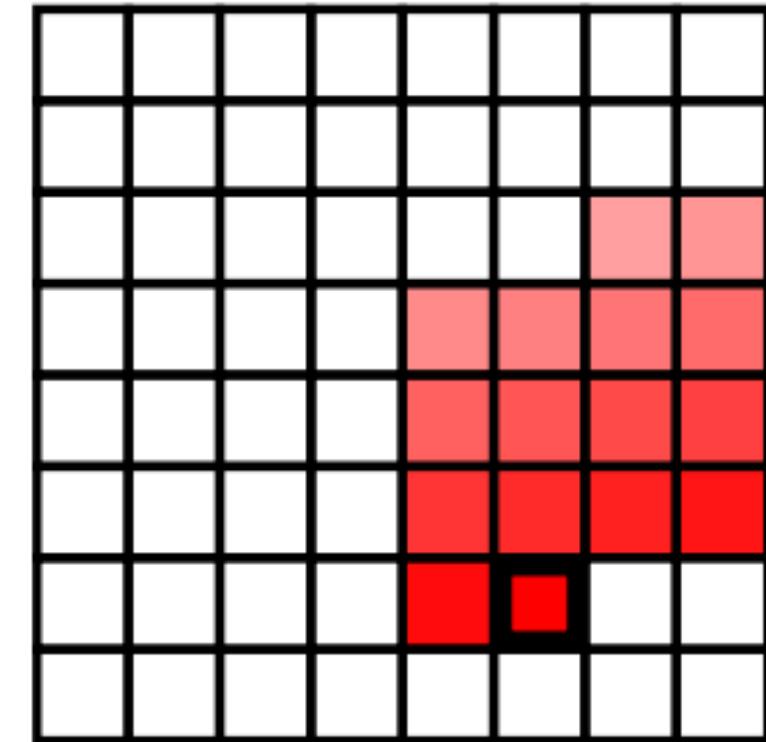
mem:690

B mem:230 cache hit:198

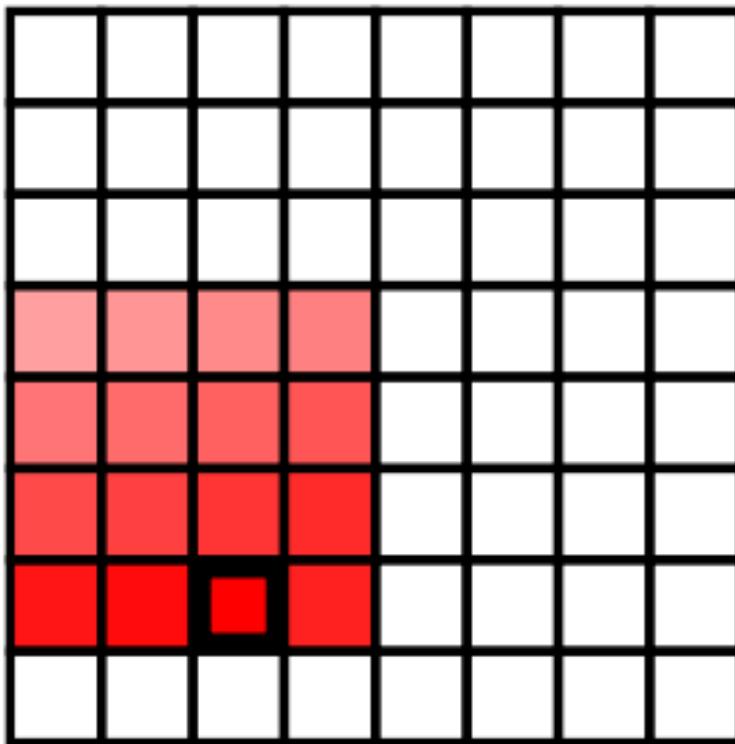


cache hits:540 $\approx 78\%$

C mem:230 cache hit:172

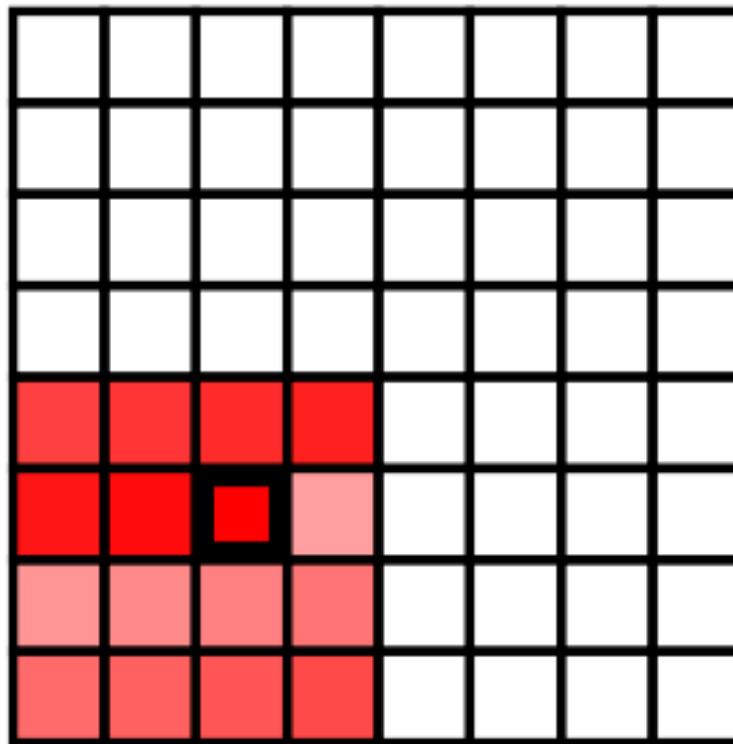


A mem:231 cache hit:171



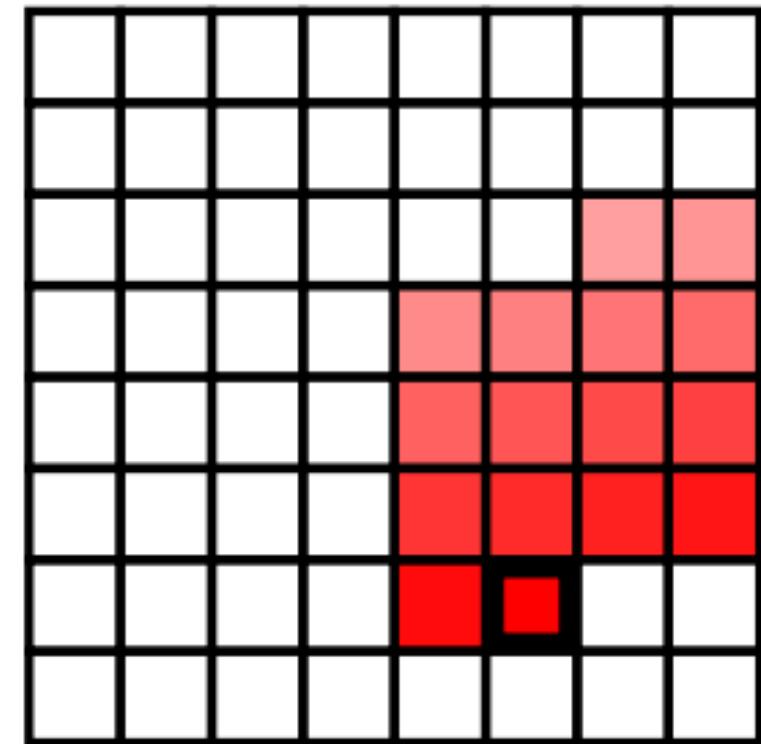
mem:693

B mem:231 cache hit:199

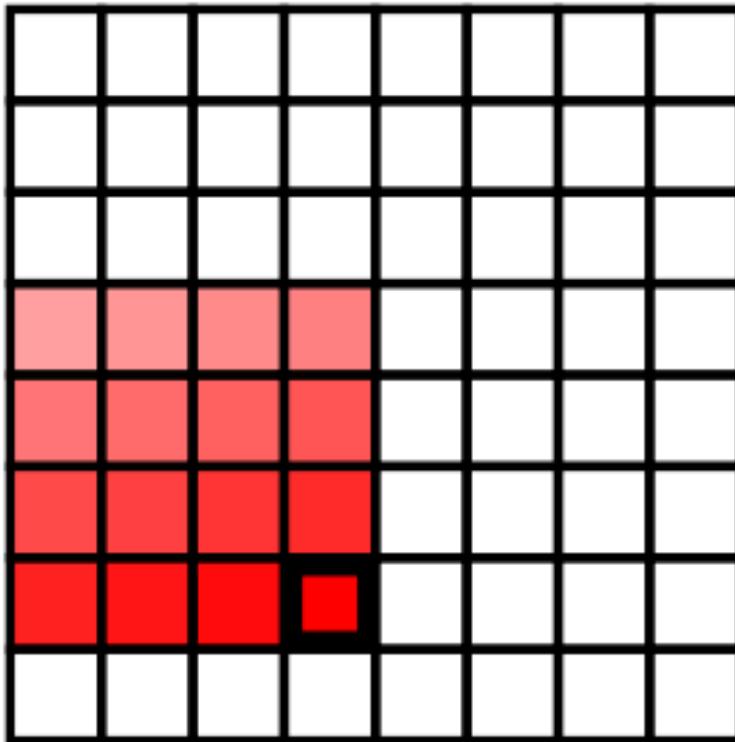


cache hits:543 ≈78%

C mem:231 cache hit:173

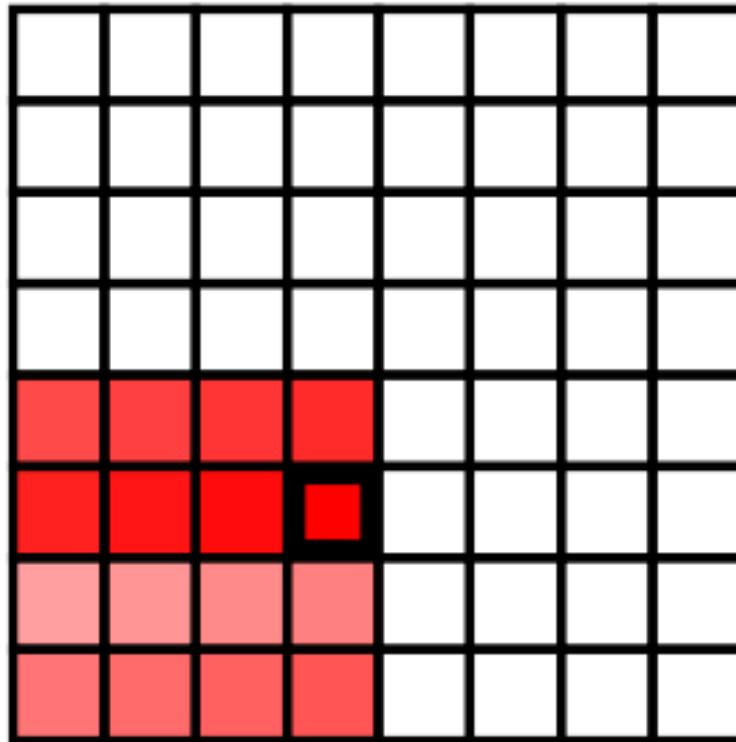


A mem:232 cache hit:172



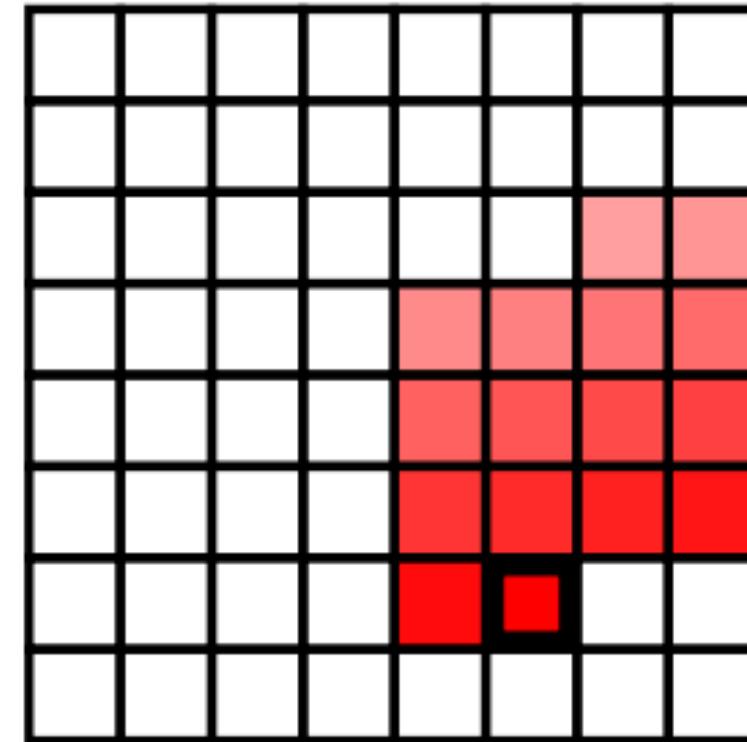
mem:696

B mem:232 cache hit:200

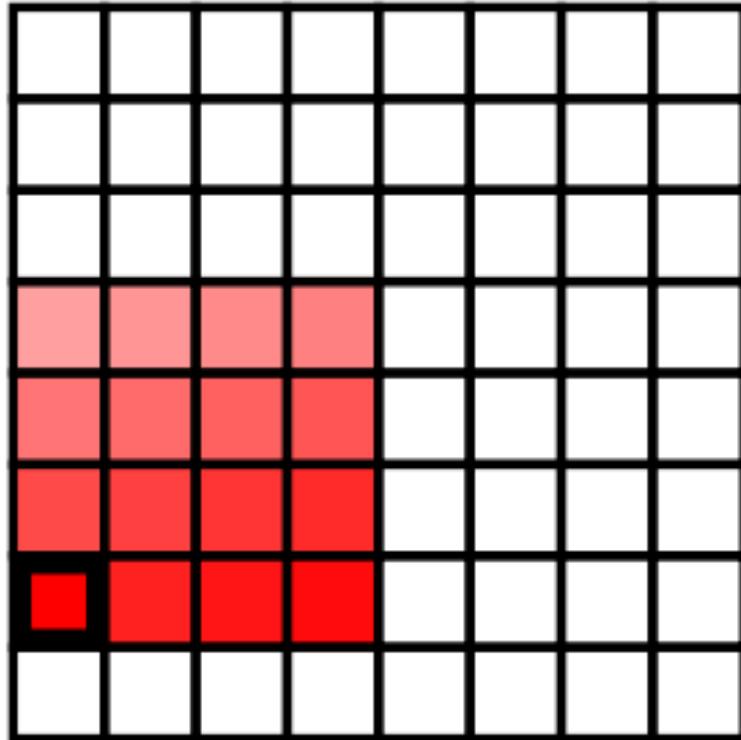


cache hits:546 ≈78%

C mem:232 cache hit:174

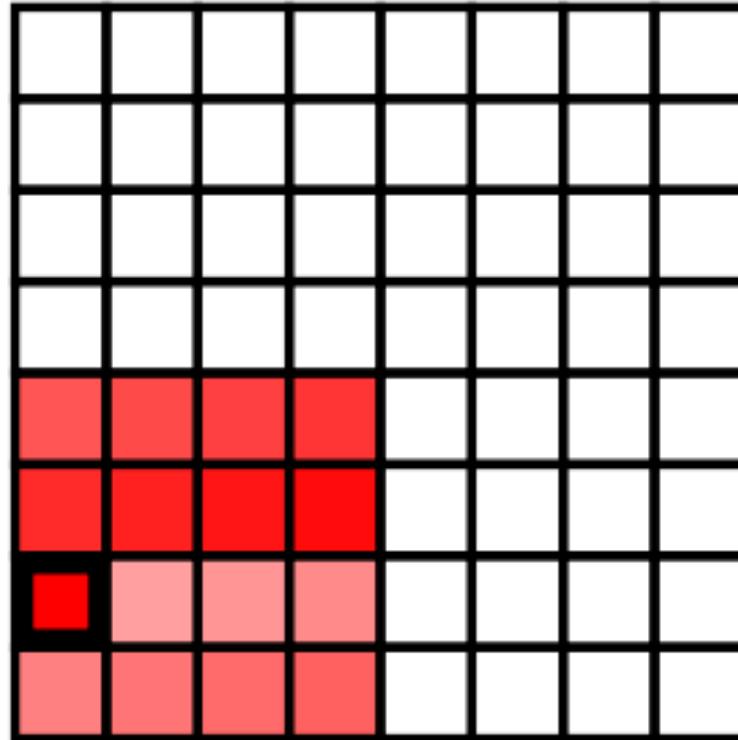


A mem:233 cache hit:173



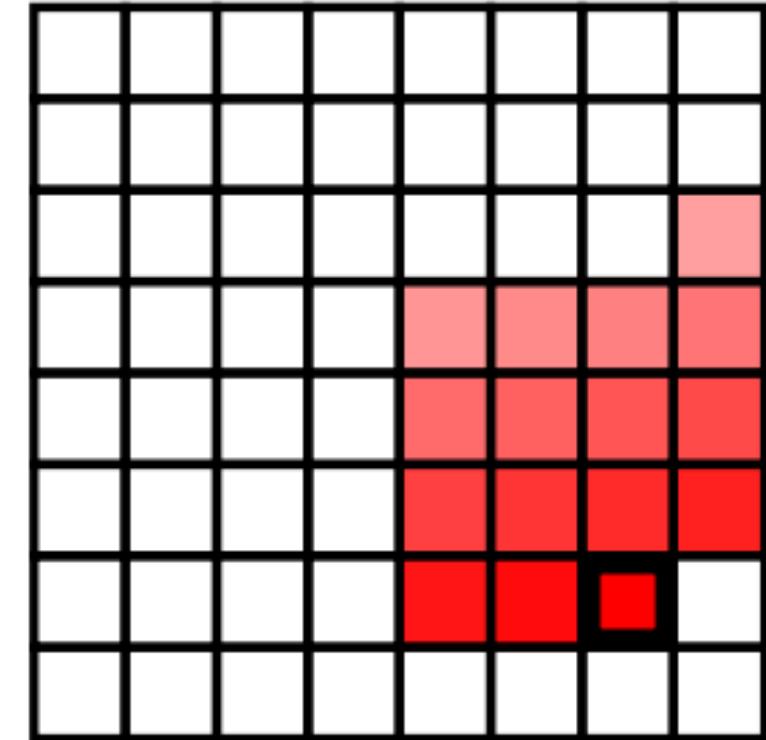
mem:699

B mem:233 cache hit:201

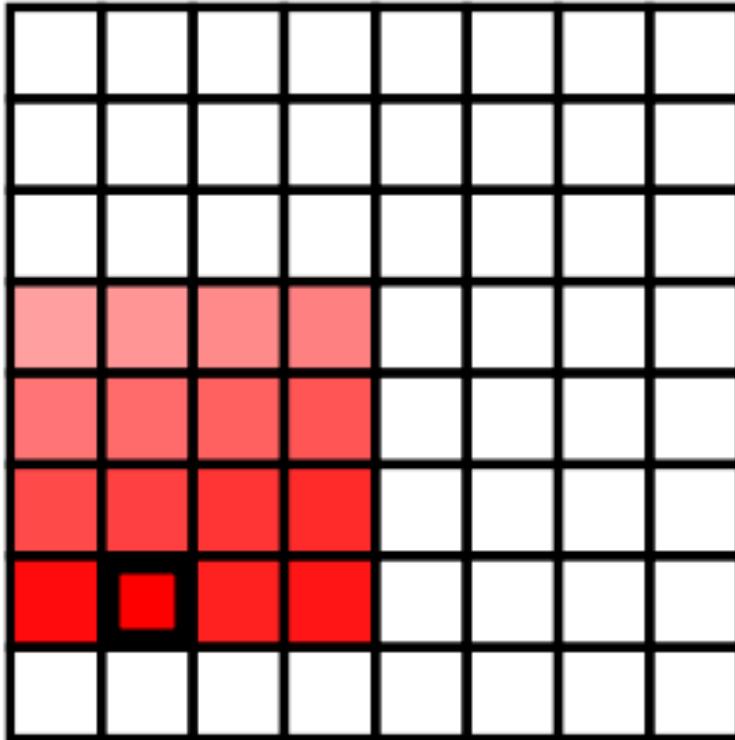


cache hits:548 ≈78%

C mem:233 cache hit:174

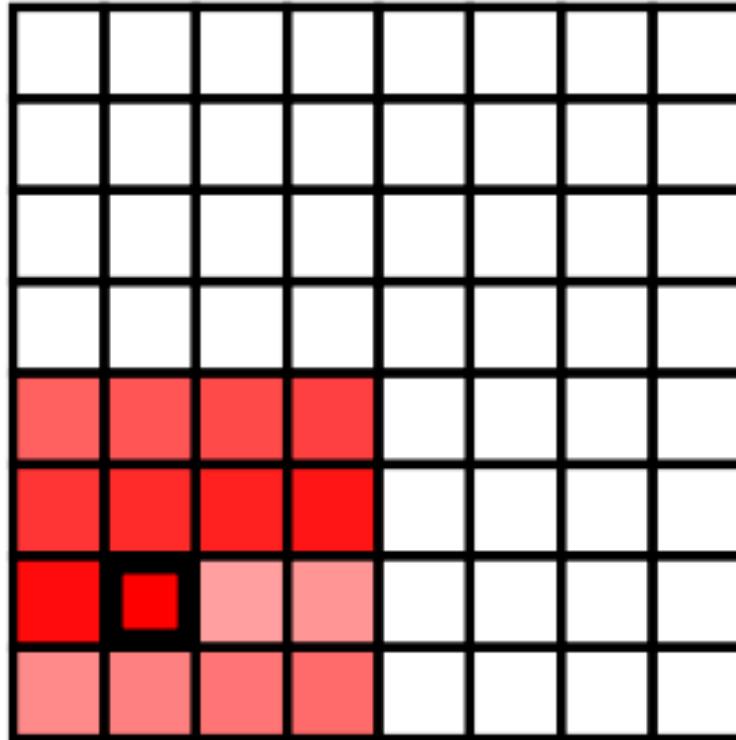


A mem:234 cache hit:174



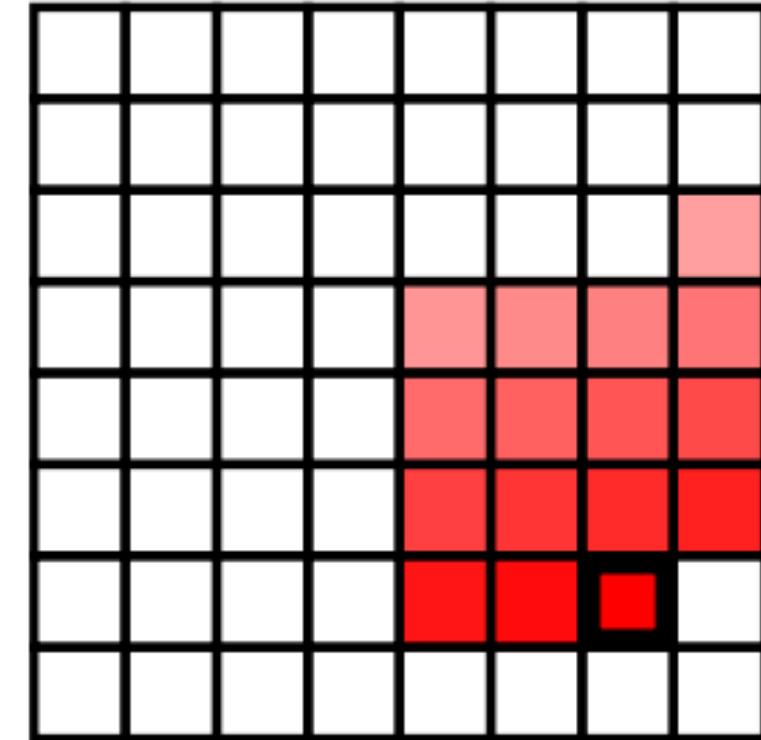
mem:702

B mem:234 cache hit:202

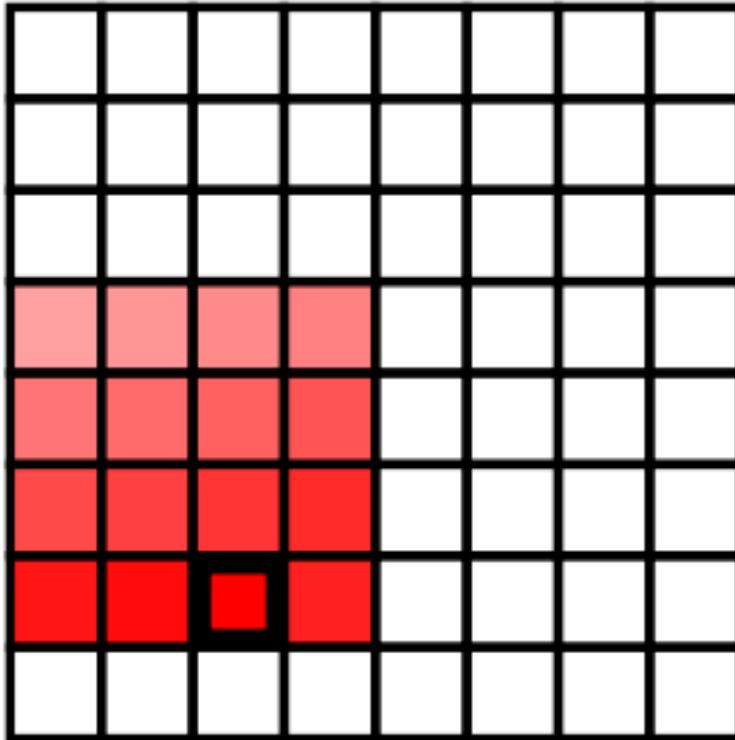


cache hits:551 ≈78%

C mem:234 cache hit:175

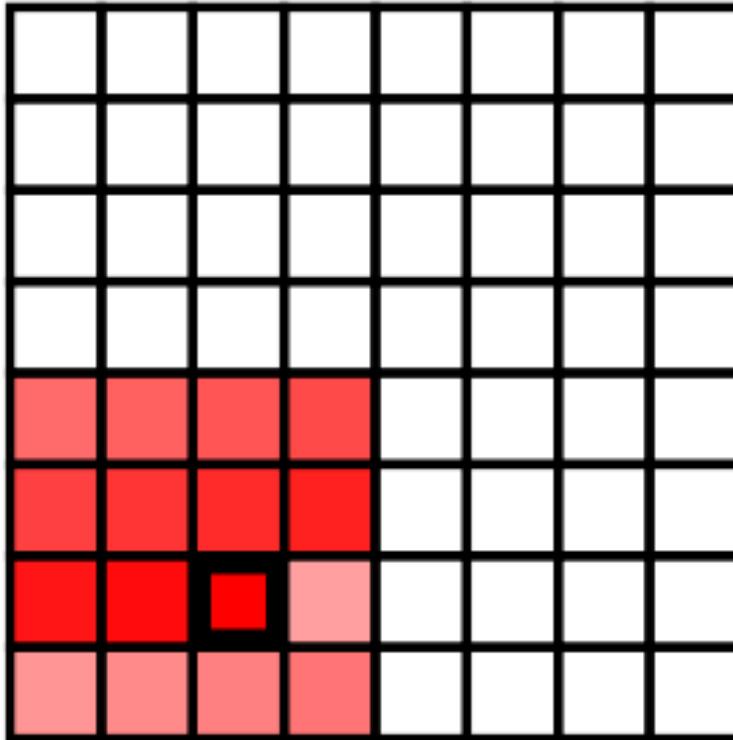


A mem:235 cache hit:175



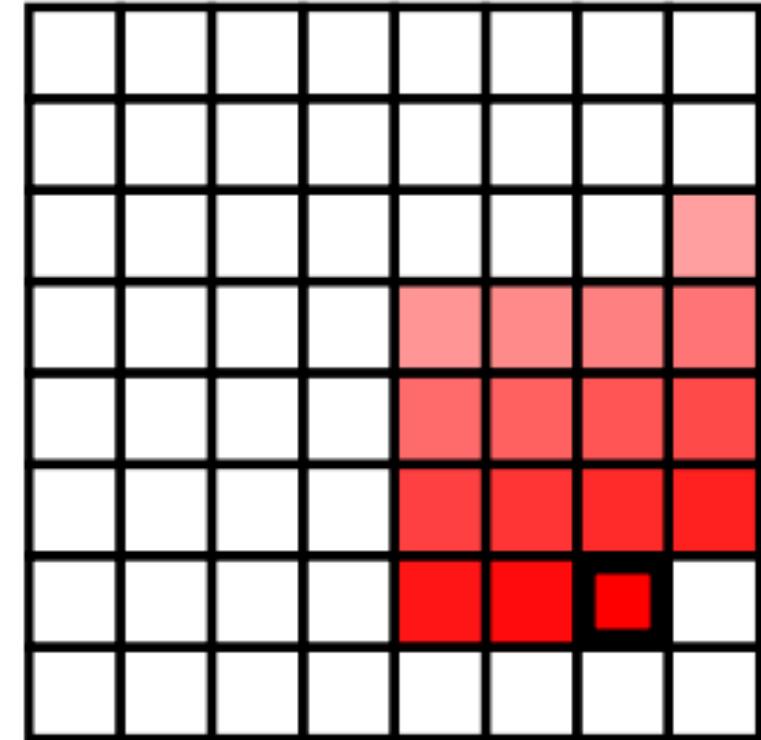
mem:705

B mem:235 cache hit:203

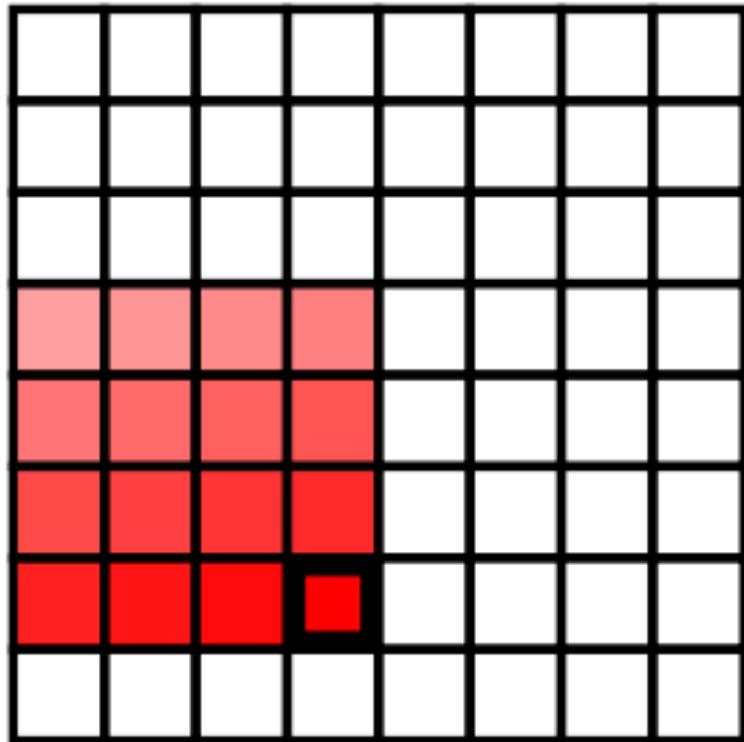


cache hits:554 ≈78%

C mem:235 cache hit:176

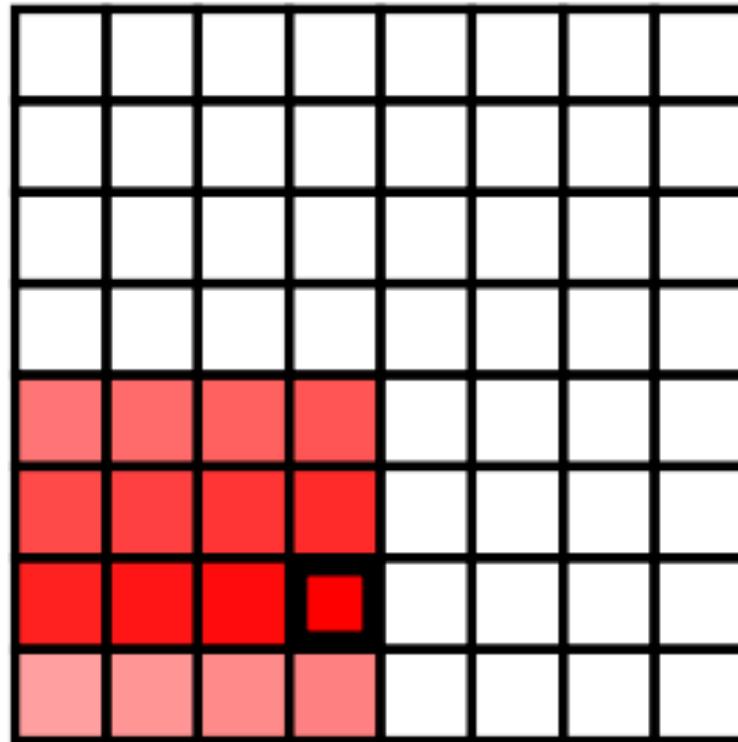


A mem:236 cache hit:176



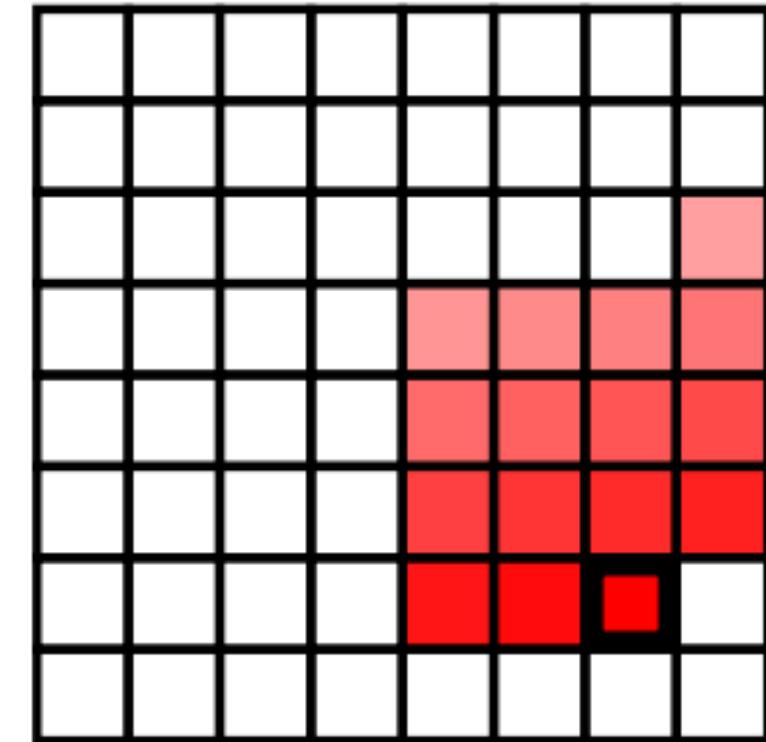
mem:708

B mem:236 cache hit:204

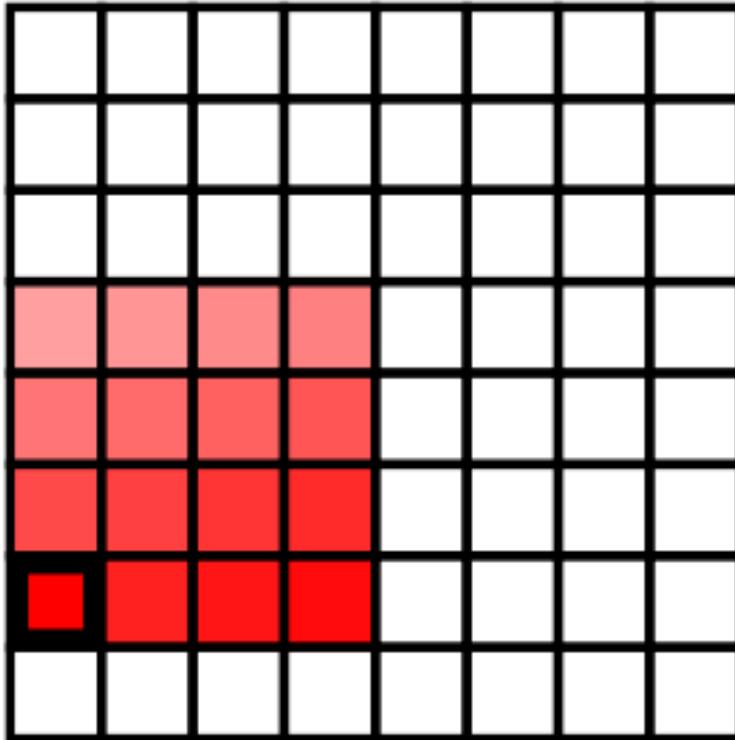


cache hits:557 ≈78%

C mem:236 cache hit:177

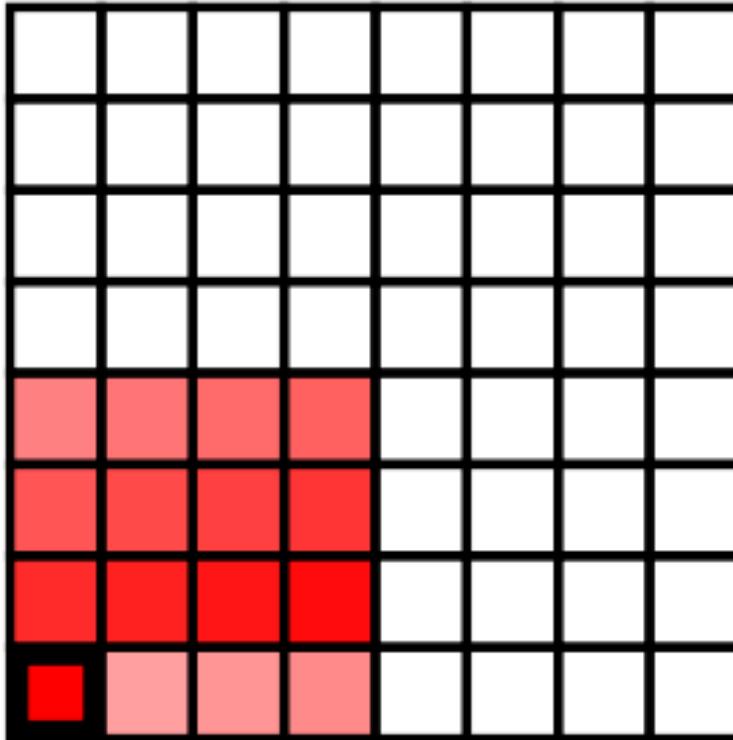


A mem:237 cache hit:177



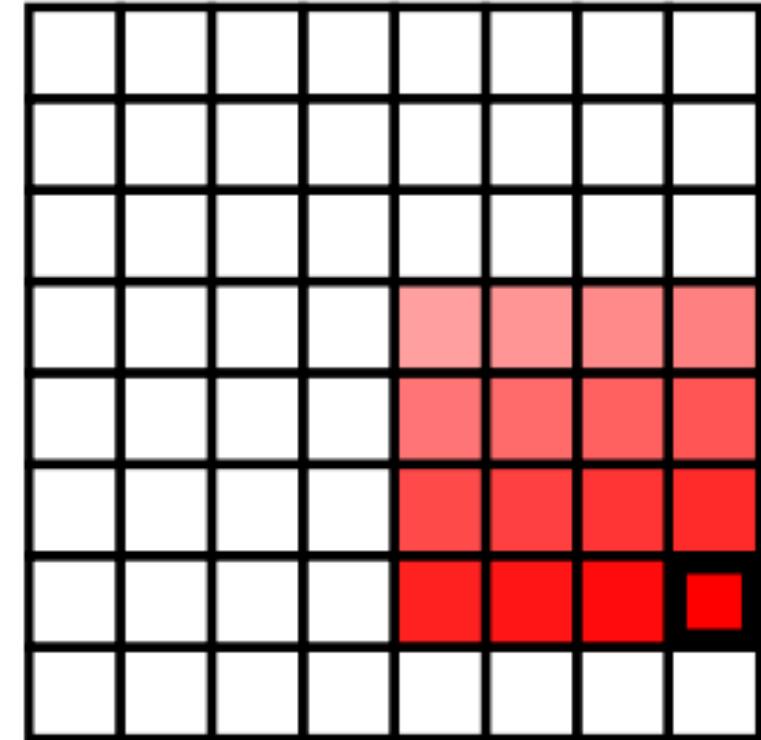
mem:711

B mem:237 cache hit:205

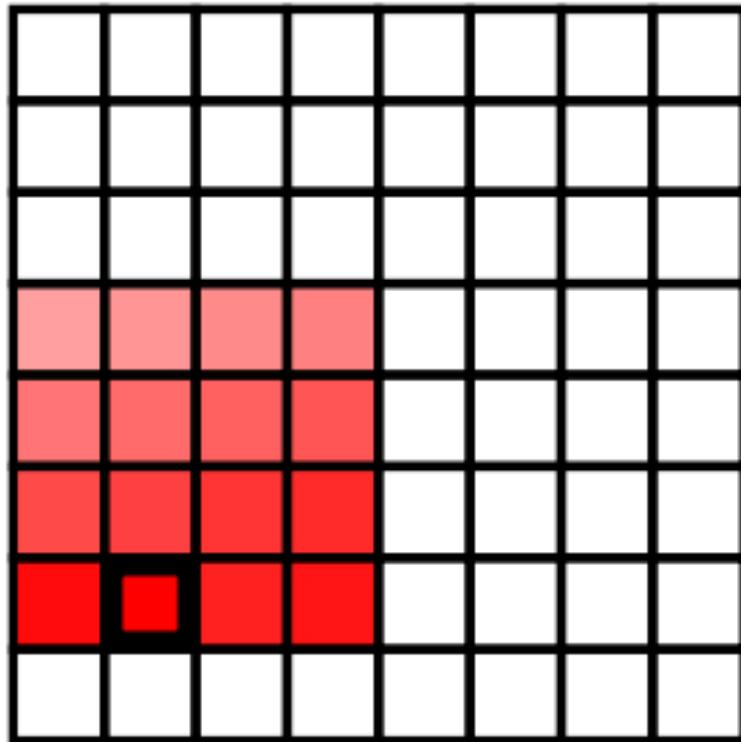


cache hits:559 ≈78%

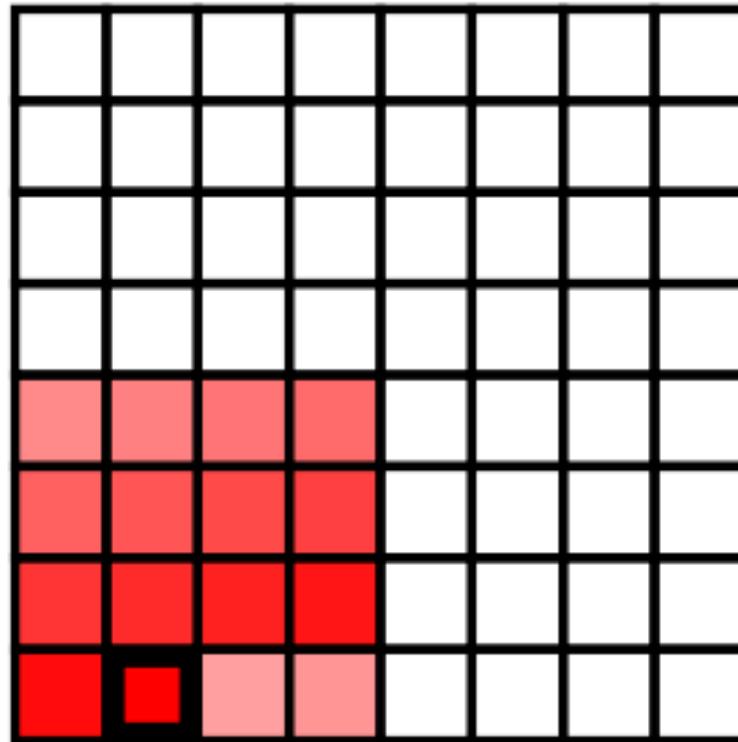
C mem:237 cache hit:177



A mem:238 cache hit:178



B mem:238 cache hit:206

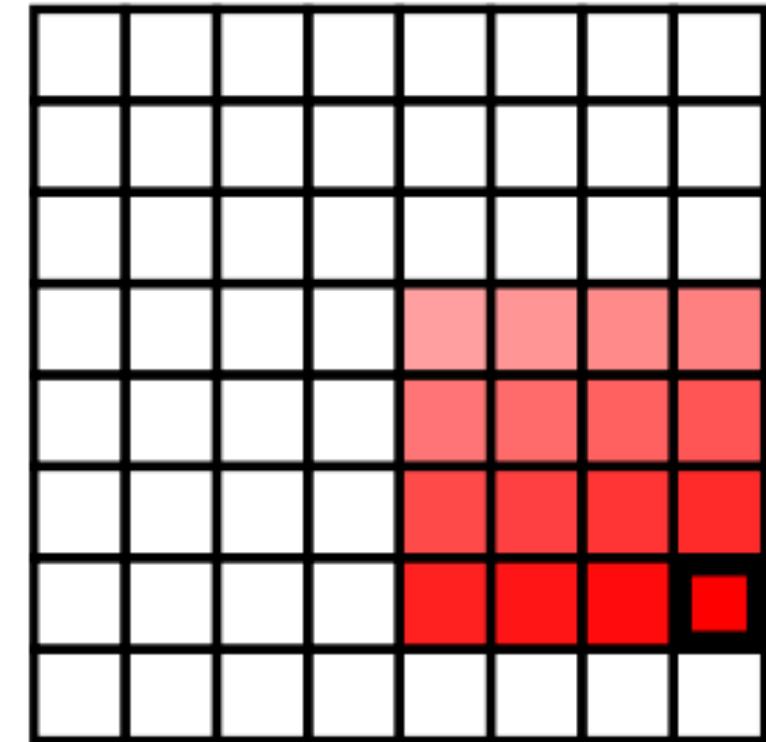


mem:714

cache hits:562 ≈78%

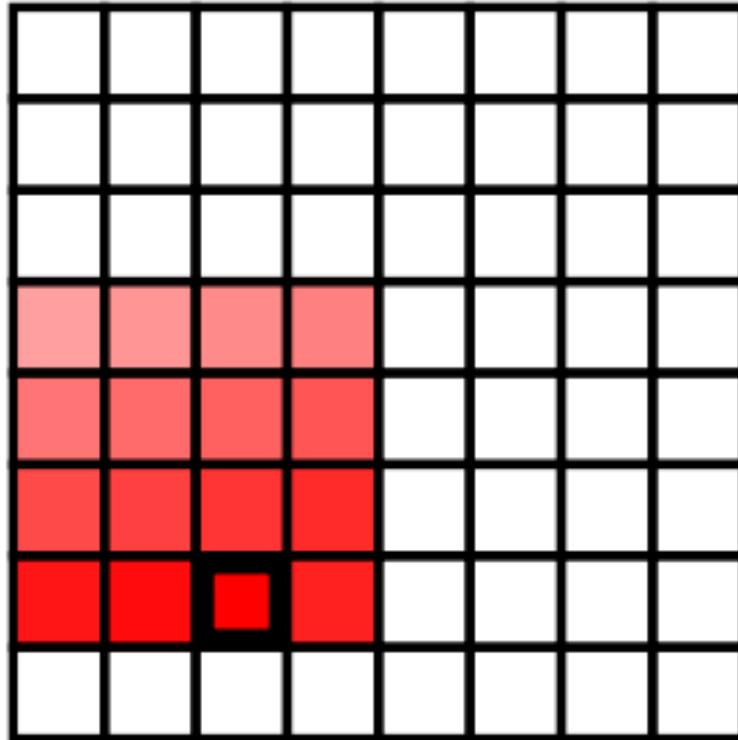
×

C mem:238 cache hit:178



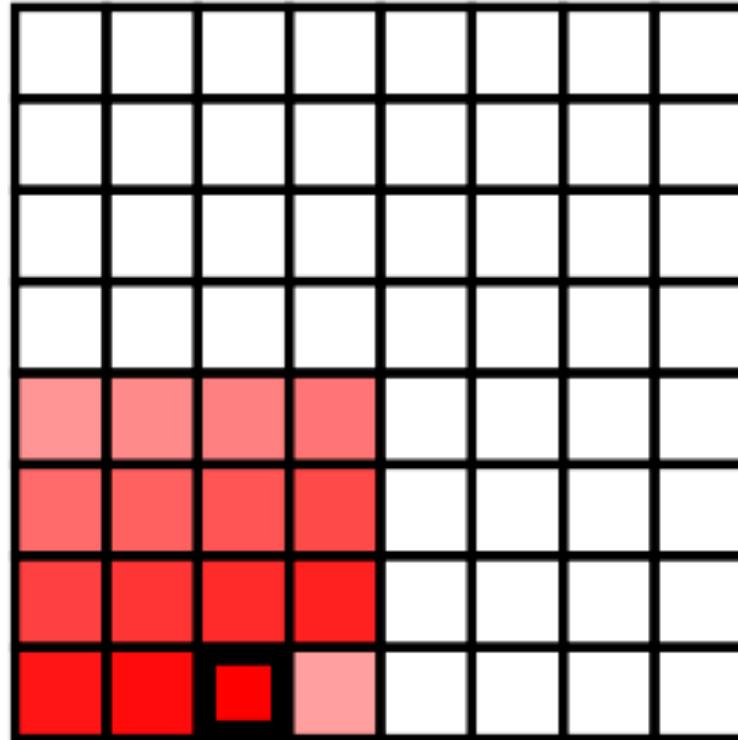
=

A mem:239 cache hit:179



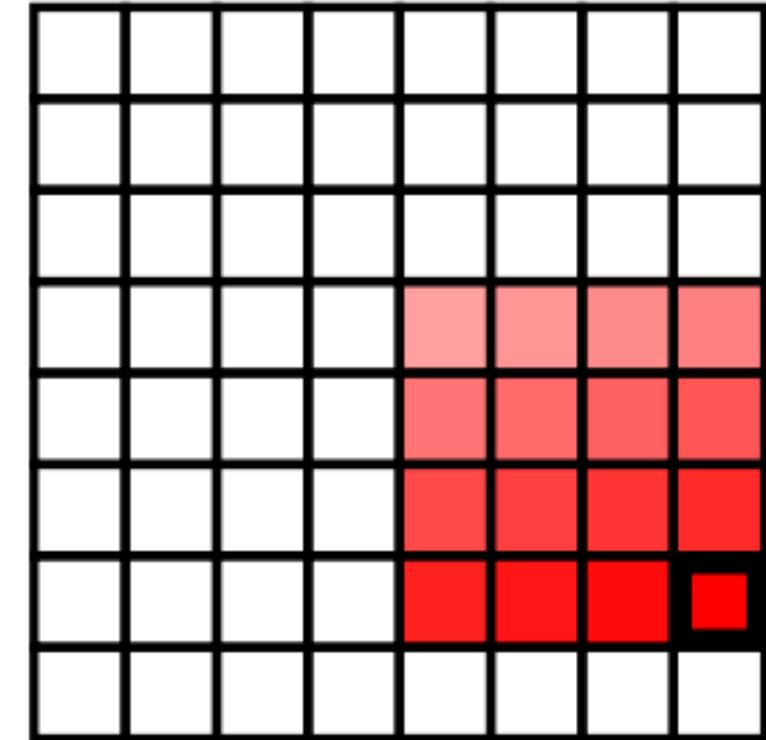
mem:717

B mem:239 cache hit:207

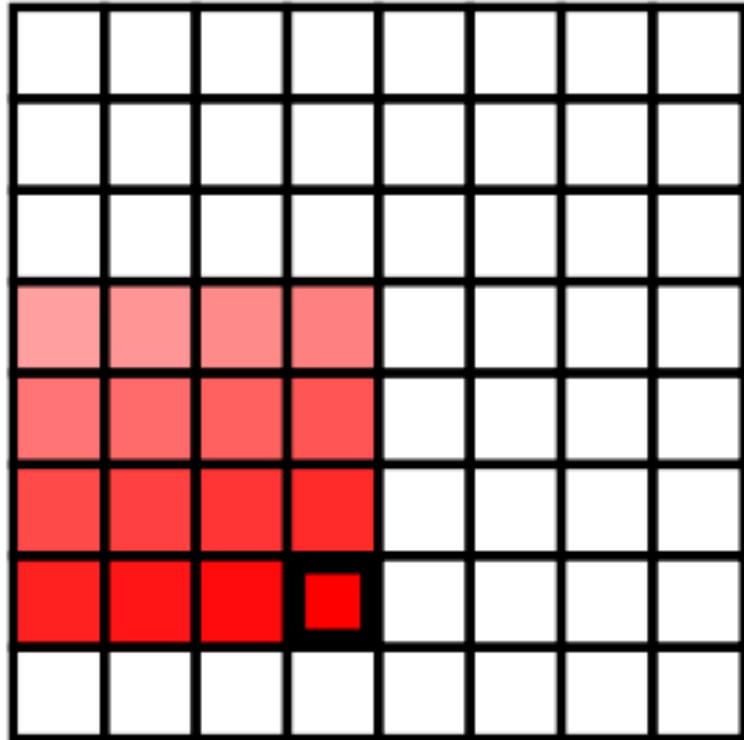


cache hits:565 ≈78%

C mem:239 cache hit:179

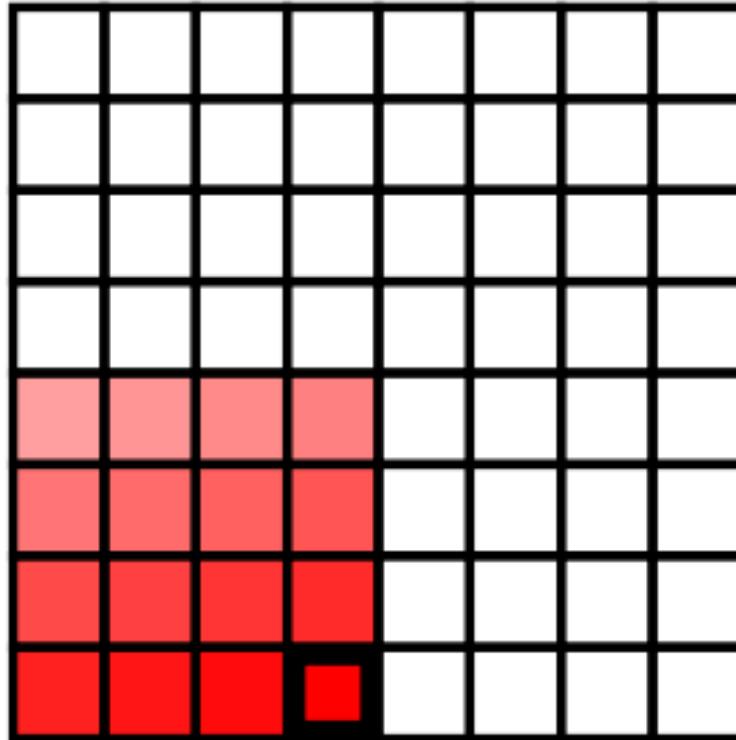


A mem:240 cache hit:180



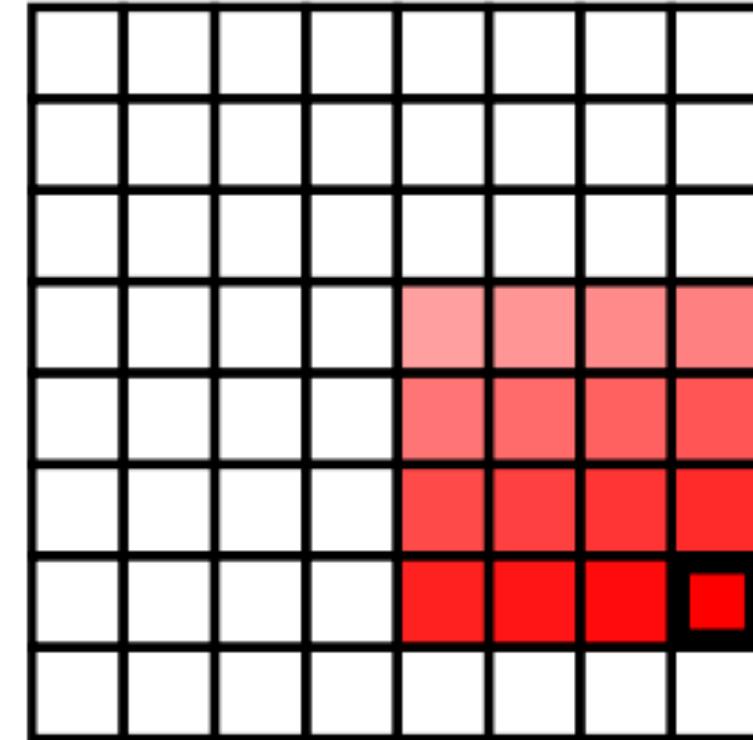
mem:720

B mem:240 cache hit:208

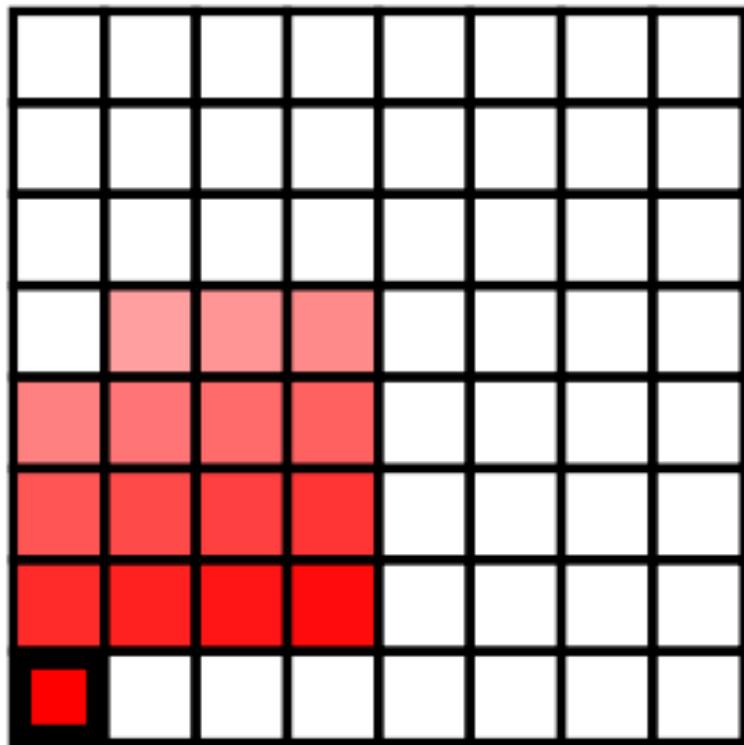


cache hits:568 ≈78%

C mem:240 cache hit:180

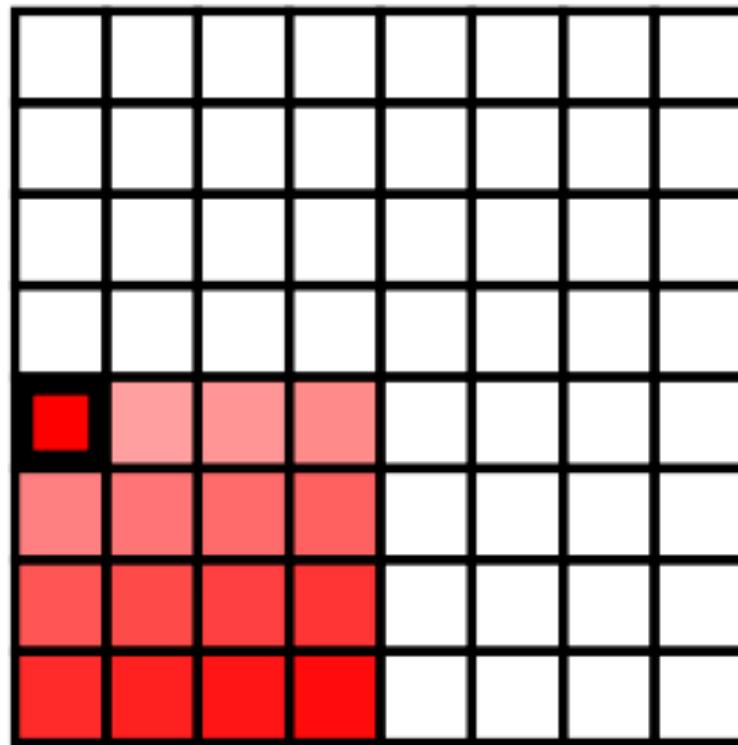


A mem:241 cache hit:180



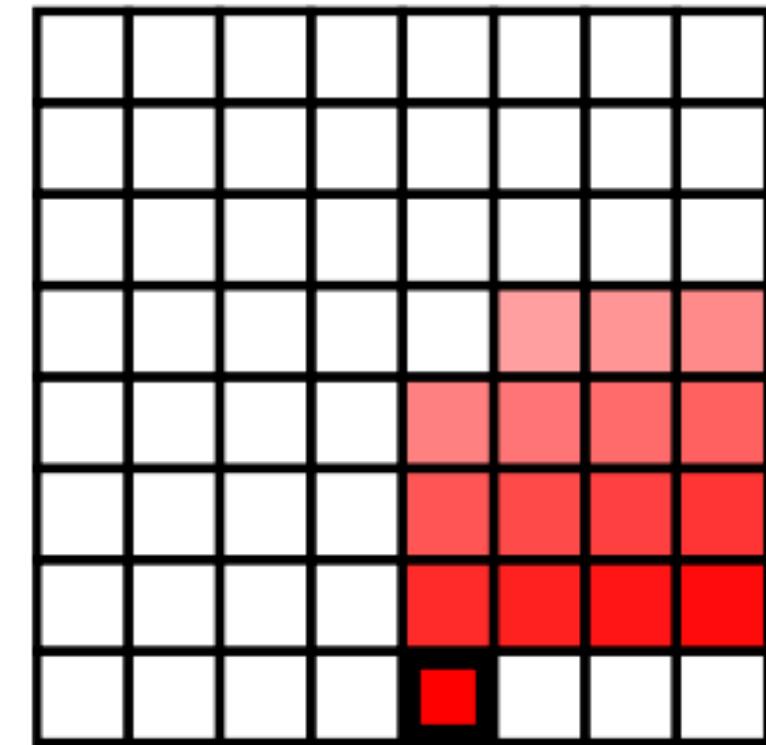
mem:723

B mem:241 cache hit:209

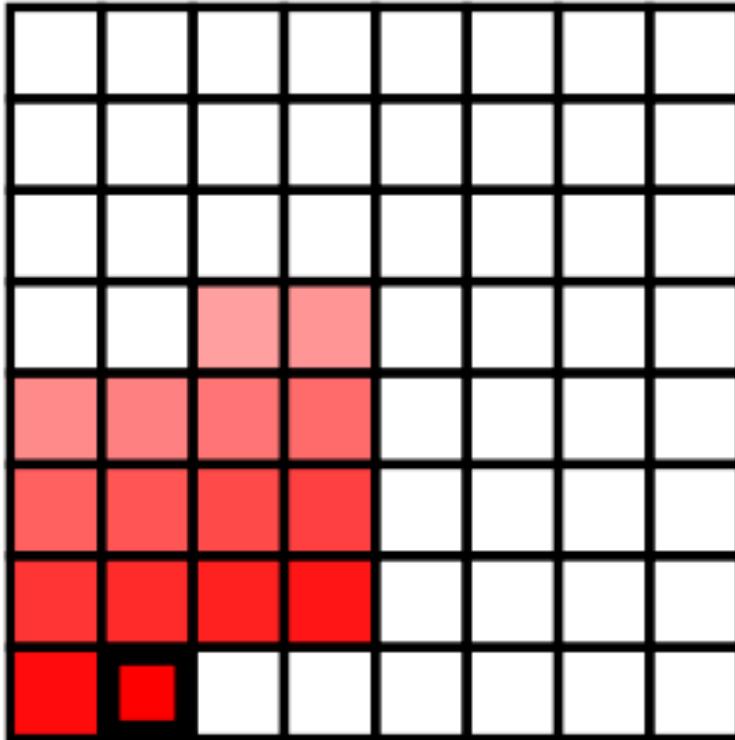


cache hits:569 ≈78%

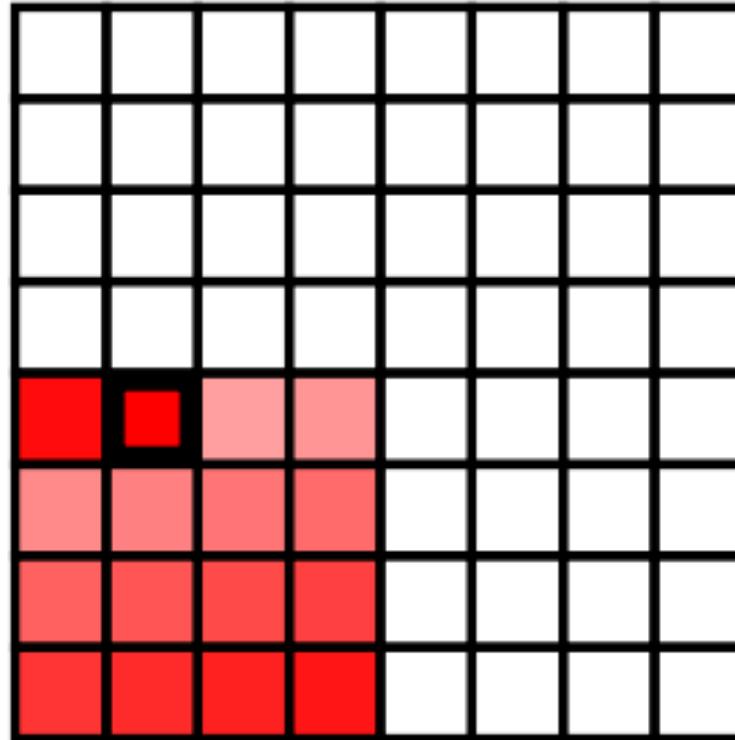
C mem:241 cache hit:180



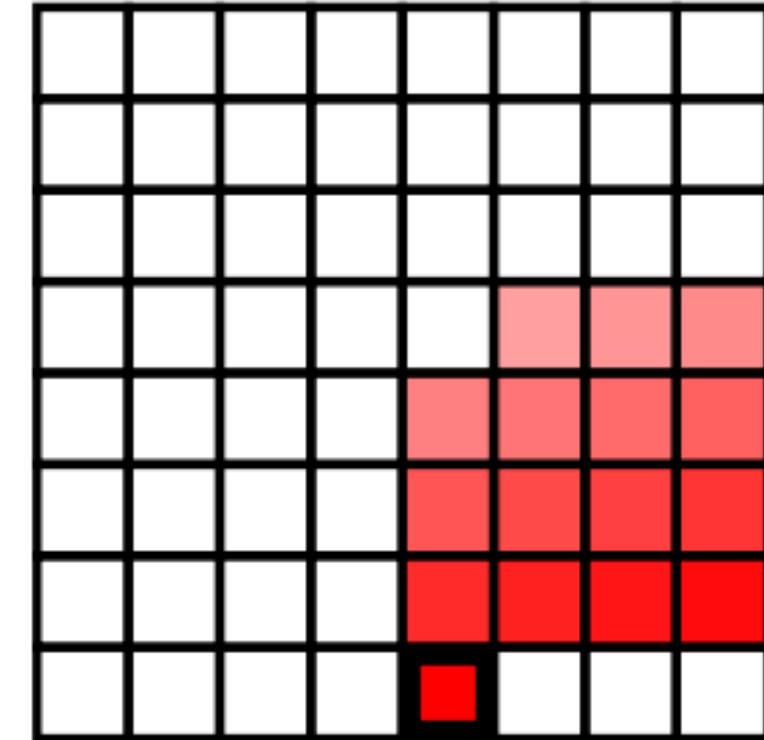
A mem:242 cache hit:180



B mem:242 cache hit:210



C mem:242 cache hit:181



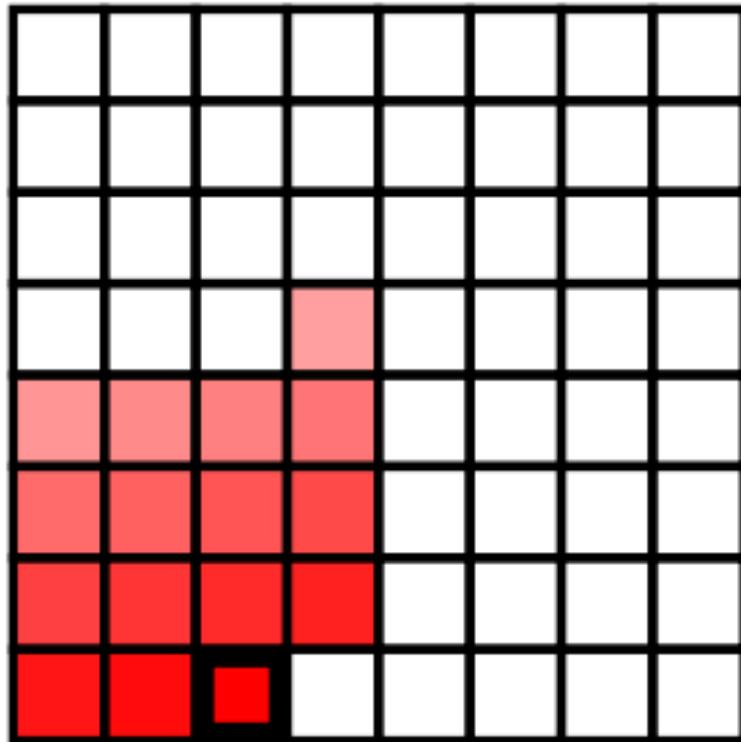
×

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mem:726

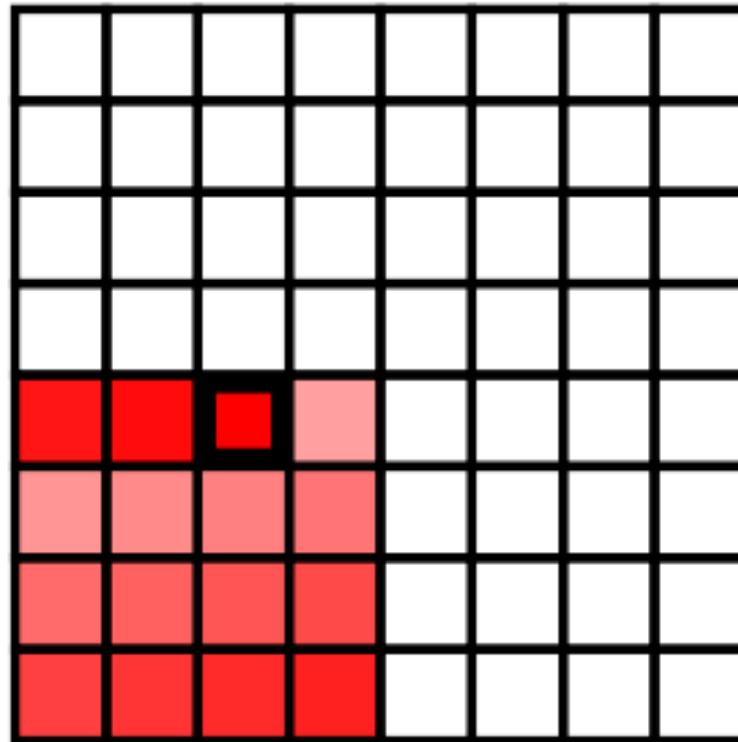
cache hits:571 $\approx 78\%$

A mem:243 cache hit:180



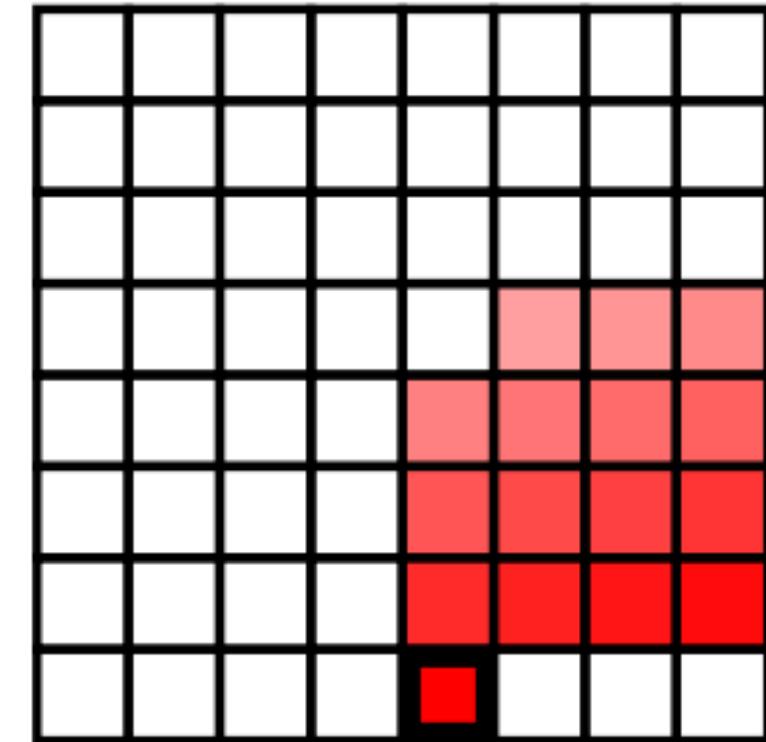
mem:729

B mem:243 cache hit:211

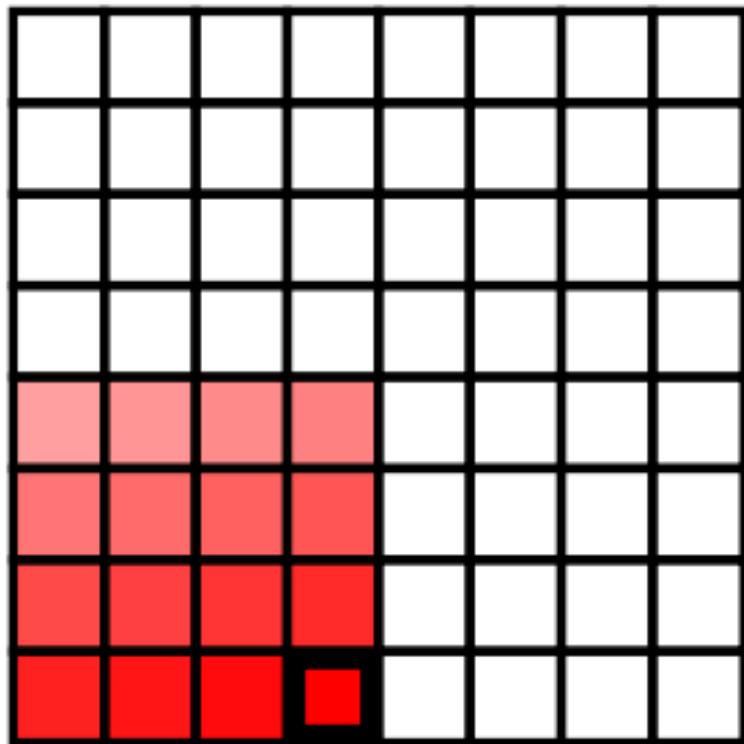


cache hits:573 ≈78%

C mem:243 cache hit:182

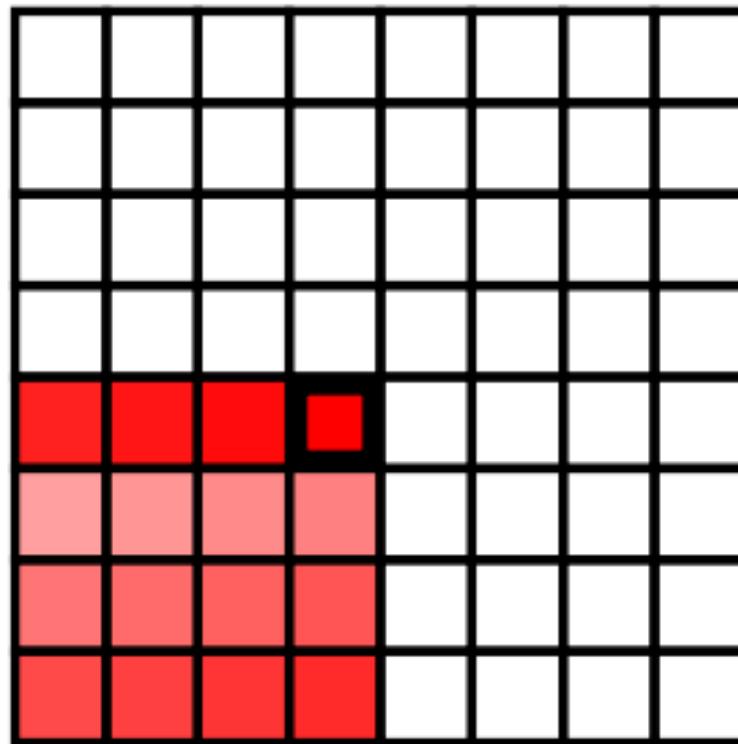


A mem:244 cache hit:180



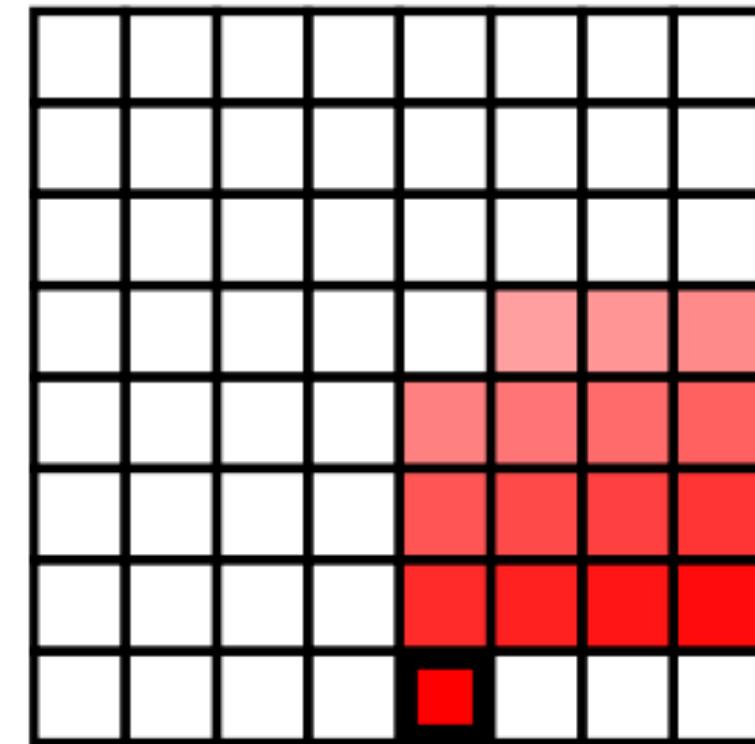
mem:732

B mem:244 cache hit:212

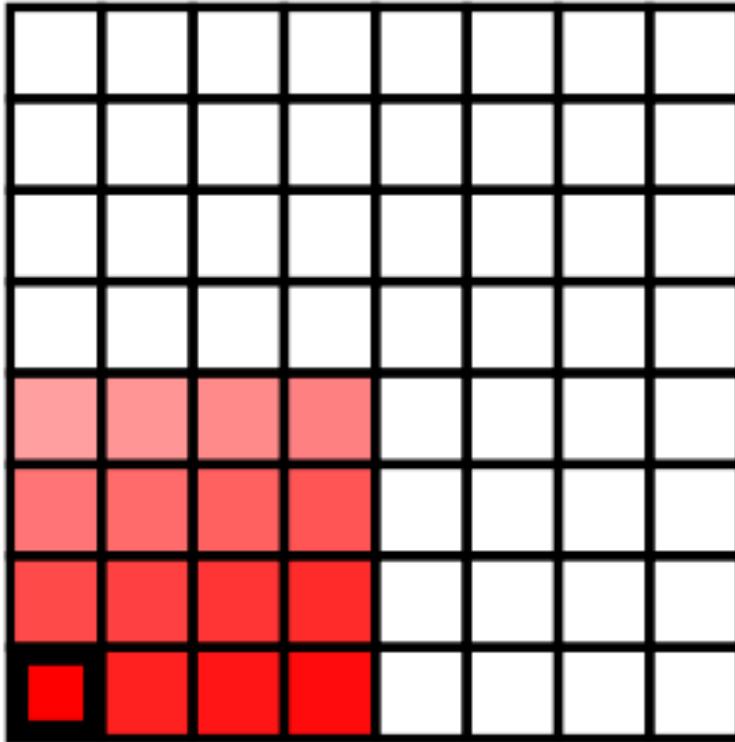


cache hits:575 ≈78%

C mem:244 cache hit:183

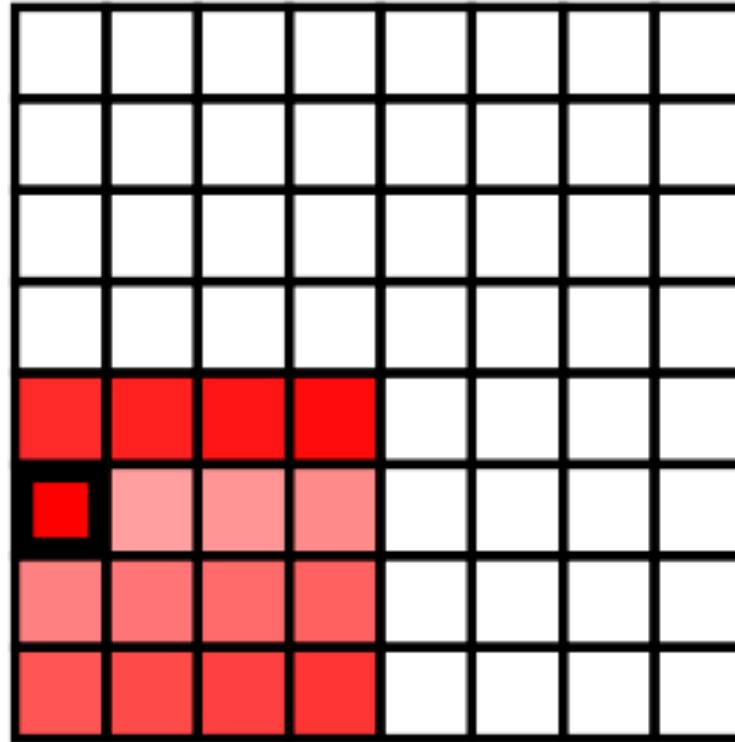


A mem:245 cache hit:181



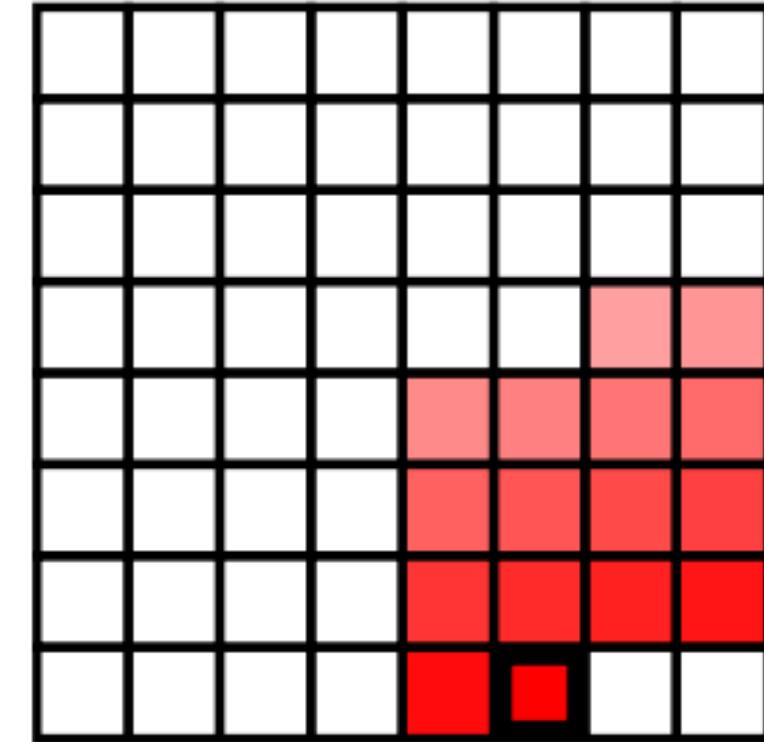
mem:735

B mem:245 cache hit:213

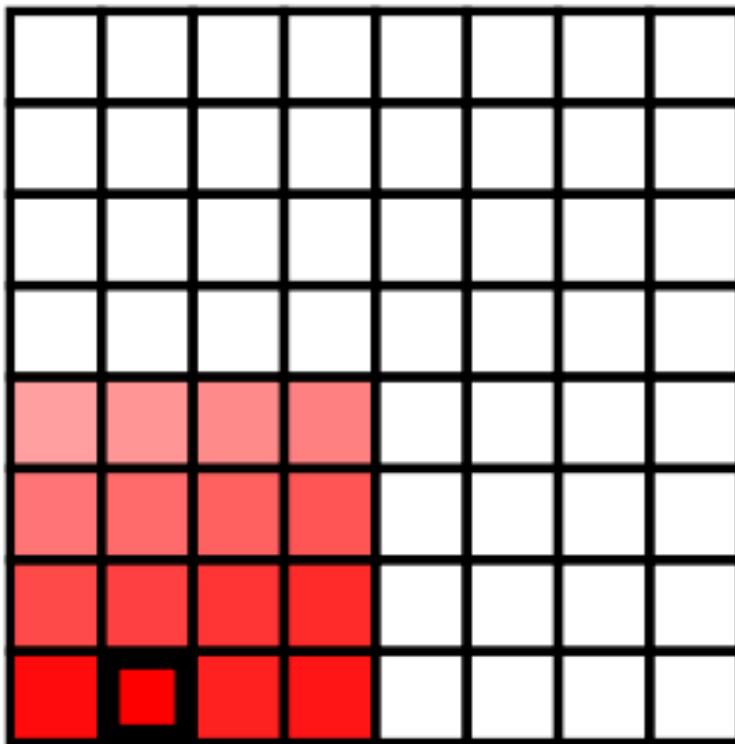


cache hits:577 ≈78%

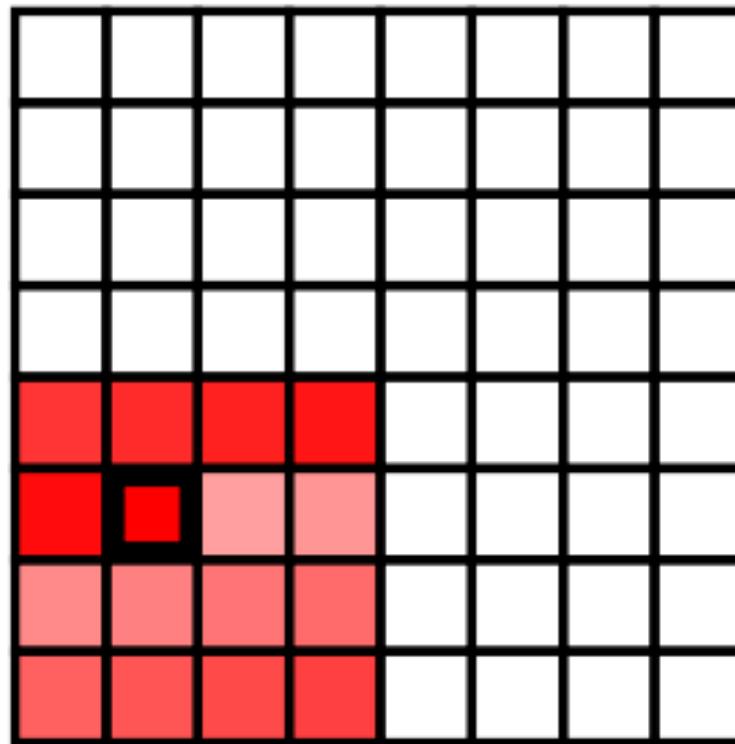
C mem:245 cache hit:183



A mem:246 cache hit:182



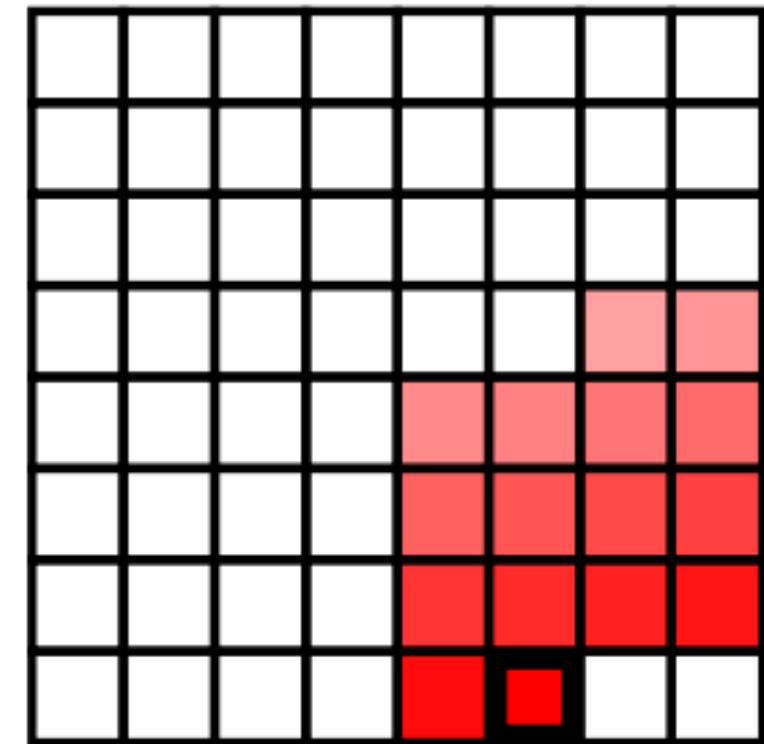
B mem:246 cache hit:214



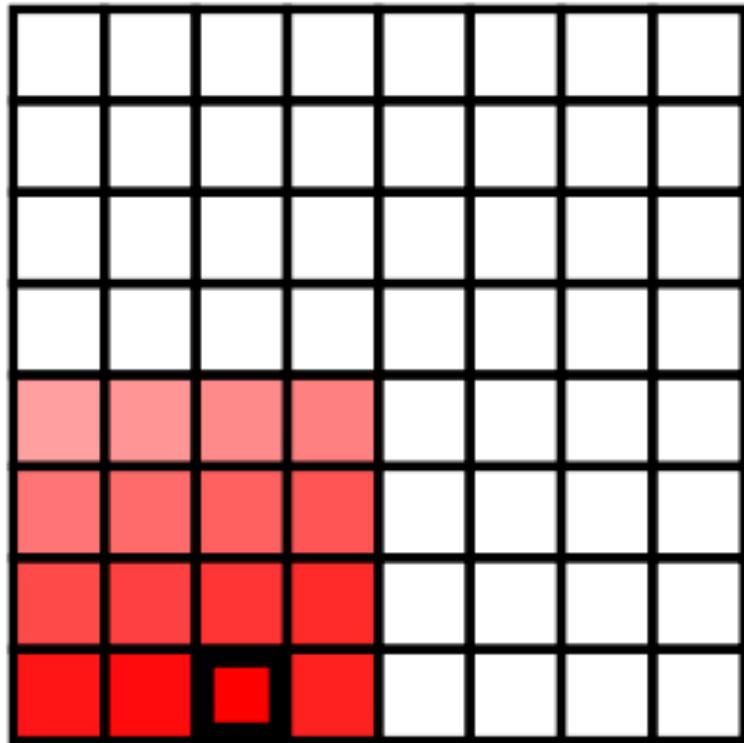
mem:738

cache hits:580 $\approx 78\%$

C mem:246 cache hit:184

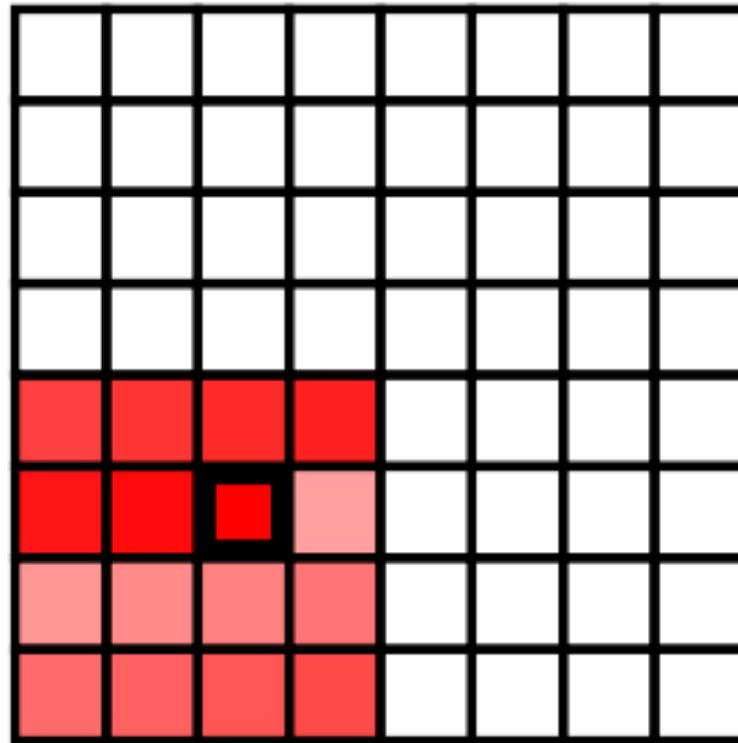


A mem:247 cache hit:183



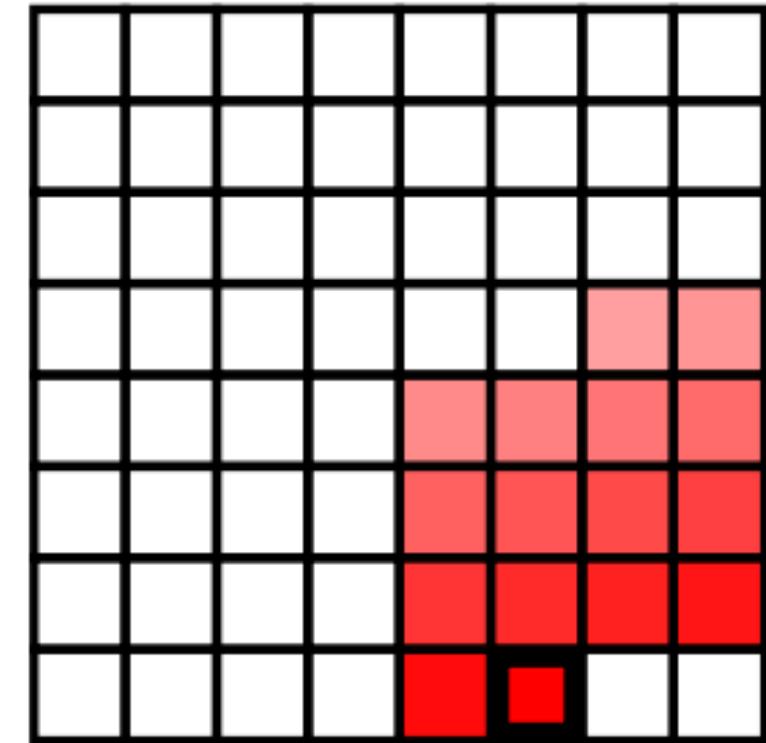
mem:741

B mem:247 cache hit:215

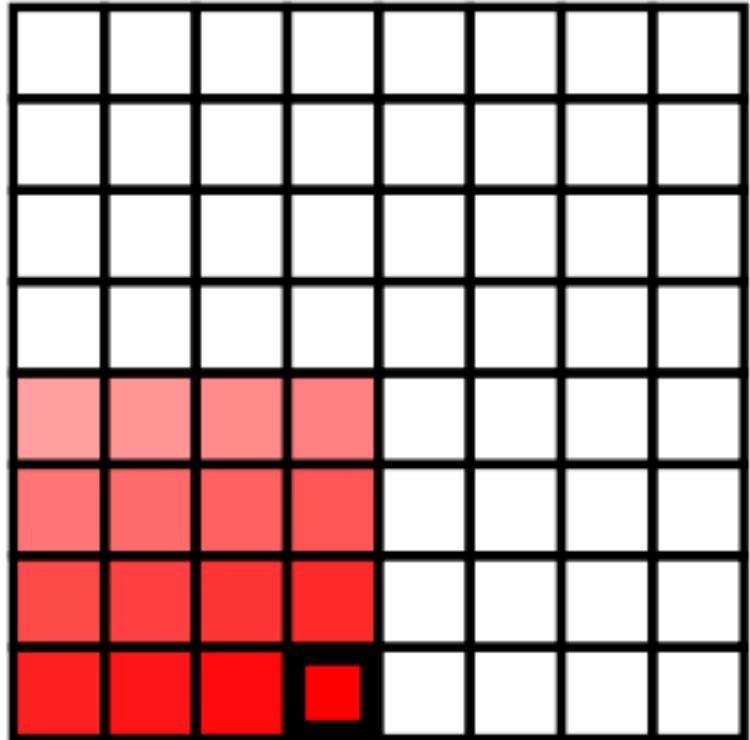


cache hits:583 ≈78%

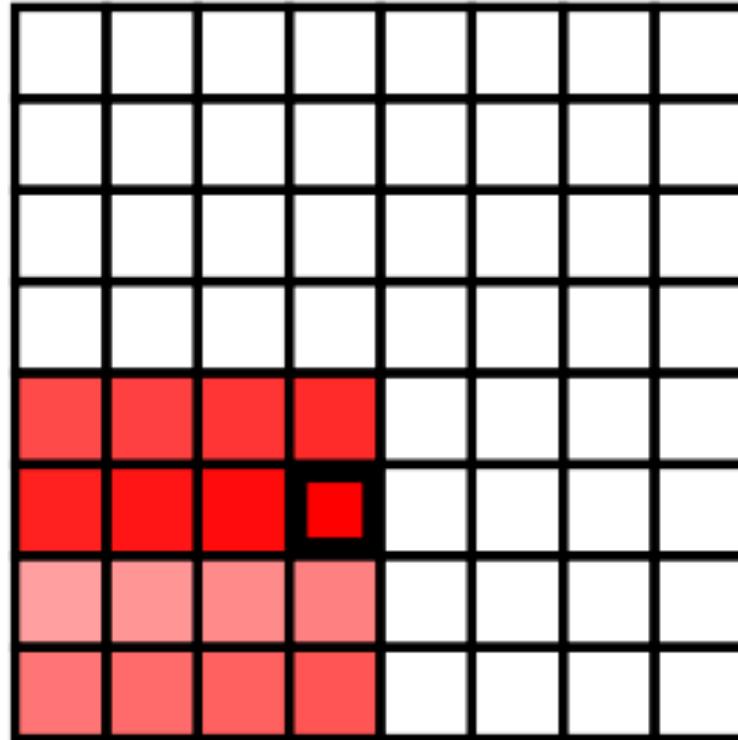
C mem:247 cache hit:185



A mem:248 cache hit:184



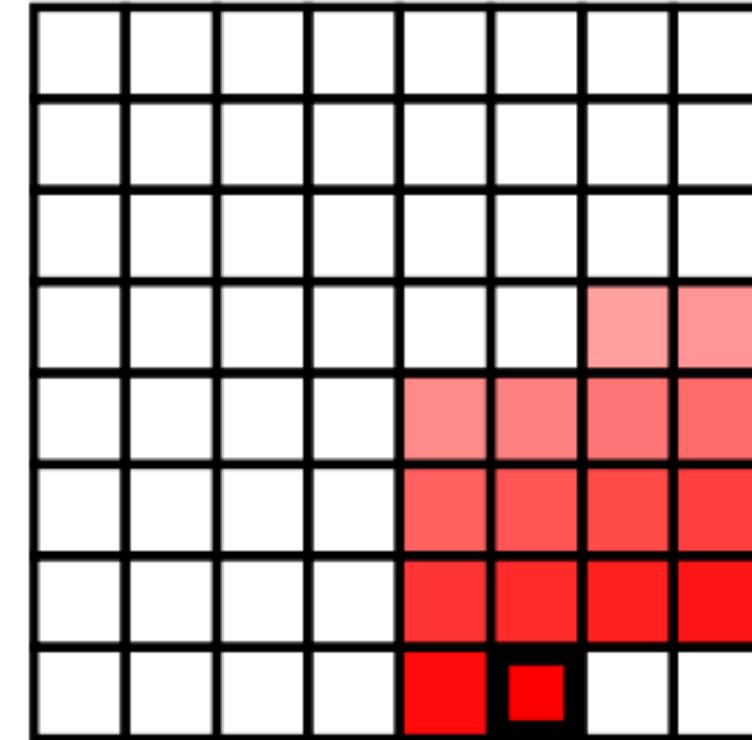
B mem:248 cache hit:216



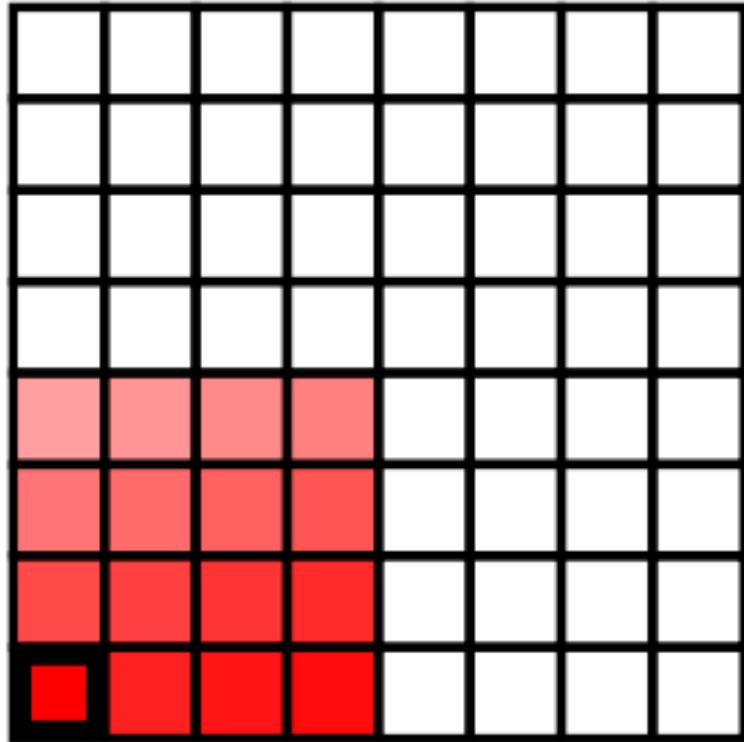
mem:744

cache hits:586 $\approx 78\%$

C mem:248 cache hit:186

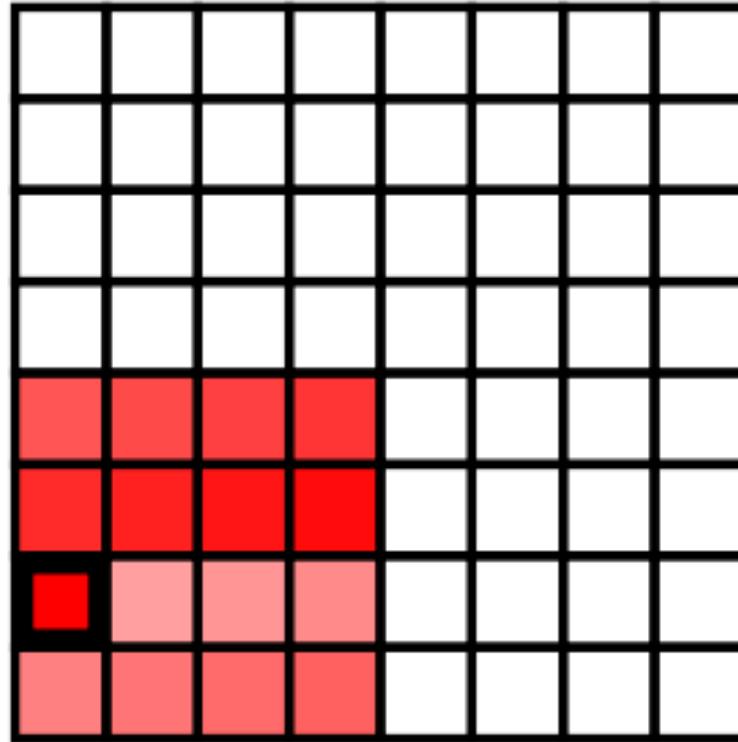


A mem:249 cache hit:185



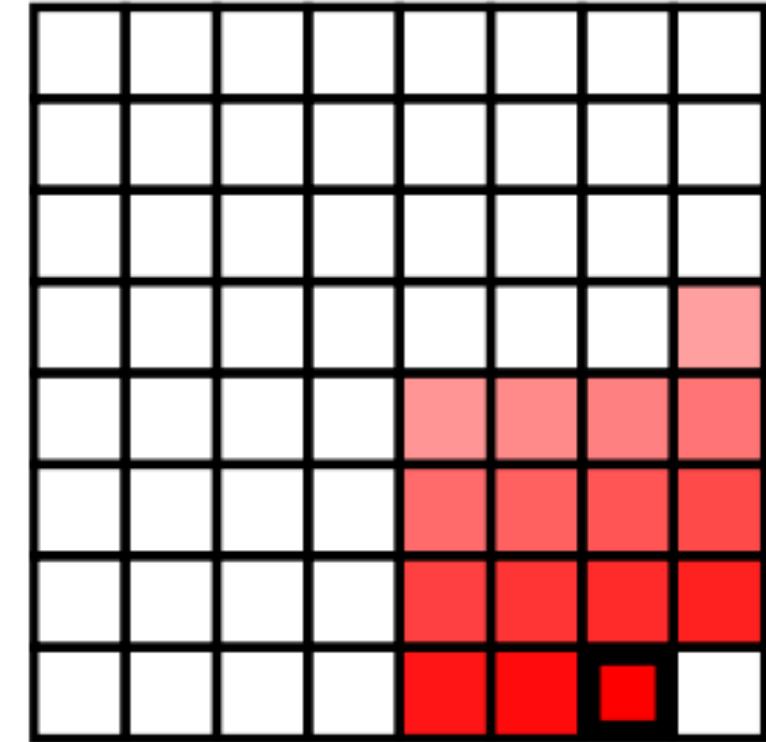
mem:747

B mem:249 cache hit:217

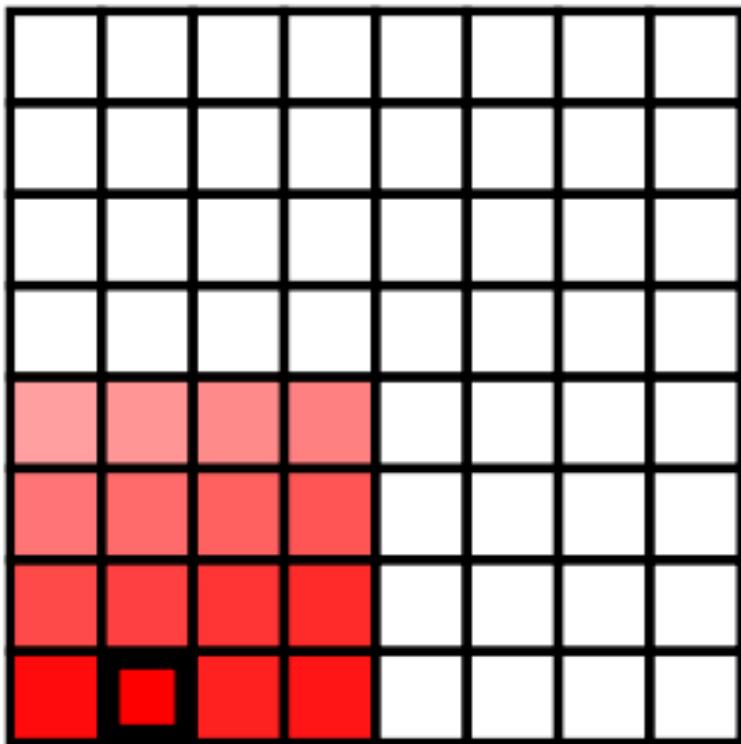


cache hits:588 ≈78%

C mem:249 cache hit:186

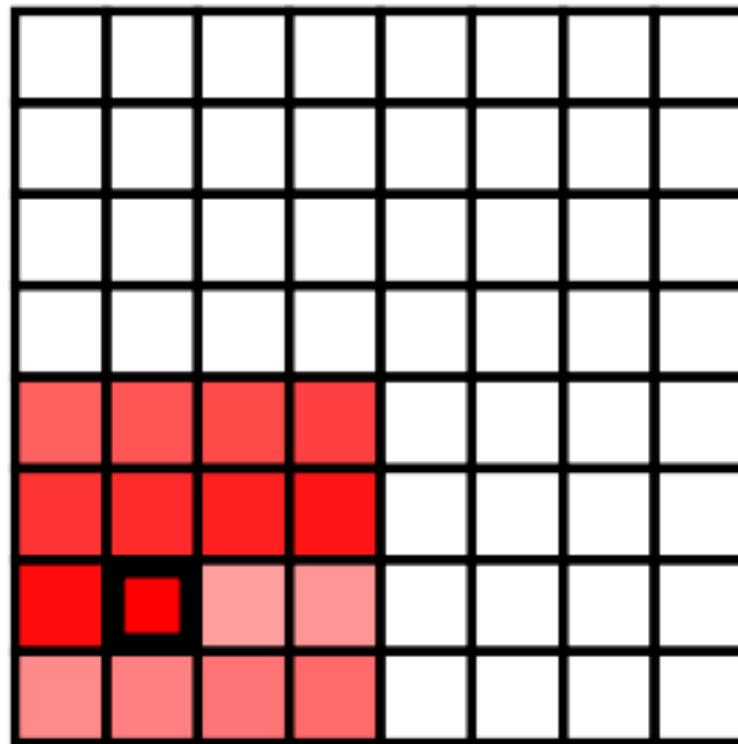


A mem:250 cache hit:186



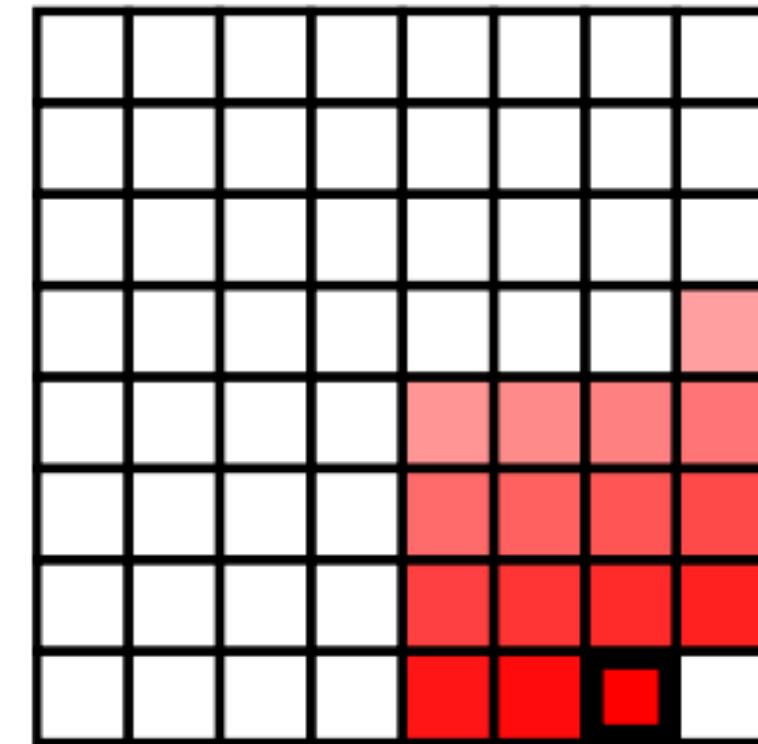
mem:750

B mem:250 cache hit:218

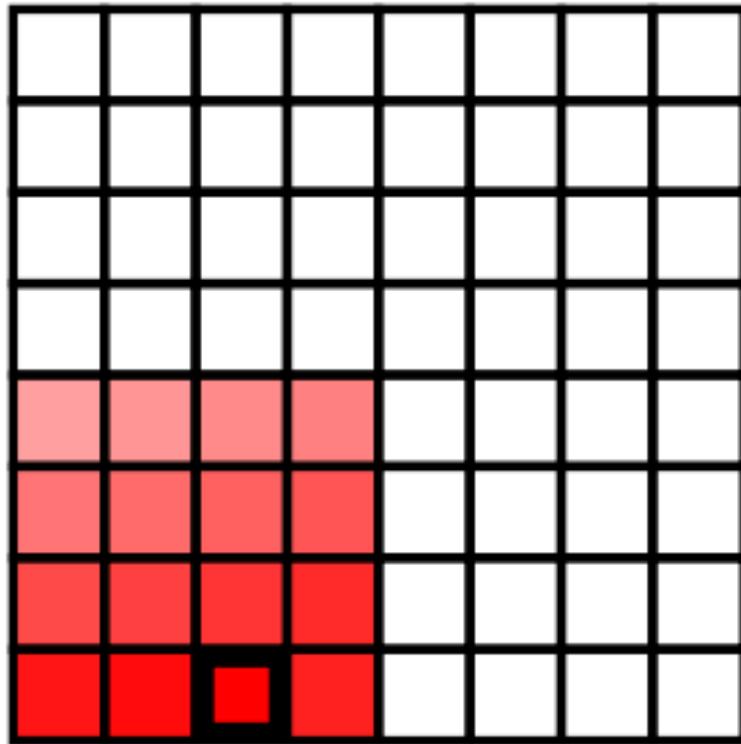


cache hits:591 ≈78%

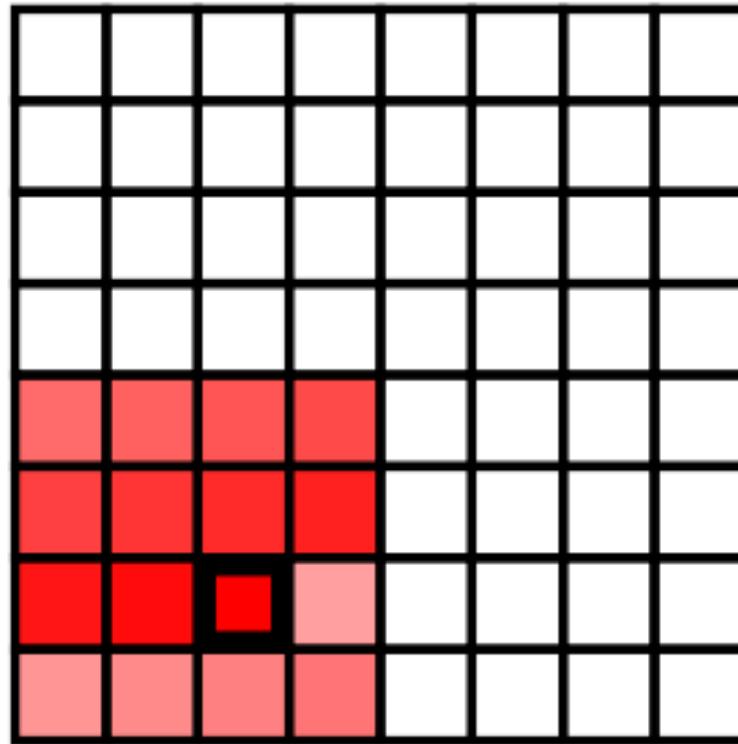
C mem:250 cache hit:187



A mem:251 cache hit:187



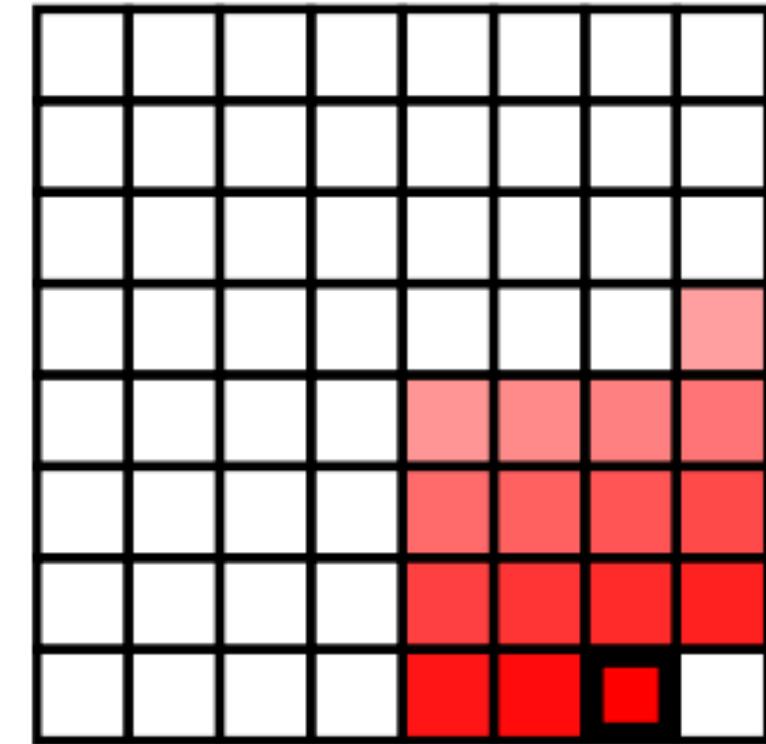
B mem:251 cache hit:219



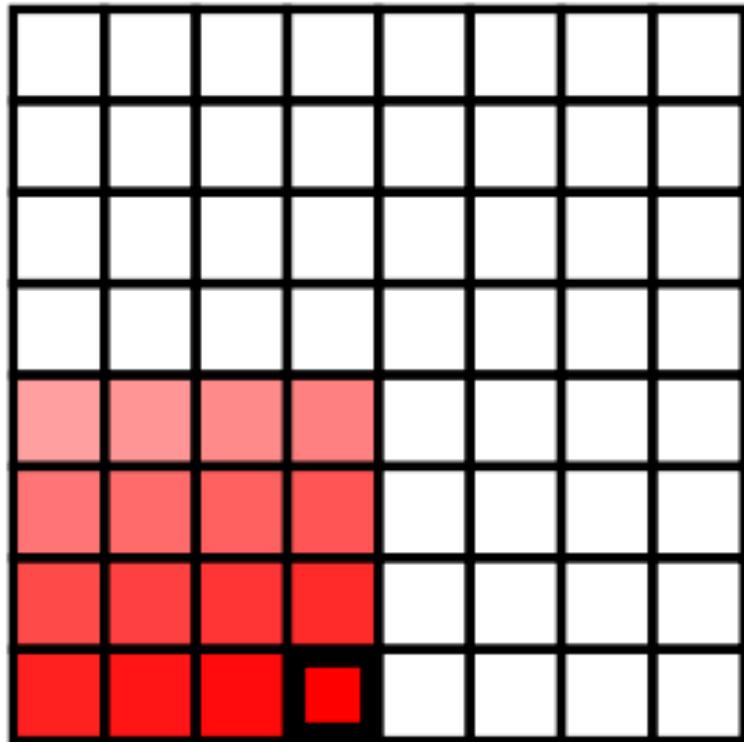
mem:753

cache hits:594 $\approx 78\%$

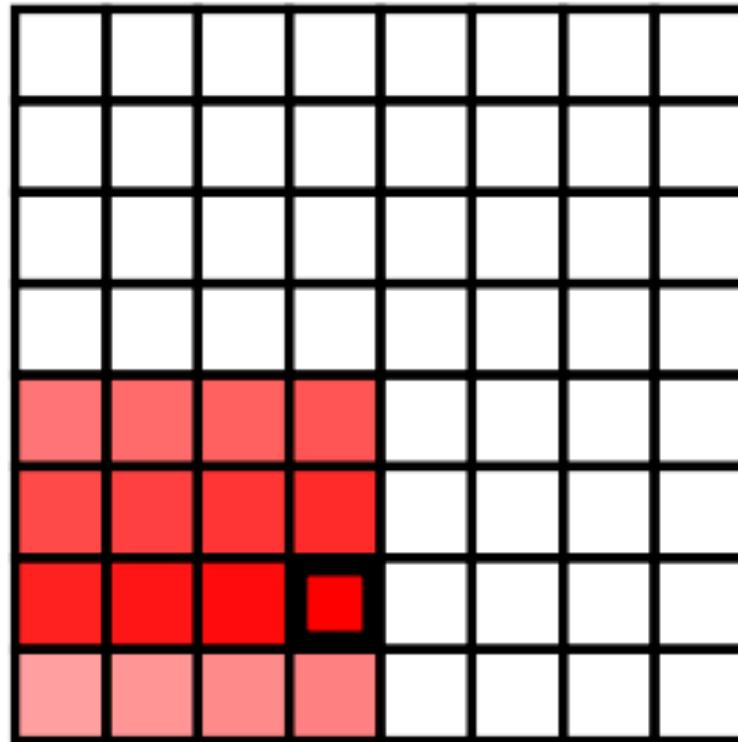
C mem:251 cache hit:188



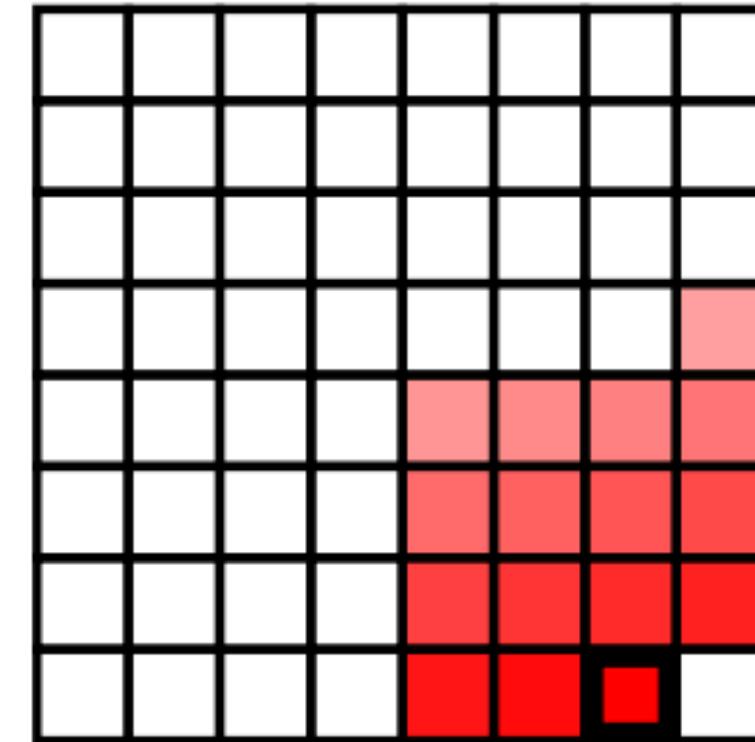
A mem:252 cache hit:188



B mem:252 cache hit:220



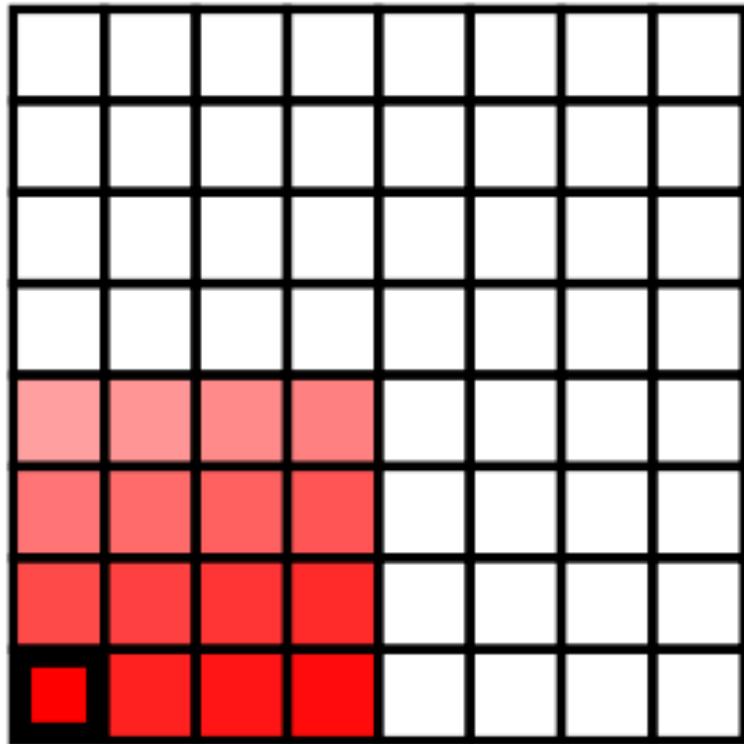
C mem:252 cache hit:189



mem:756

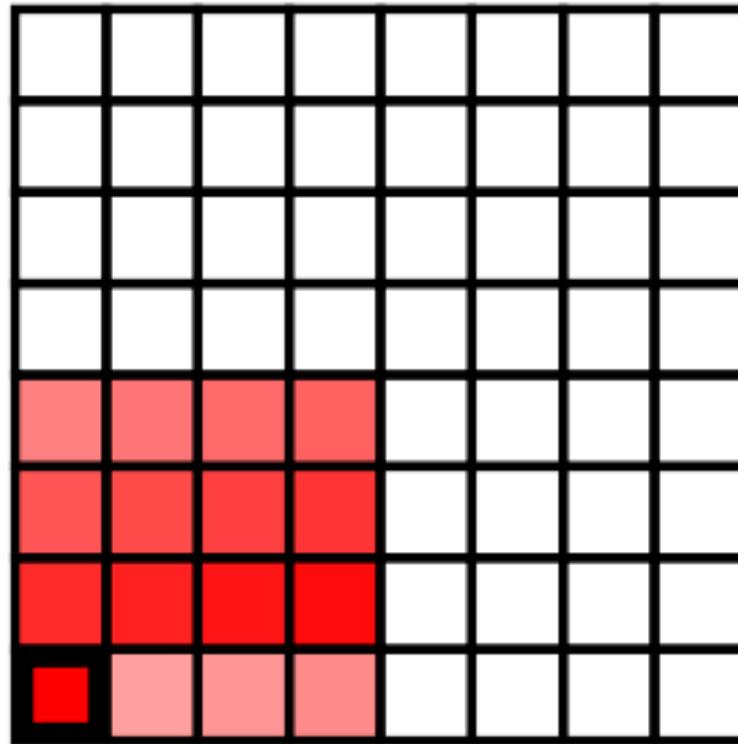
cache hits:597 $\approx 78\%$

A mem:253 cache hit:189



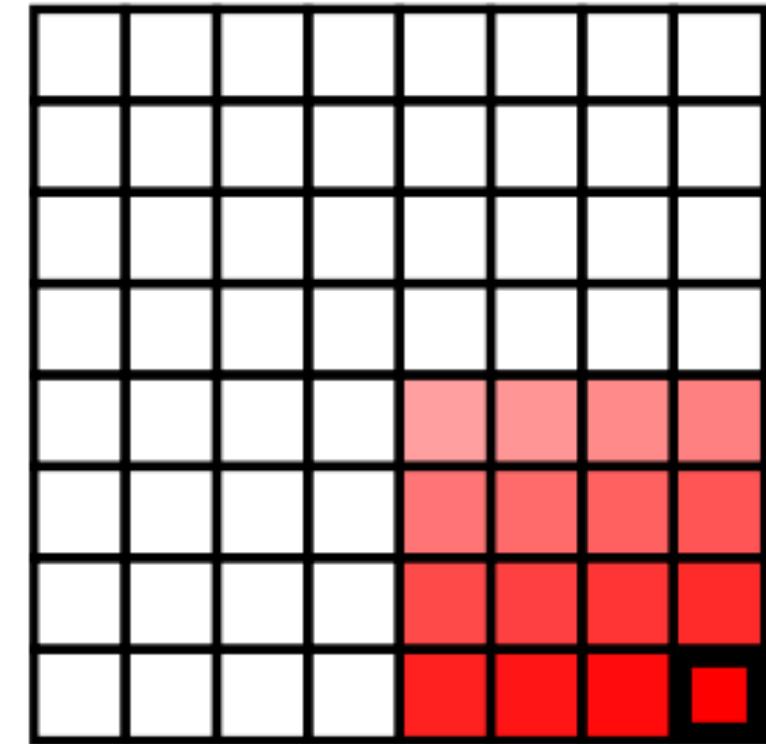
mem:759

B mem:253 cache hit:221

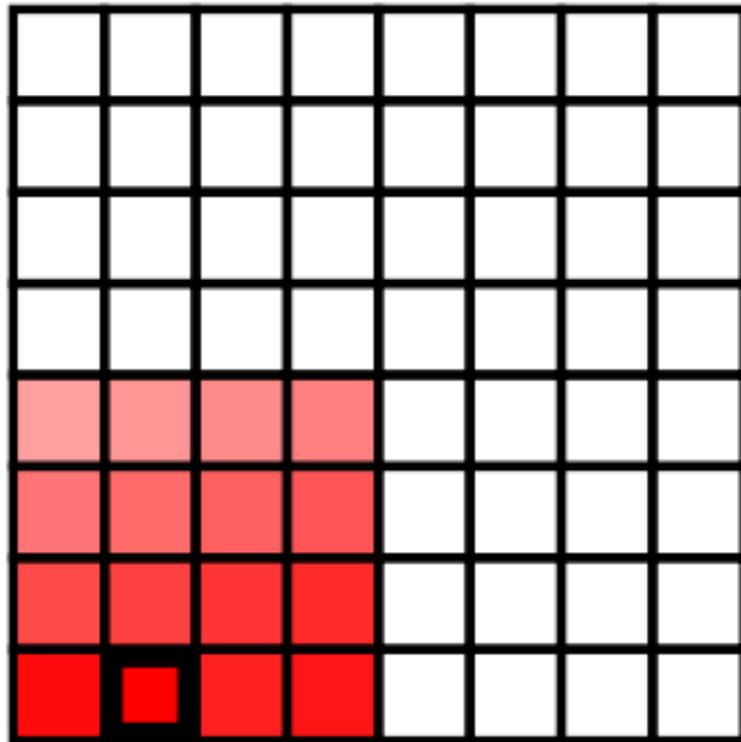


cache hits:599 ≈78%

C mem:253 cache hit:189

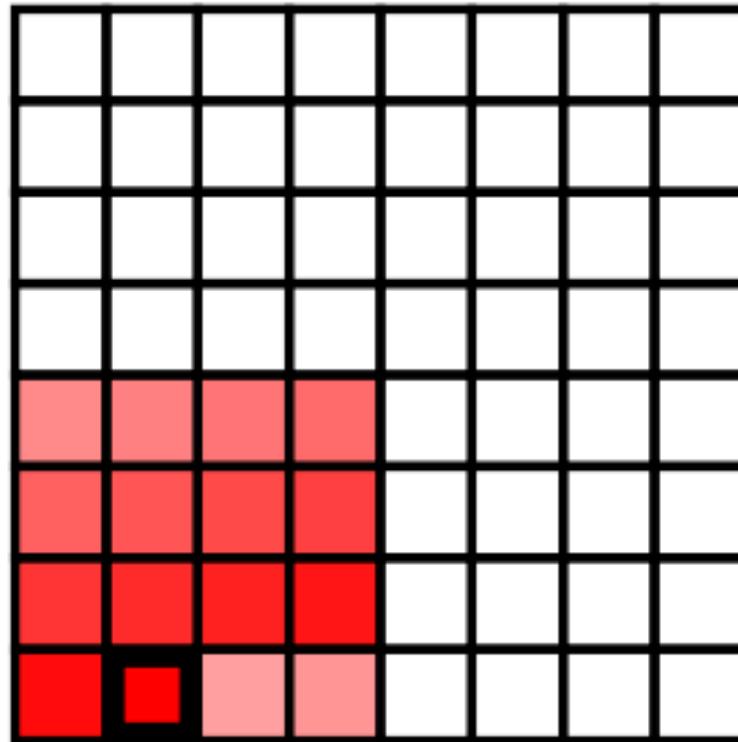


A mem:254 cache hit:190



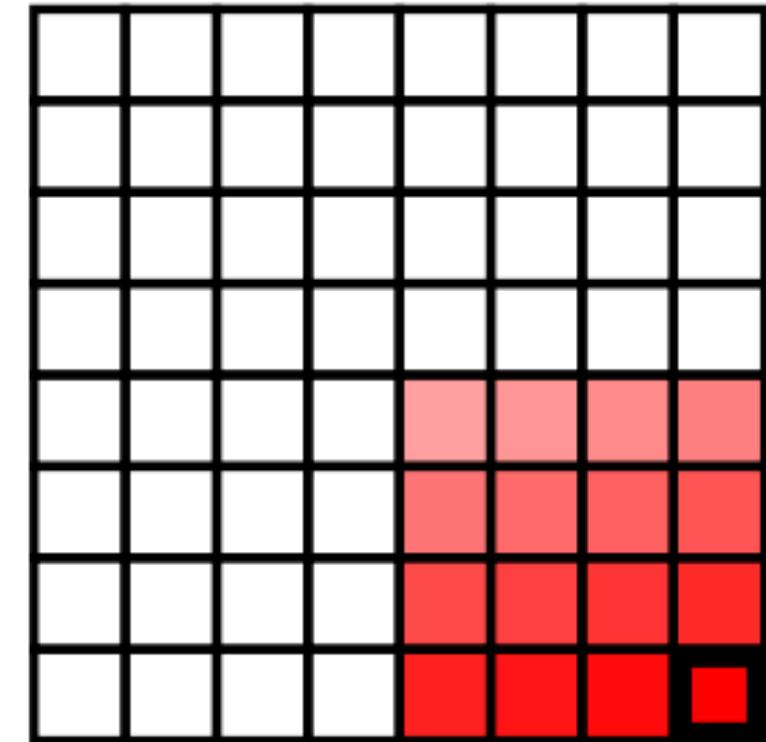
mem:762

B mem:254 cache hit:222

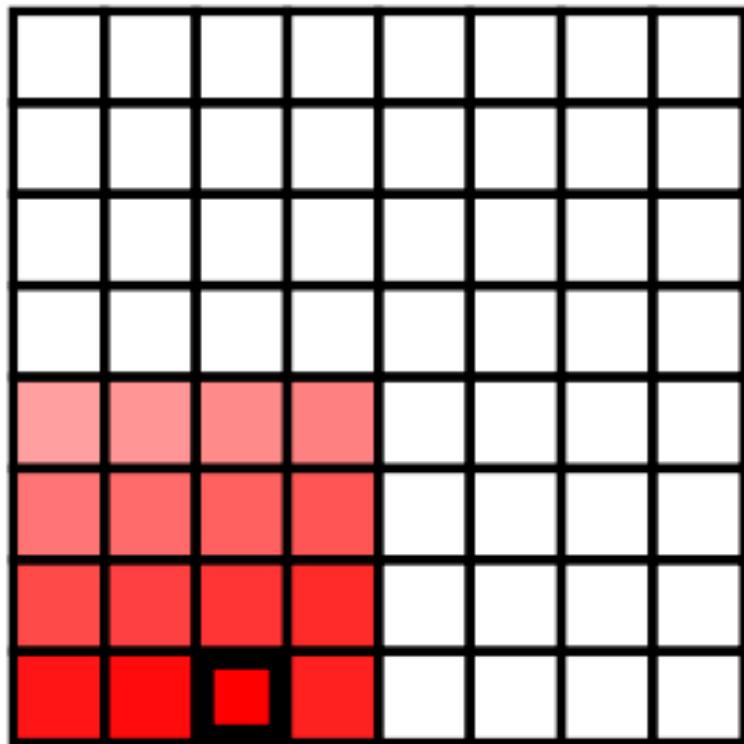


cache hits:602 $\approx 79\%$

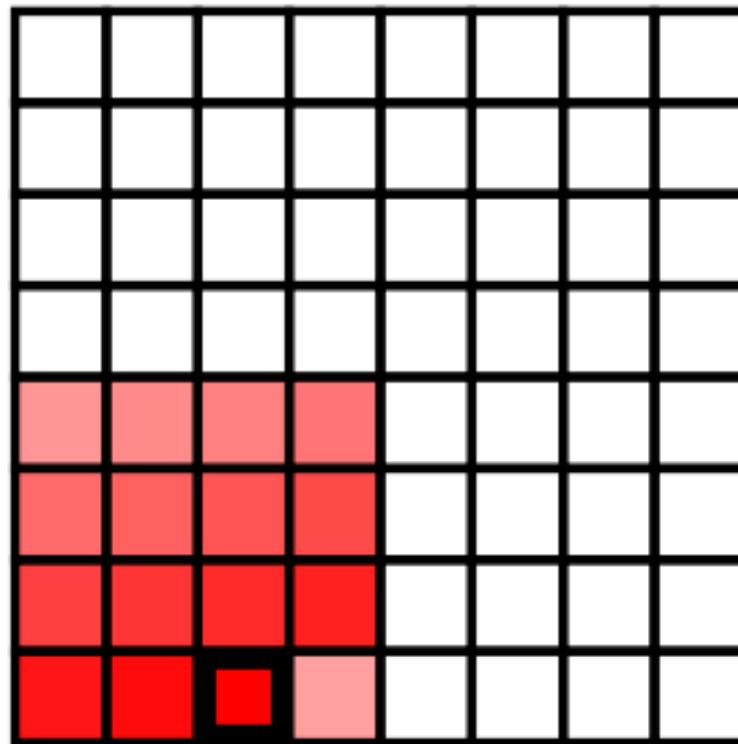
C mem:254 cache hit:190



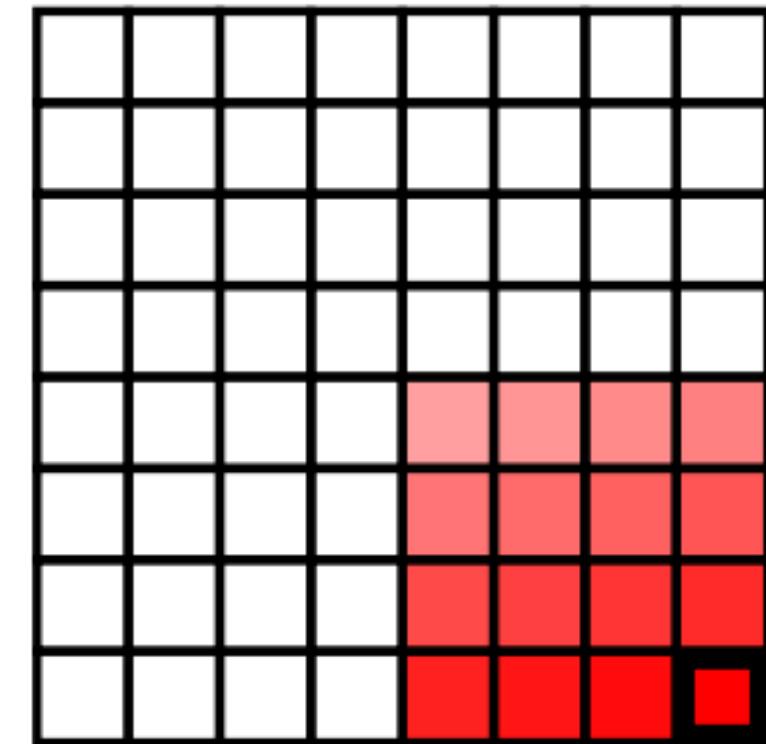
A mem:255 cache hit:191



B mem:255 cache hit:223



C mem:255 cache hit:191



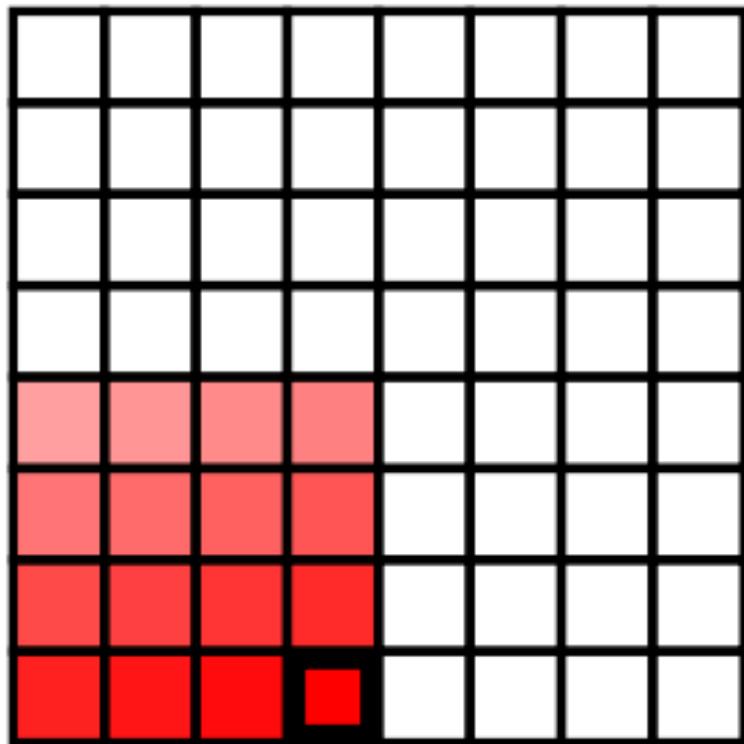
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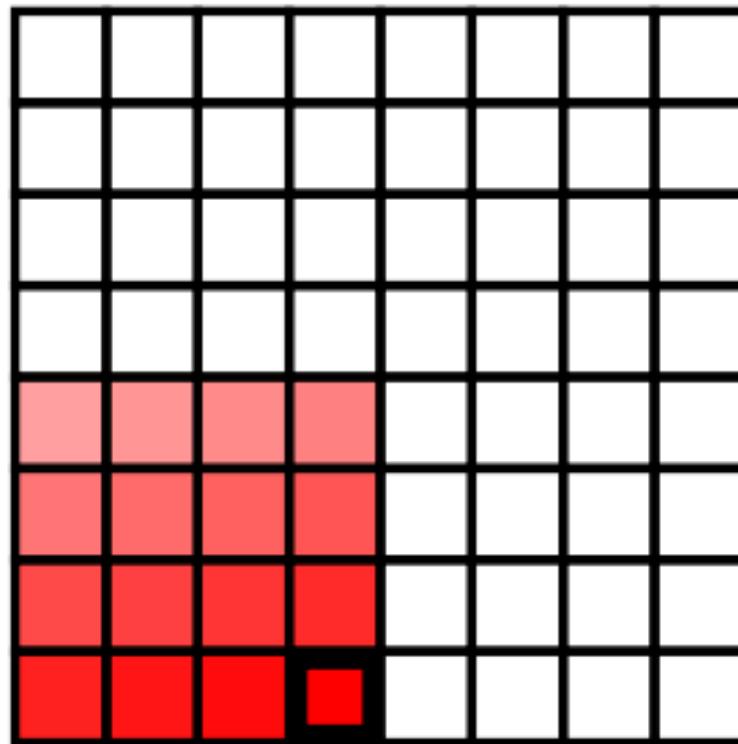
mem:765

cache hits:605 ≈79%

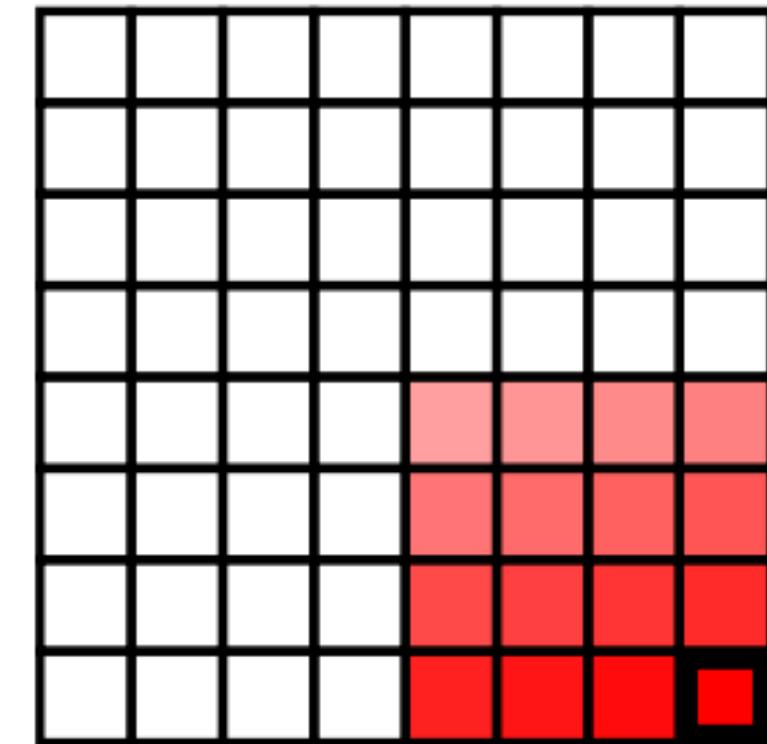
A mem:256 cache hit:192



B mem:256 cache hit:224



C mem:256 cache hit:192



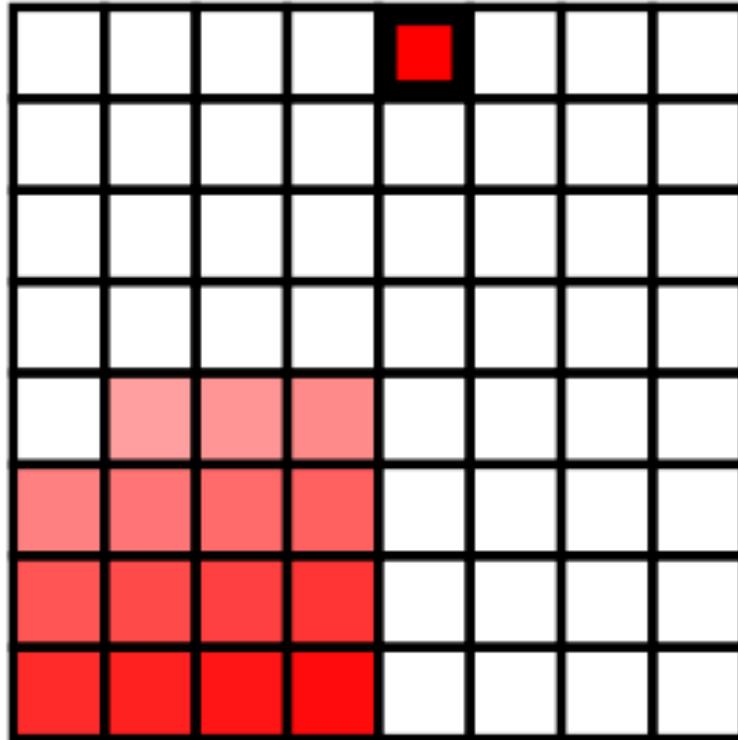
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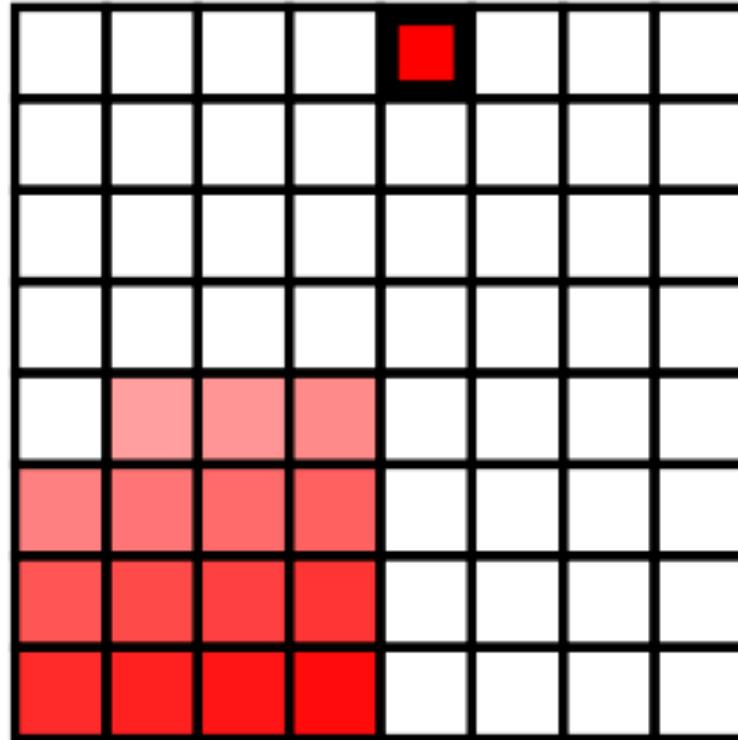
mem:768

cache hits:608 \approx 79%

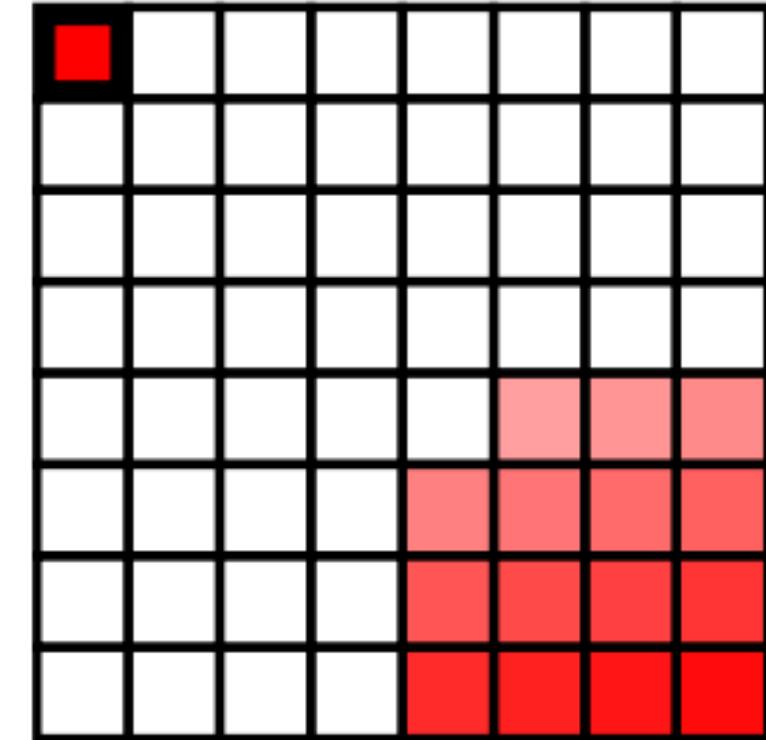
A mem:257 cache hit:192



B mem:257 cache hit:224



C mem:257 cache hit:192



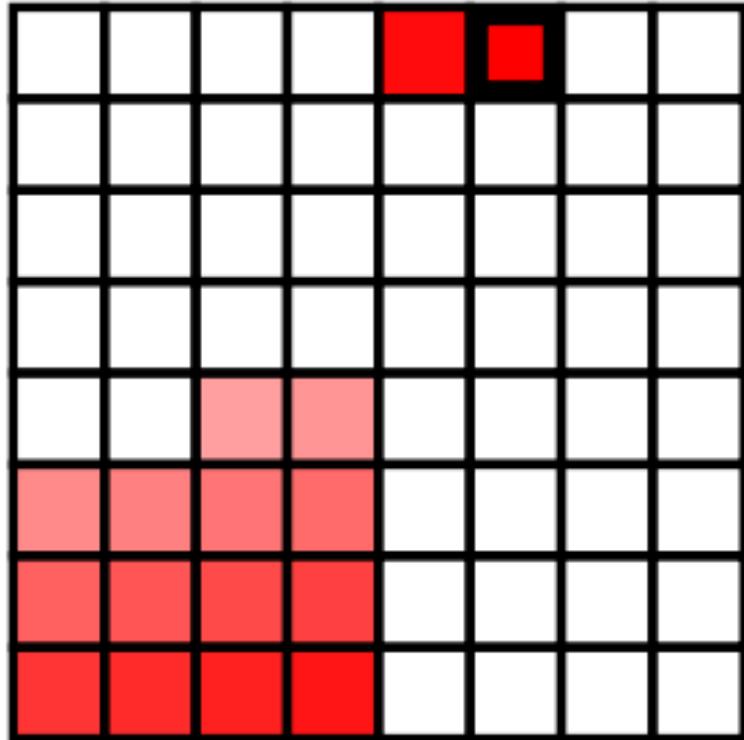
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mem:771

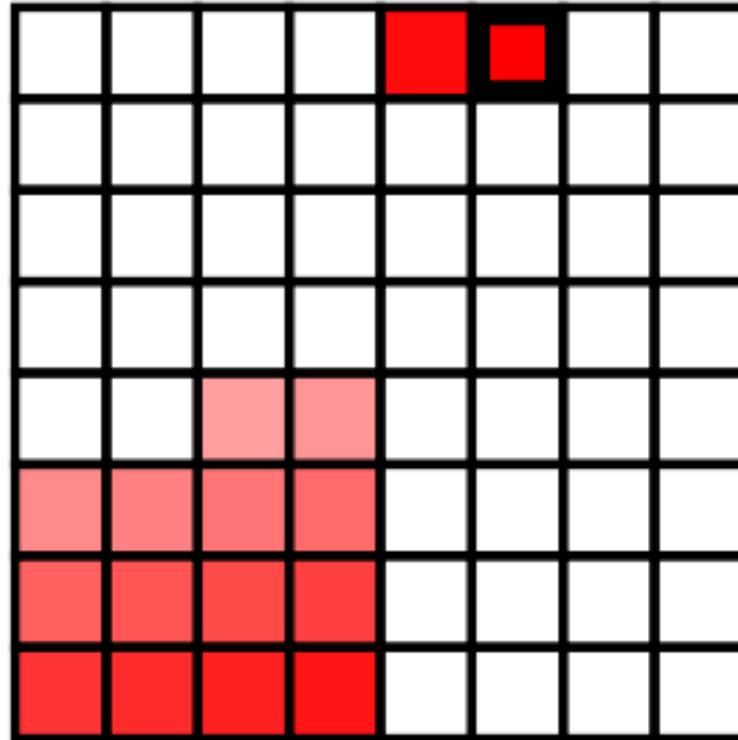
cache hits:608 $\approx 78\%$

A mem:258 cache hit:192



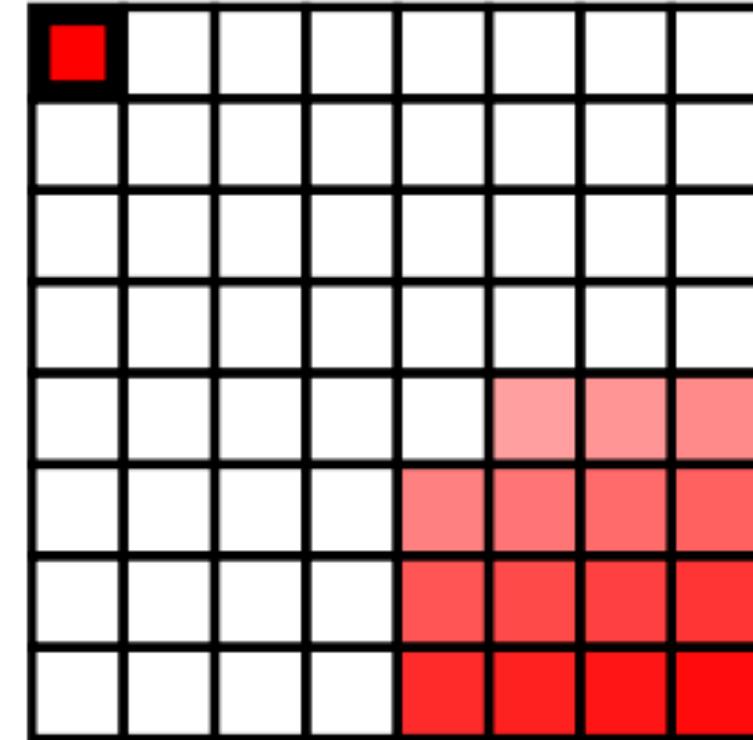
mem:774

B mem:258 cache hit:224

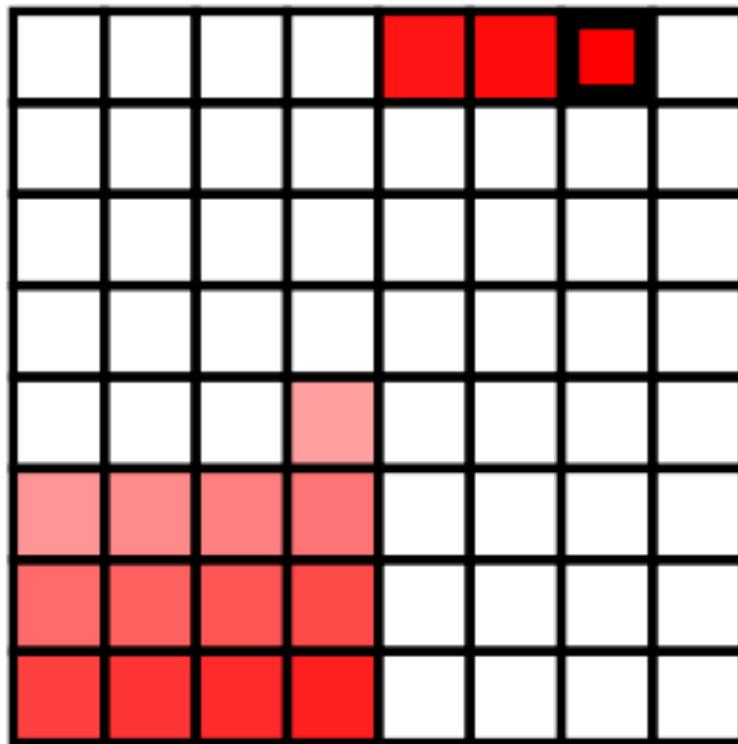


cache hits:609 $\approx 78\%$

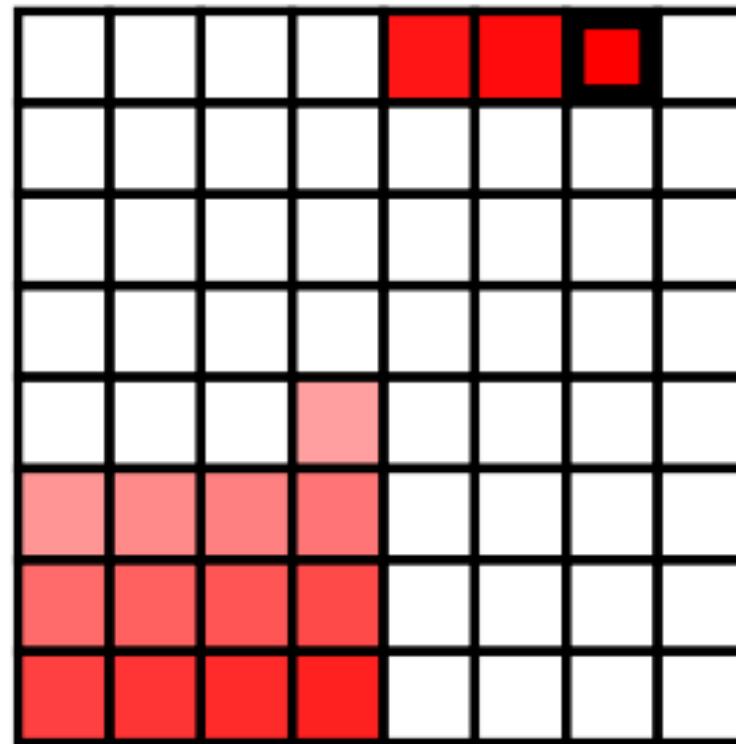
C mem:258 cache hit:193



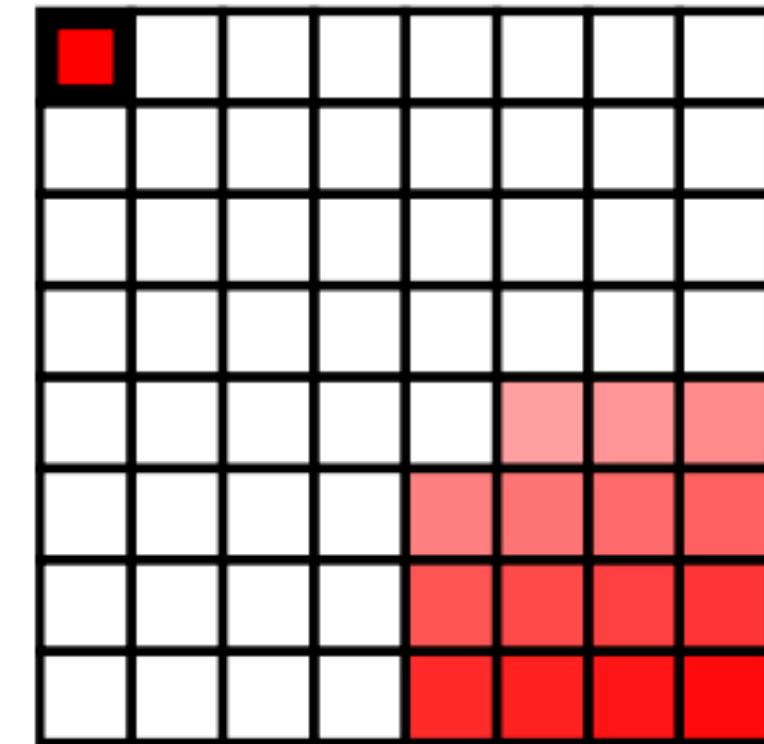
A mem:259 cache hit:192



B mem:259 cache hit:224



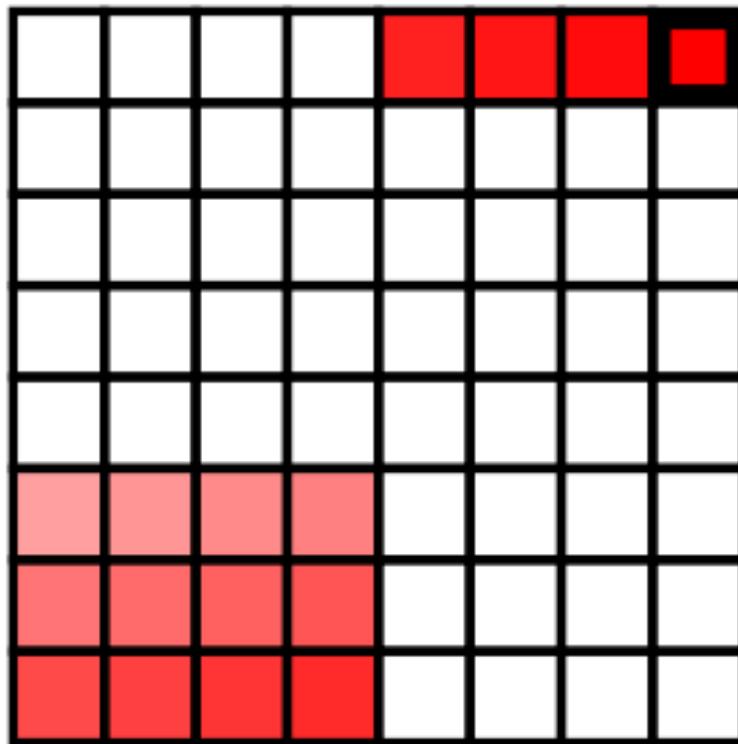
C mem:259 cache hit:194



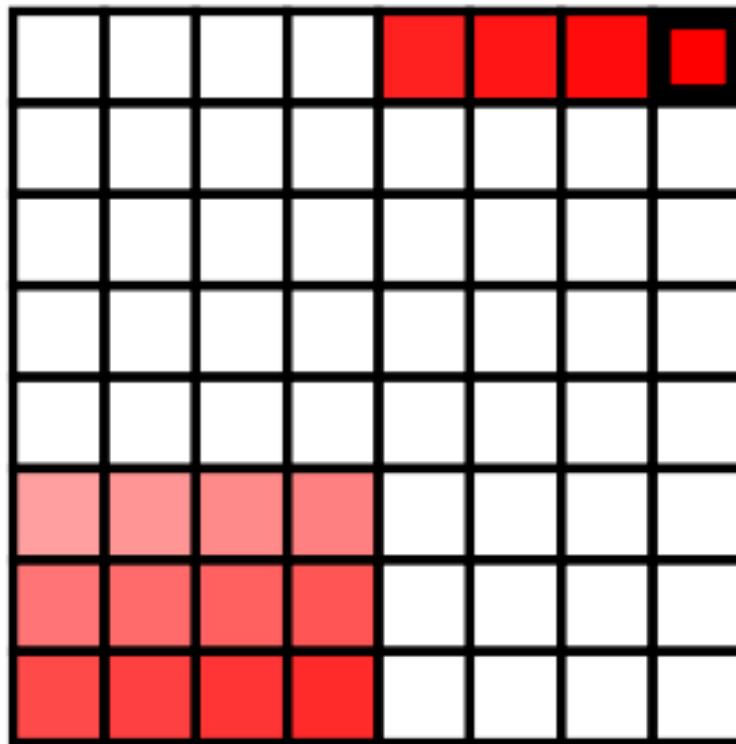
mem:777

cache hits:610 $\approx 78\%$

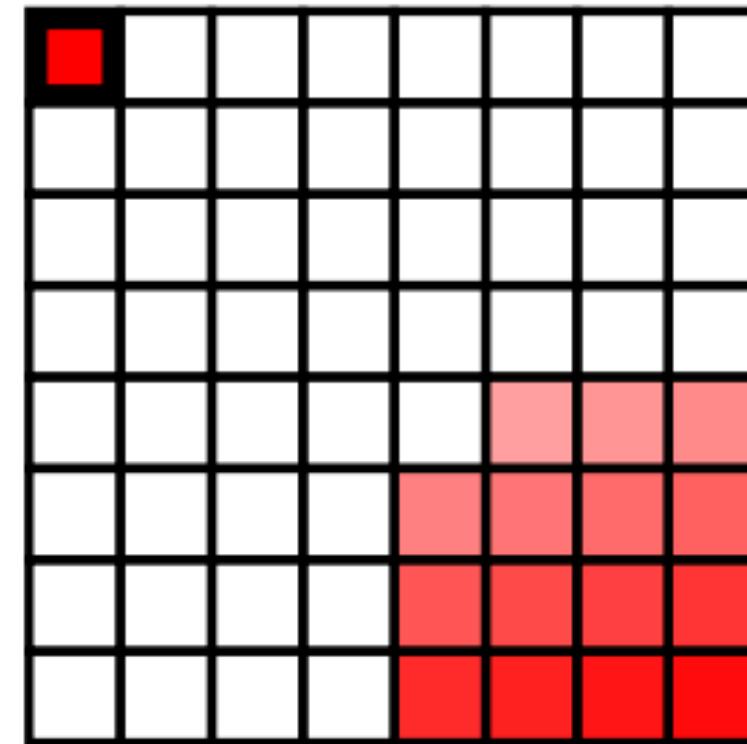
A mem:260 cache hit:192



B mem:260 cache hit:224



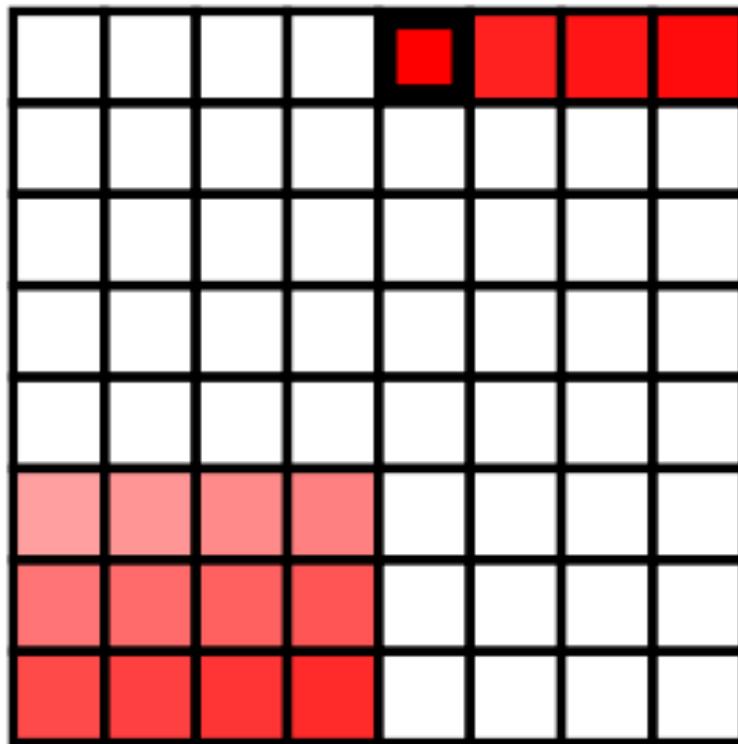
C mem:260 cache hit:195



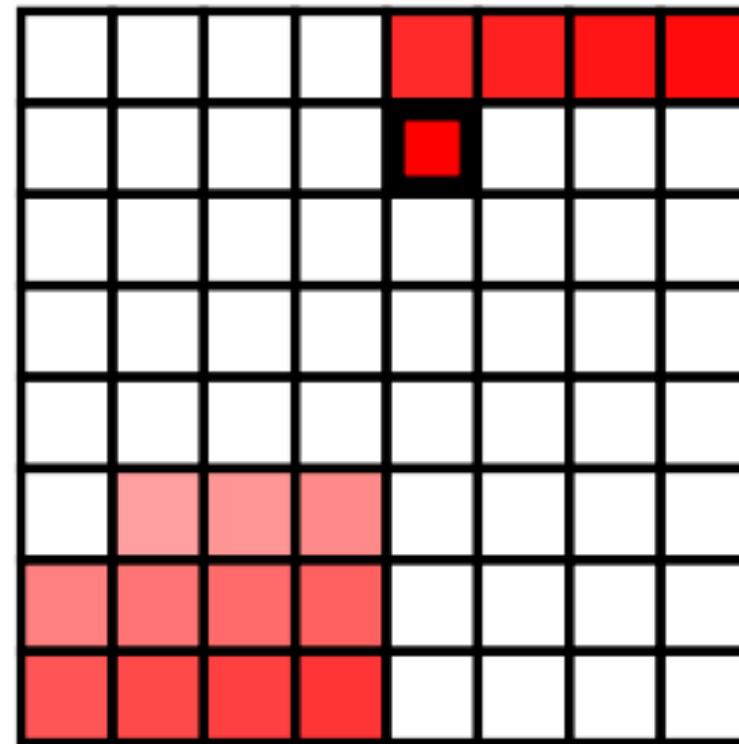
mem:780

cache hits:611 $\approx 78\%$

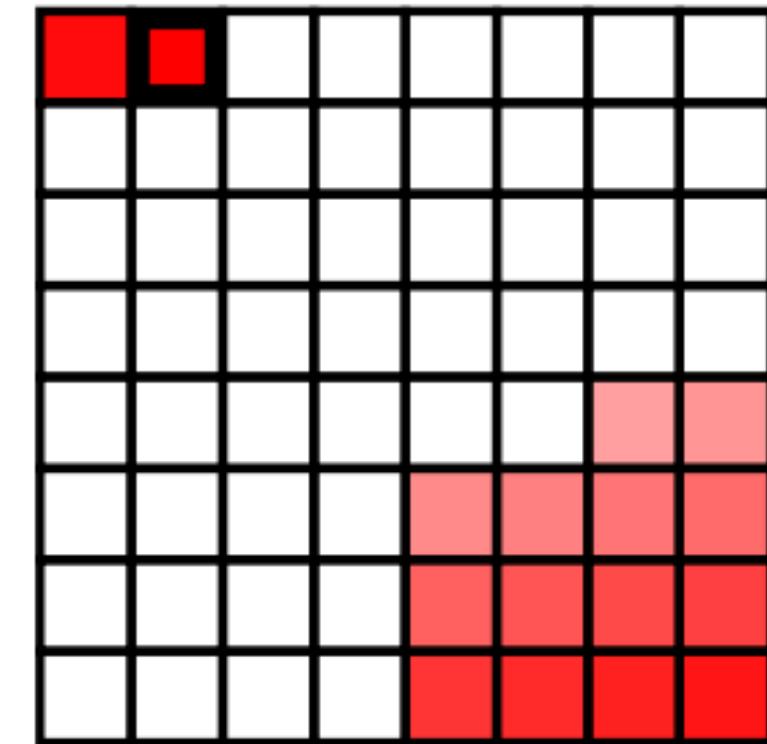
A mem:261 cache hit:193



B mem:261 cache hit:224



C mem:261 cache hit:195



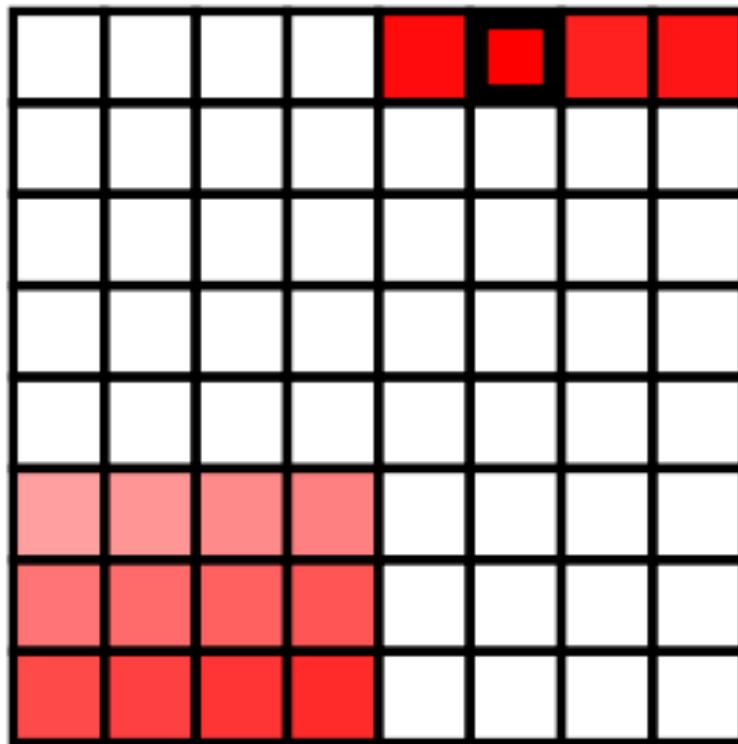
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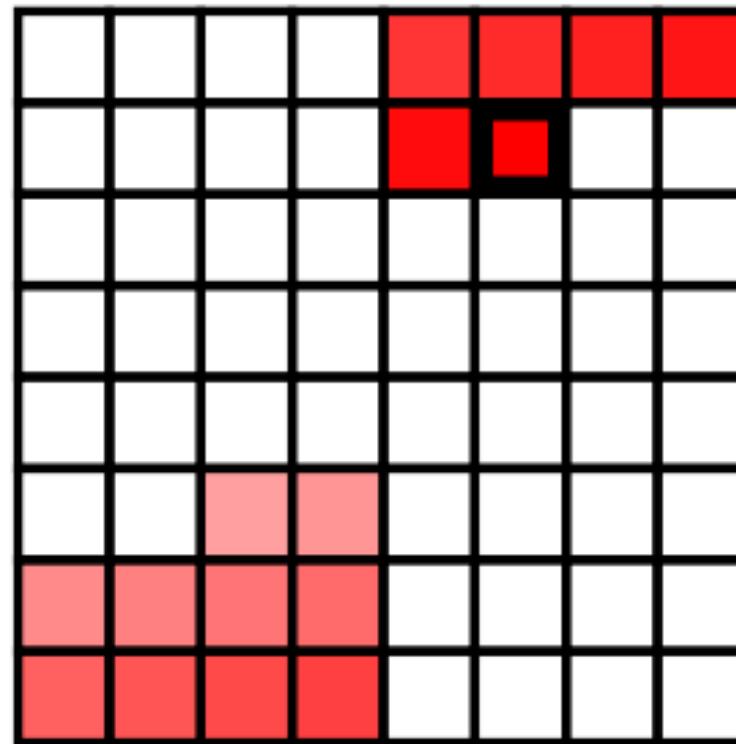
mem:783

cache hits:612 $\approx 78\%$

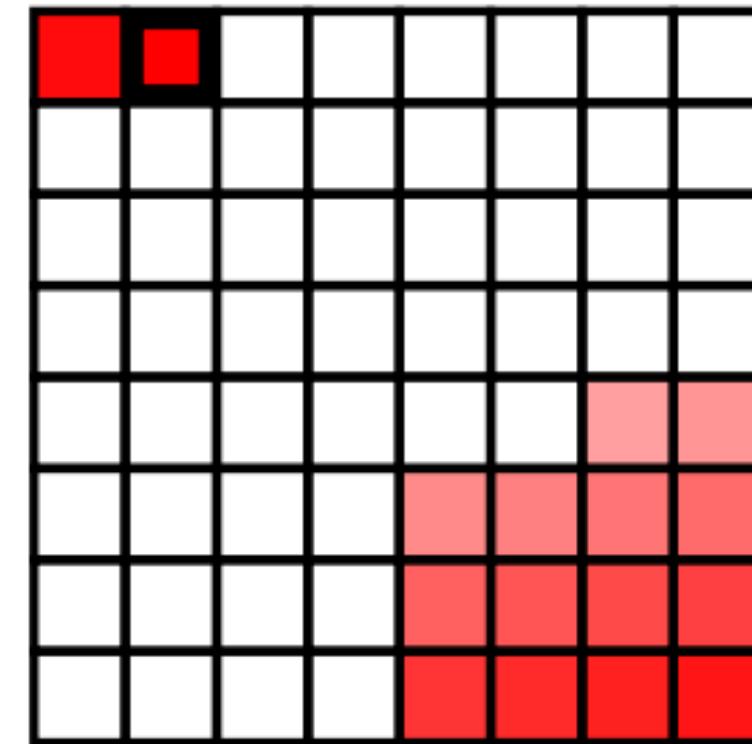
A mem:262 cache hit:194



B mem:262 cache hit:224



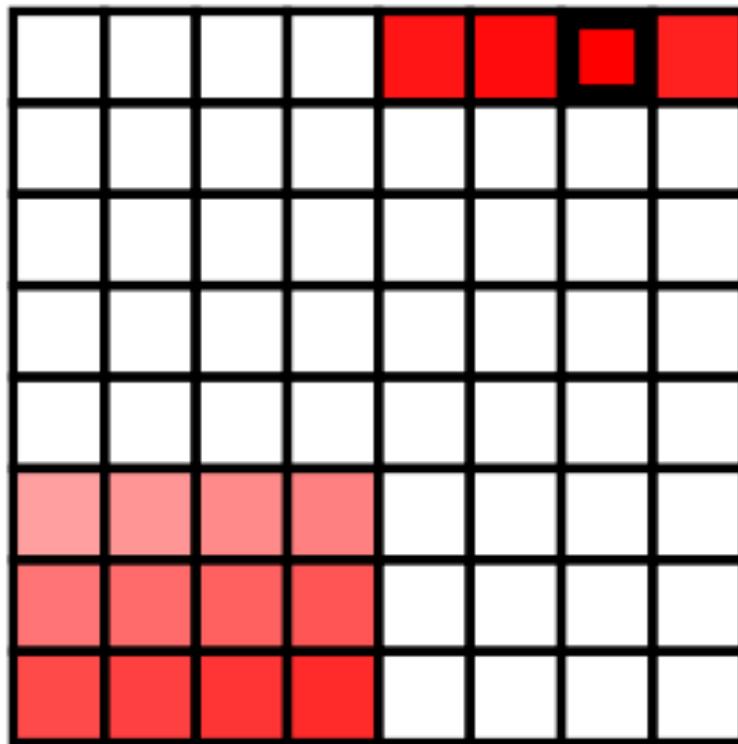
C mem:262 cache hit:196



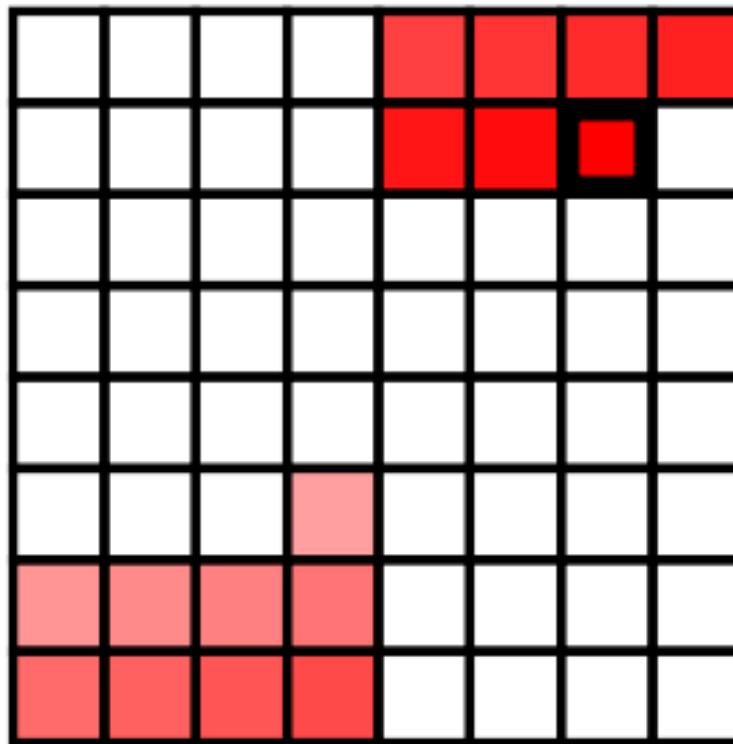
mem:786

cache hits:614 $\approx 78\%$

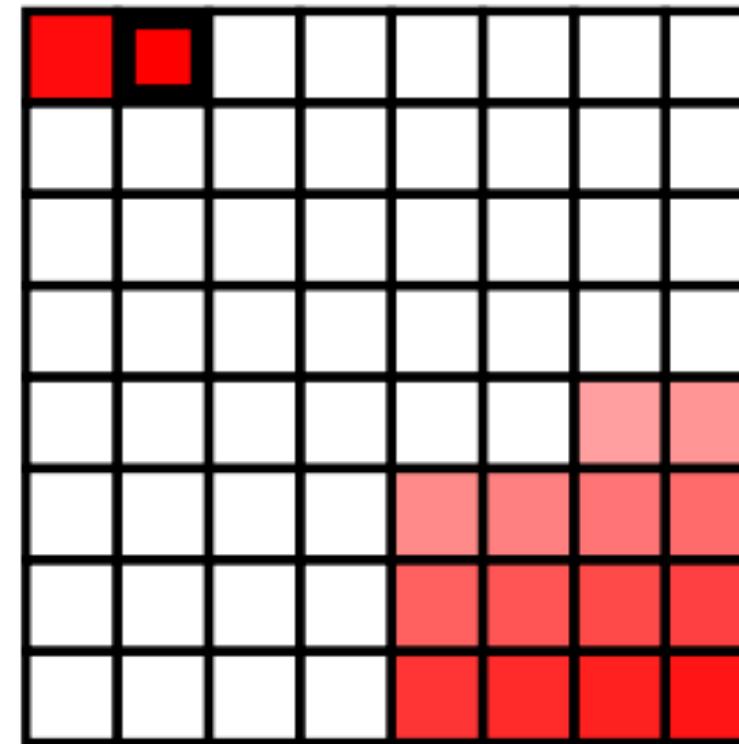
A mem:263 cache hit:195



B mem:263 cache hit:224



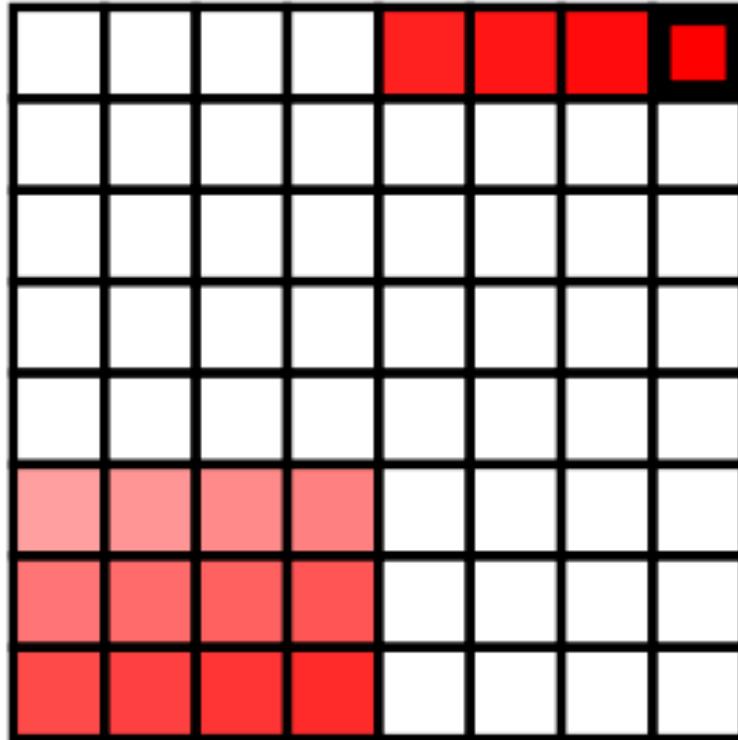
C mem:263 cache hit:197



mem:789

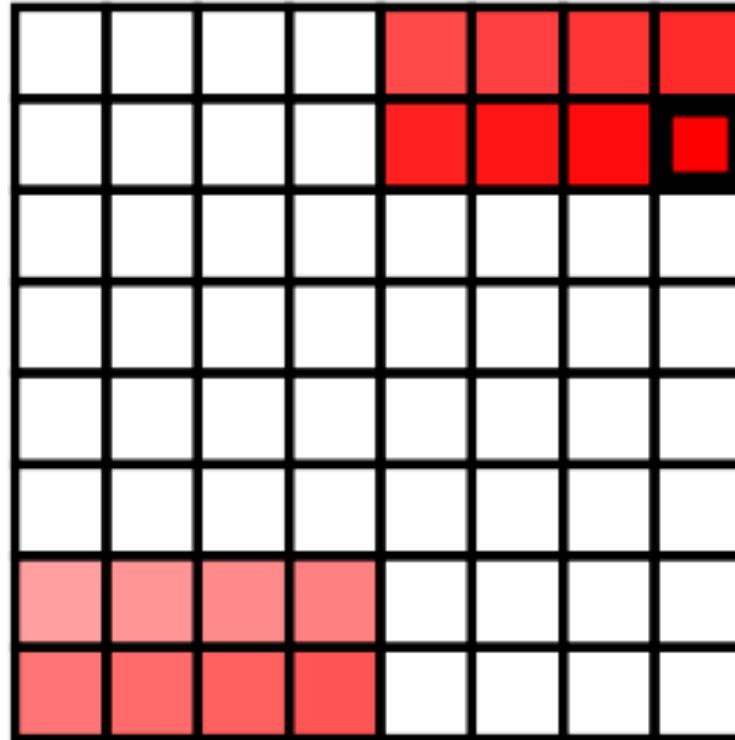
cache hits:616 $\approx 78\%$

A mem:264 cache hit:196



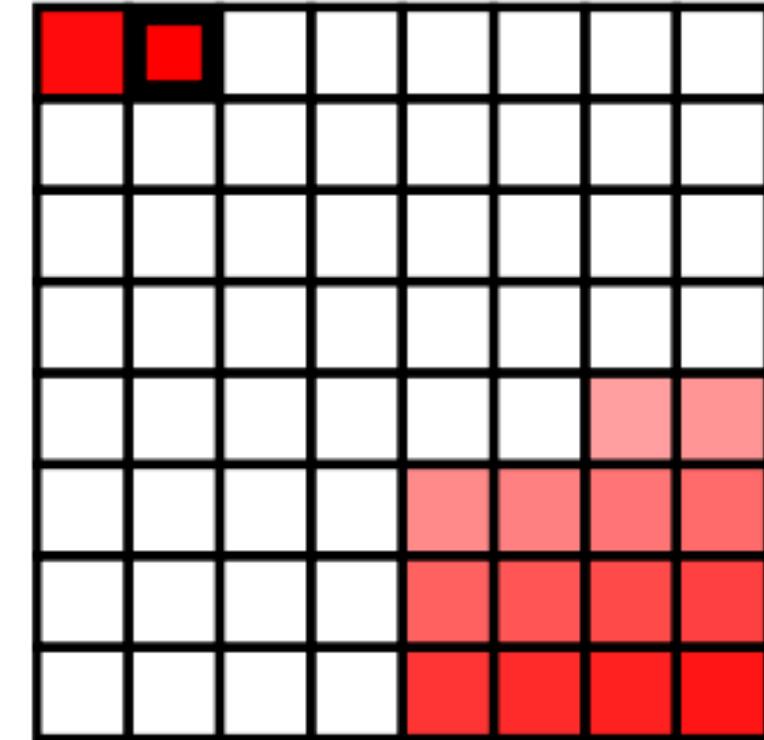
mem:792

B mem:264 cache hit:224

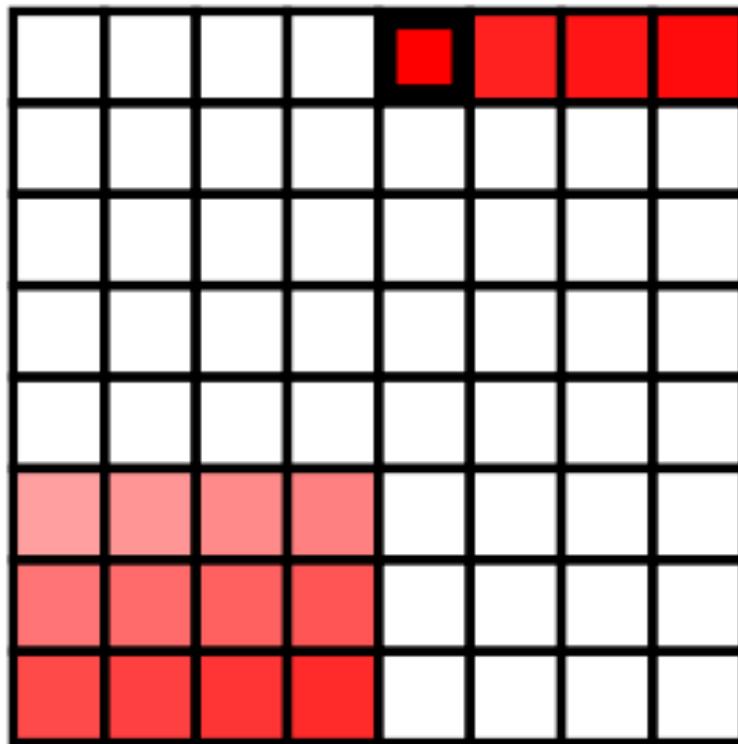


cache hits:618 $\approx 78\%$

C mem:264 cache hit:198

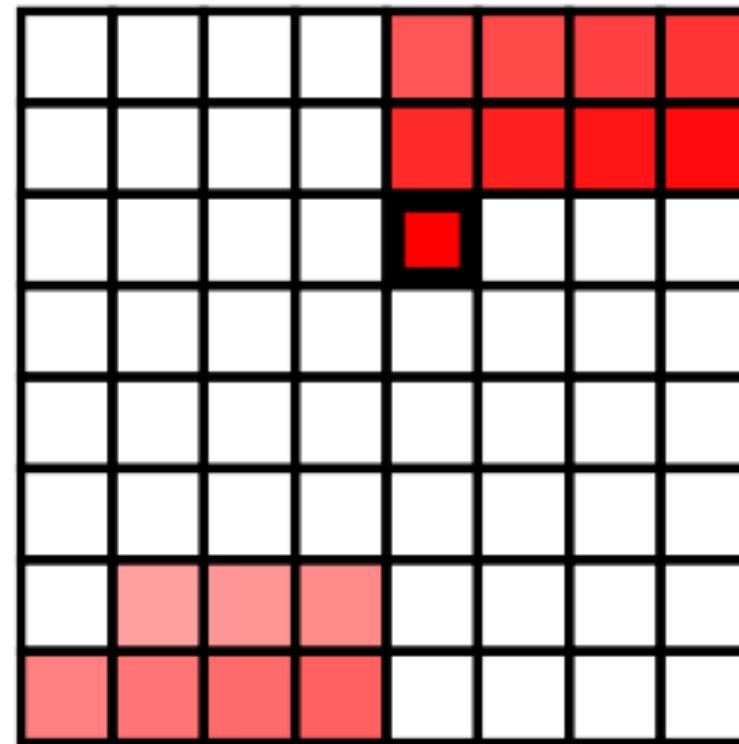


A mem:265 cache hit:197



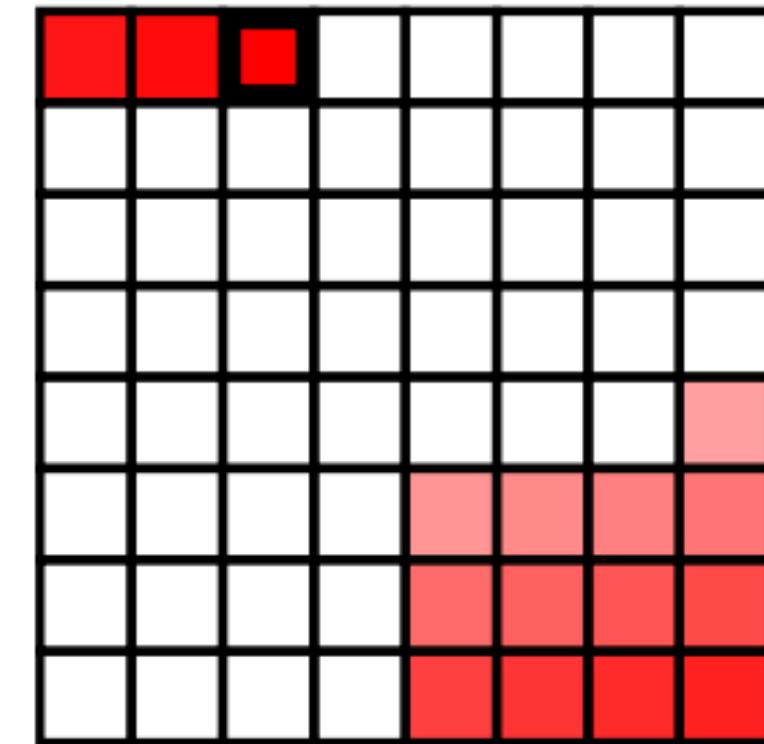
mem:795

B mem:265 cache hit:224

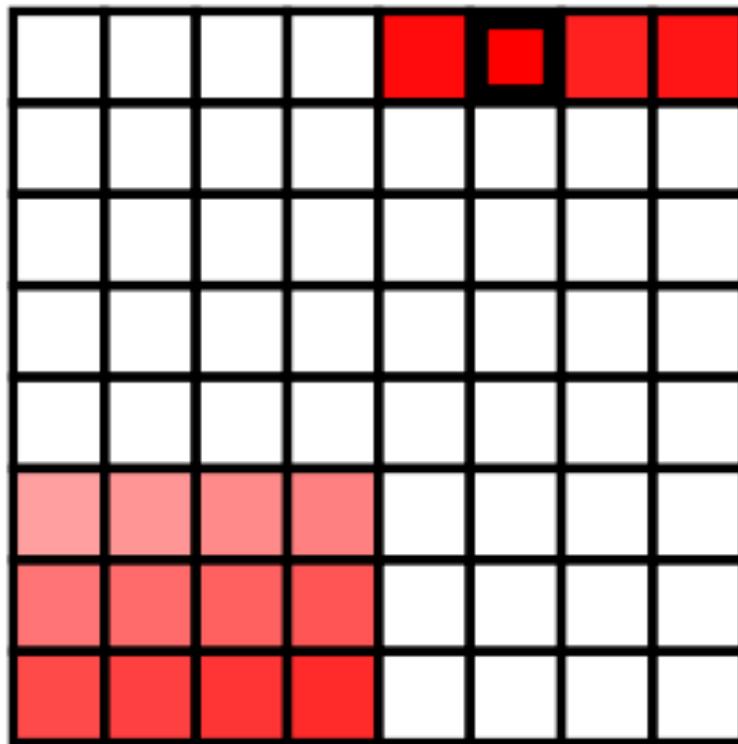


cache hits:619 $\approx 77\%$

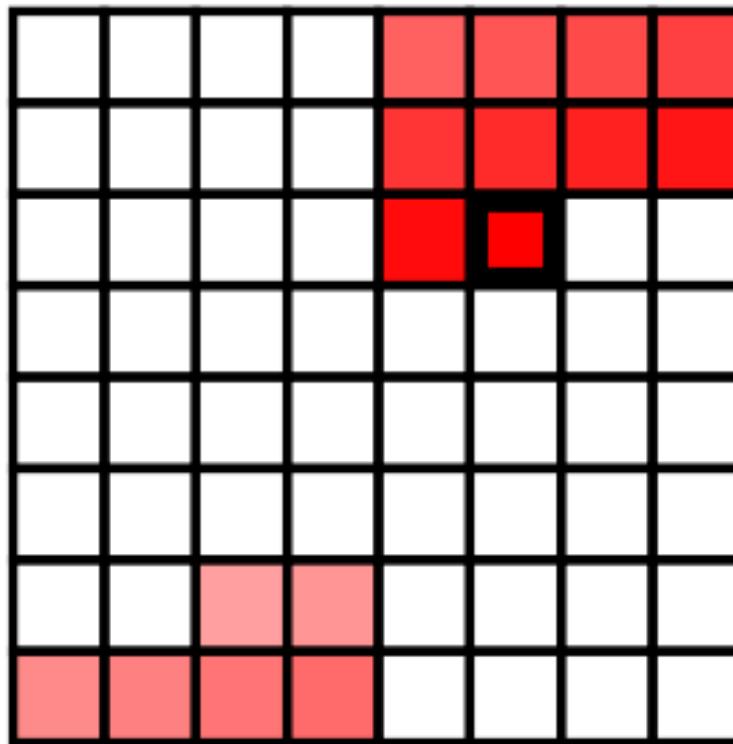
C mem:265 cache hit:198



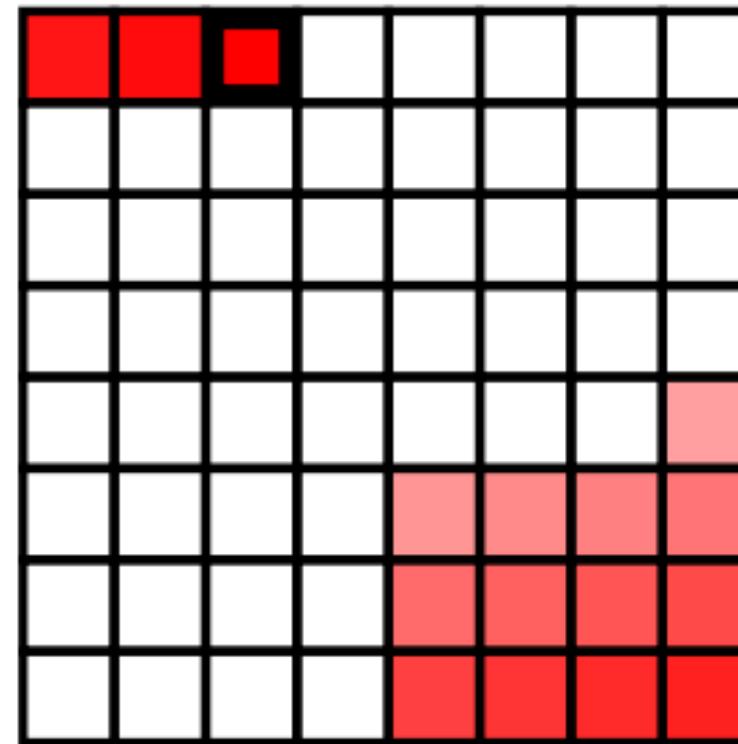
A mem:266 cache hit:198



B mem:266 cache hit:224



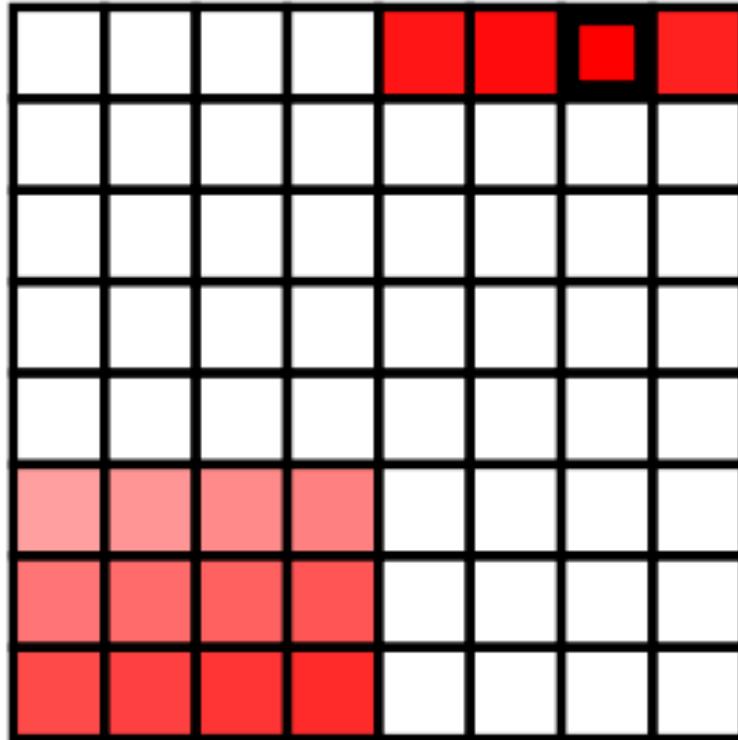
C mem:266 cache hit:199



mem:798

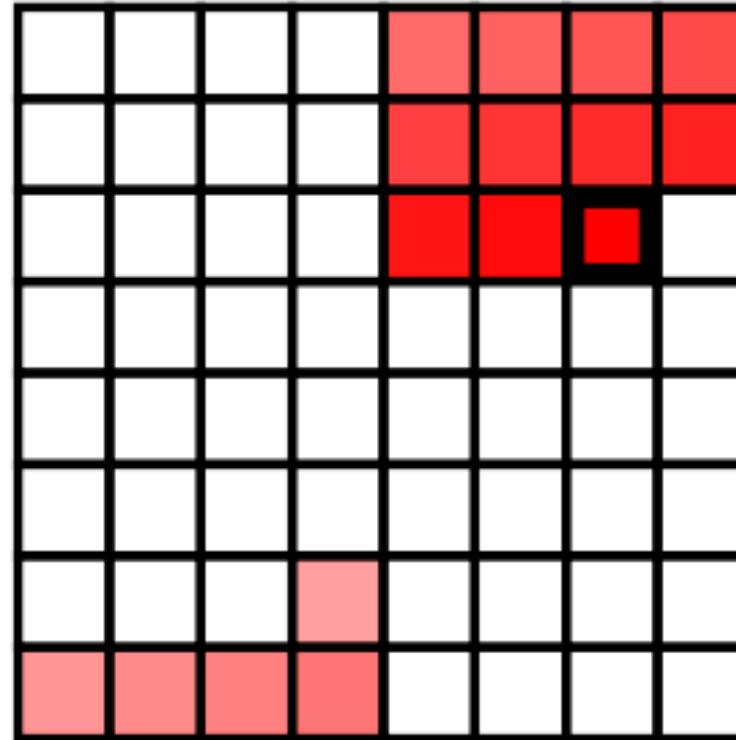
cache hits:621 $\approx 77\%$

A mem:267 cache hit:199



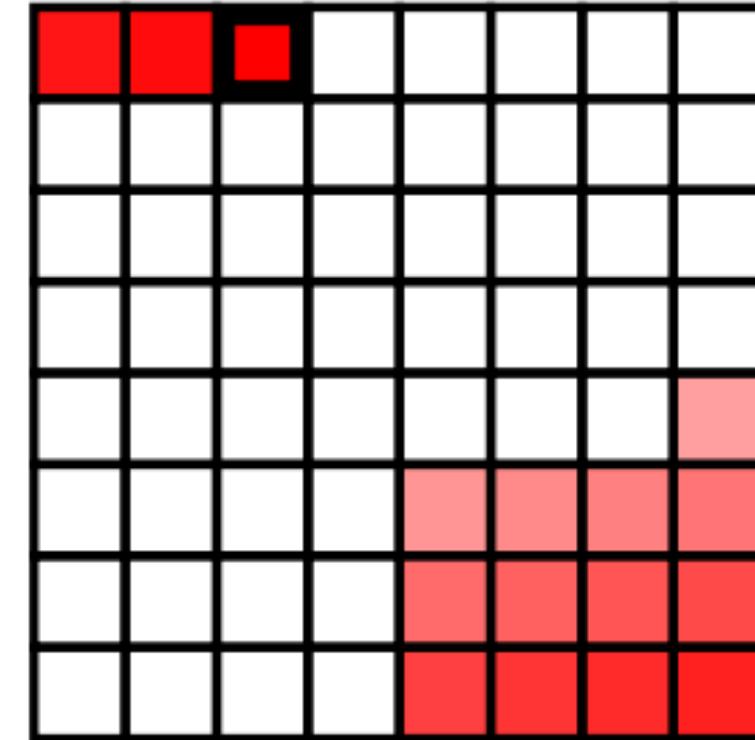
mem:801

B mem:267 cache hit:224

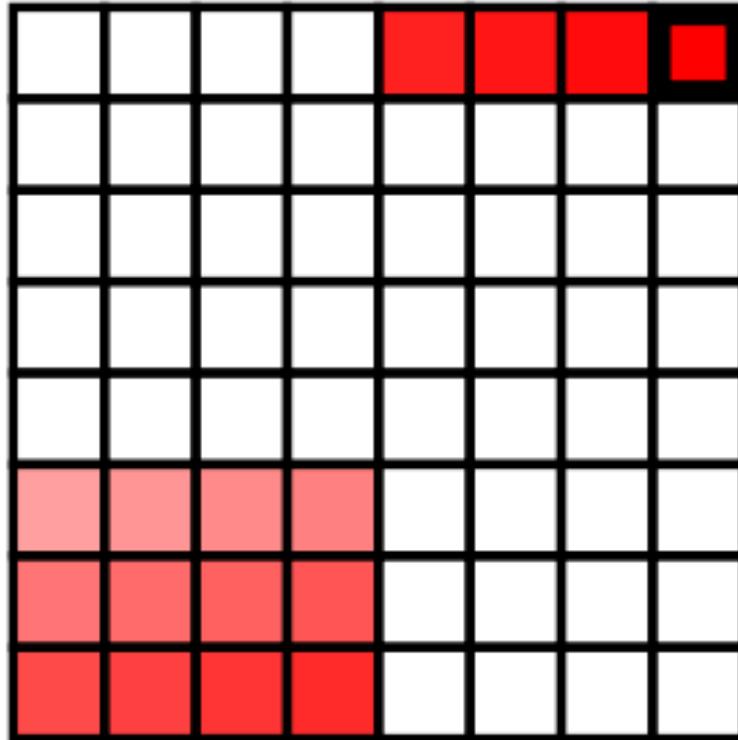


cache hits:623 ≈77%

C mem:267 cache hit:200

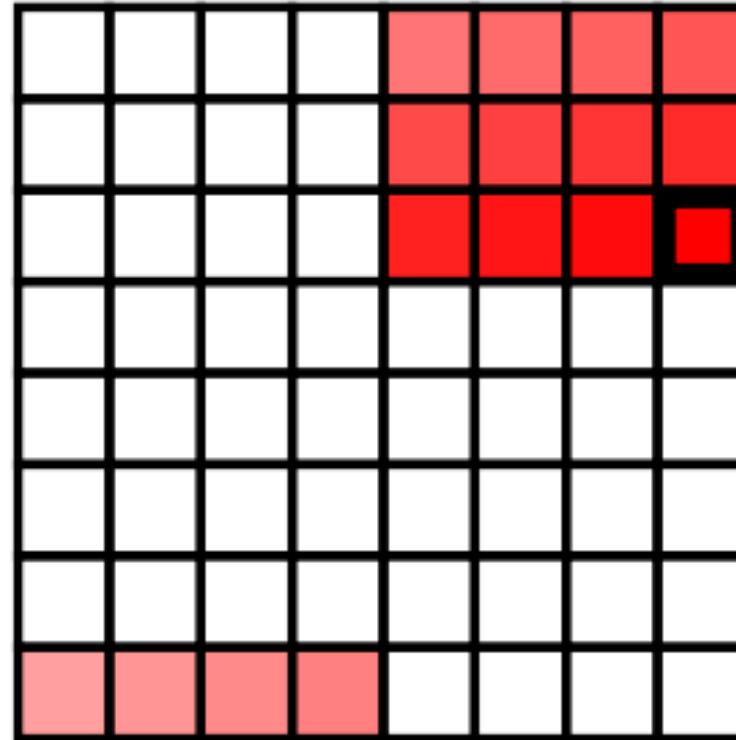


A mem:268 cache hit:200



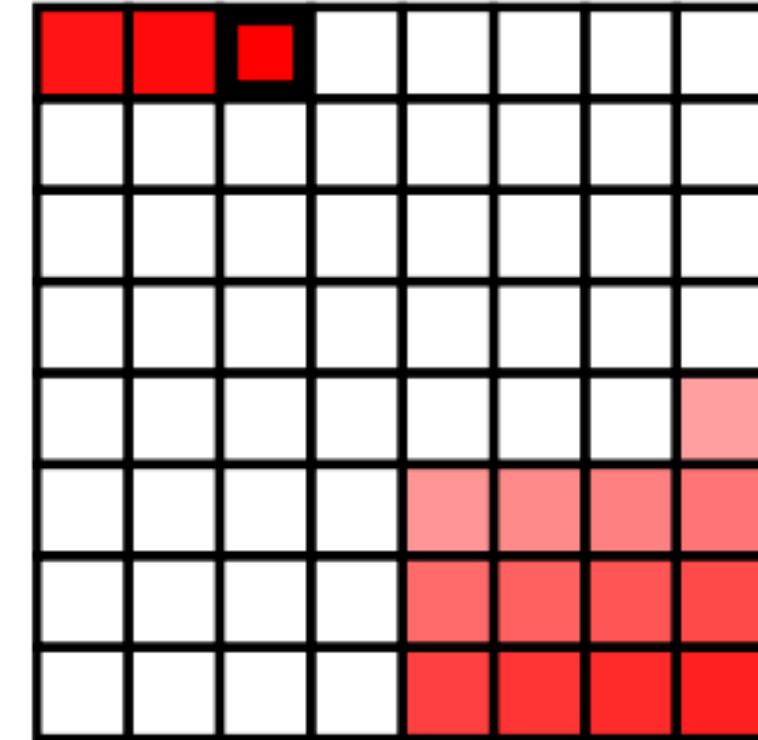
mem:804

B mem:268 cache hit:224

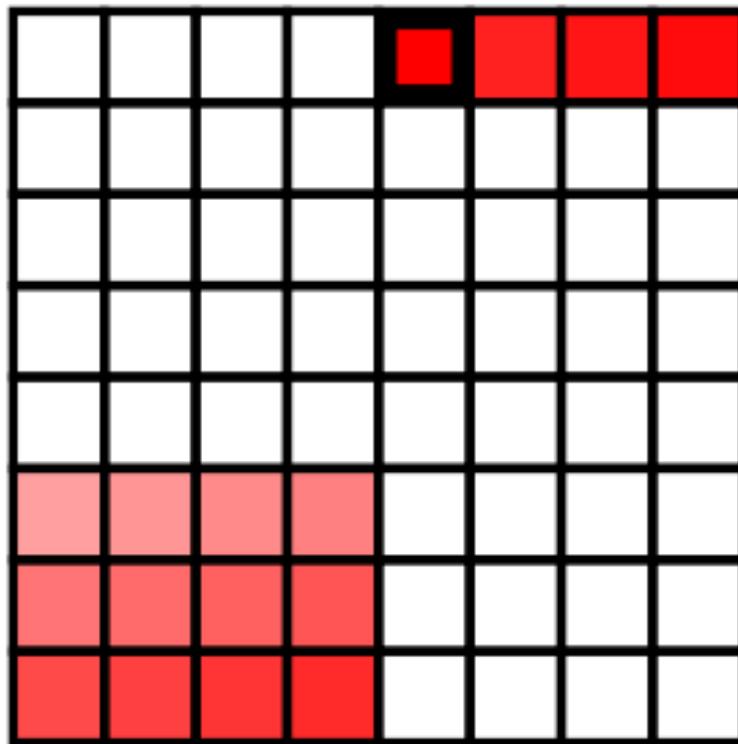


cache hits:625 ≈77%

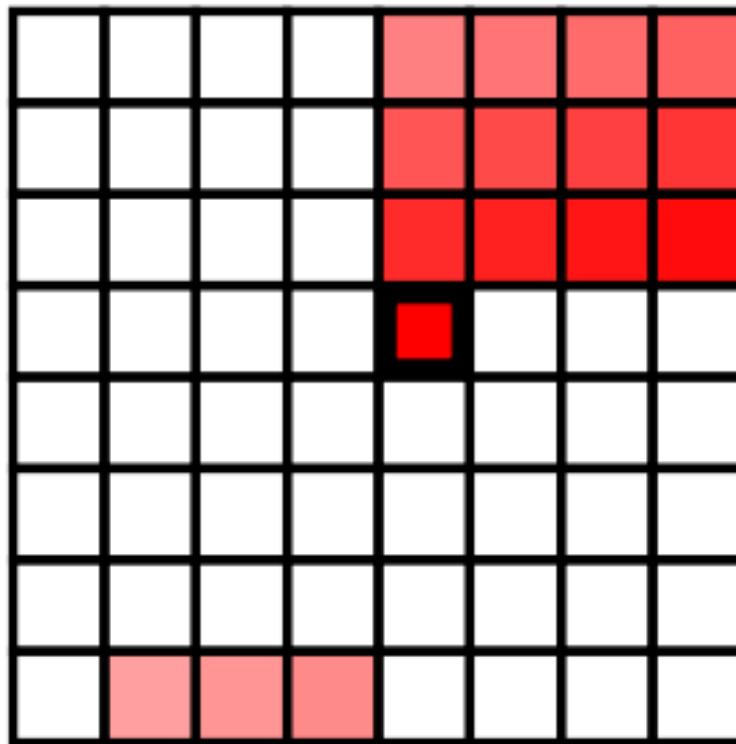
C mem:268 cache hit:201



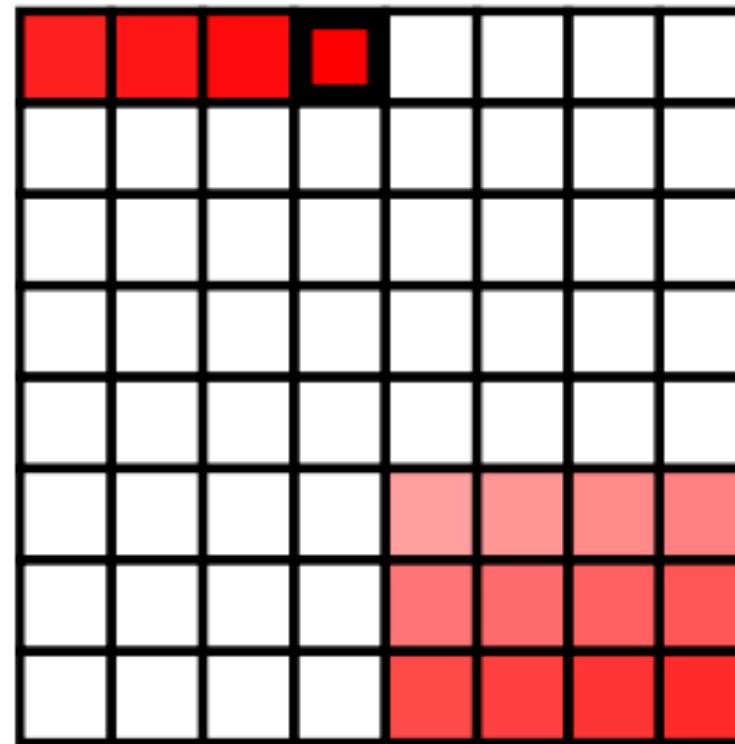
A mem:269 cache hit:201



B mem:269 cache hit:224



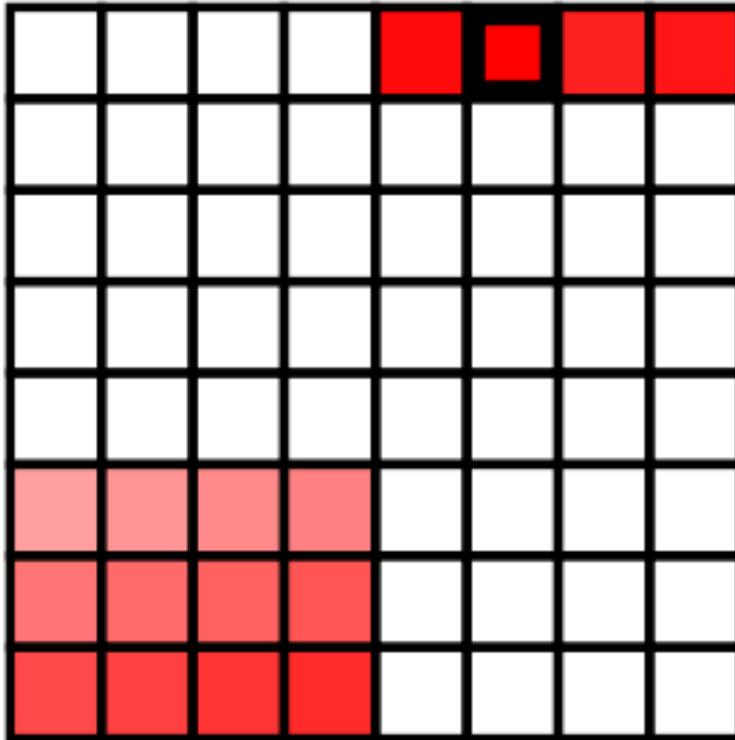
C mem:269 cache hit:201



mem:807

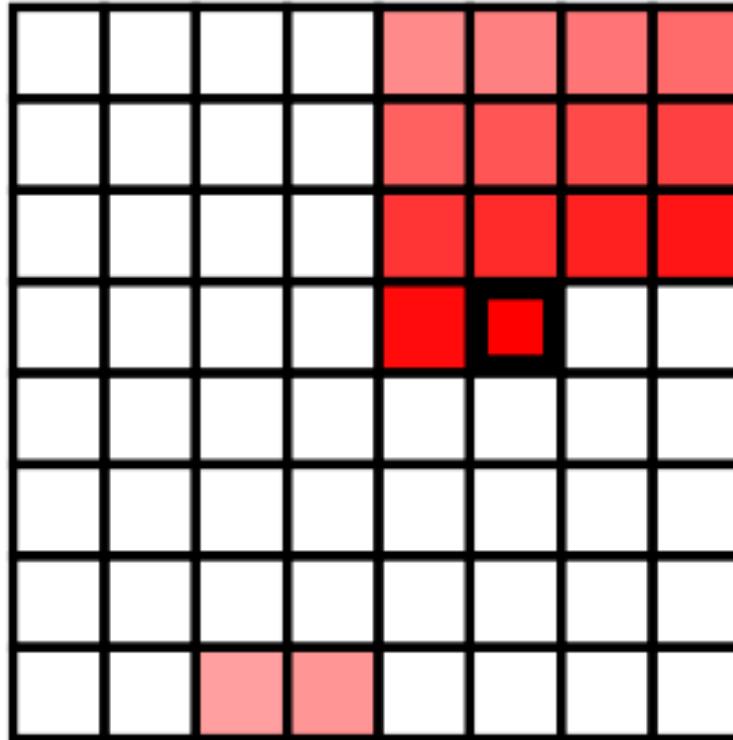
cache hits:626 $\approx 77\%$

A mem:270 cache hit:202



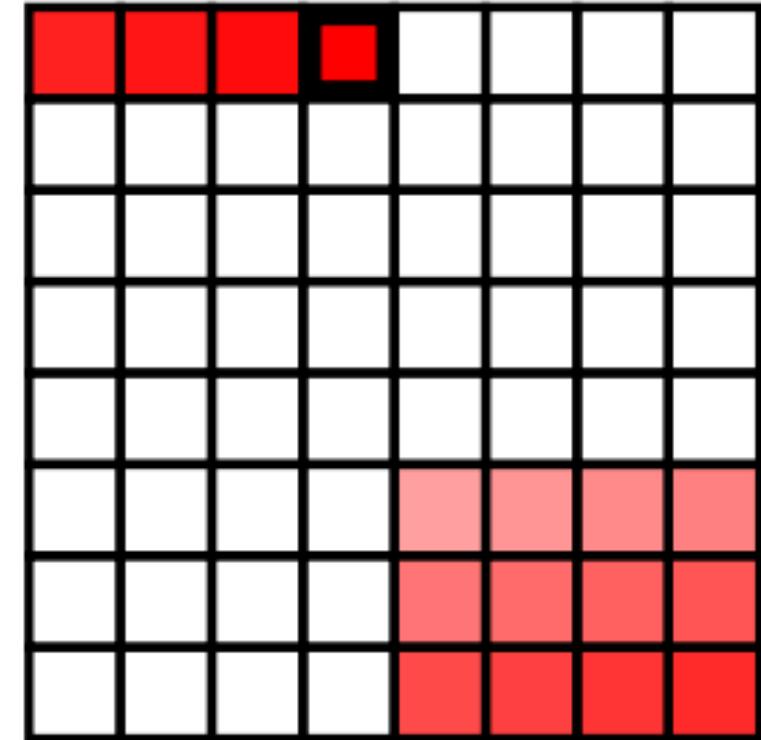
mem:810

B mem:270 cache hit:224

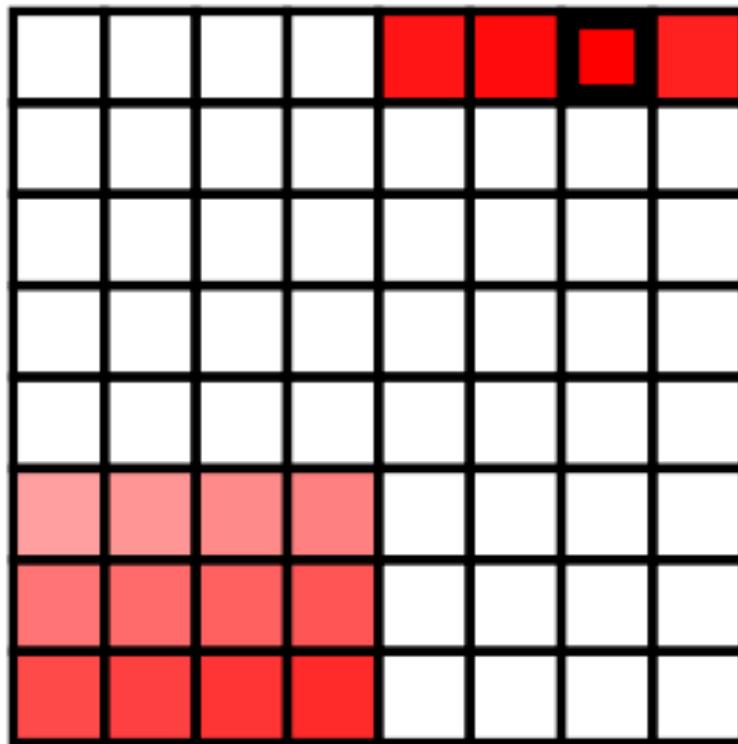


cache hits:628 ≈77%

C mem:270 cache hit:202

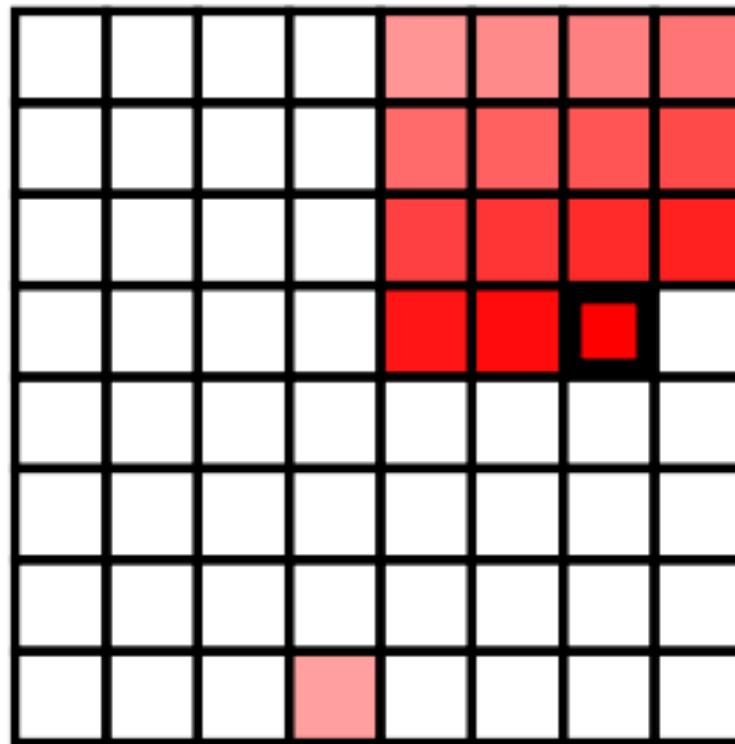


A mem:271 cache hit:203



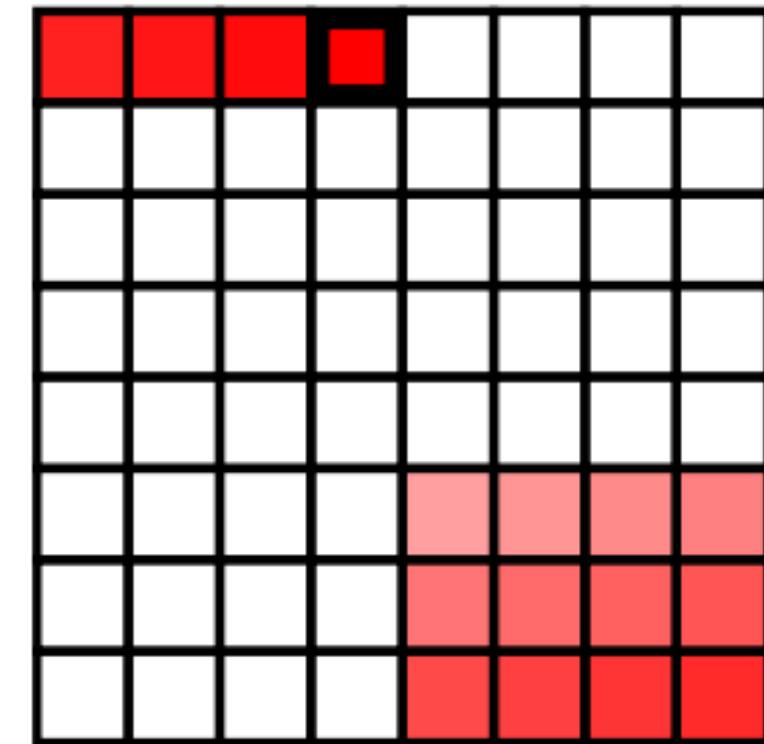
mem:813

B mem:271 cache hit:224

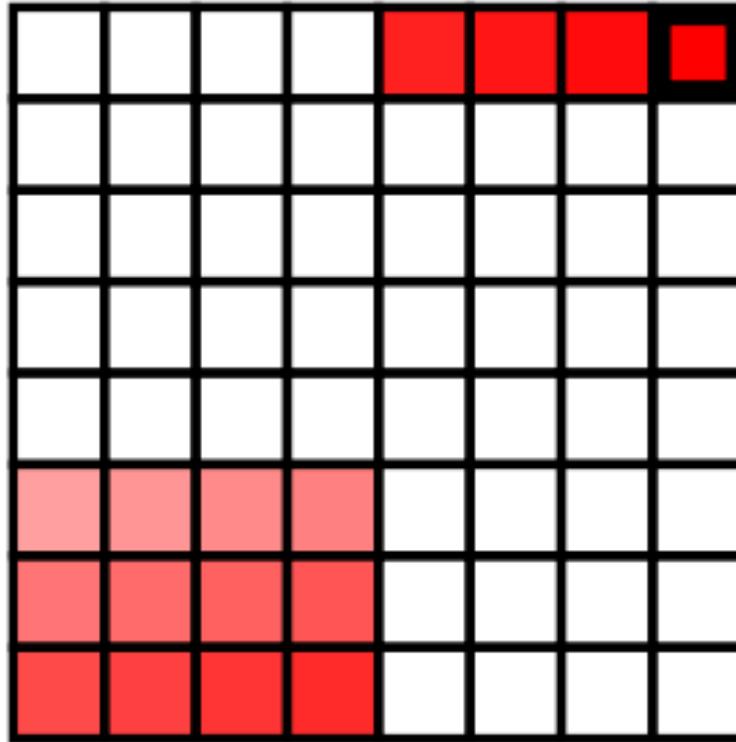


cache hits:630 ≈77%

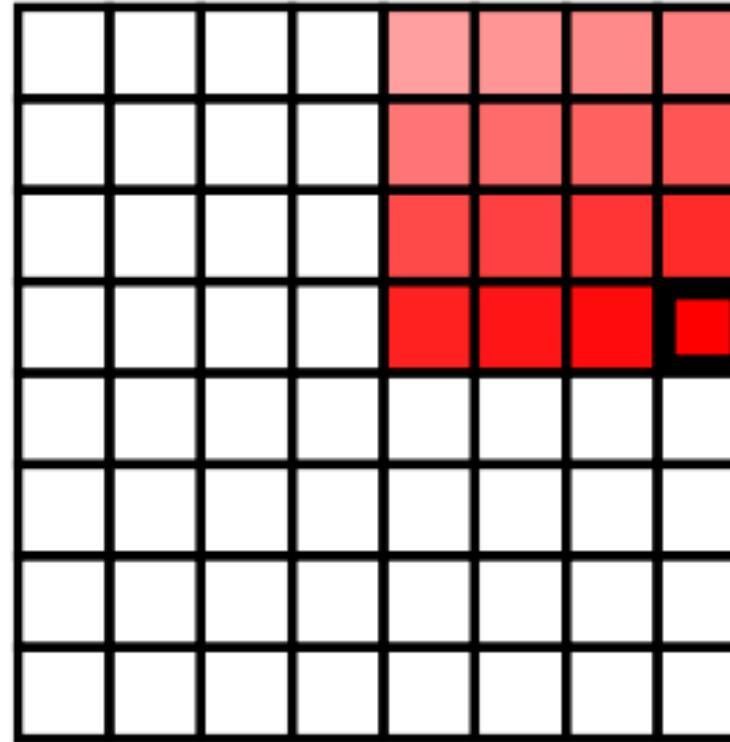
C mem:271 cache hit:203



A mem:272 cache hit:204



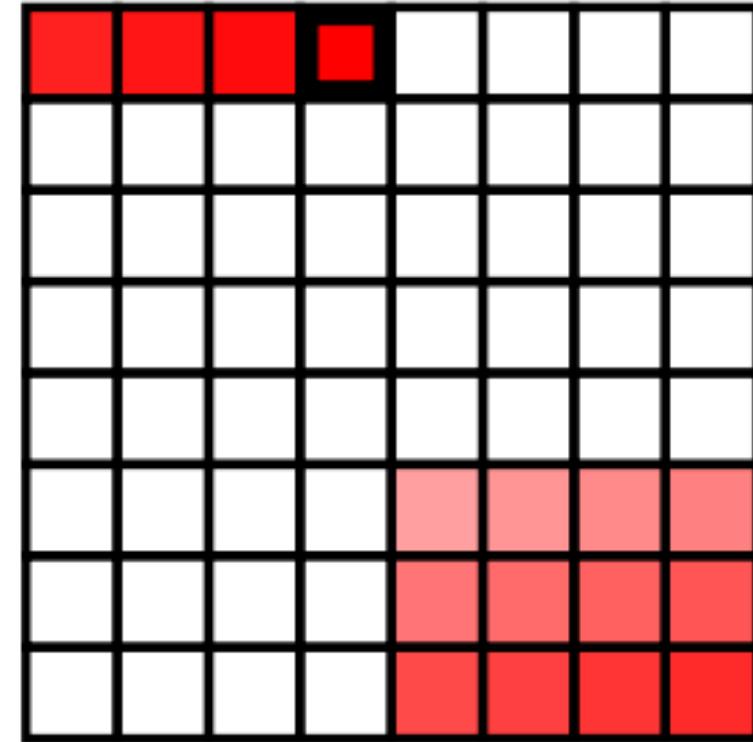
B mem:272 cache hit:224



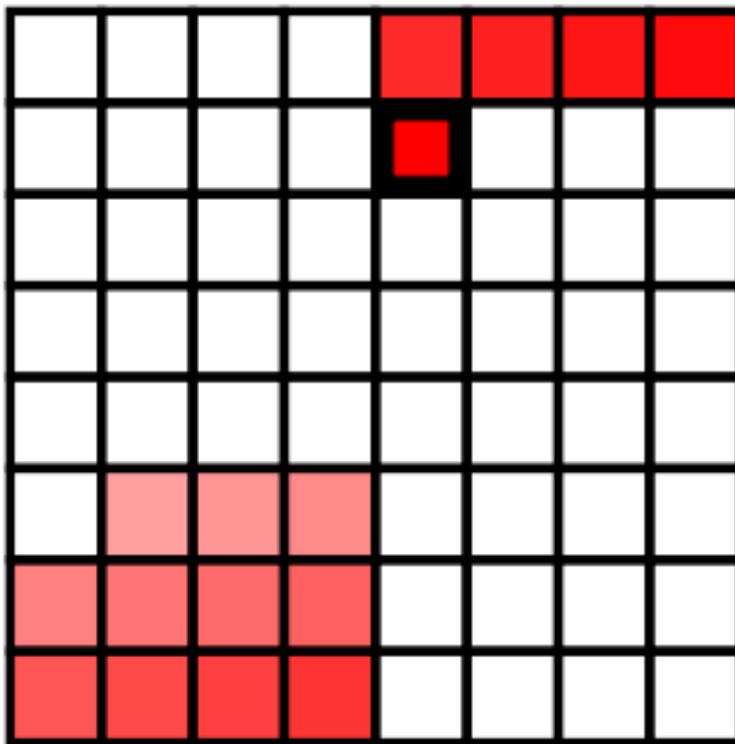
mem:816

cache hits:632 $\approx 77\%$

C mem:272 cache hit:204

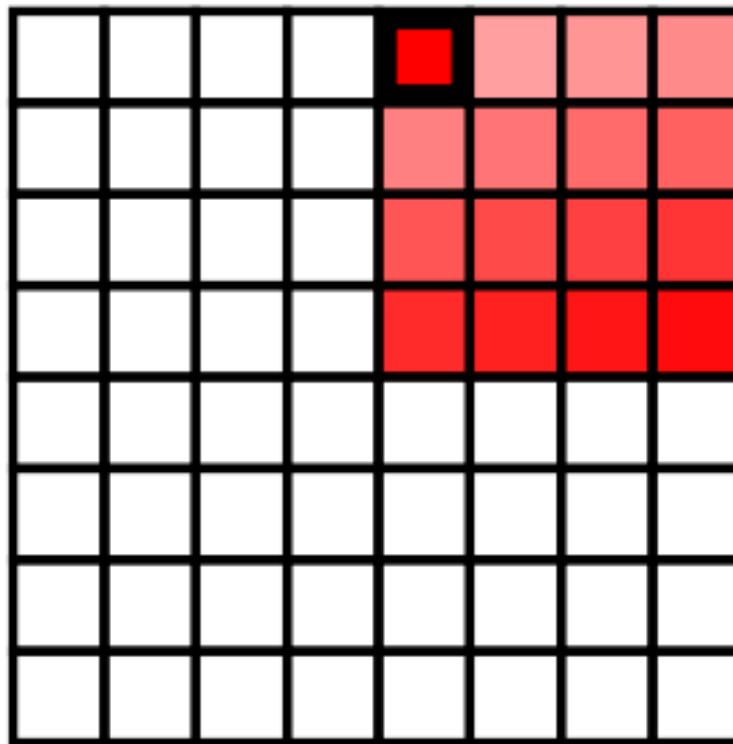


A mem:273 cache hit:204



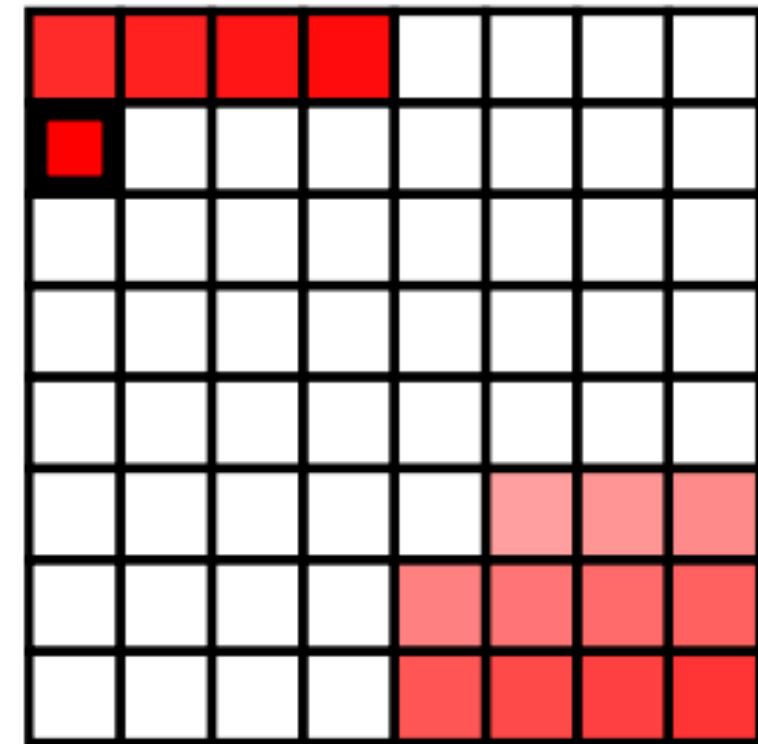
mem:819

B mem:273 cache hit:225

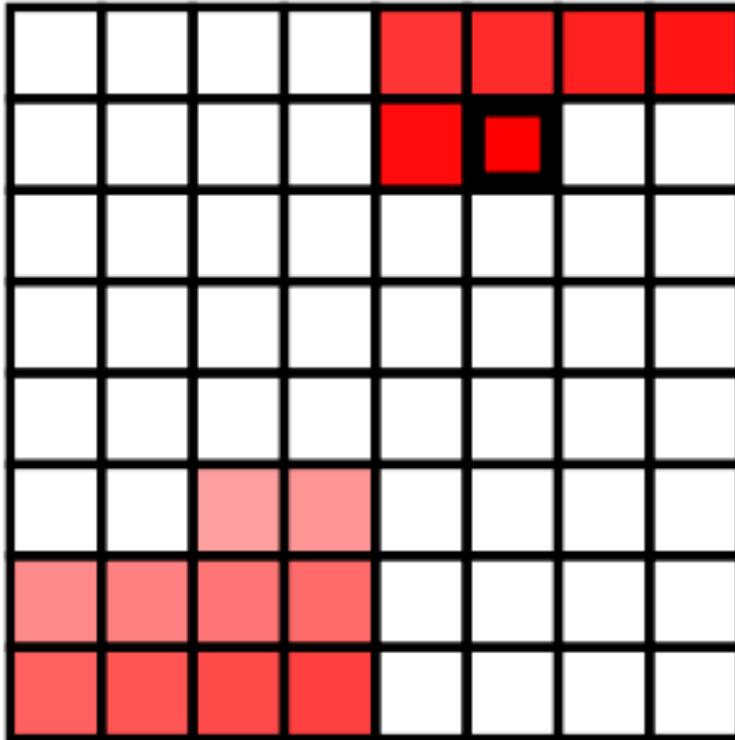


cache hits:633 ≈77%

C mem:273 cache hit:204

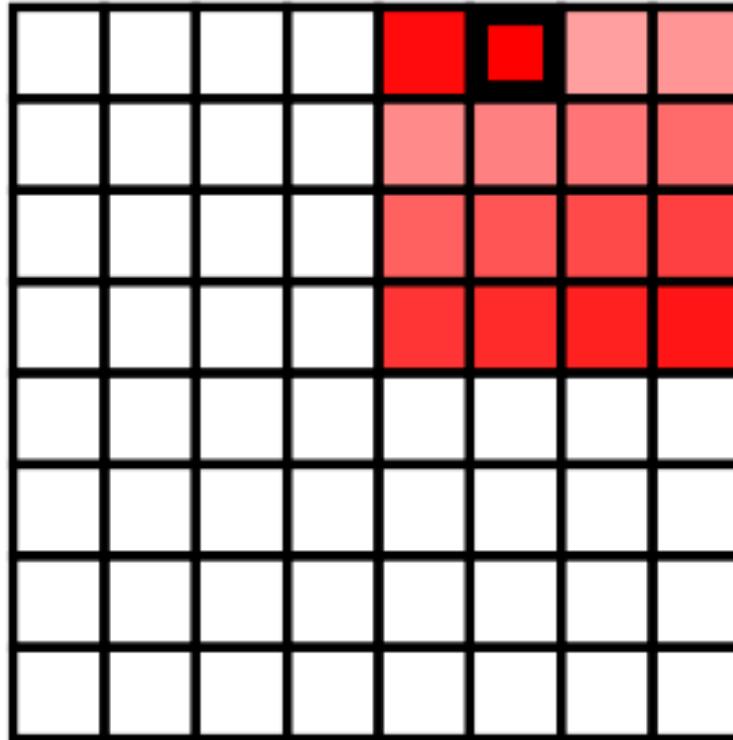


A mem:274 cache hit:204



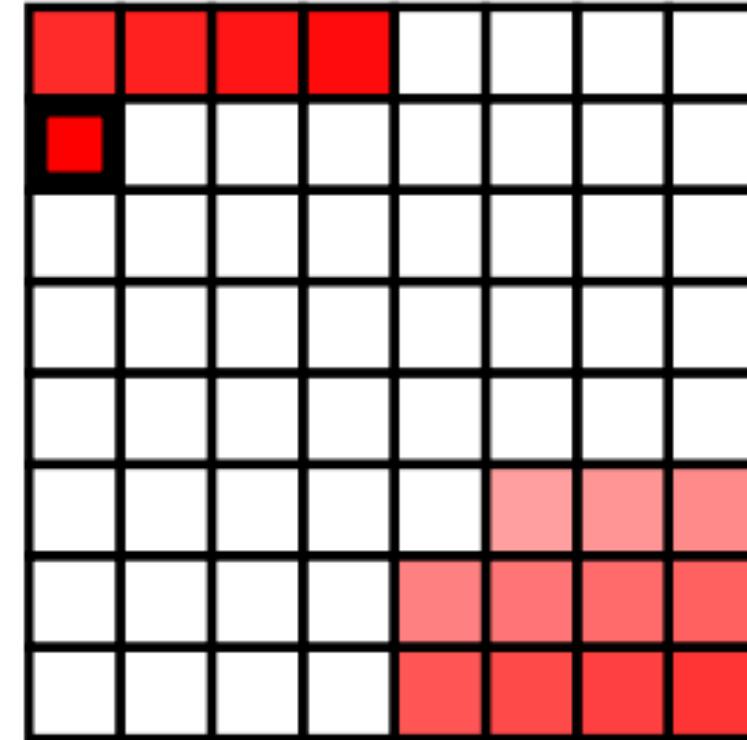
mem:822

B mem:274 cache hit:226

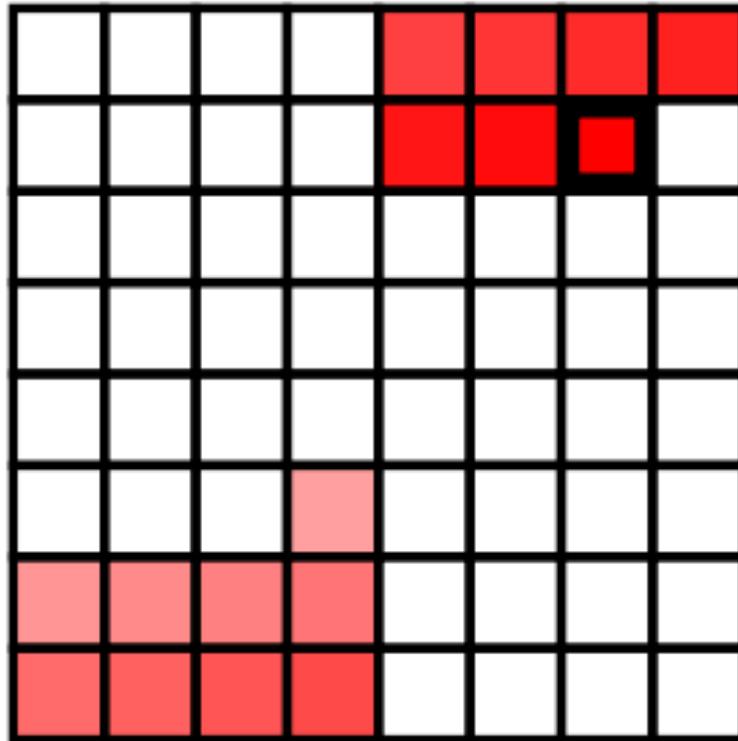


cache hits:635 ≈77%

C mem:274 cache hit:205

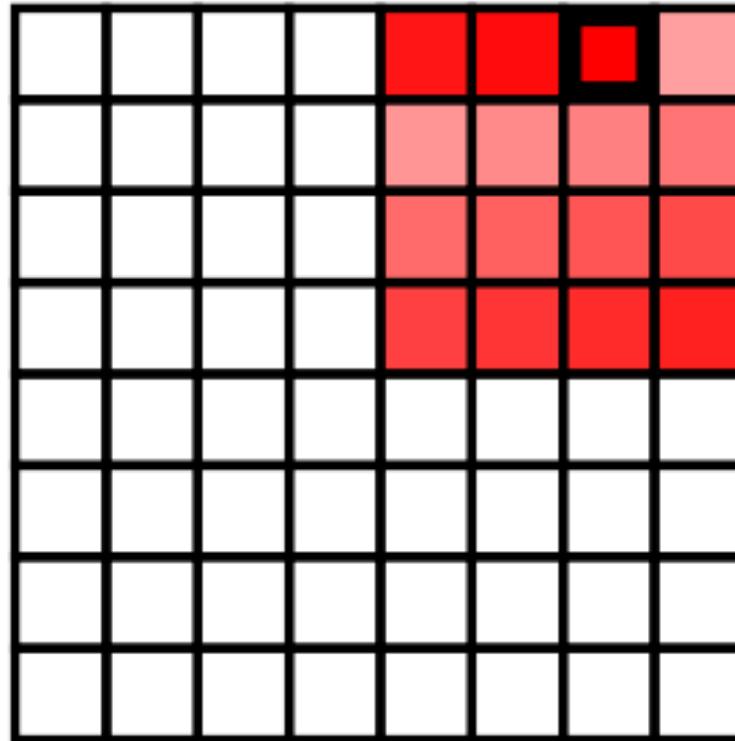


A mem:275 cache hit:204



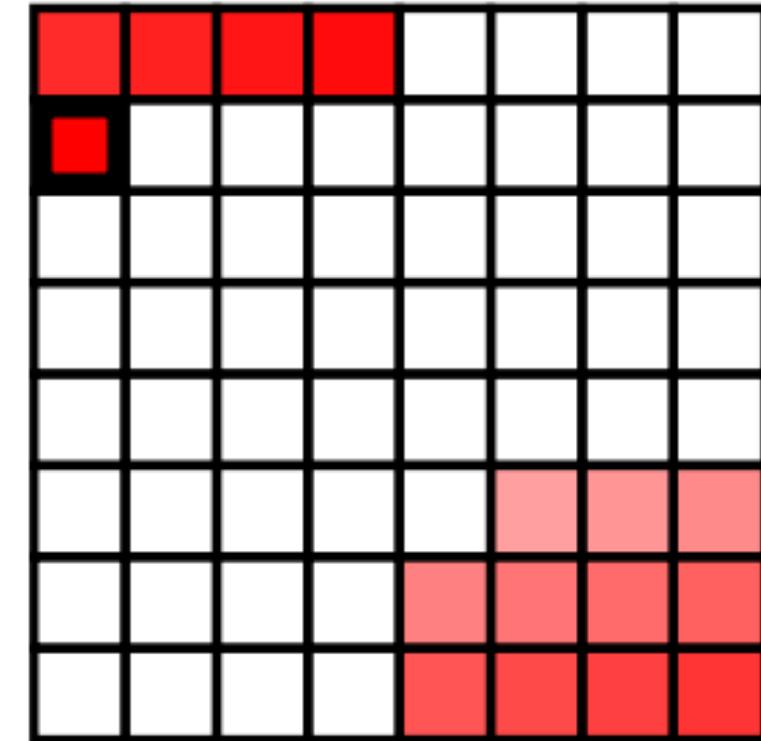
mem:825

B mem:275 cache hit:227

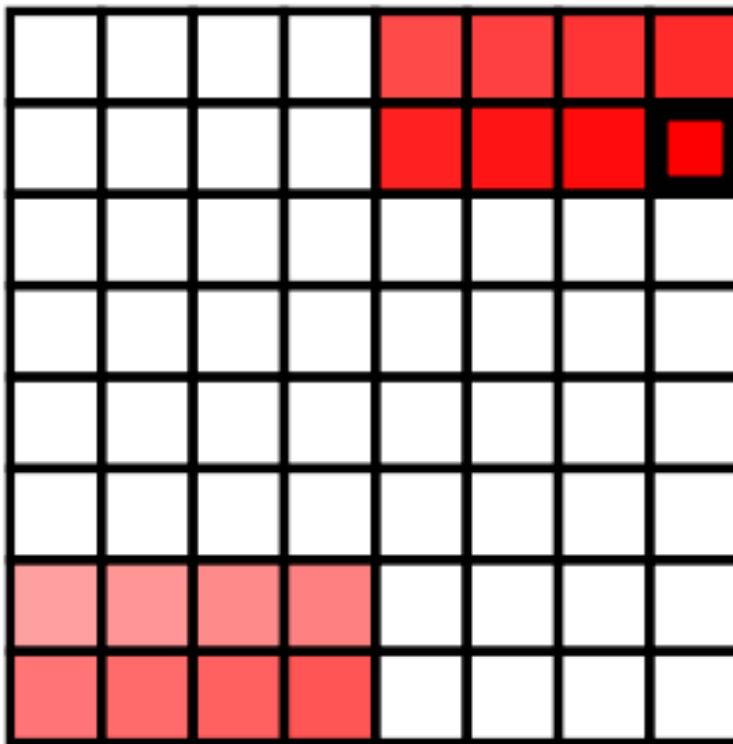


cache hits:637 ≈77%

C mem:275 cache hit:206

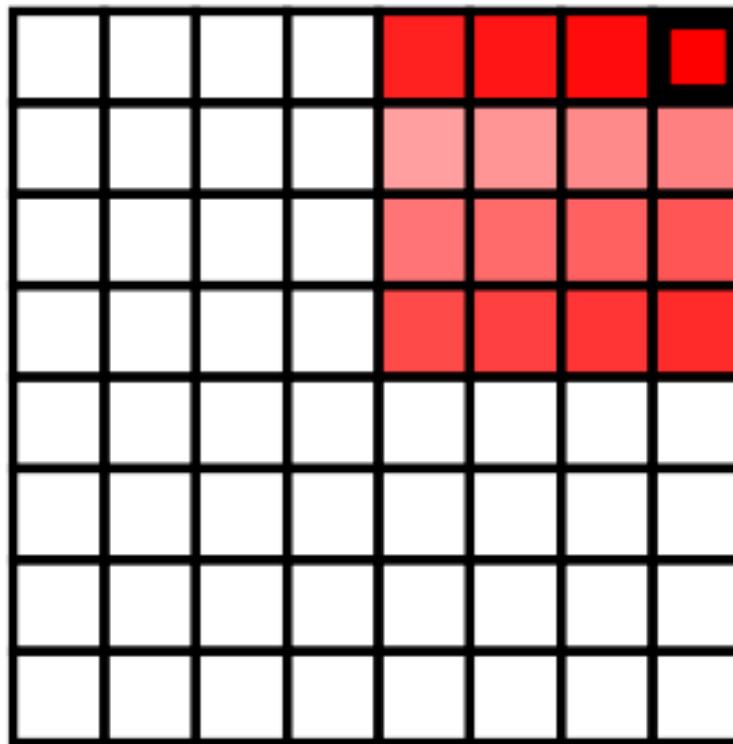


A mem:276 cache hit:204



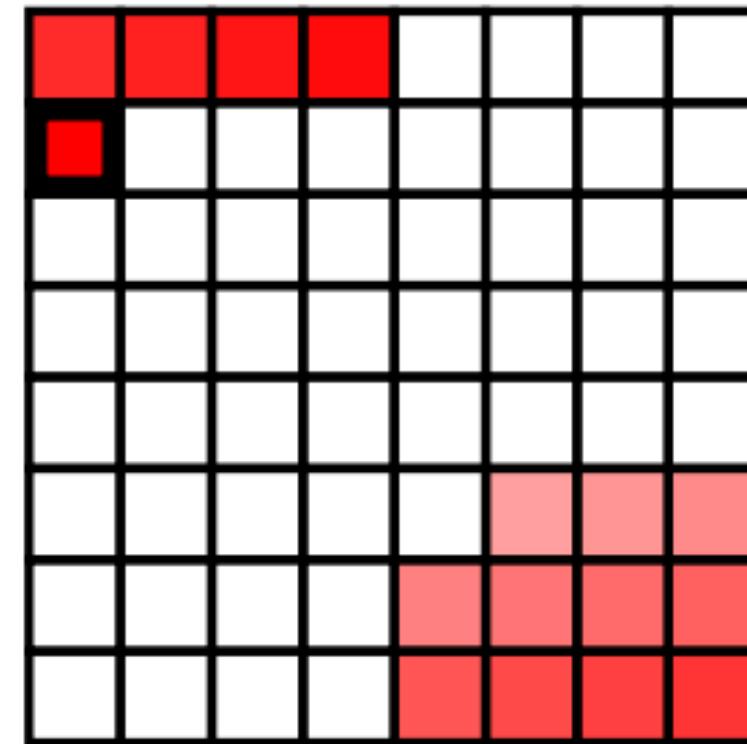
mem:828

B mem:276 cache hit:228

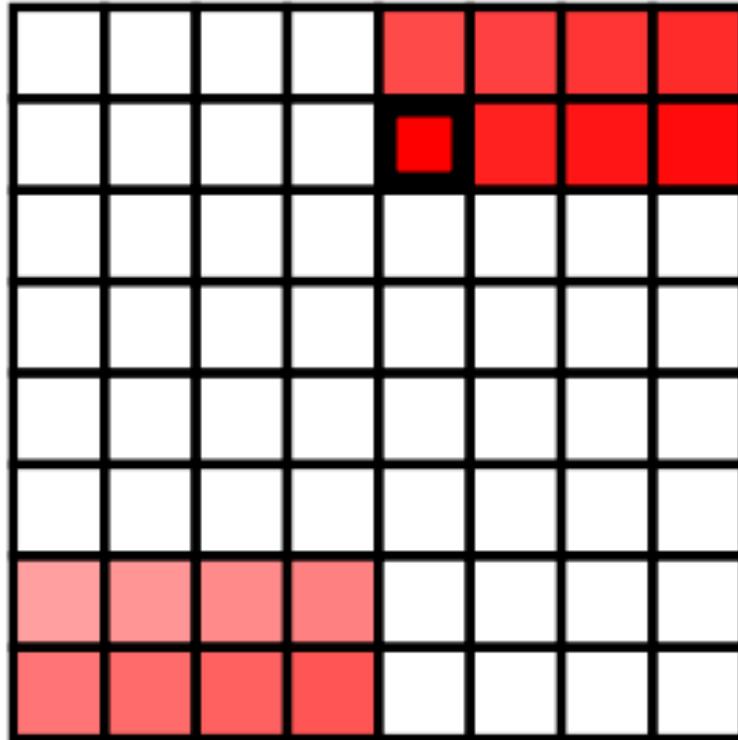


cache hits:639 ≈77%

C mem:276 cache hit:207

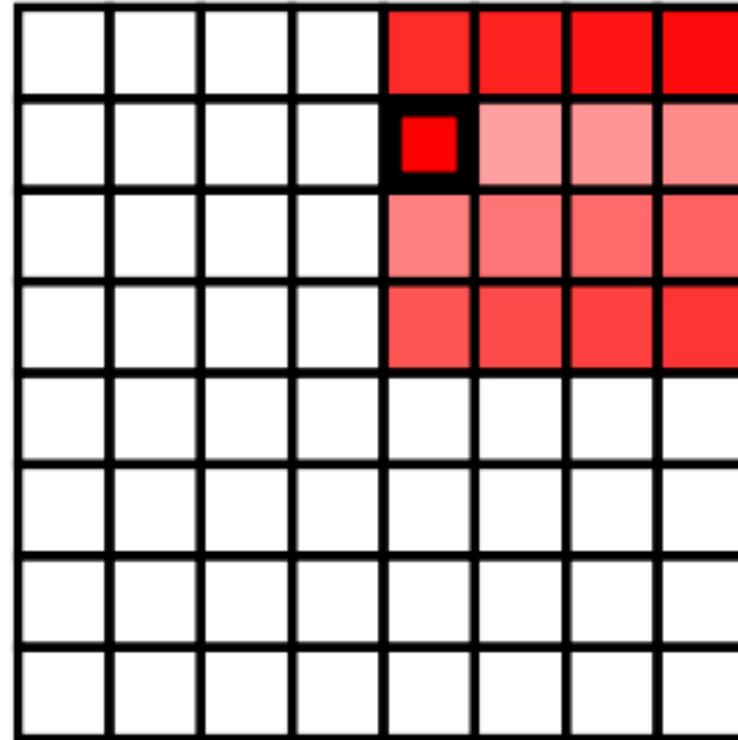


A mem:277 cache hit:205



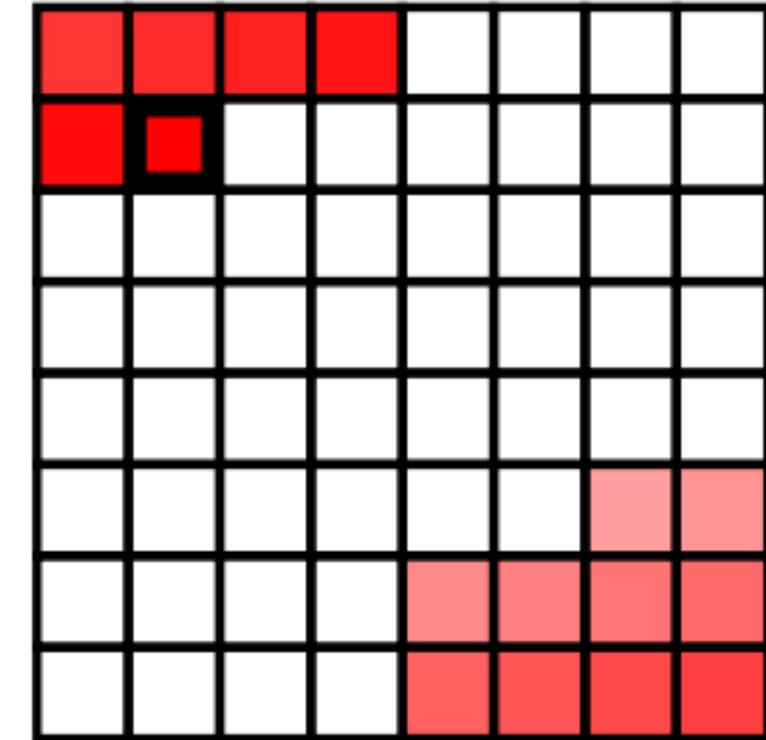
mem:831

B mem:277 cache hit:229

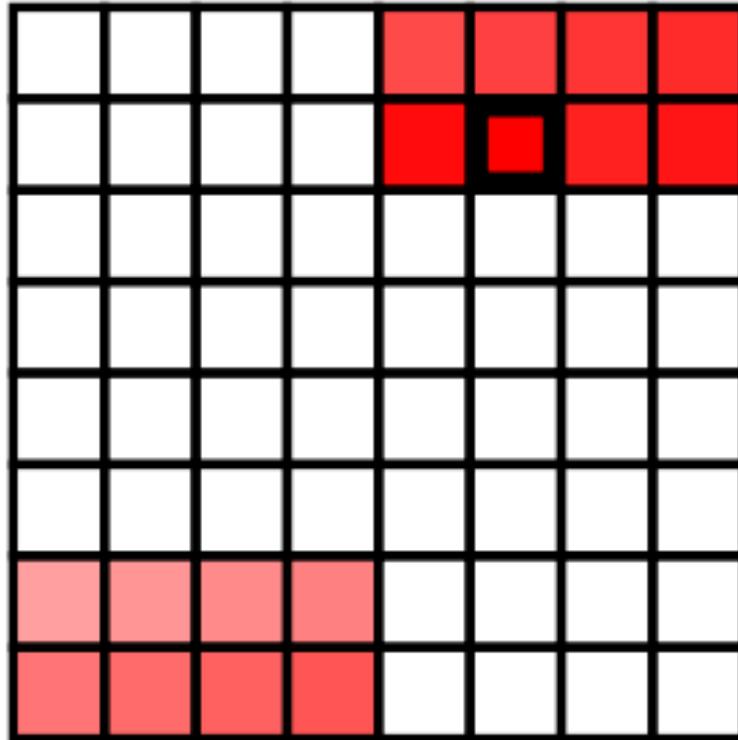


cache hits:641 ≈77%

C mem:277 cache hit:207

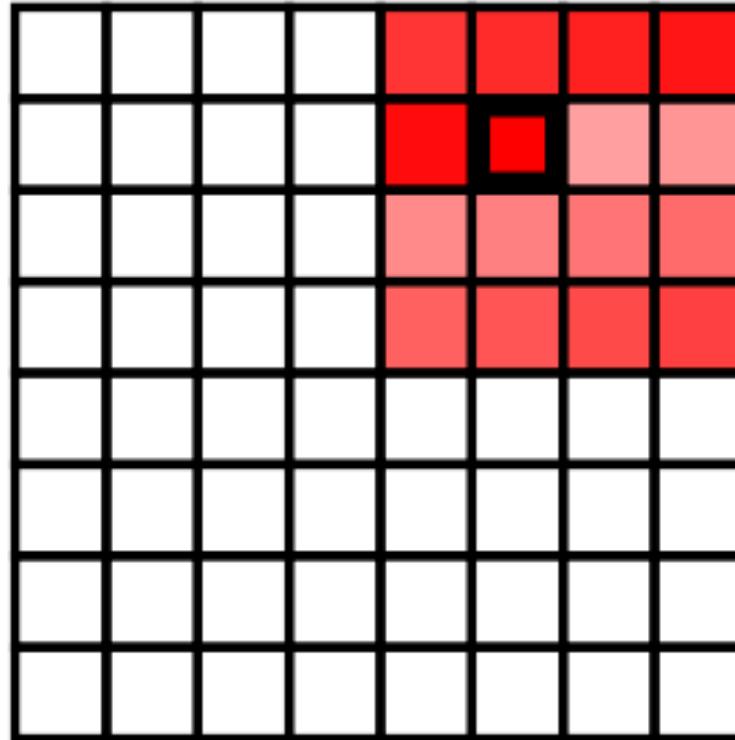


A mem:278 cache hit:206



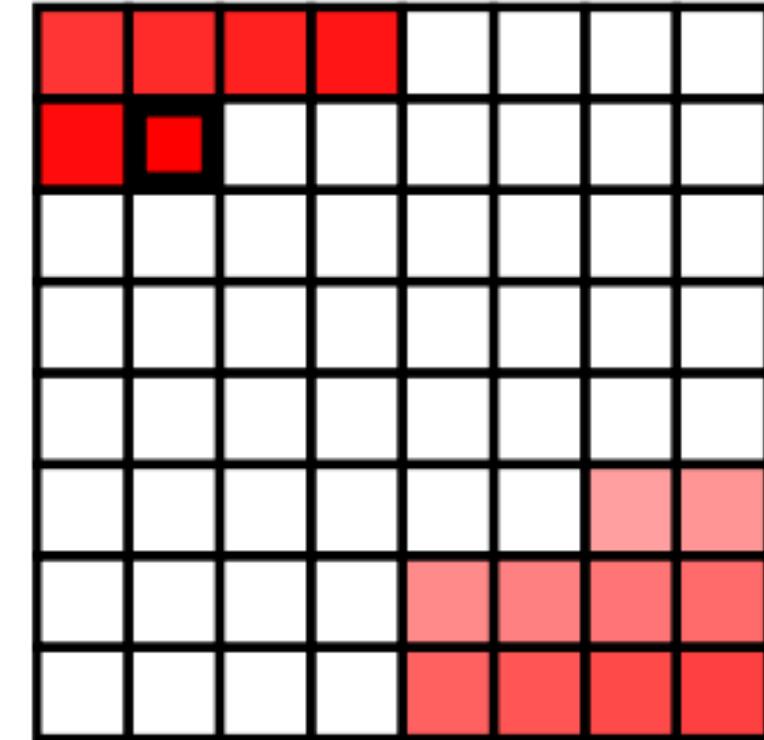
mem:834

B mem:278 cache hit:230

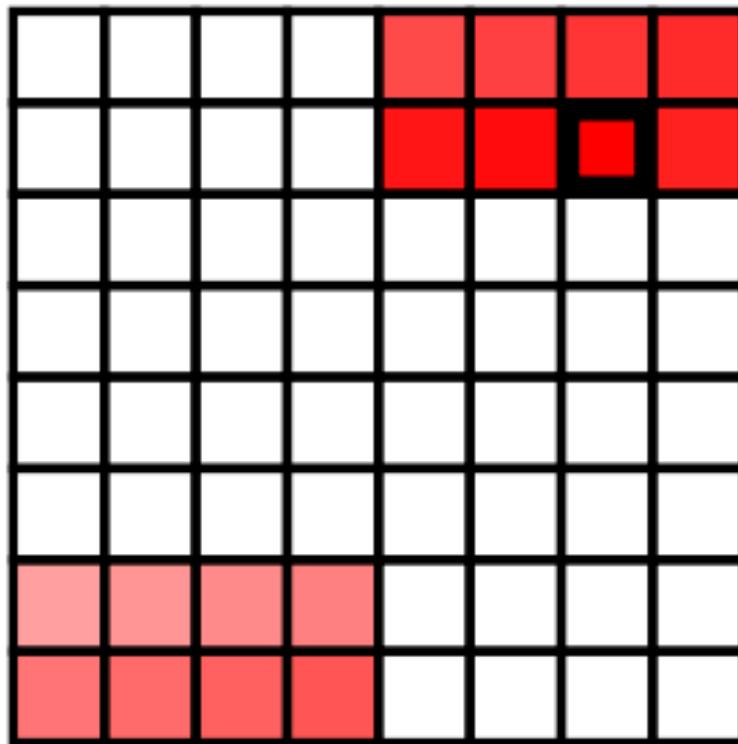


cache hits:644 ≈77%

C mem:278 cache hit:208

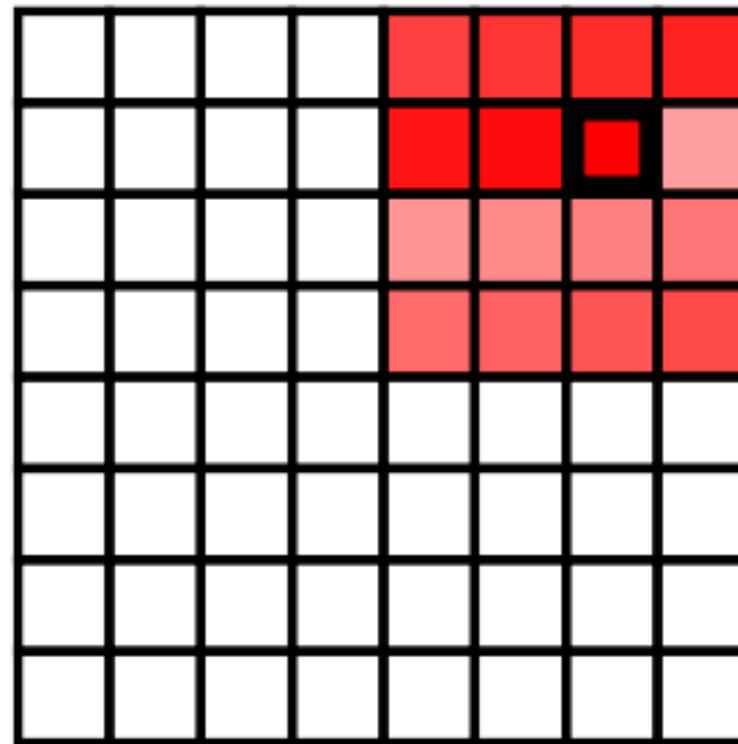


A mem:279 cache hit:207



mem:837

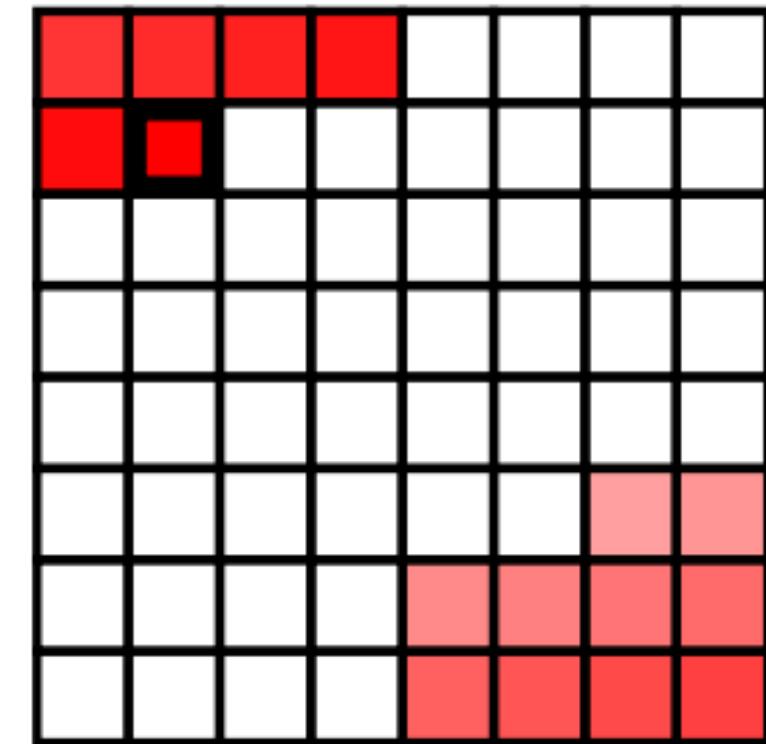
B mem:279 cache hit:231



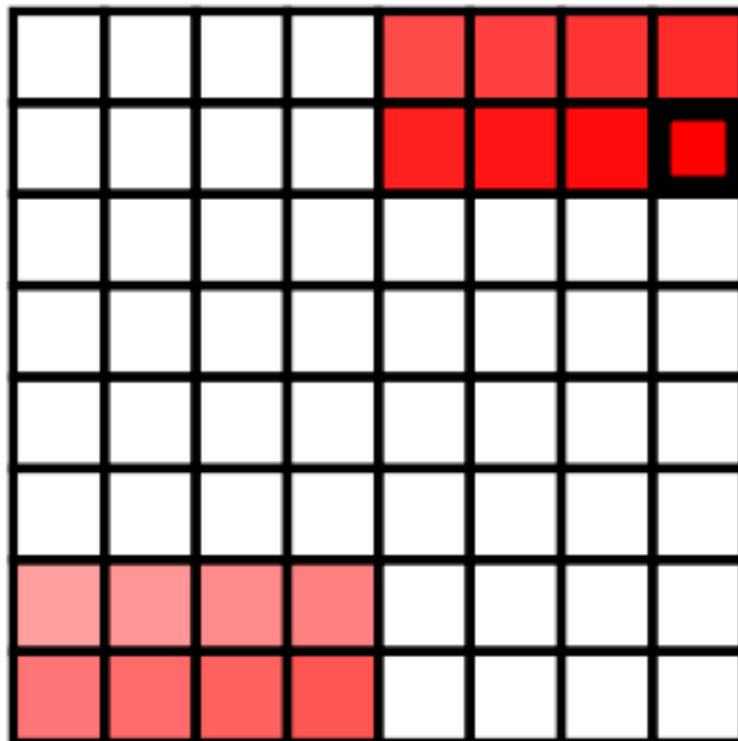
×

cache hits:647 ≈77%

C mem:279 cache hit:209

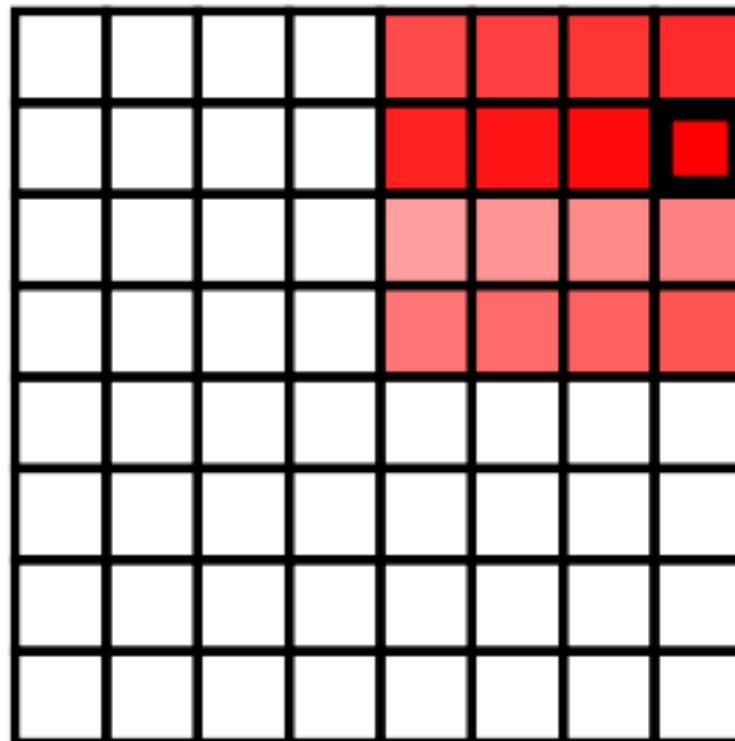


A mem:280 cache hit:208



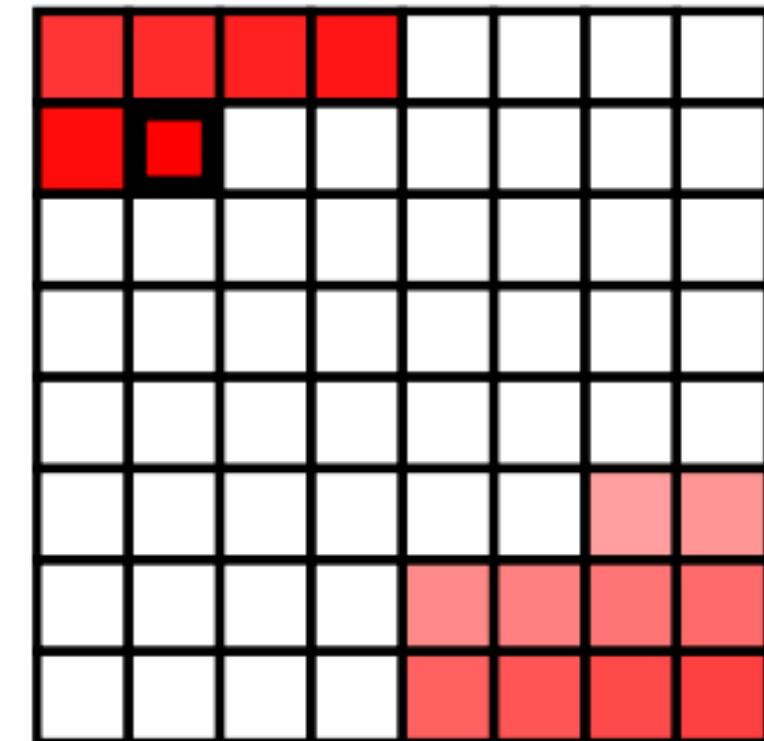
mem:840

B mem:280 cache hit:232

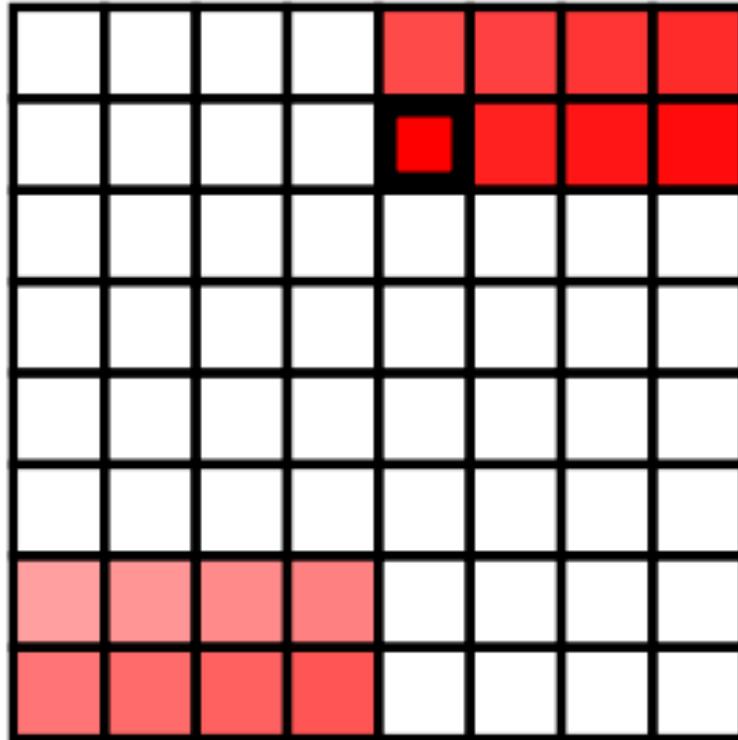


cache hits:650 $\approx 77\%$

C mem:280 cache hit:210

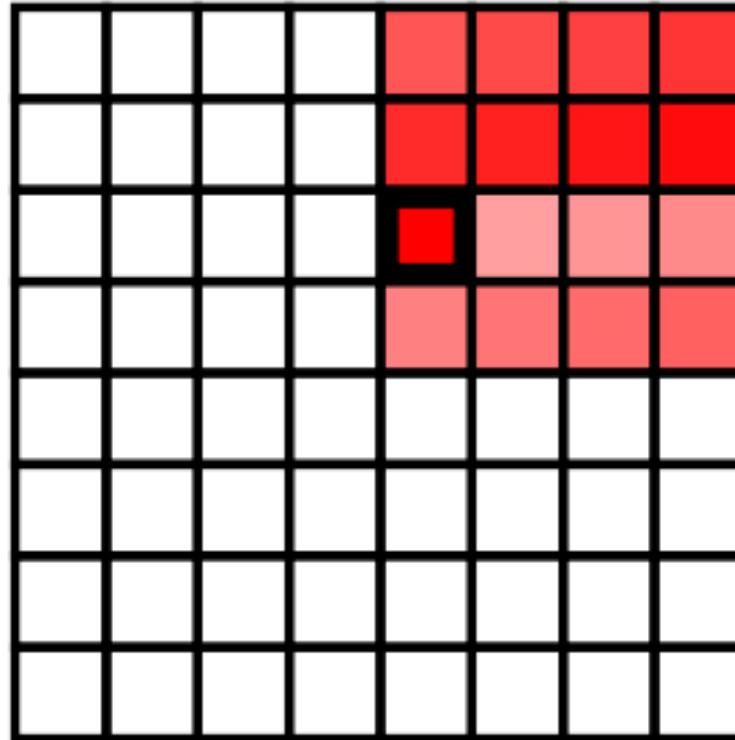


A mem:281 cache hit:209



mem:843

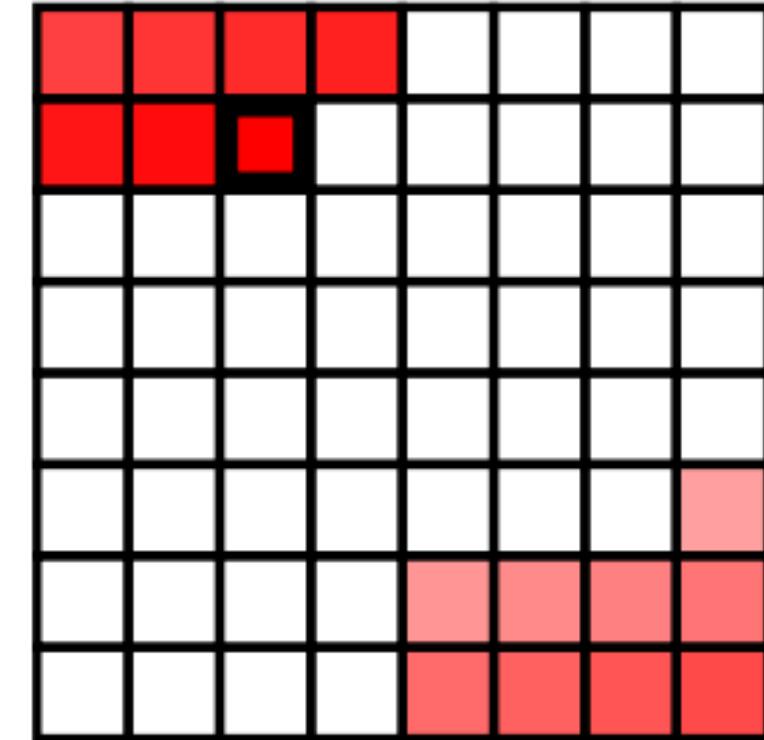
B mem:281 cache hit:233



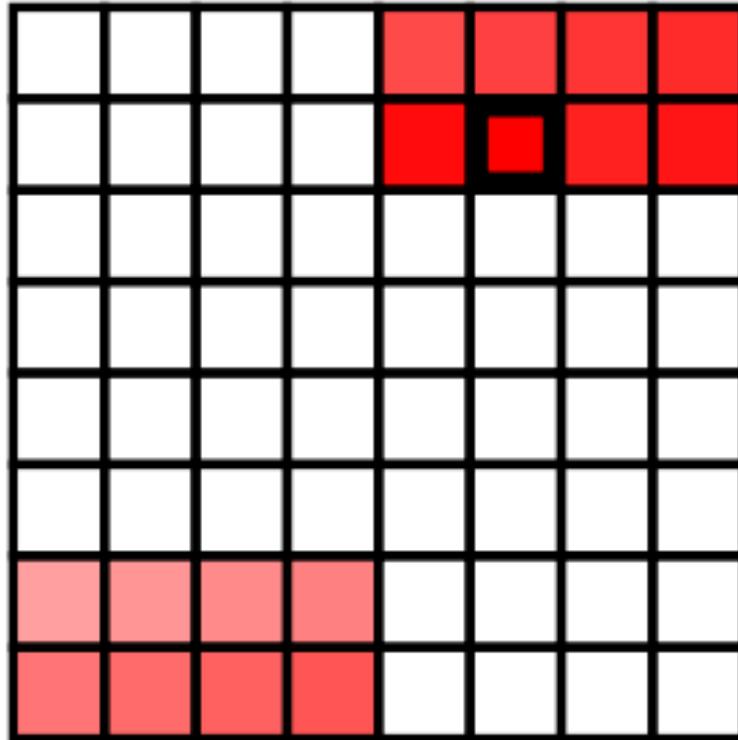
×

cache hits:652 ≈77%

C mem:281 cache hit:210

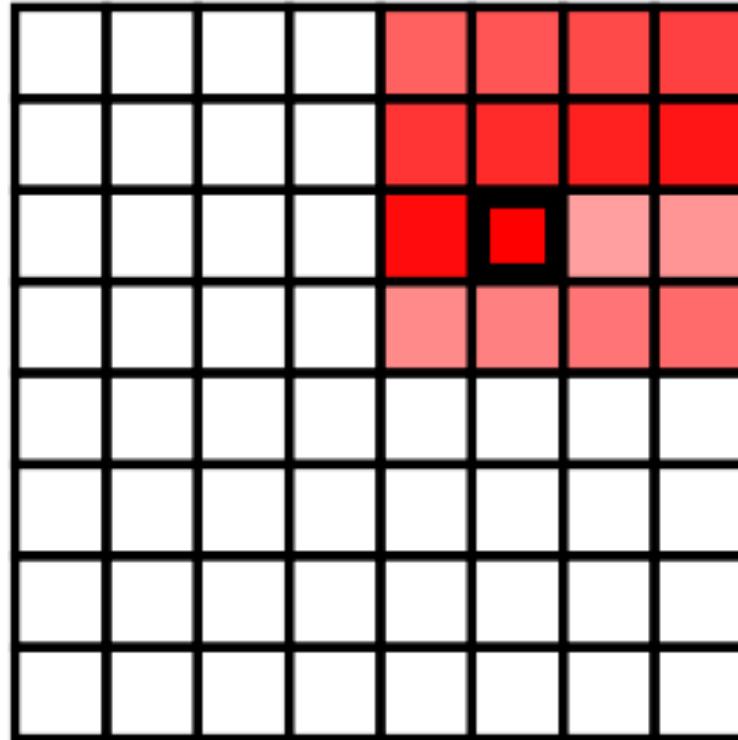


A mem:282 cache hit:210



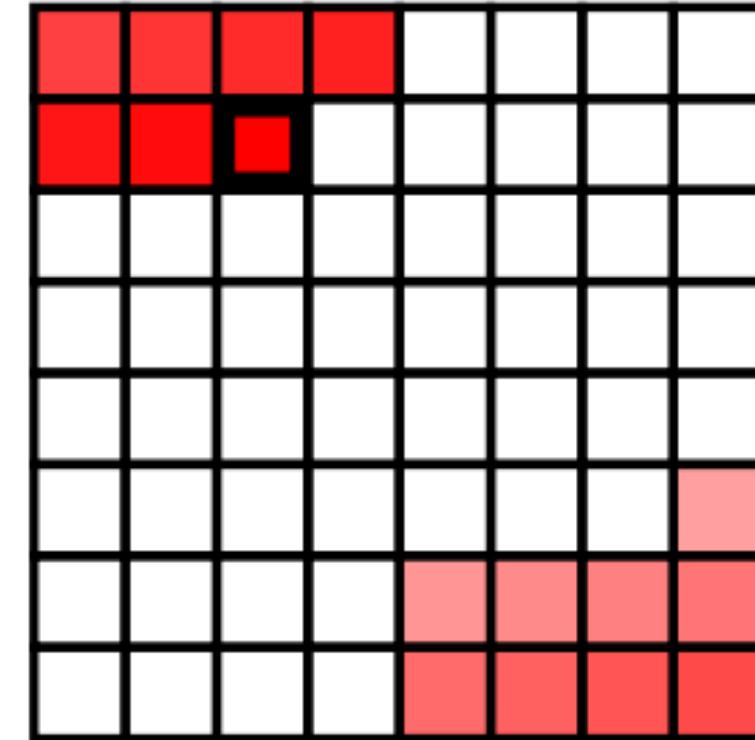
mem:846

B mem:282 cache hit:234

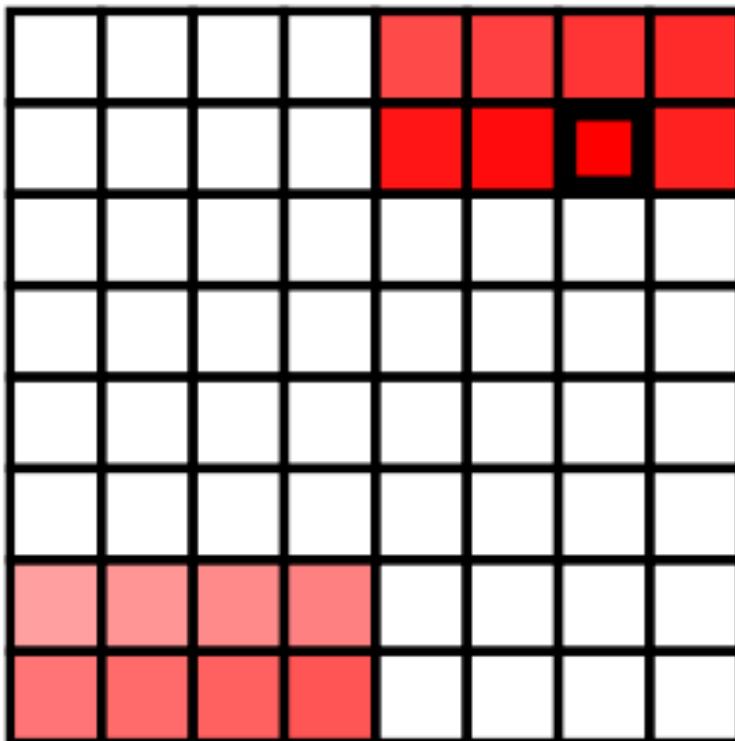


cache hits:655 ≈77%

C mem:282 cache hit:211

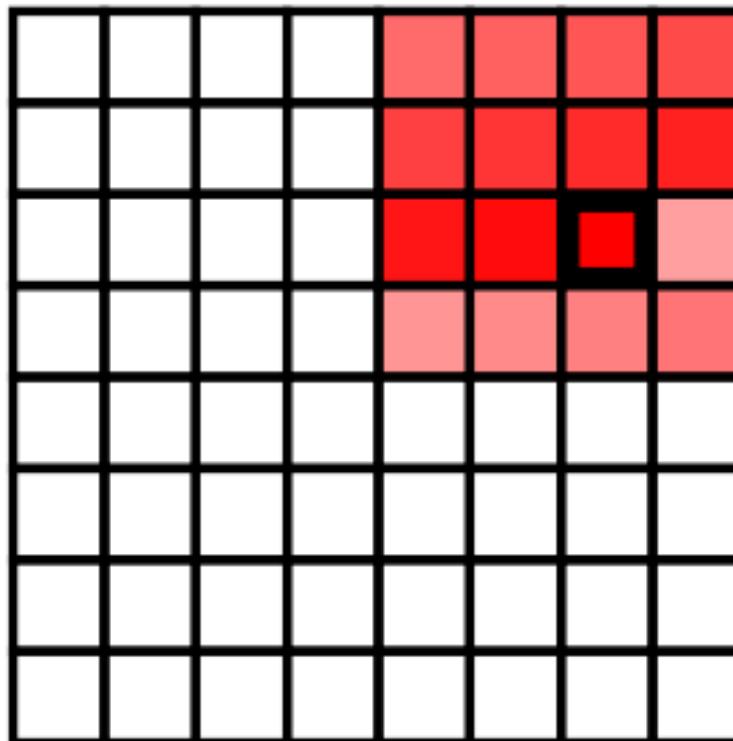


A mem:283 cache hit:211



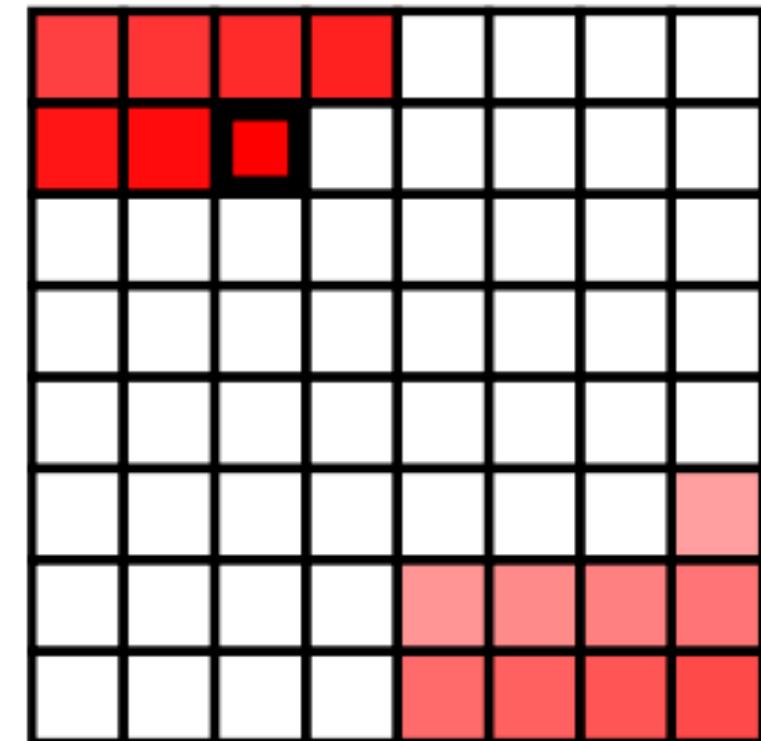
mem:849

B mem:283 cache hit:235

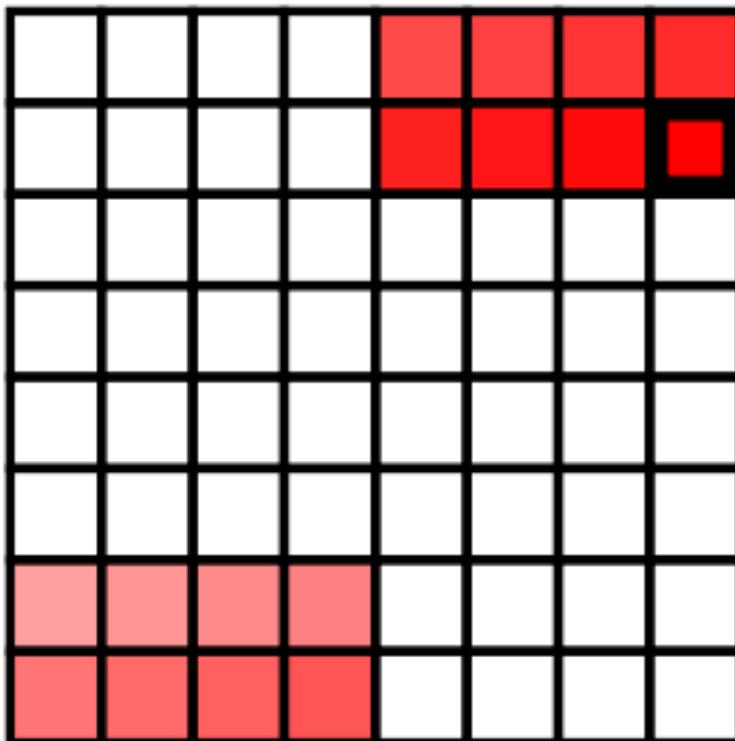


cache hits:658 ≈77%

C mem:283 cache hit:212

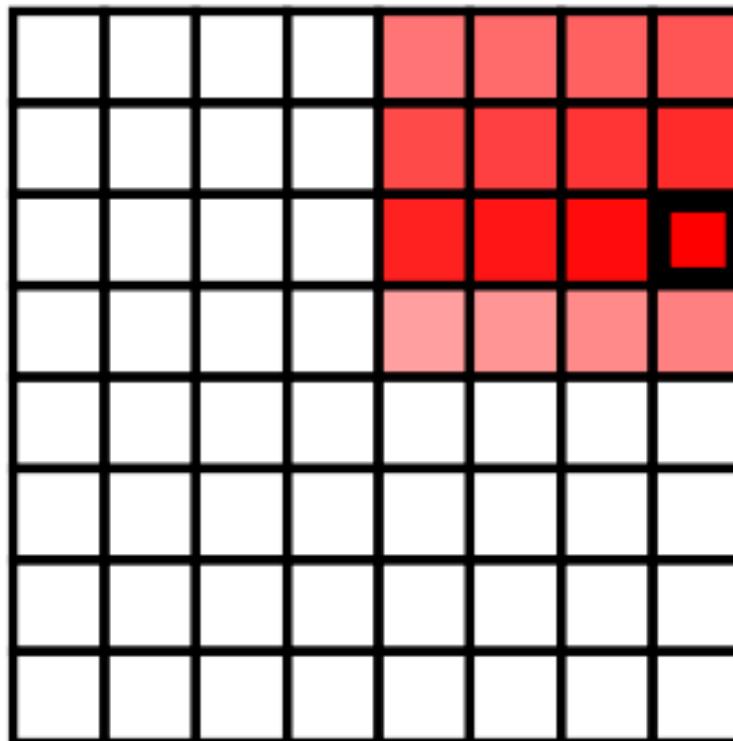


A mem:284 cache hit:212



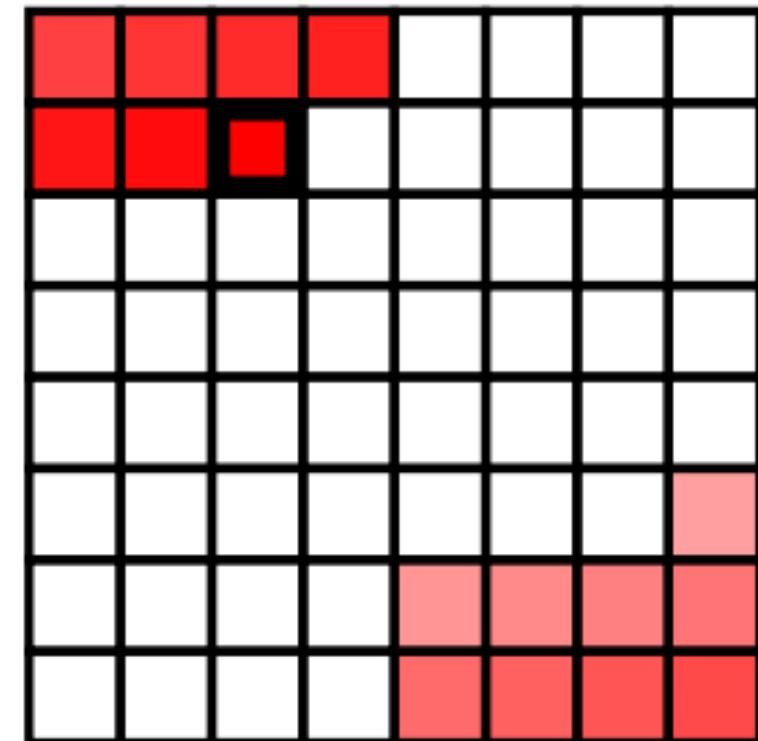
mem:852

B mem:284 cache hit:236

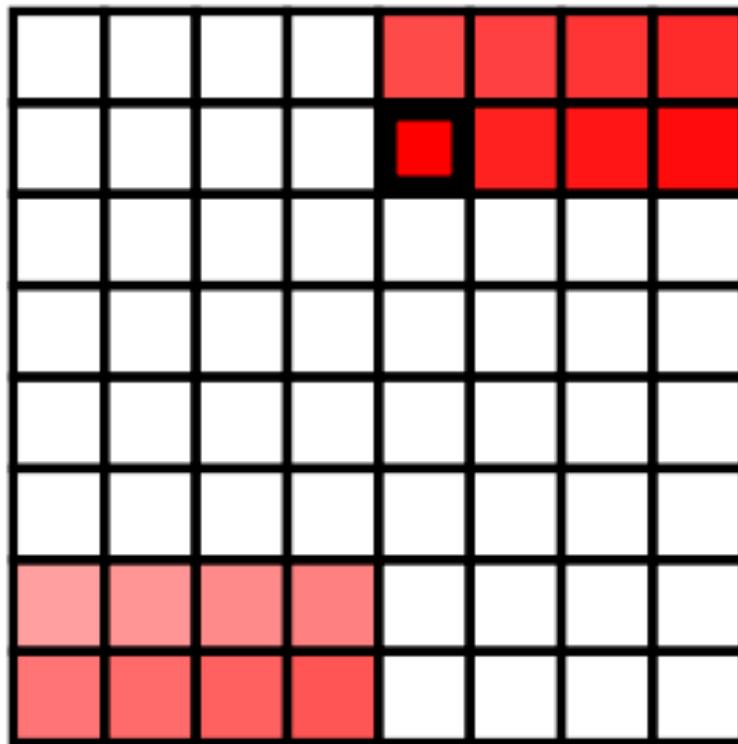


cache hits:661 $\approx 77\%$

C mem:284 cache hit:213

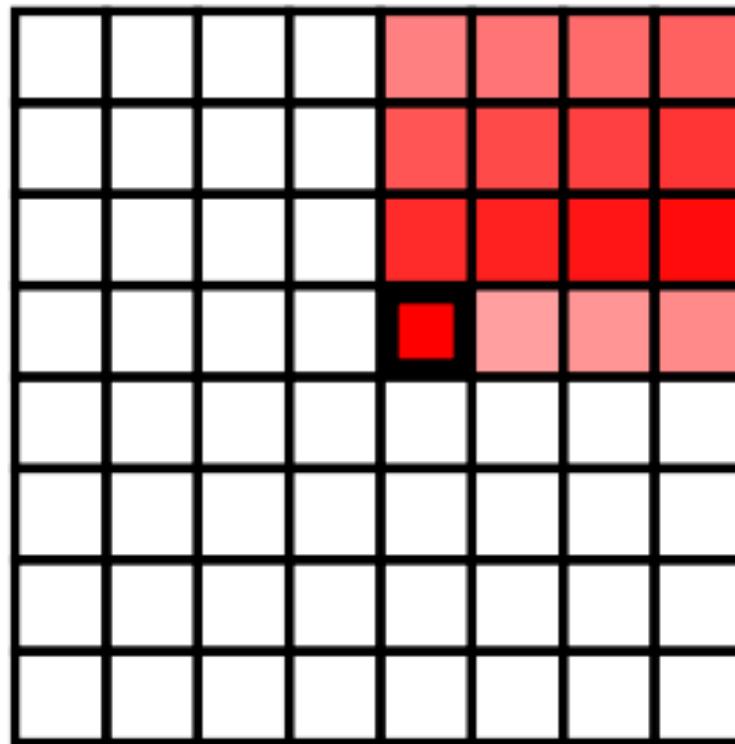


A mem:285 cache hit:213



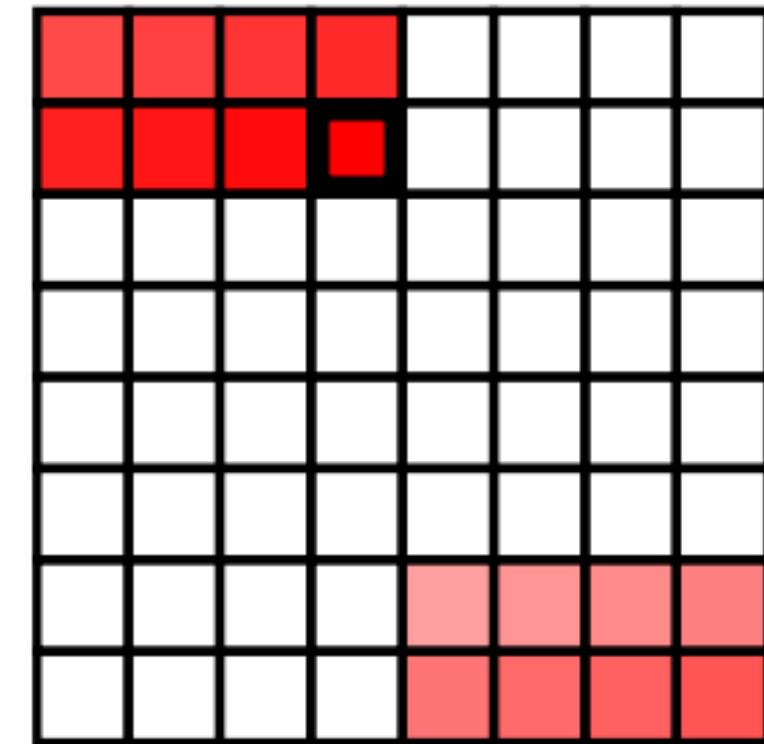
mem:855

B mem:285 cache hit:237

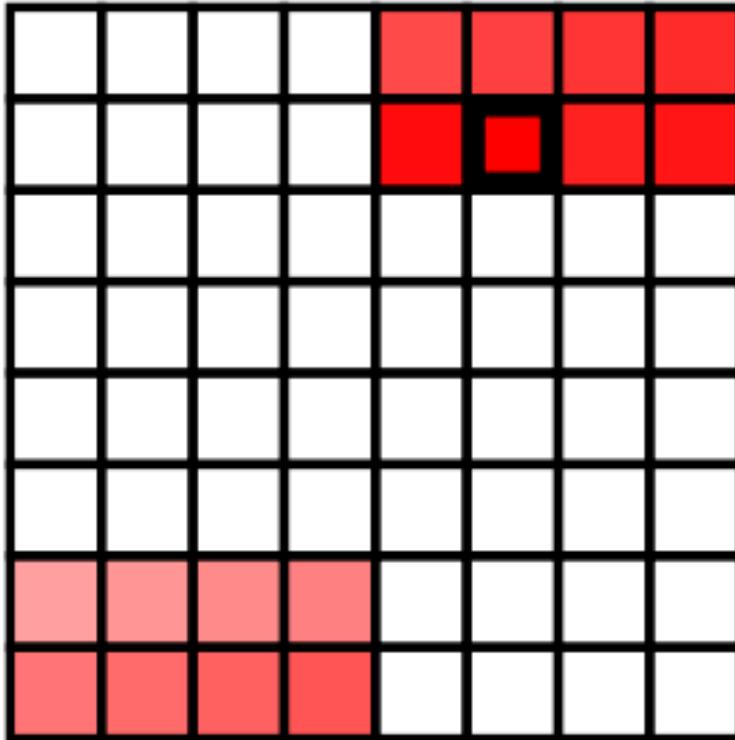


cache hits:663 ≈77%

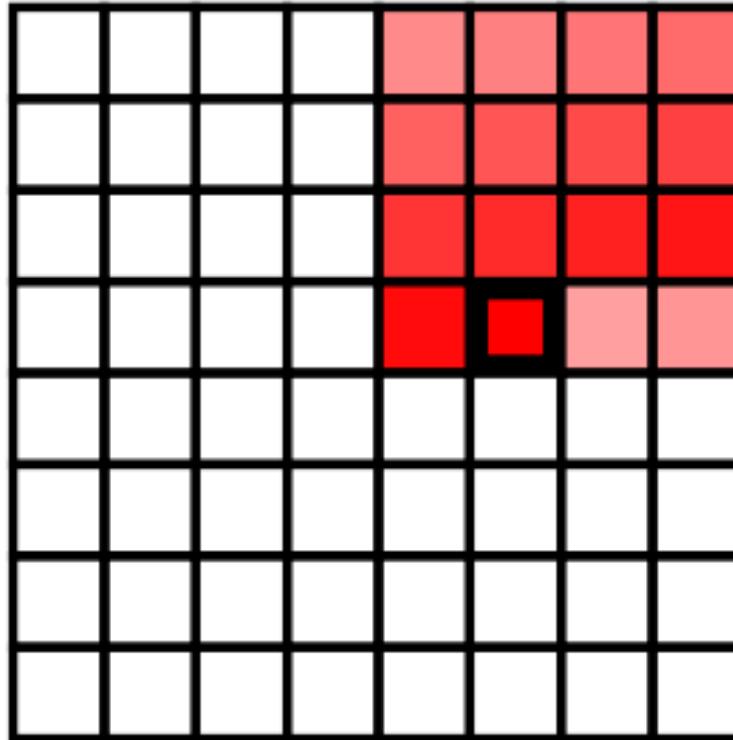
C mem:285 cache hit:213



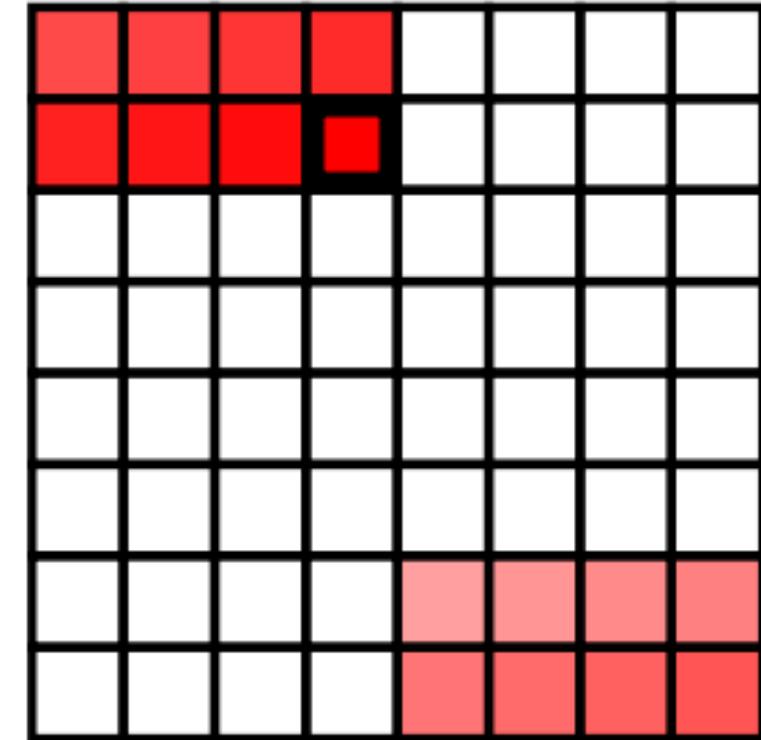
A mem:286 cache hit:214



B mem:286 cache hit:238



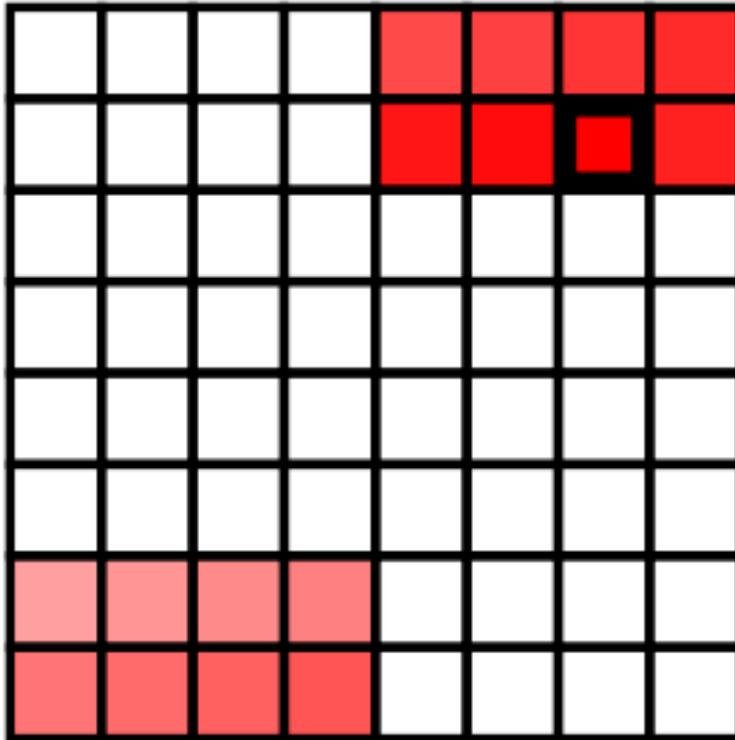
C mem:286 cache hit:214



mem:858

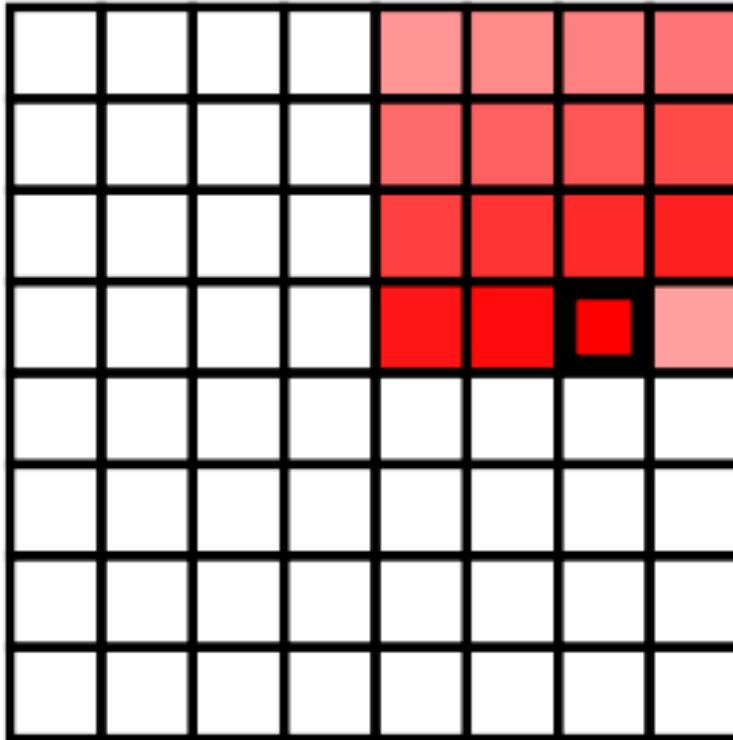
cache hits:666 $\approx 77\%$

A mem:287 cache hit:215



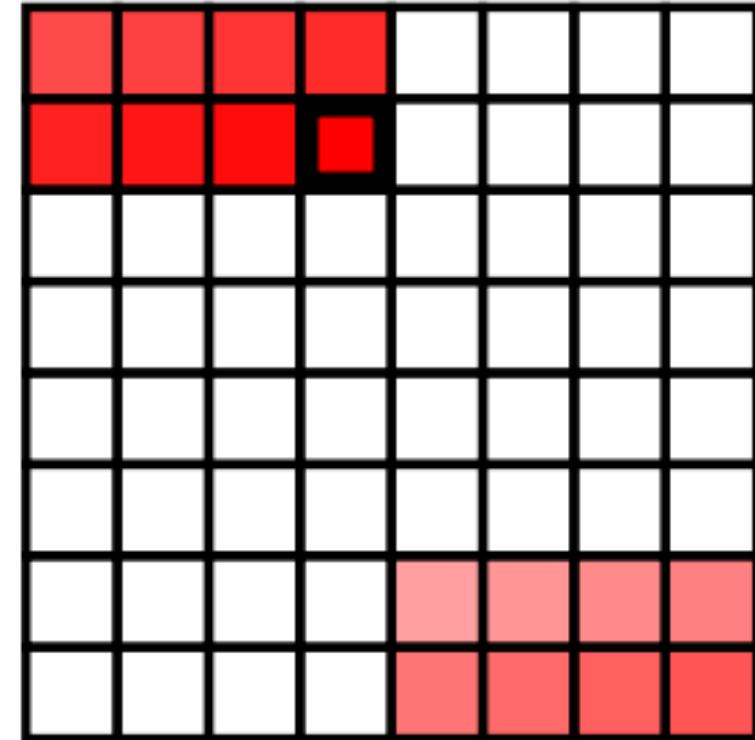
mem:861

B mem:287 cache hit:239

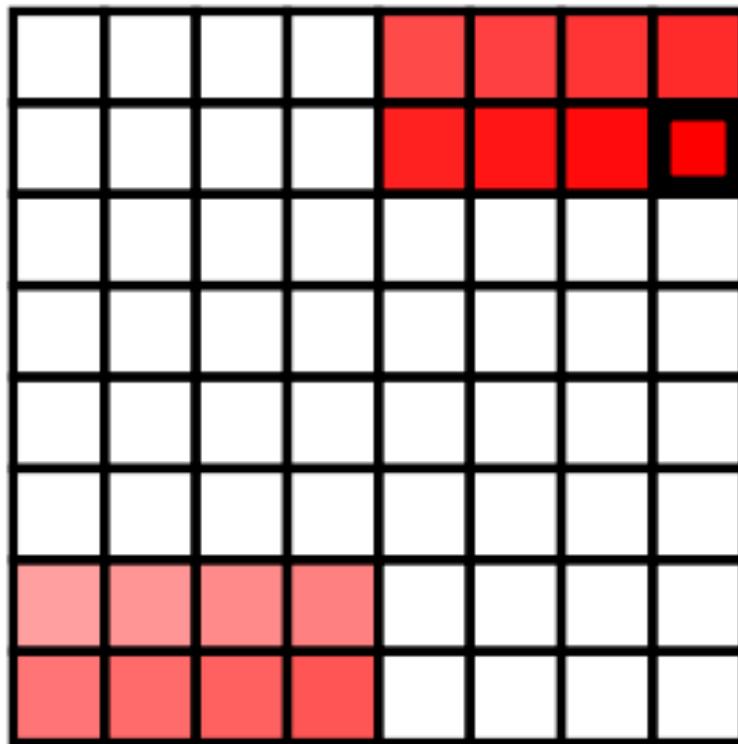


cache hits:669 ≈77%

C mem:287 cache hit:215

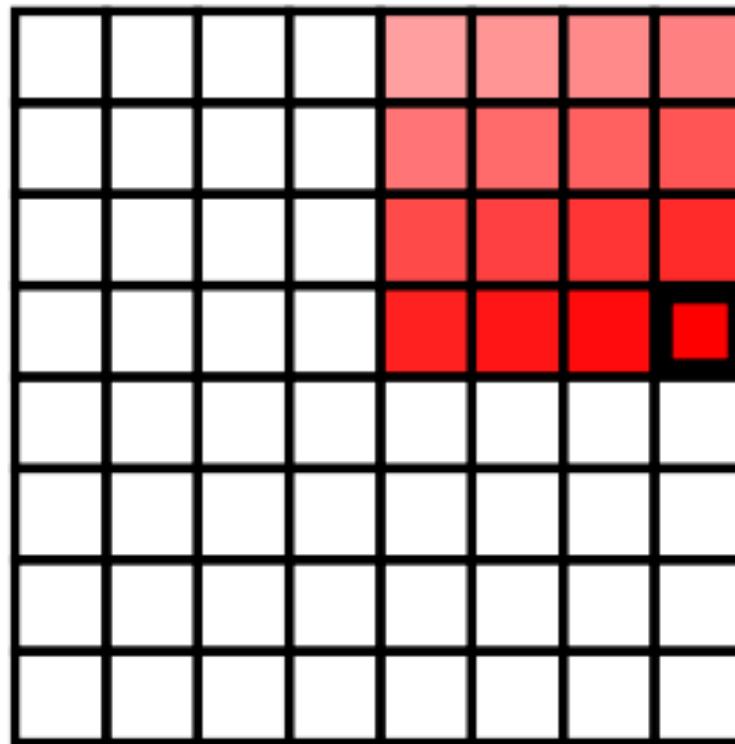


A mem:288 cache hit:216



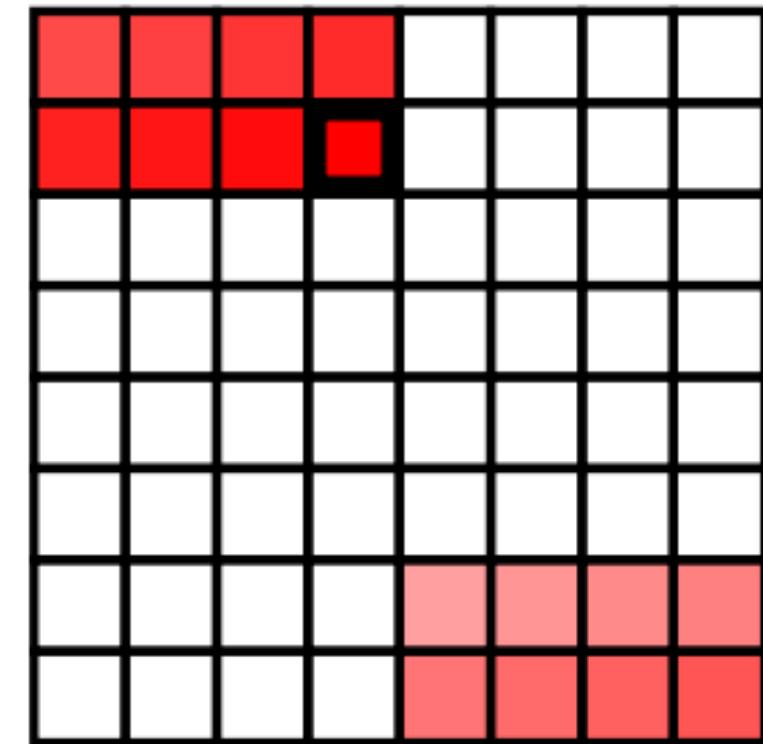
mem:864

B mem:288 cache hit:240

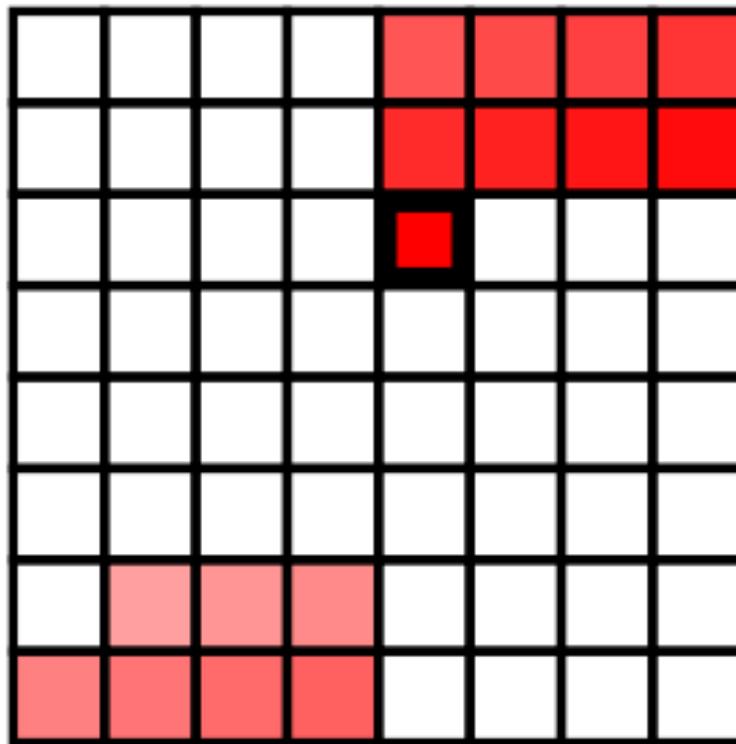


cache hits:672 $\approx 77\%$

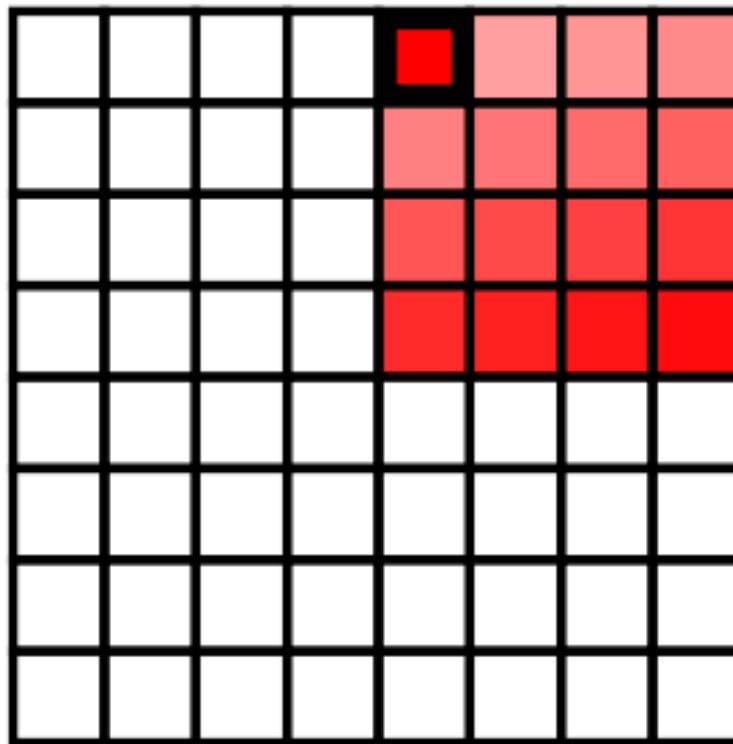
C mem:288 cache hit:216



A mem:289 cache hit:216



B mem:289 cache hit:241

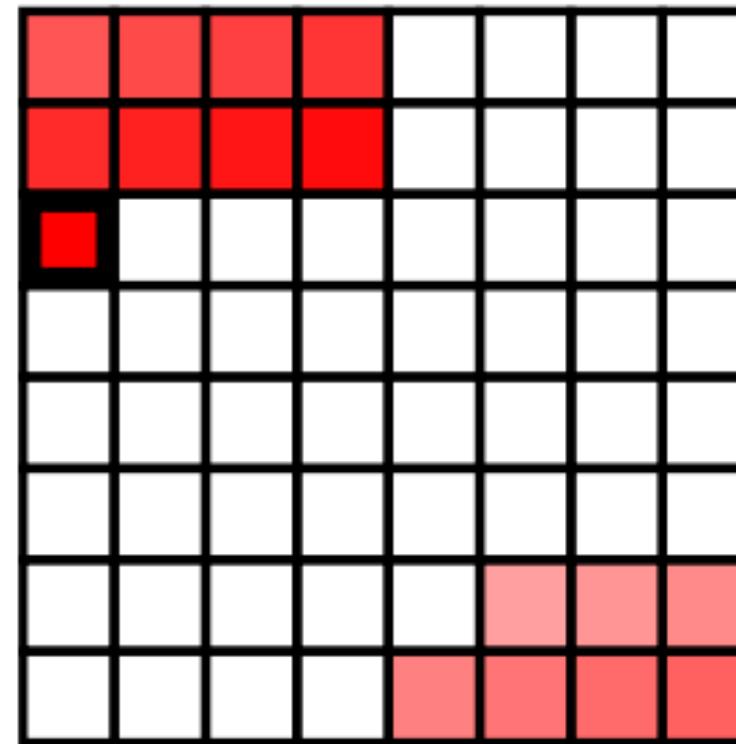


mem:867

cache hits:673 $\approx 77\%$

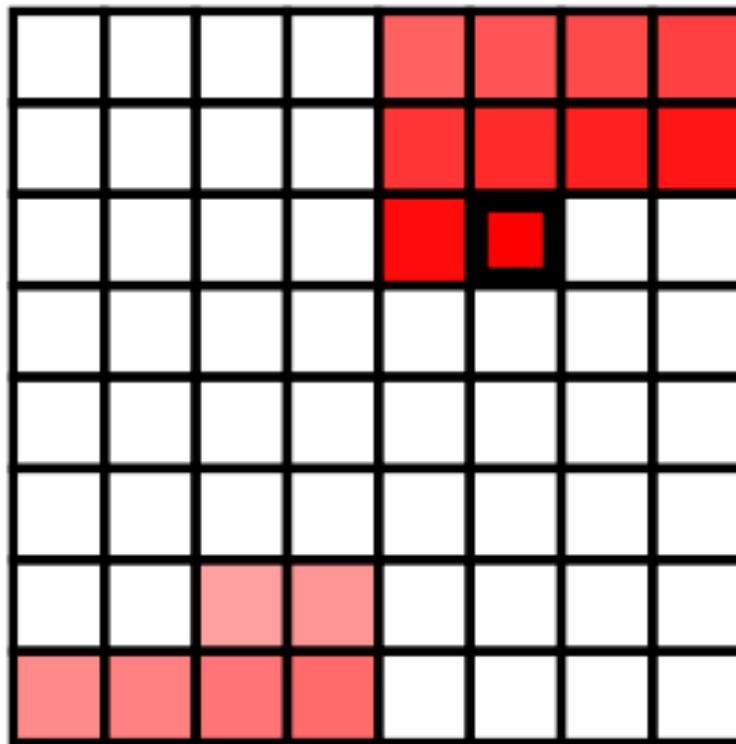
\times

C mem:289 cache hit:216



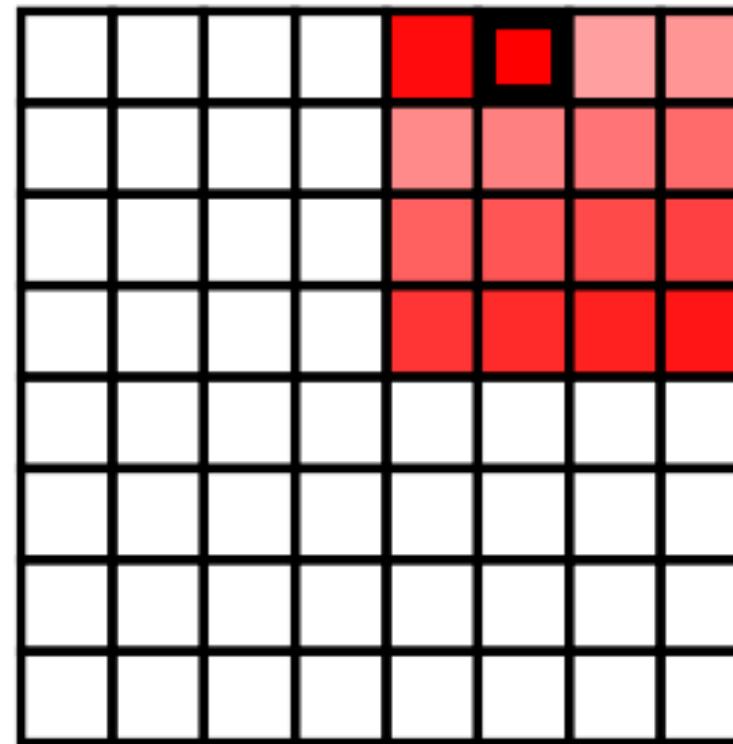
=

A mem:290 cache hit:216



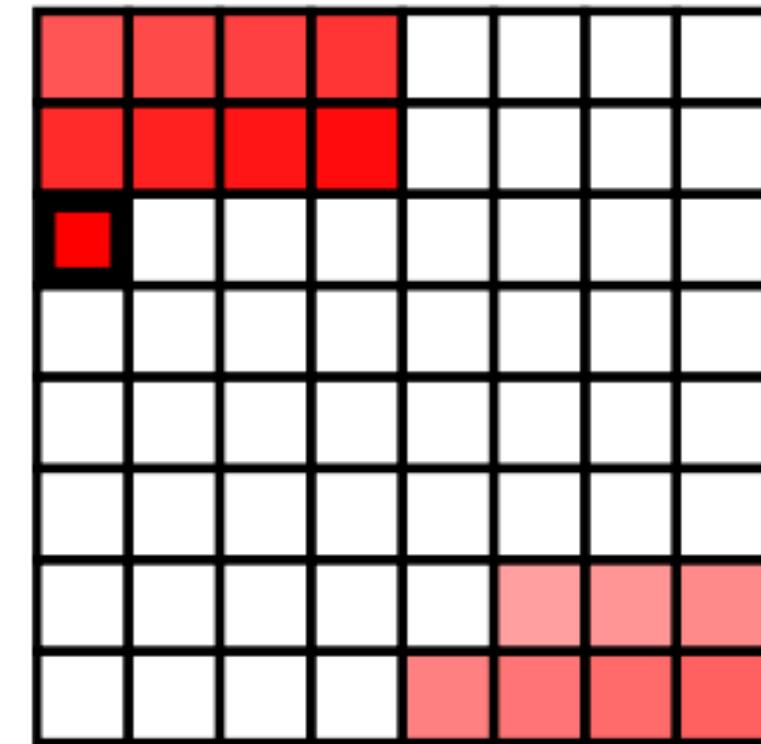
mem:870

B mem:290 cache hit:242

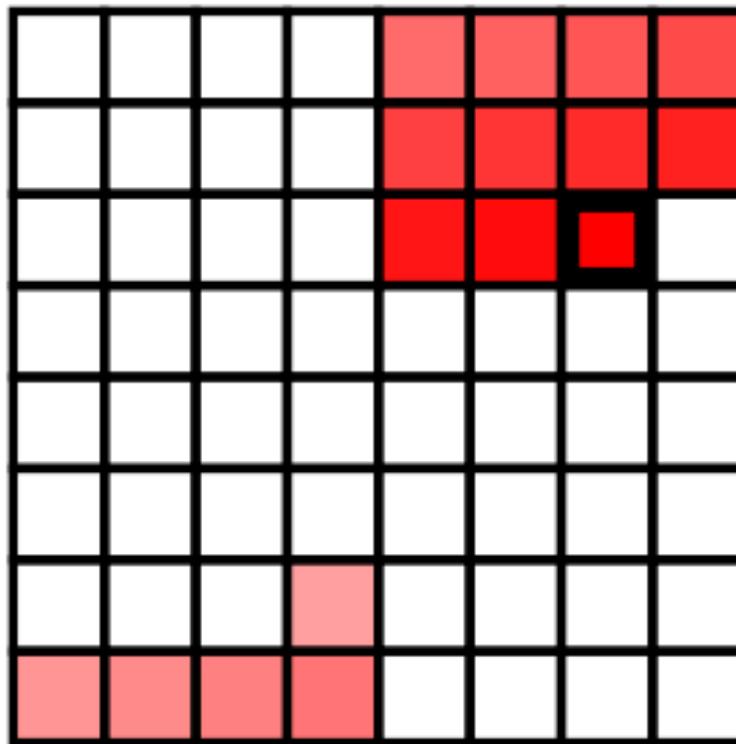


cache hits:675 ≈77%

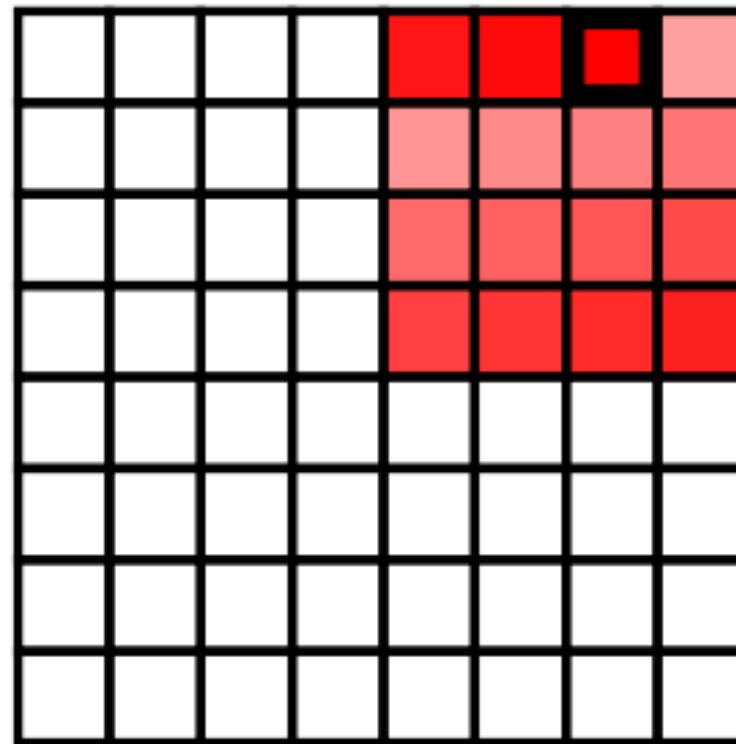
C mem:290 cache hit:217



A mem:291 cache hit:216



B mem:291 cache hit:243

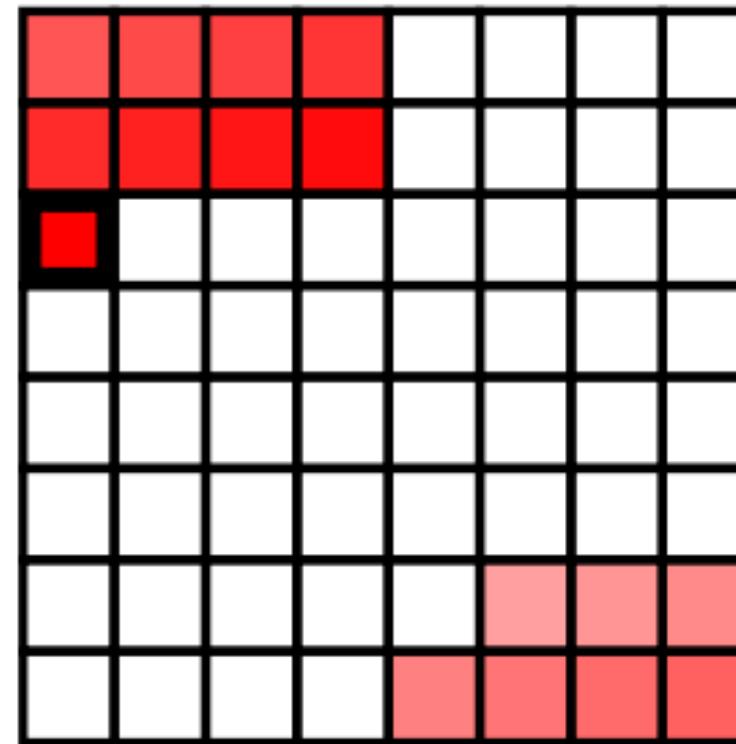


mem:873

cache hits:677 $\approx 77\%$

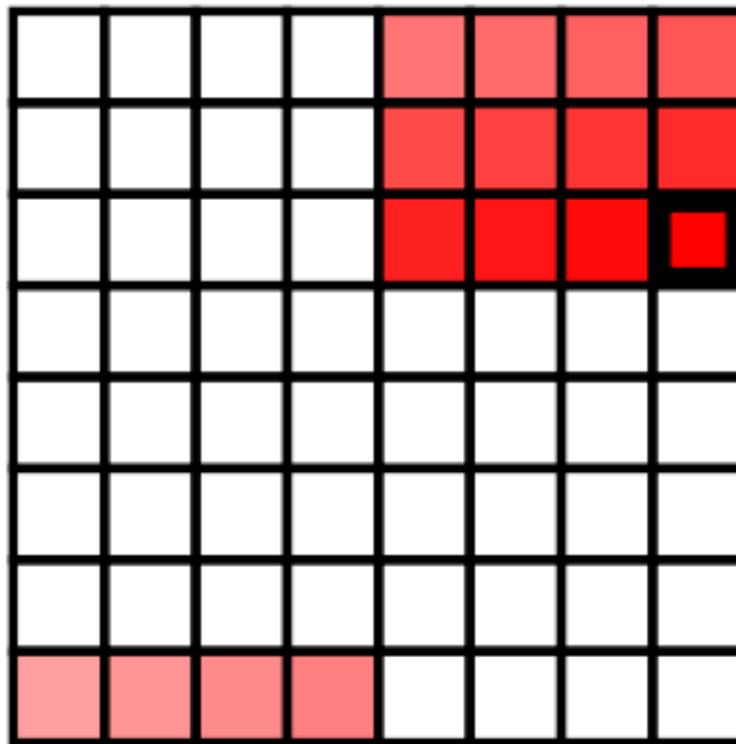
\times

C mem:291 cache hit:218

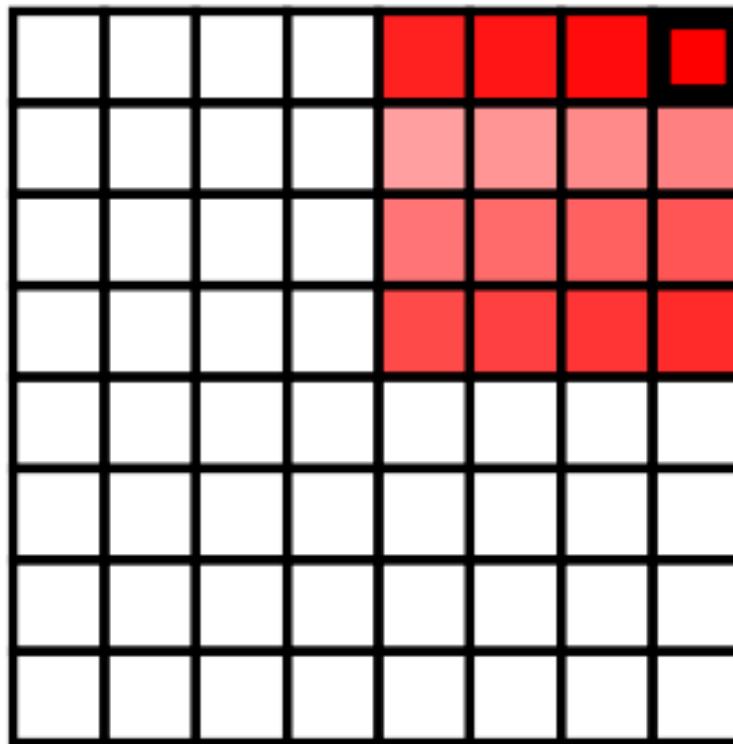


=

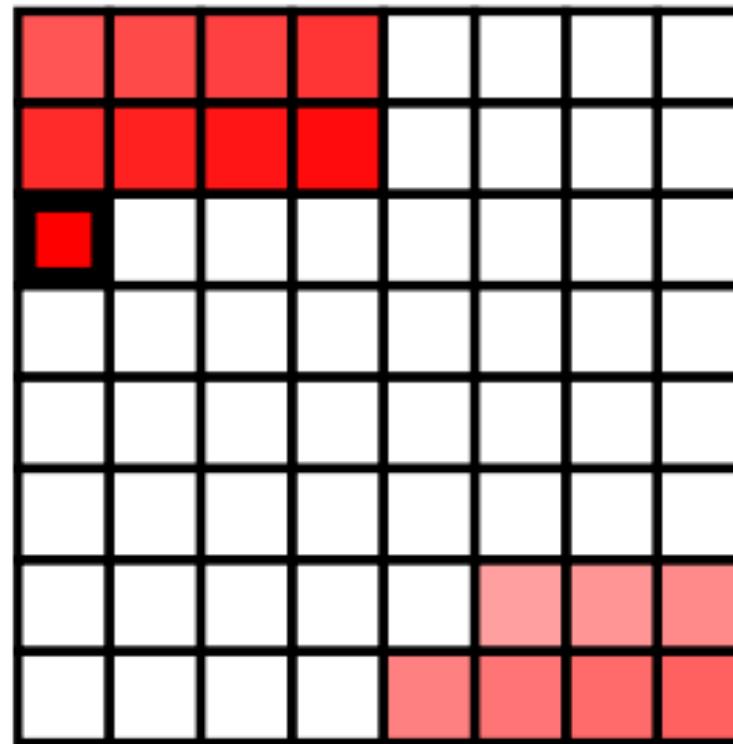
A mem:292 cache hit:216



B mem:292 cache hit:244



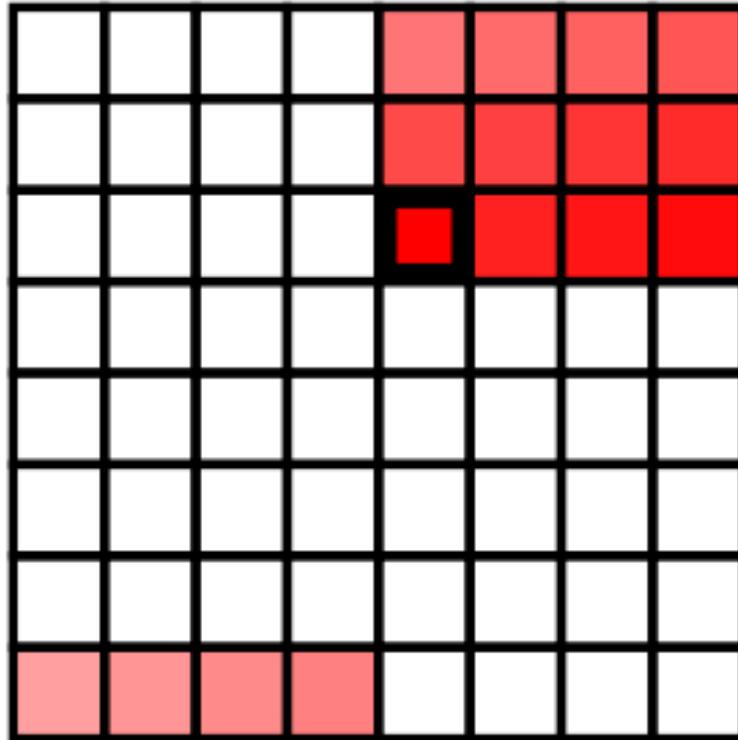
C mem:292 cache hit:219



mem:876

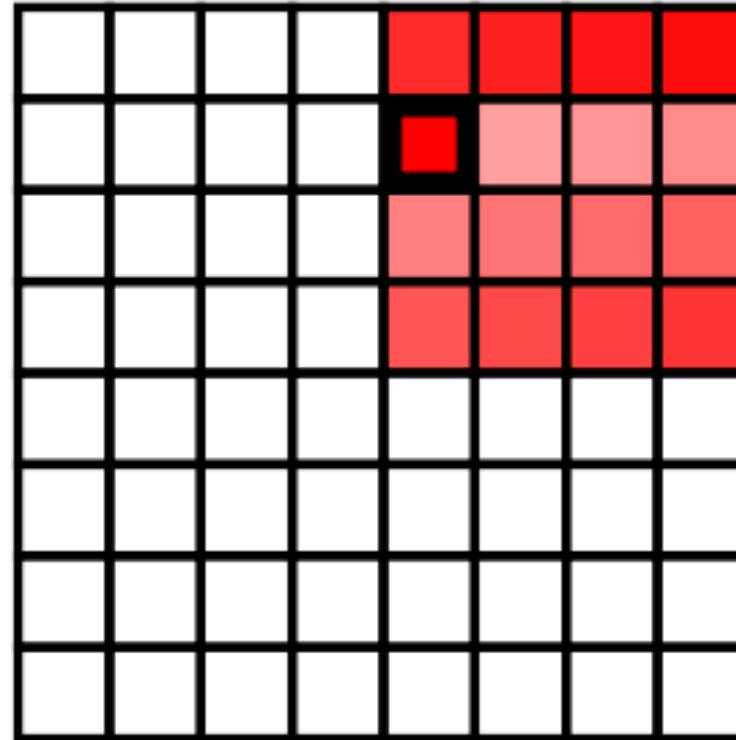
cache hits:679 $\approx 77\%$

A mem:293 cache hit:217



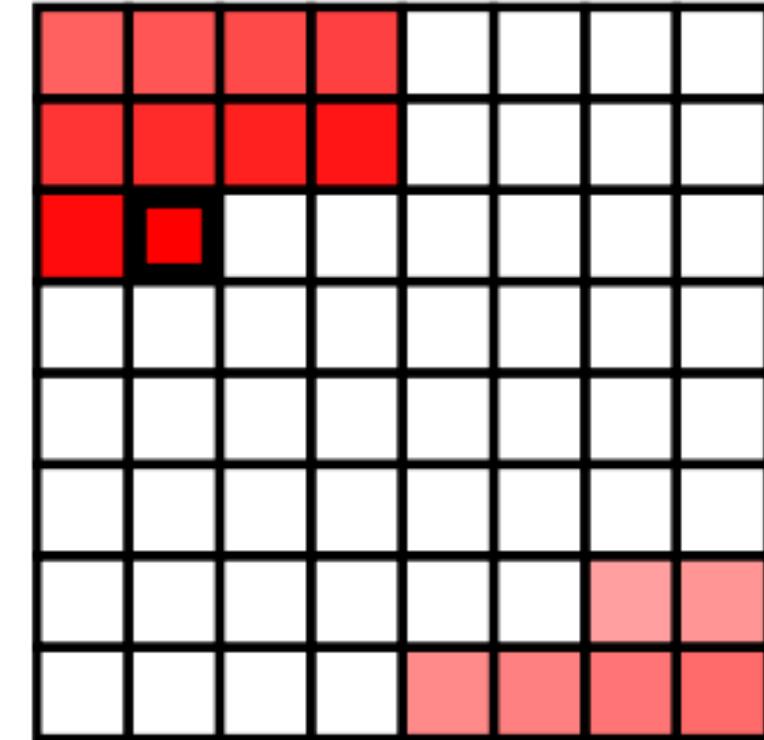
mem:879

B mem:293 cache hit:245

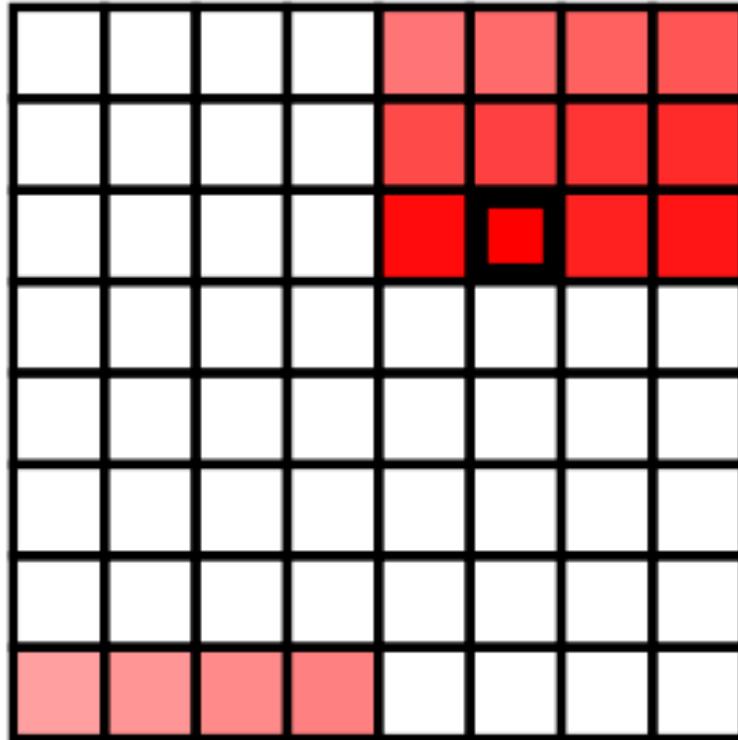


cache hits:681 $\approx 77\%$

C mem:293 cache hit:219

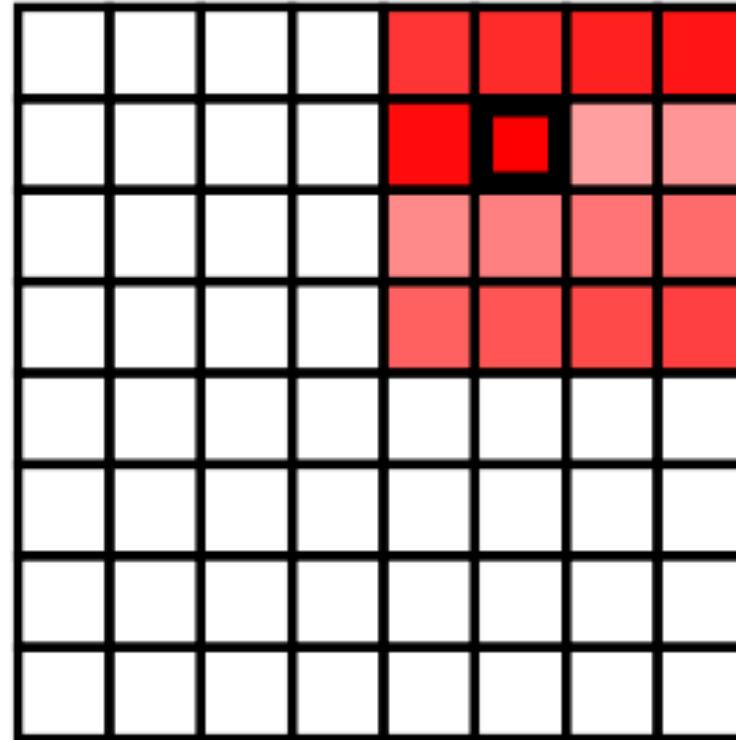


A mem:294 cache hit:218



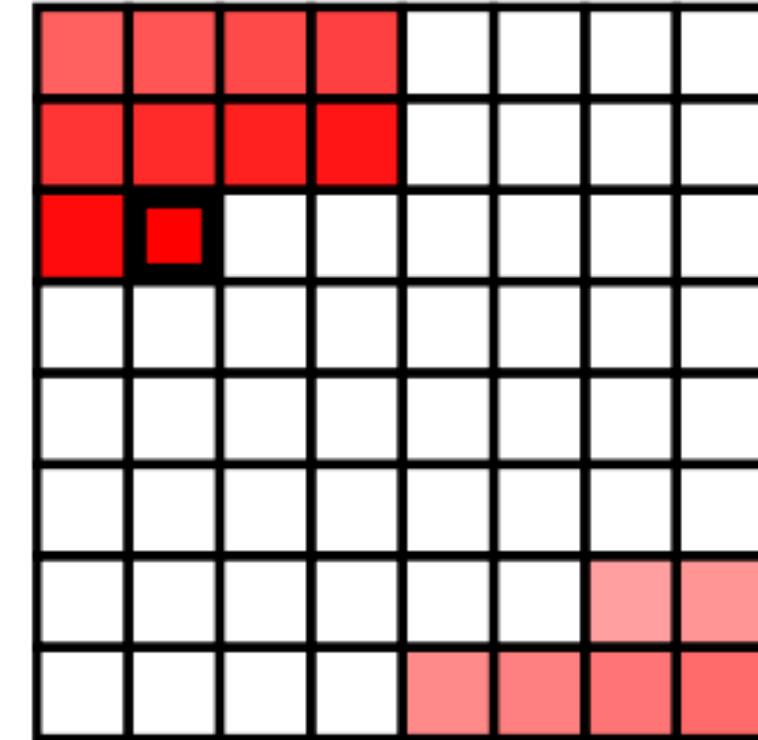
mem:882

B mem:294 cache hit:246

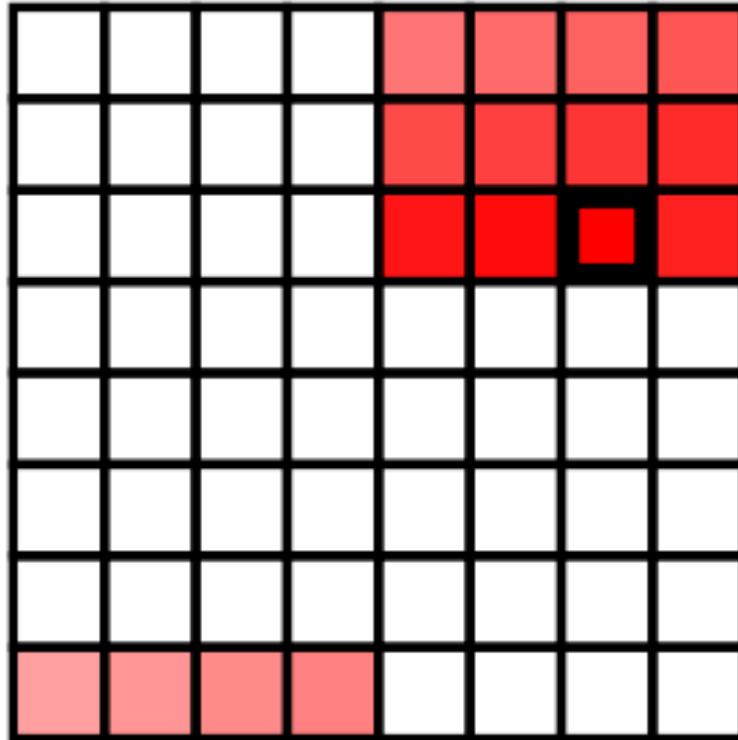


cache hits:684 $\approx 77\%$

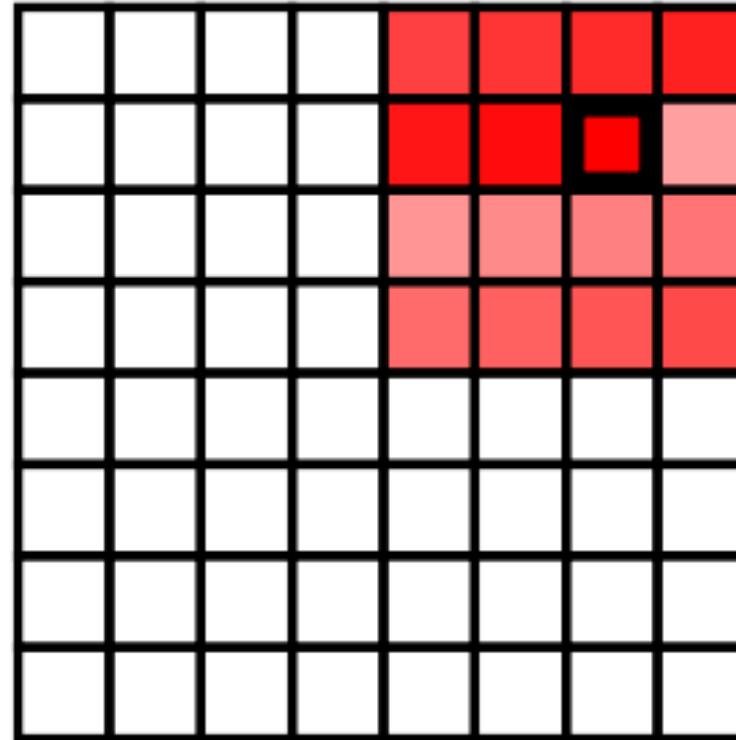
C mem:294 cache hit:220



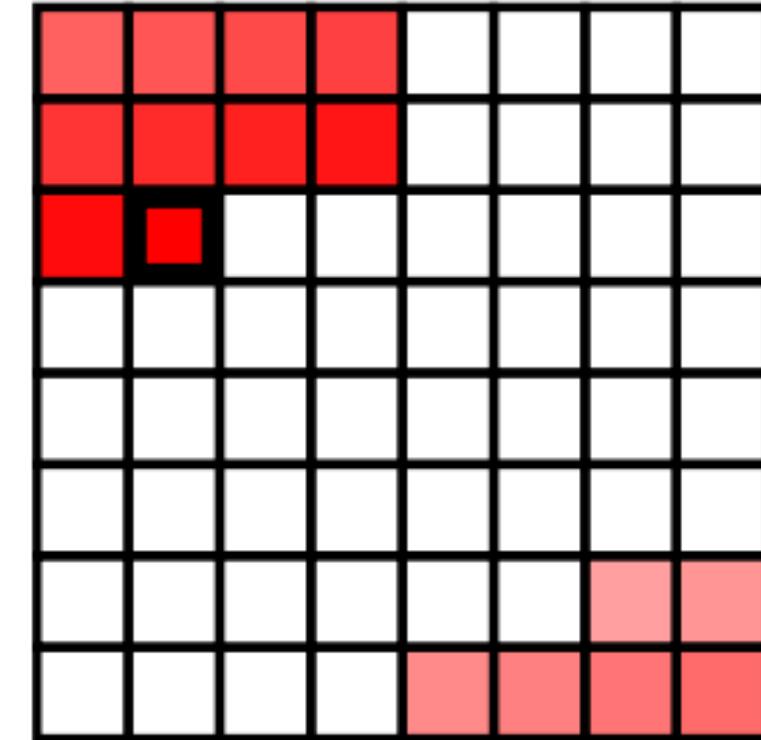
A mem:295 cache hit:219



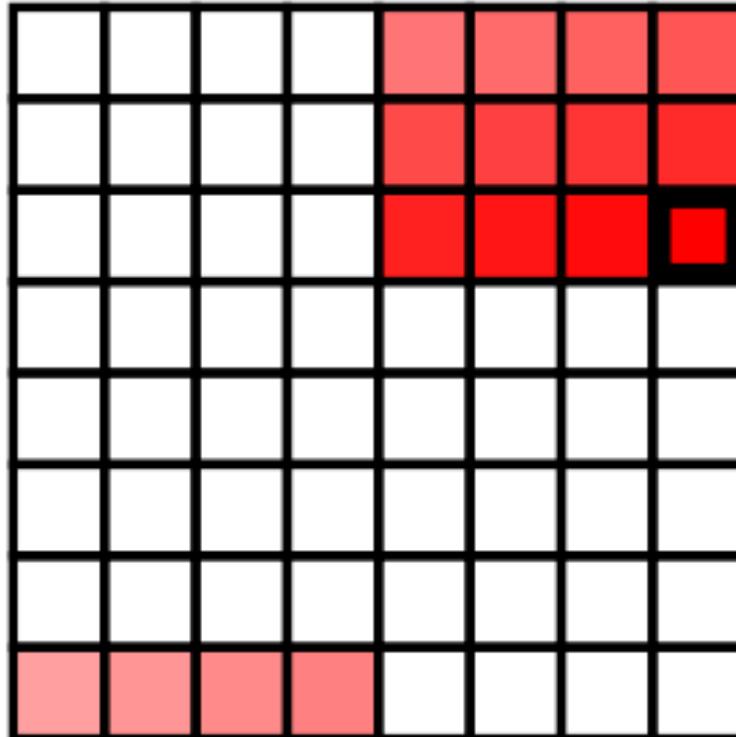
B mem:295 cache hit:247



C mem:295 cache hit:221

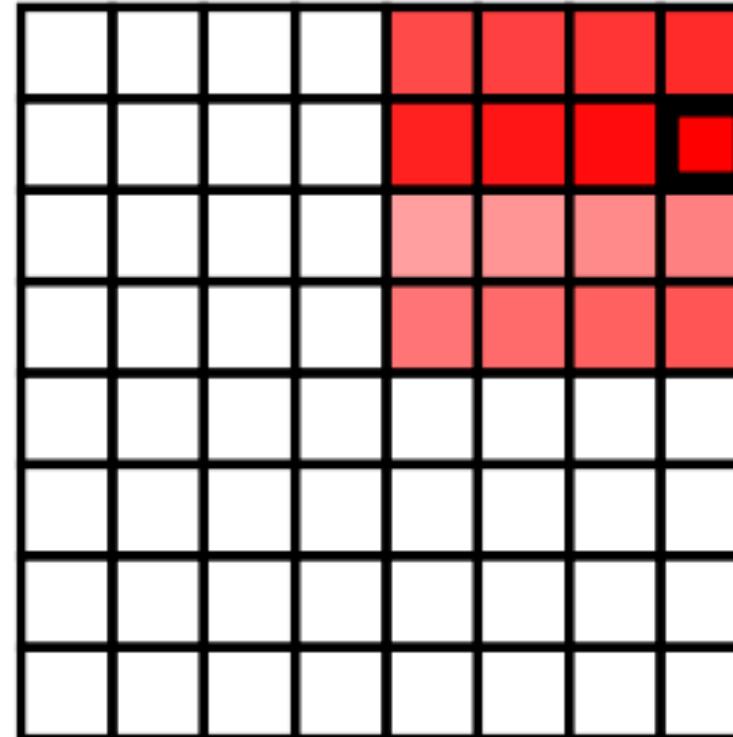


A mem:296 cache hit:220



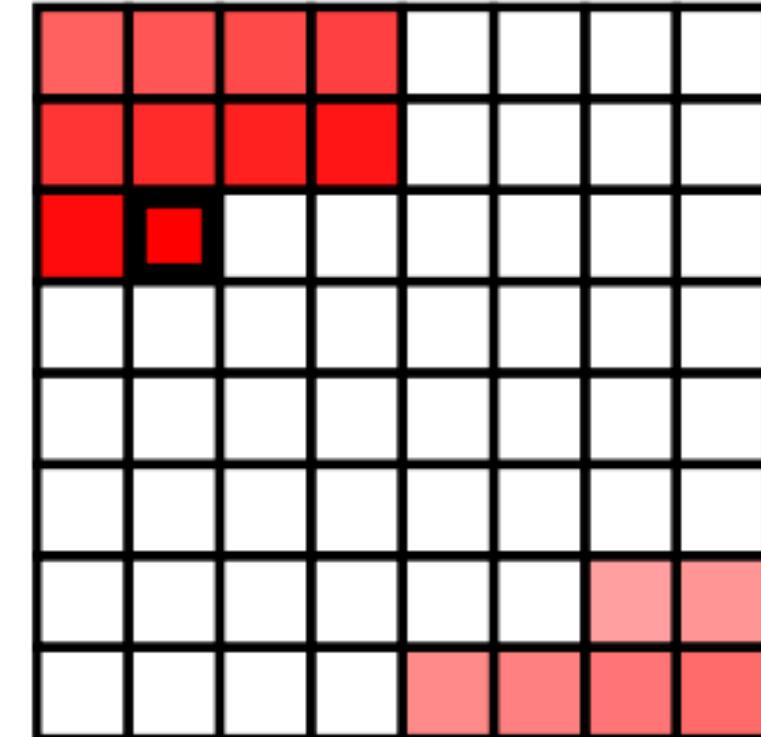
mem:888

B mem:296 cache hit:248

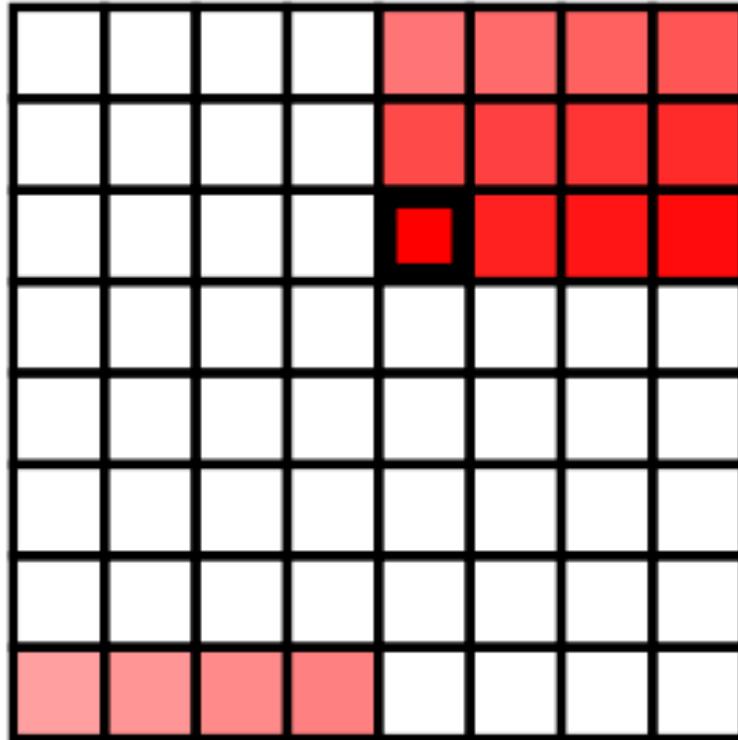


cache hits:690 $\approx 77\%$

C mem:296 cache hit:222

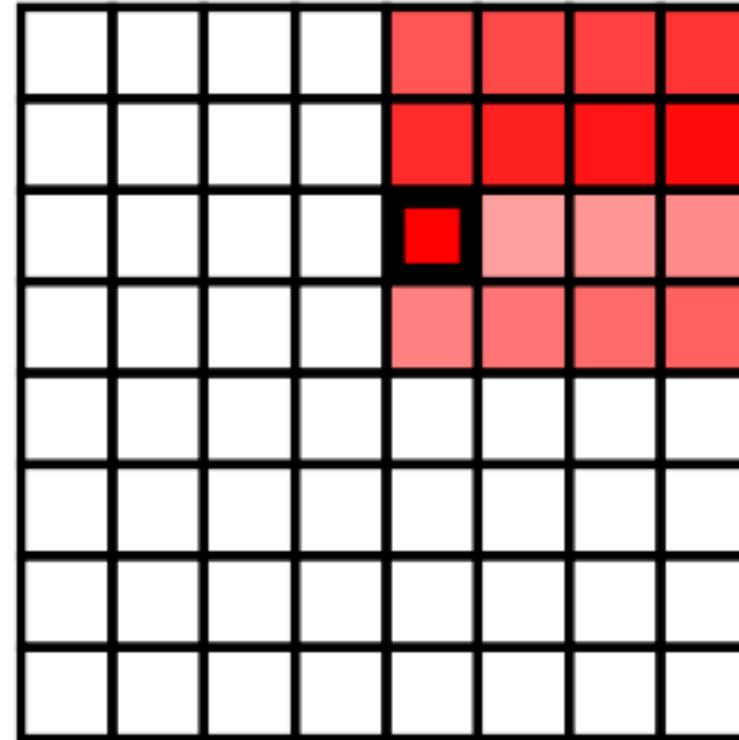


A mem:297 cache hit:221



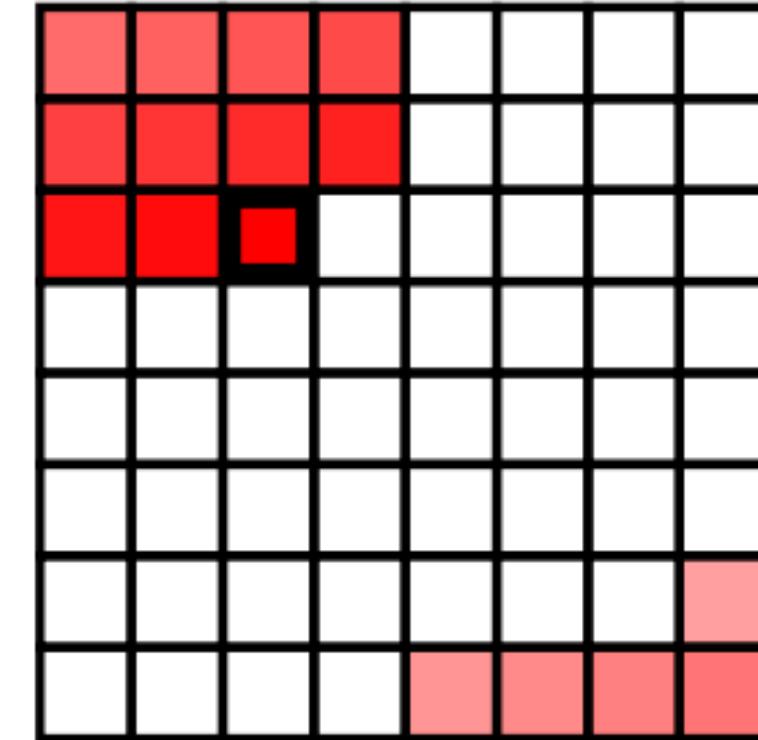
mem:891

B mem:297 cache hit:249

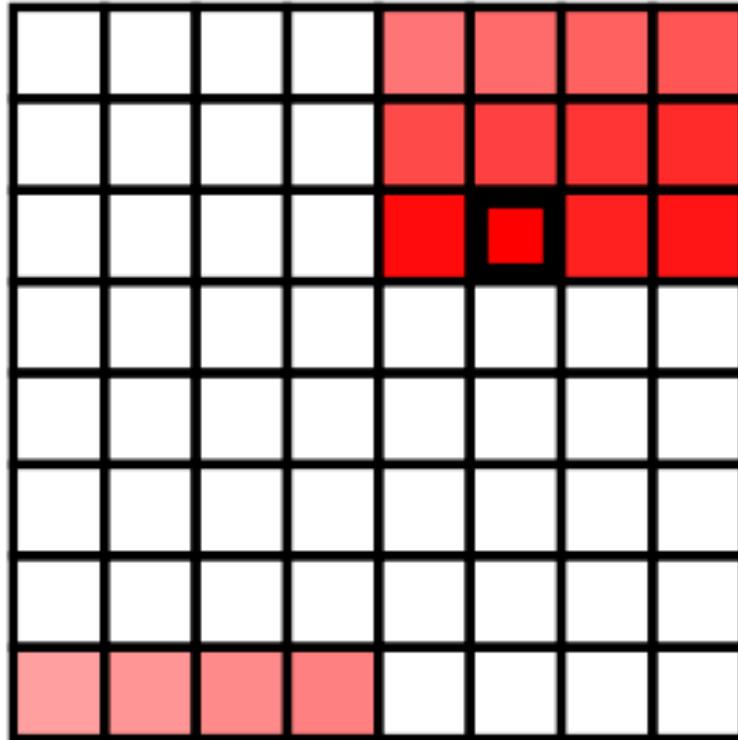


cache hits:692 ≈77%

C mem:297 cache hit:222

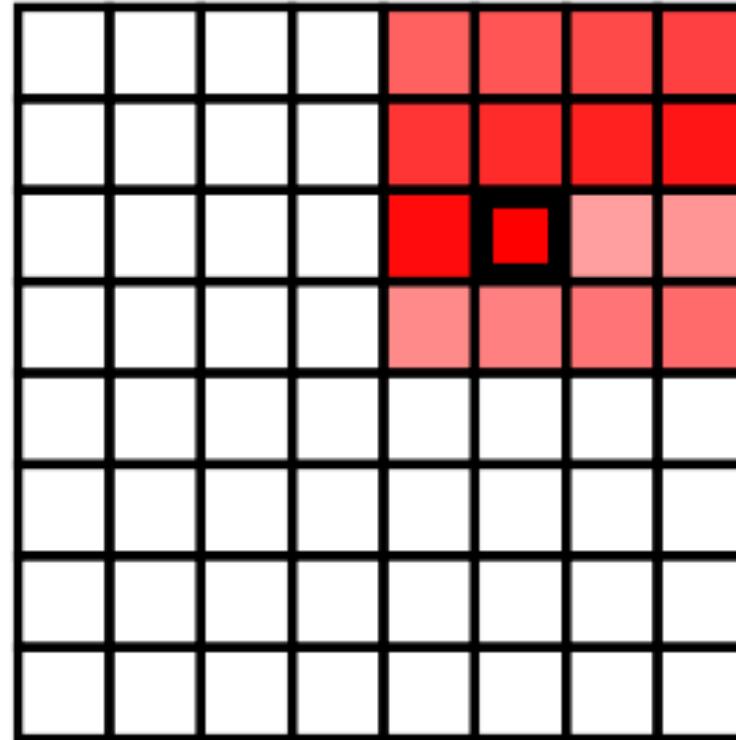


A mem:298 cache hit:222



mem:894

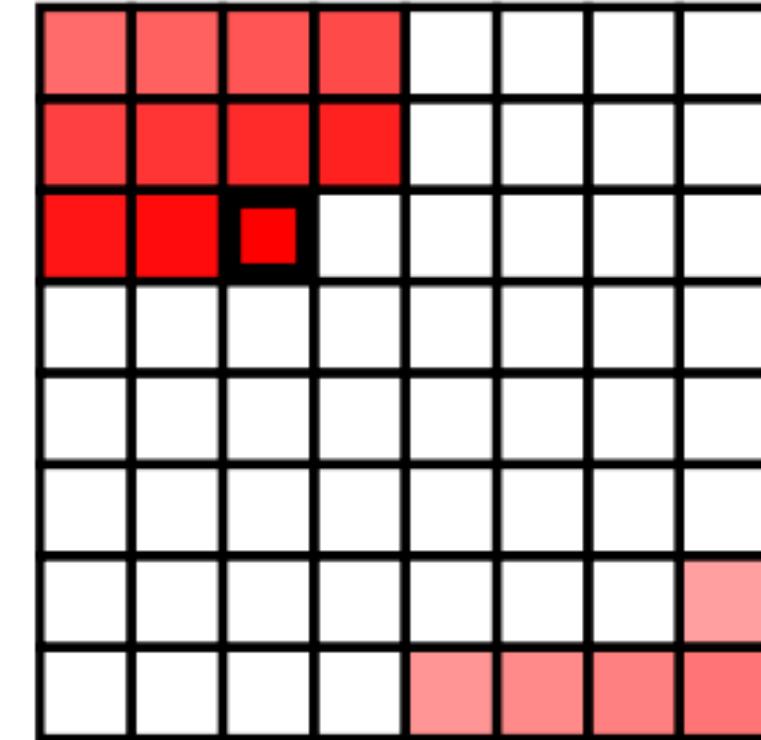
B mem:298 cache hit:250



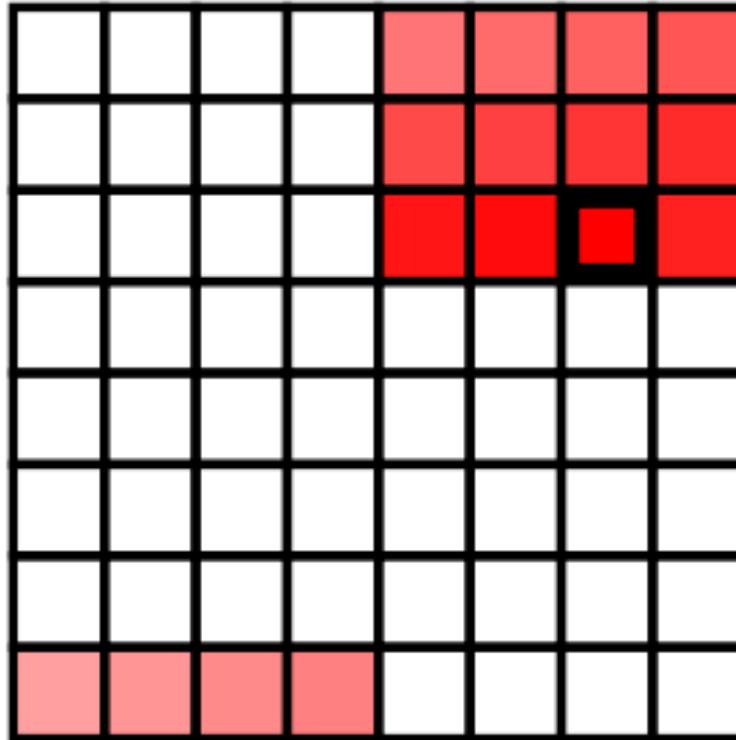
×

cache hits:695 $\approx 77\%$

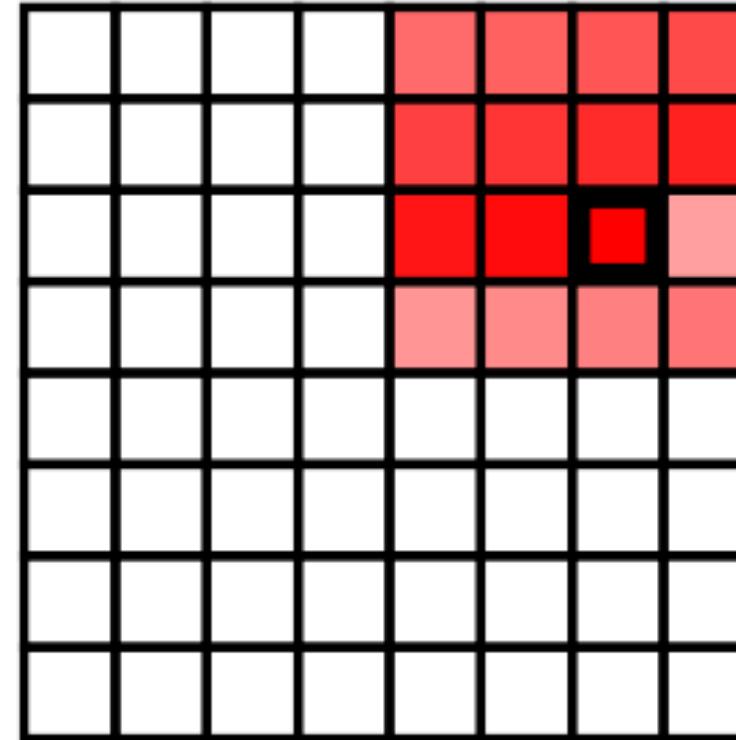
C mem:298 cache hit:223



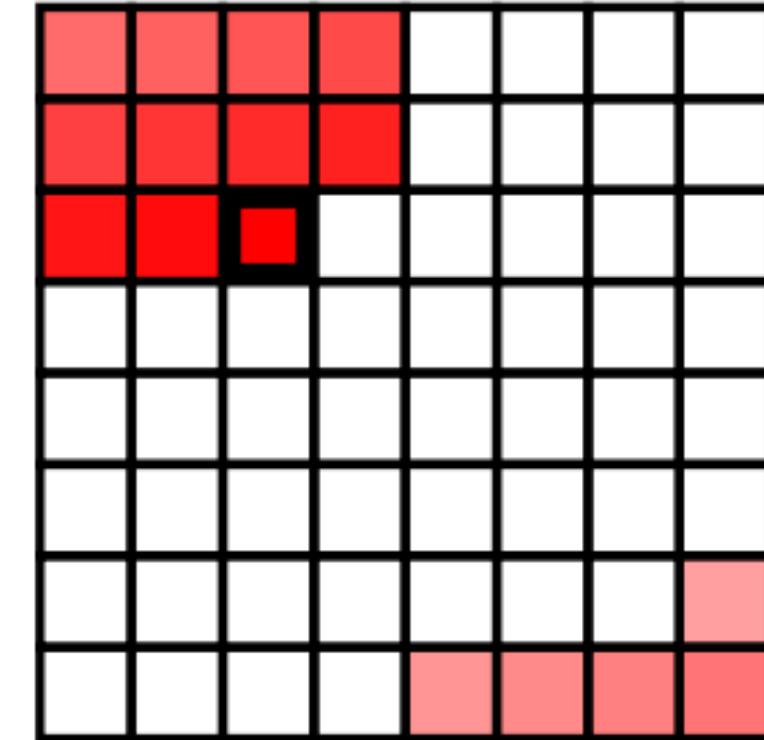
A mem:299 cache hit:223



B mem:299 cache hit:251



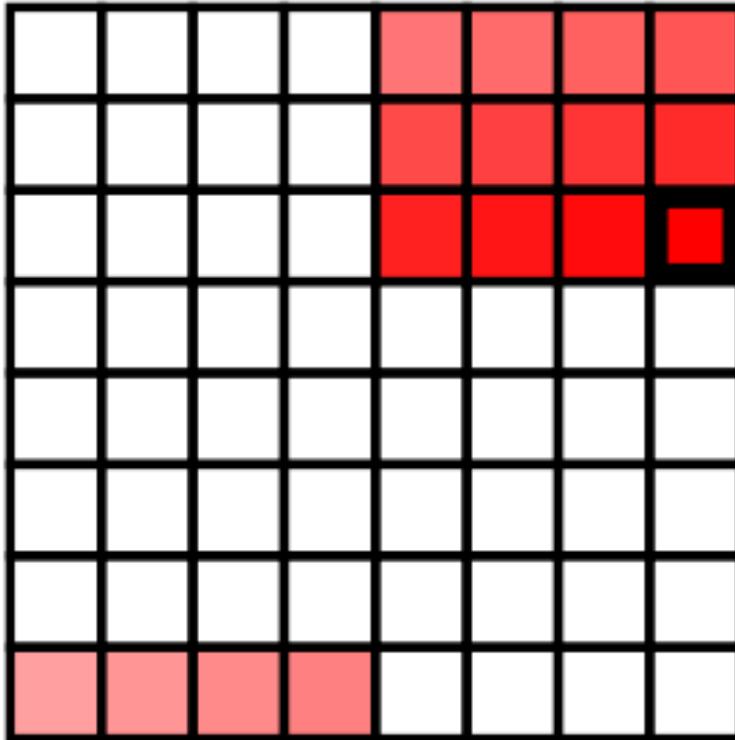
C mem:299 cache hit:224



mem:897

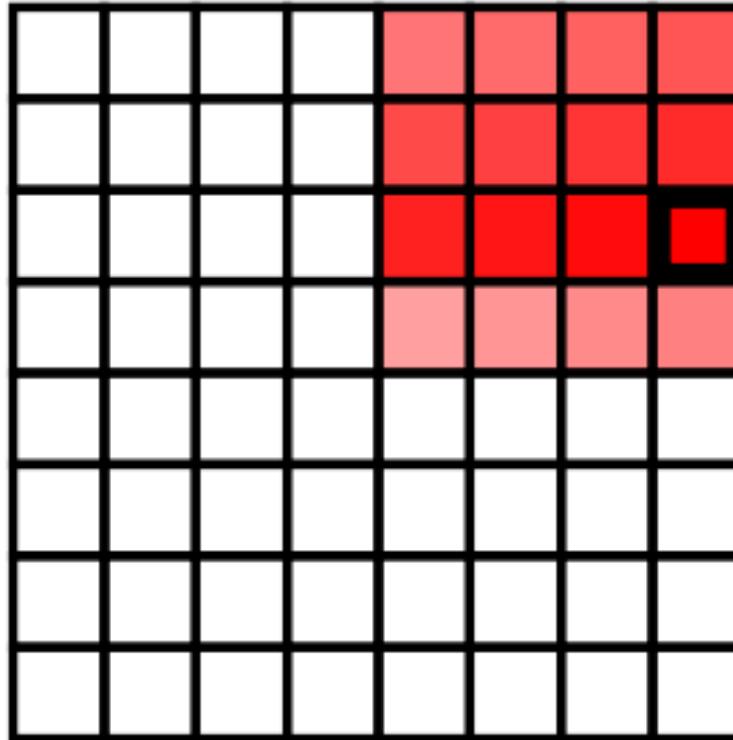
cache hits:698 $\approx 77\%$

A mem:300 cache hit:224



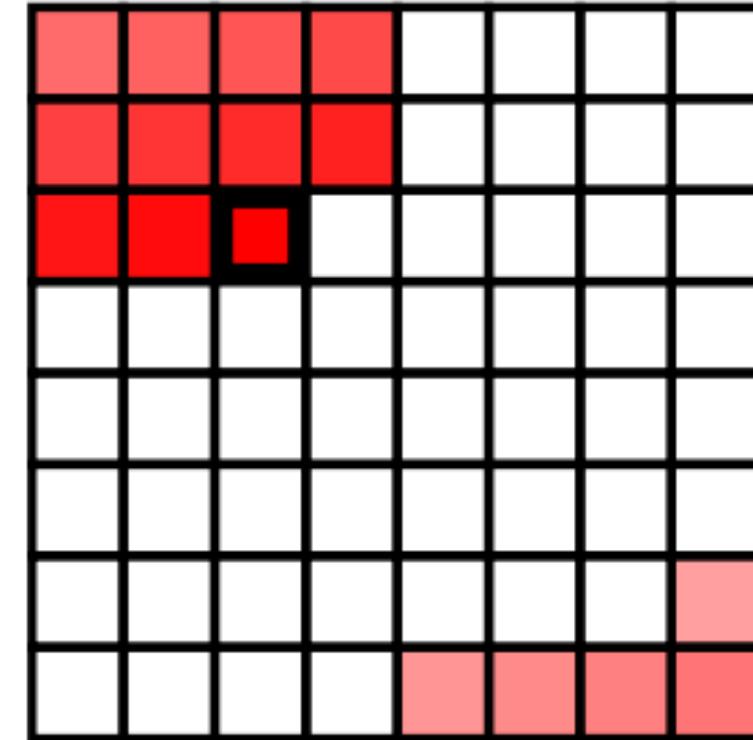
mem:900

B mem:300 cache hit:252

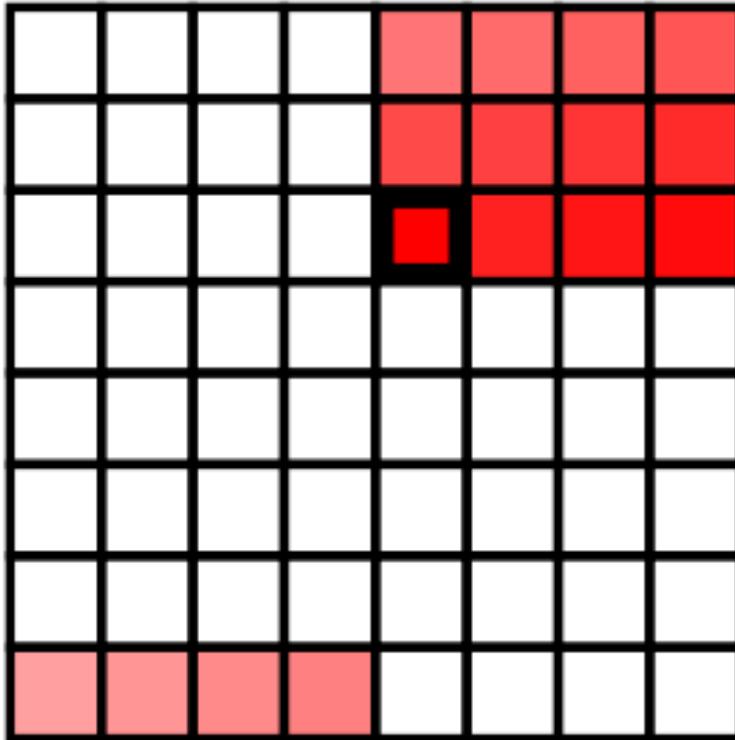


cache hits:701 $\approx 77\%$

C mem:300 cache hit:225

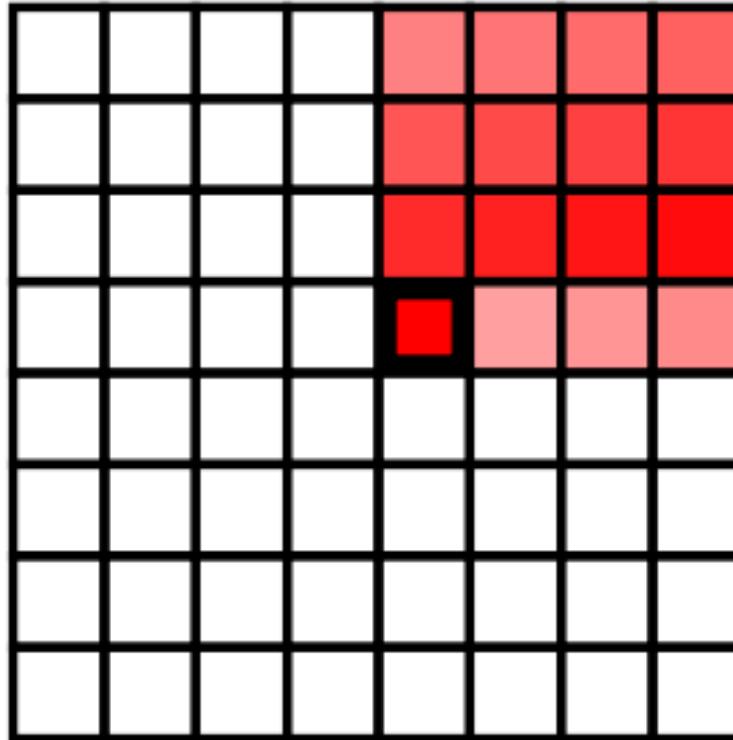


A mem:301 cache hit:225



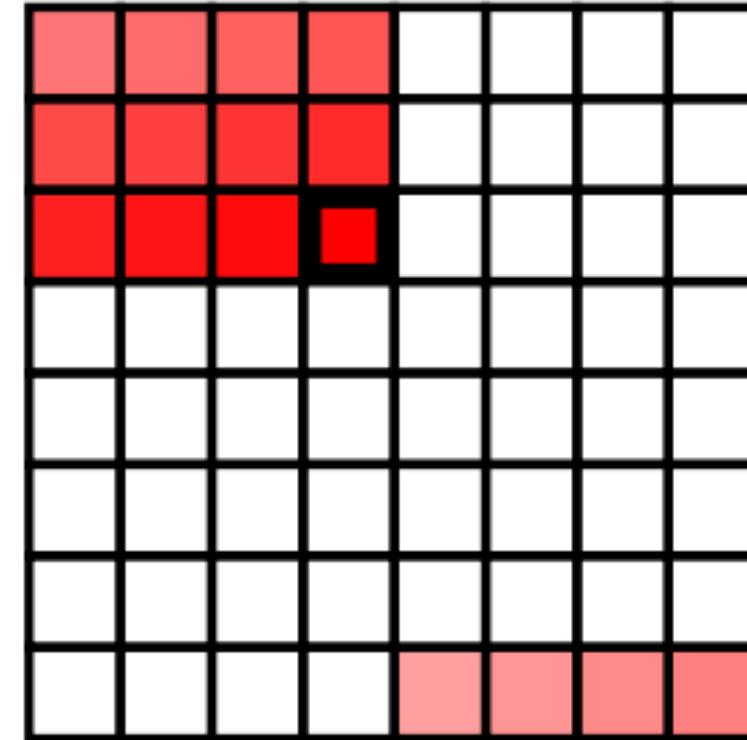
mem:903

B mem:301 cache hit:253

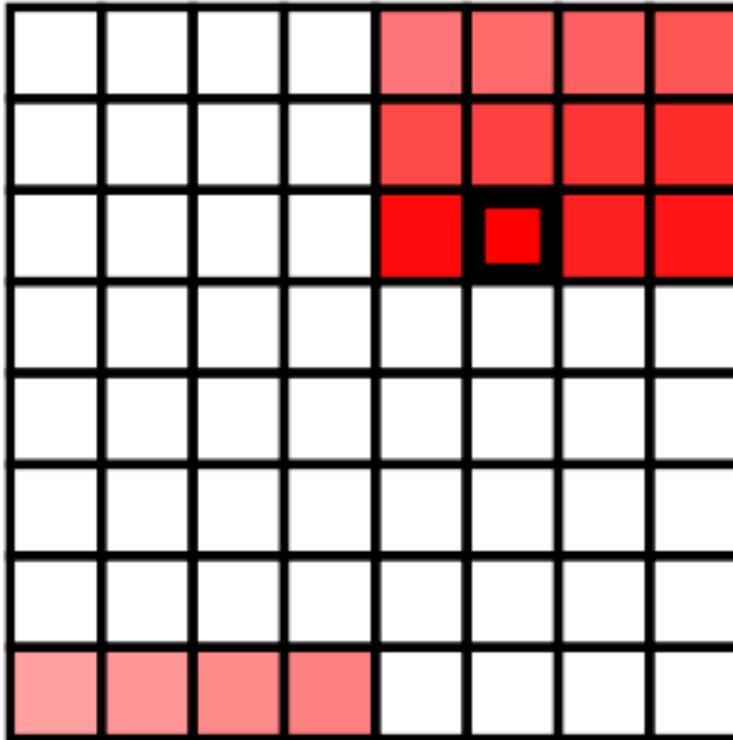


cache hits:703 ≈77%

C mem:301 cache hit:225

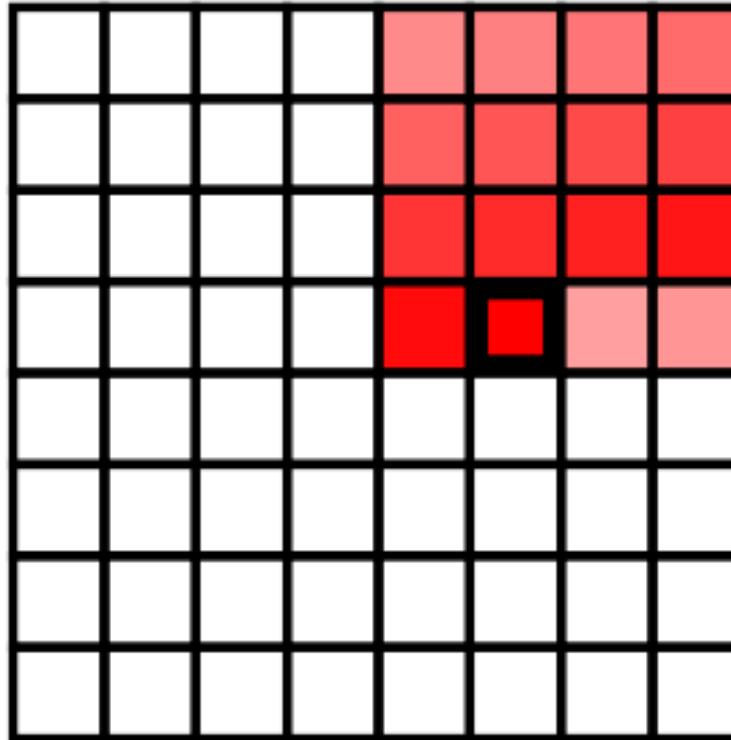


A mem:302 cache hit:226



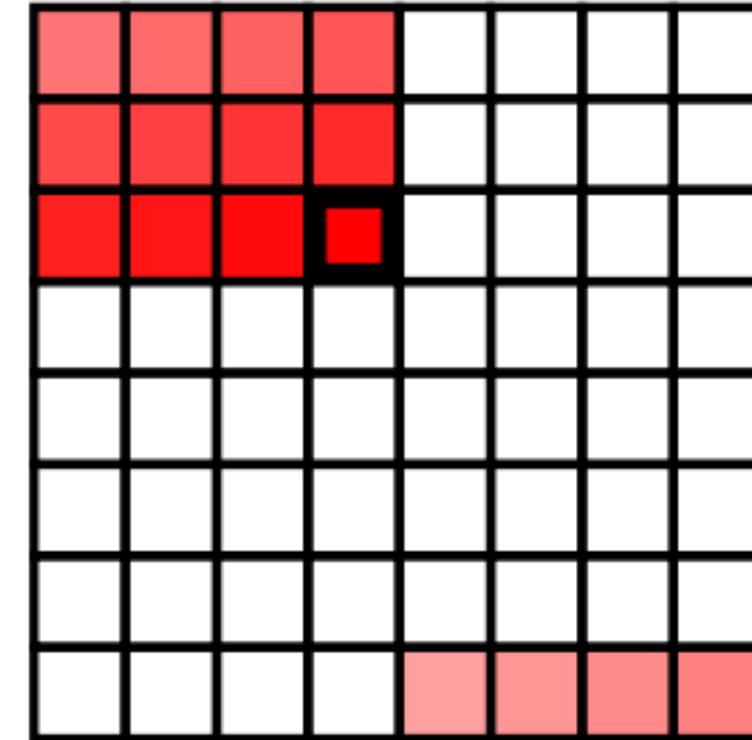
mem:906

B mem:302 cache hit:254

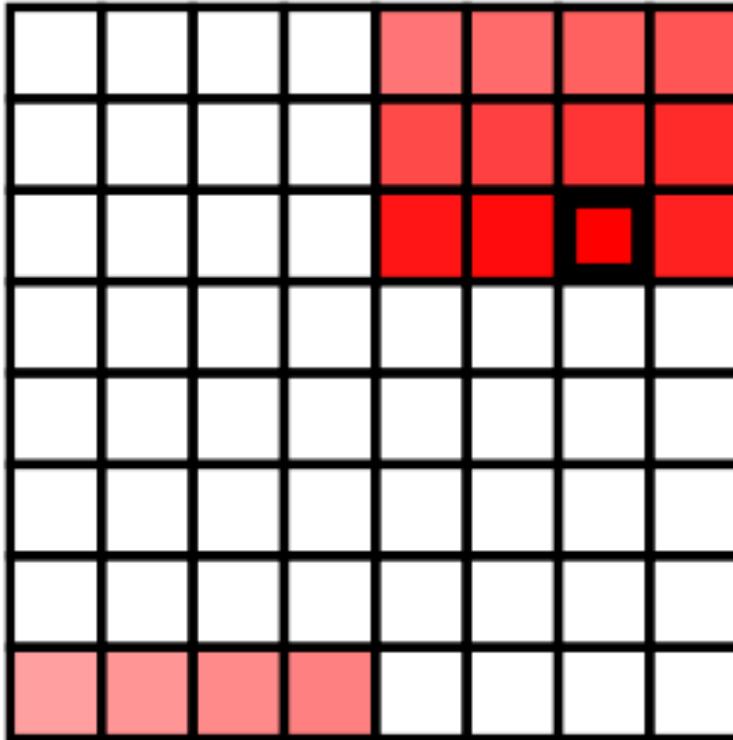


cache hits:706 ≈77%

C mem:302 cache hit:226

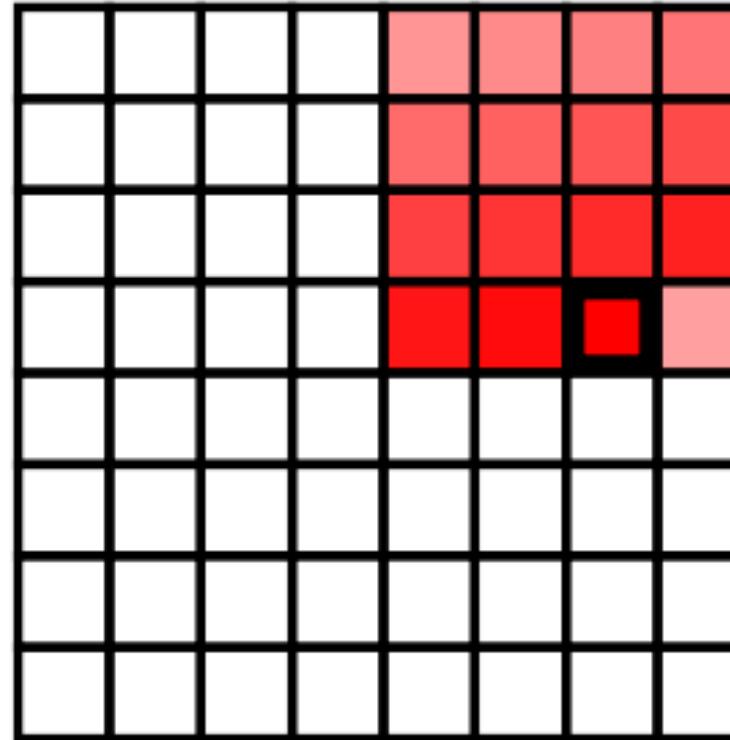


A mem:303 cache hit:227



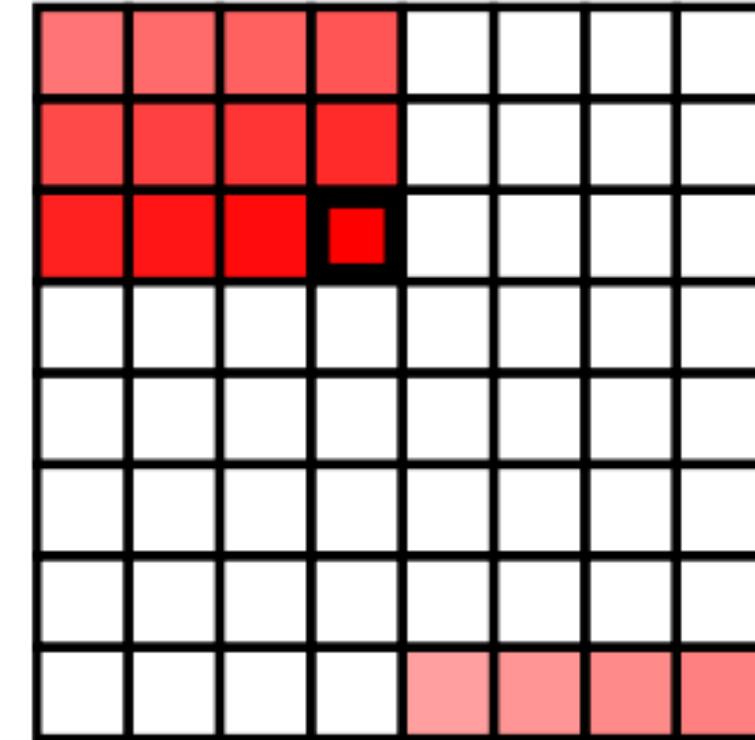
mem:909

B mem:303 cache hit:255

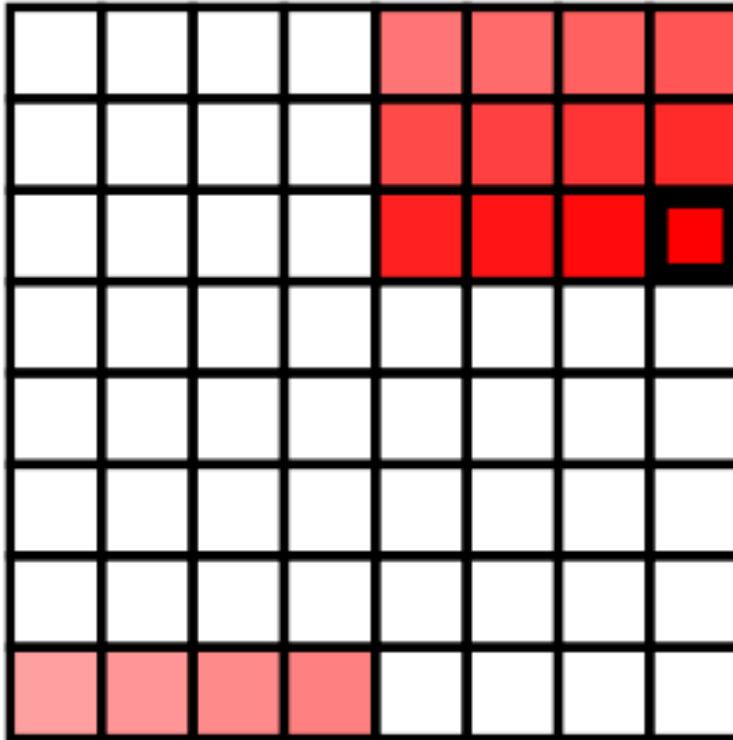


cache hits:709 ≈77%

C mem:303 cache hit:227

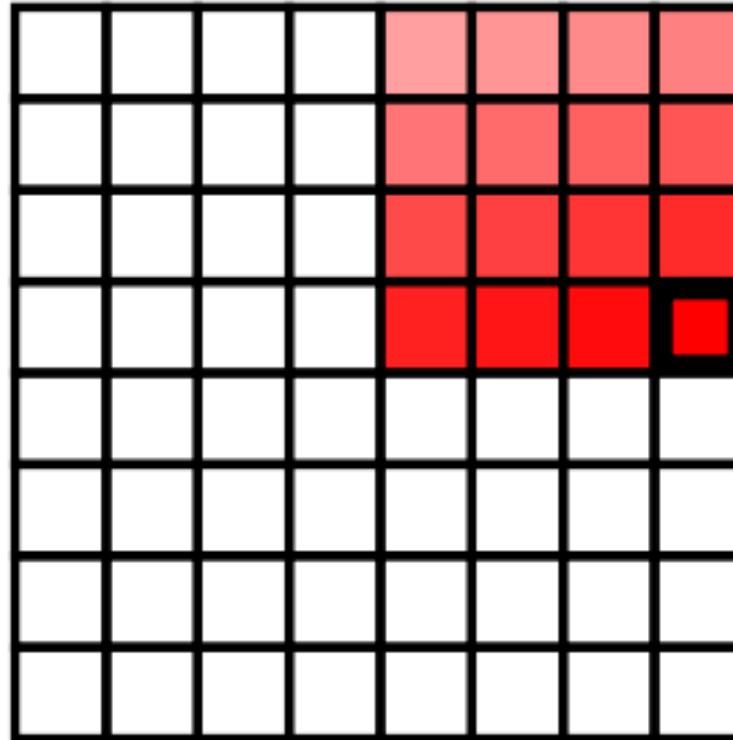


A mem:304 cache hit:228



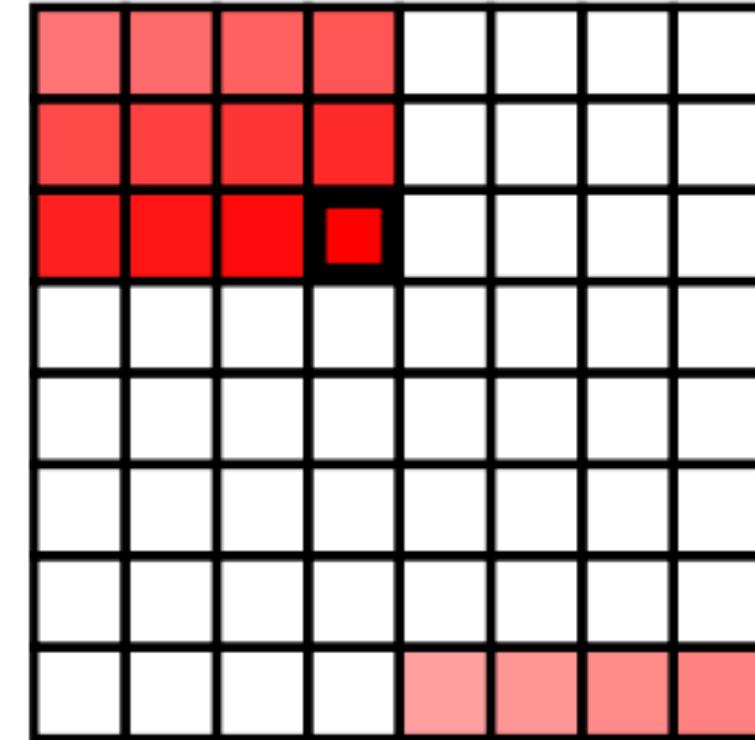
mem:912

B mem:304 cache hit:256

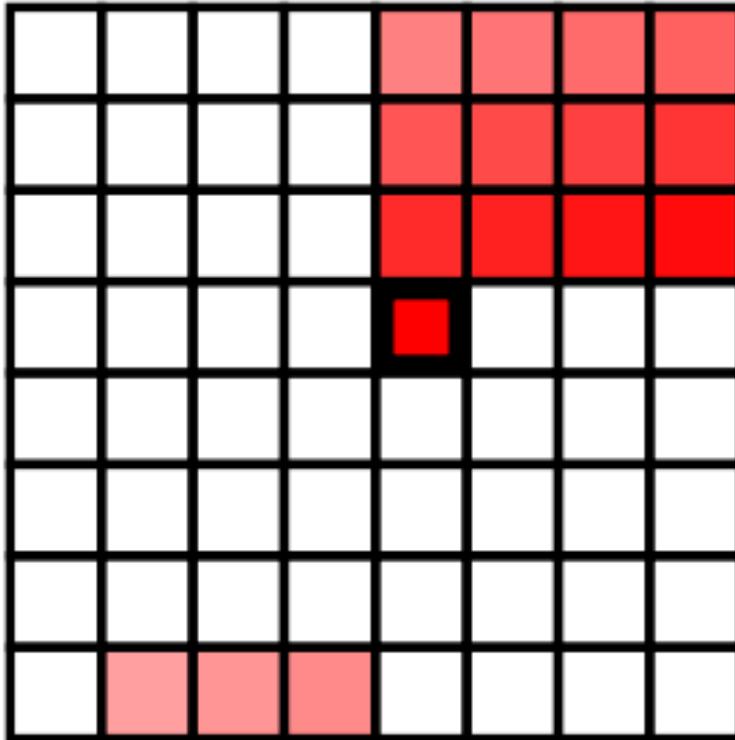


cache hits:712 ≈78%

C mem:304 cache hit:228

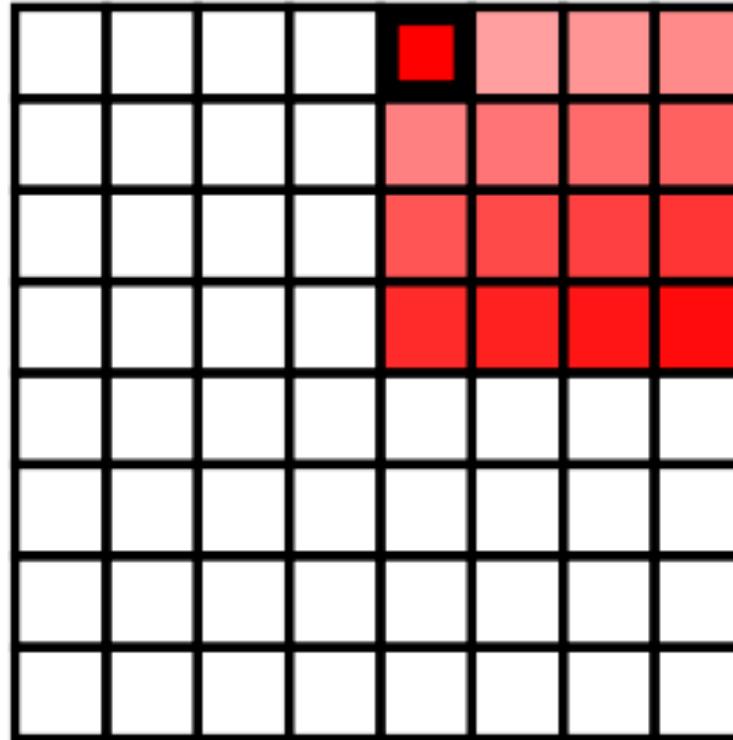


A mem:305 cache hit:228



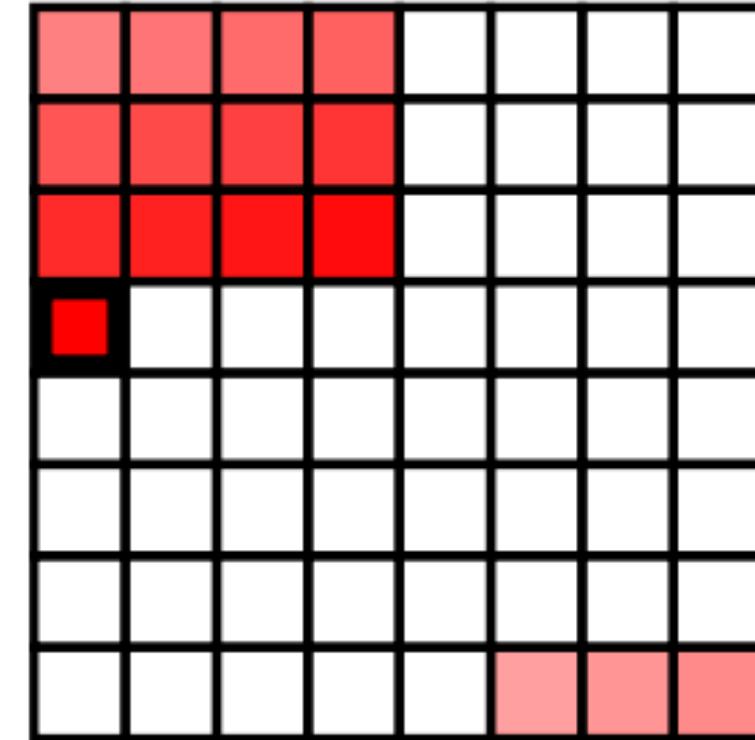
mem:915

B mem:305 cache hit:257

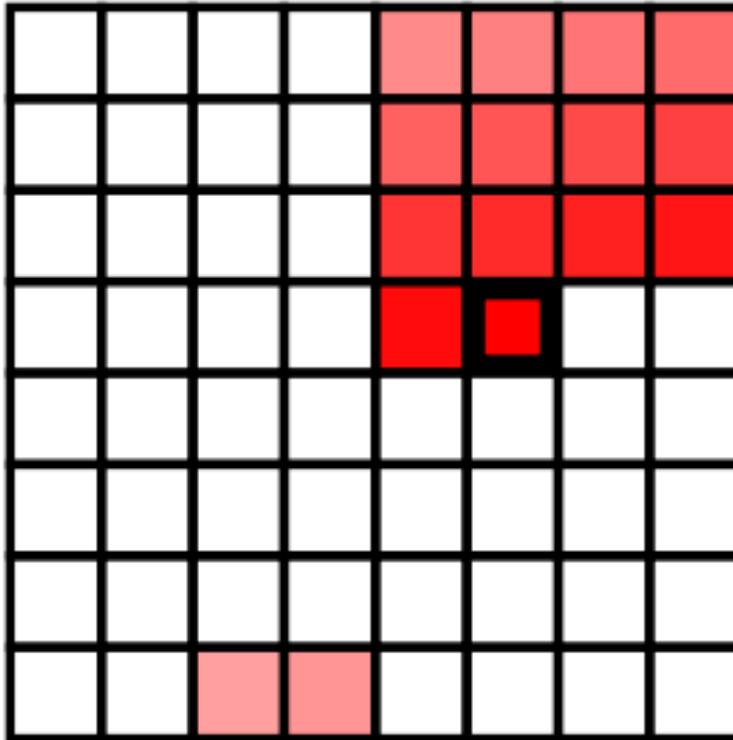


cache hits:713 ≈77%

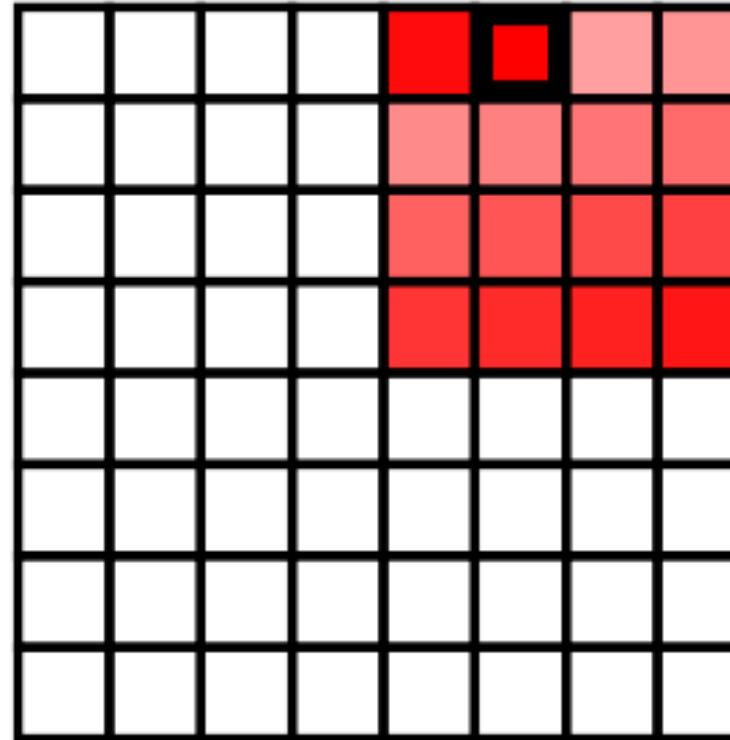
C mem:305 cache hit:228



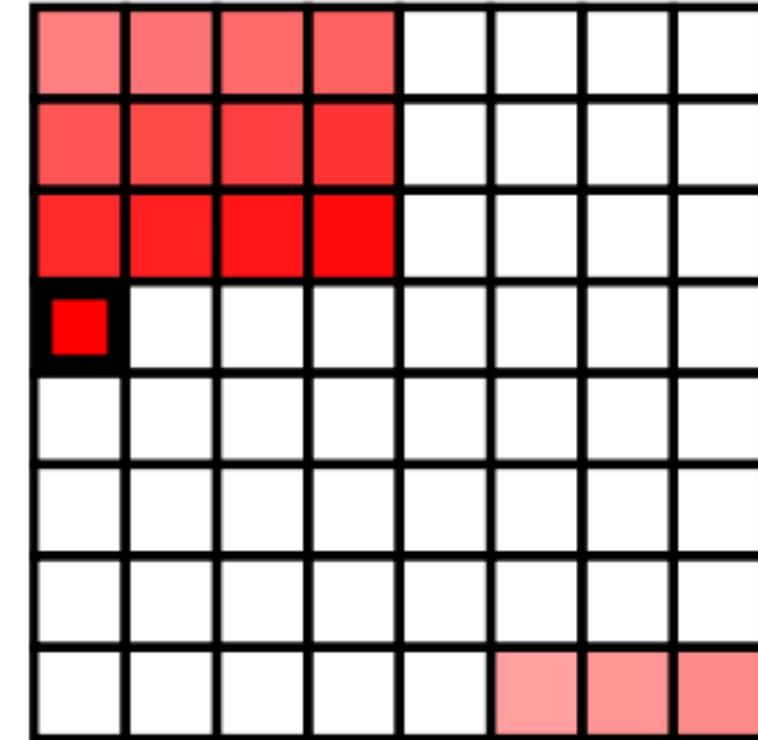
A mem:306 cache hit:228



B mem:306 cache hit:258



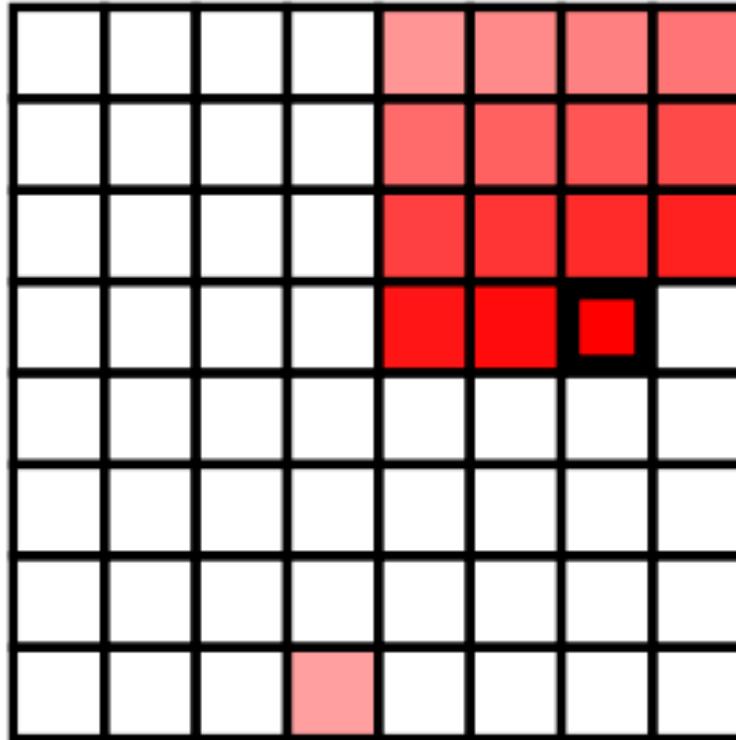
C mem:306 cache hit:229



mem:918

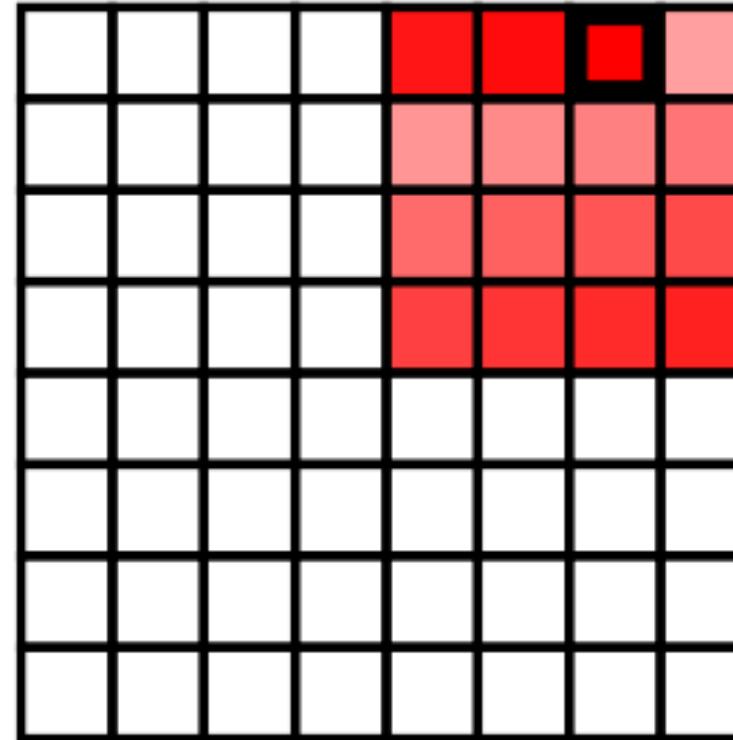
cache hits:715 $\approx 77\%$

A mem:307 cache hit:228



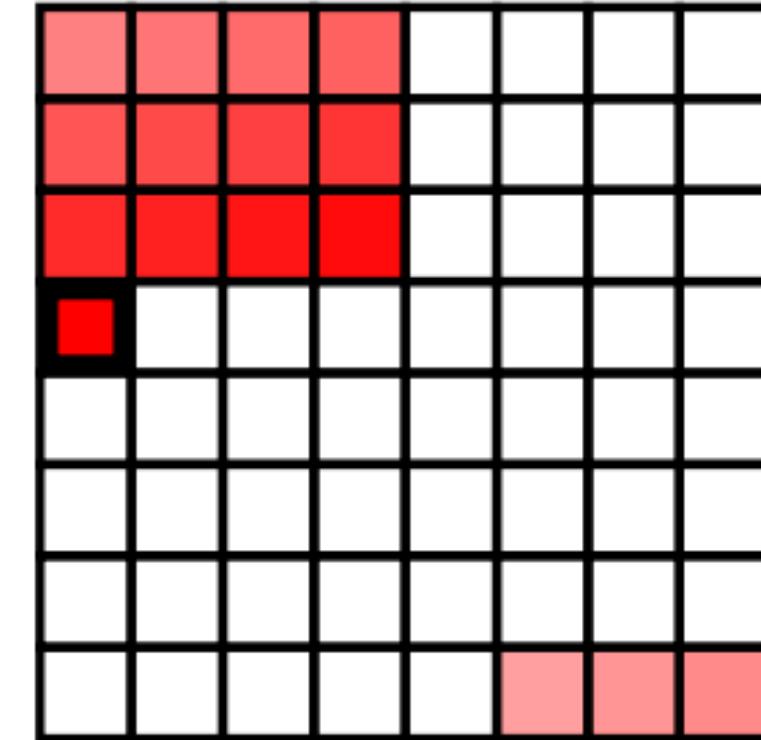
mem:921

B mem:307 cache hit:259

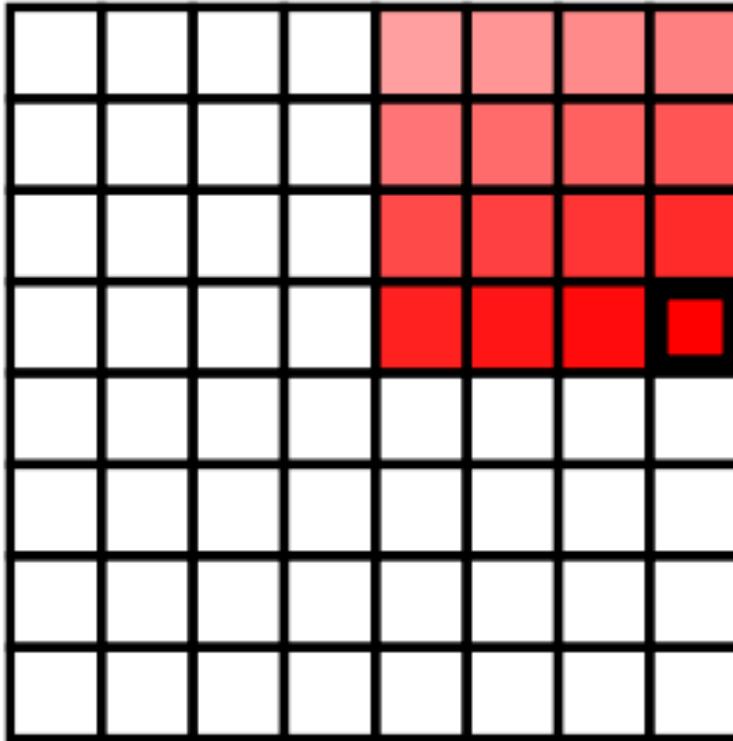


cache hits:717 ≈77%

C mem:307 cache hit:230

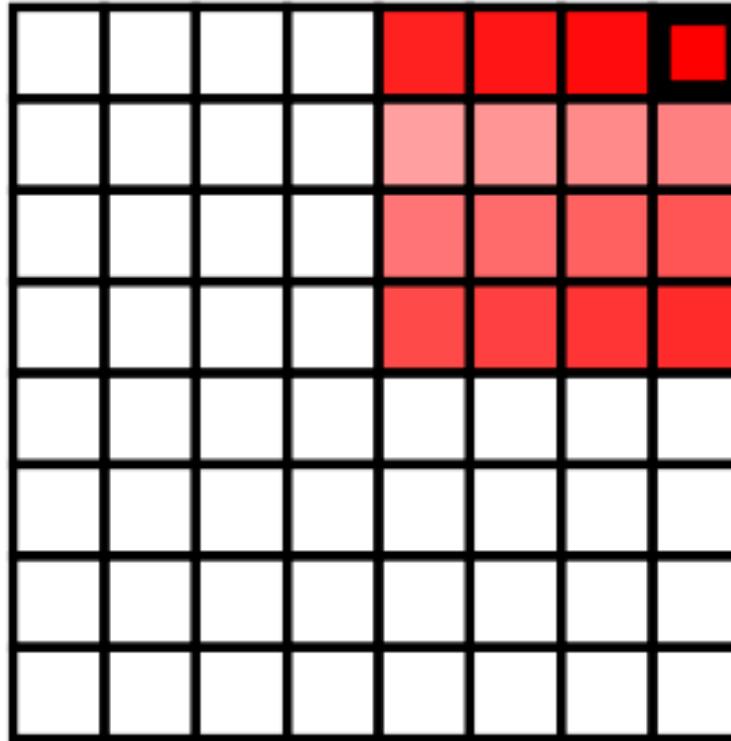


A mem:308 cache hit:228



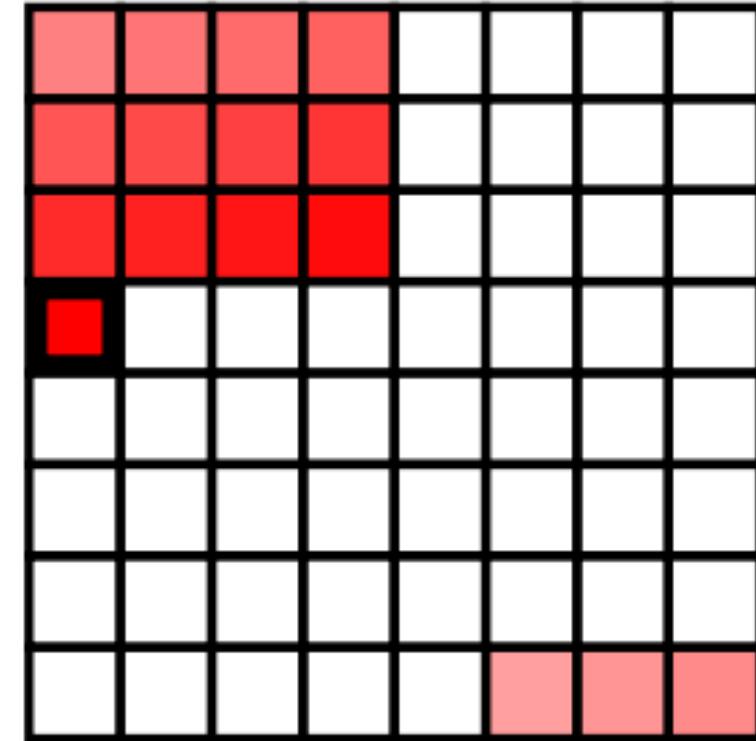
mem:924

B mem:308 cache hit:260

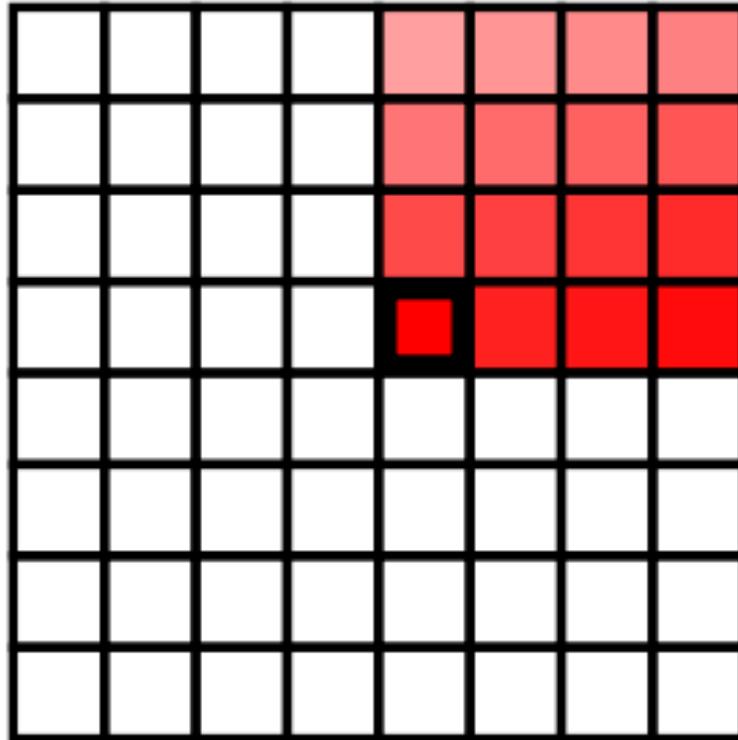


cache hits:719 ≈77%

C mem:308 cache hit:231

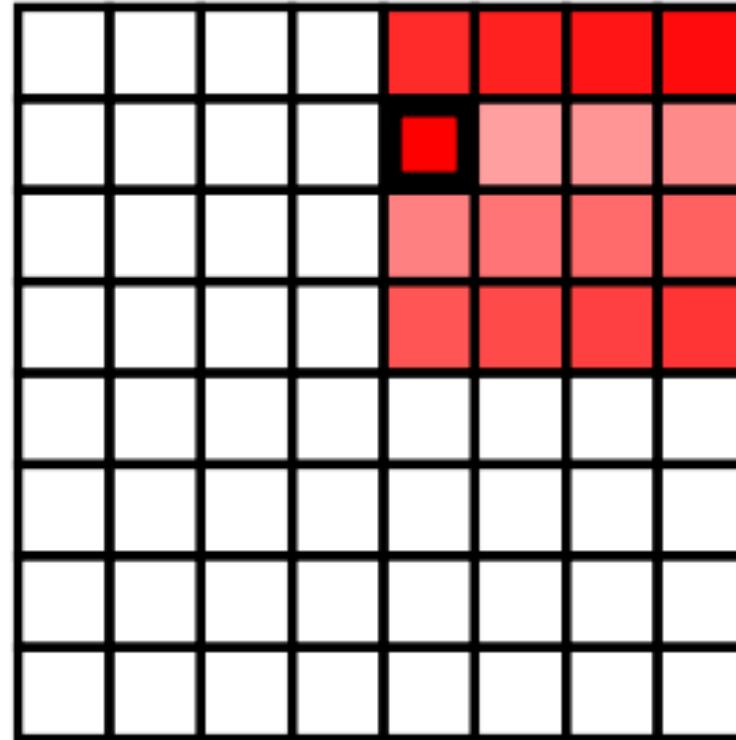


A mem:309 cache hit:229



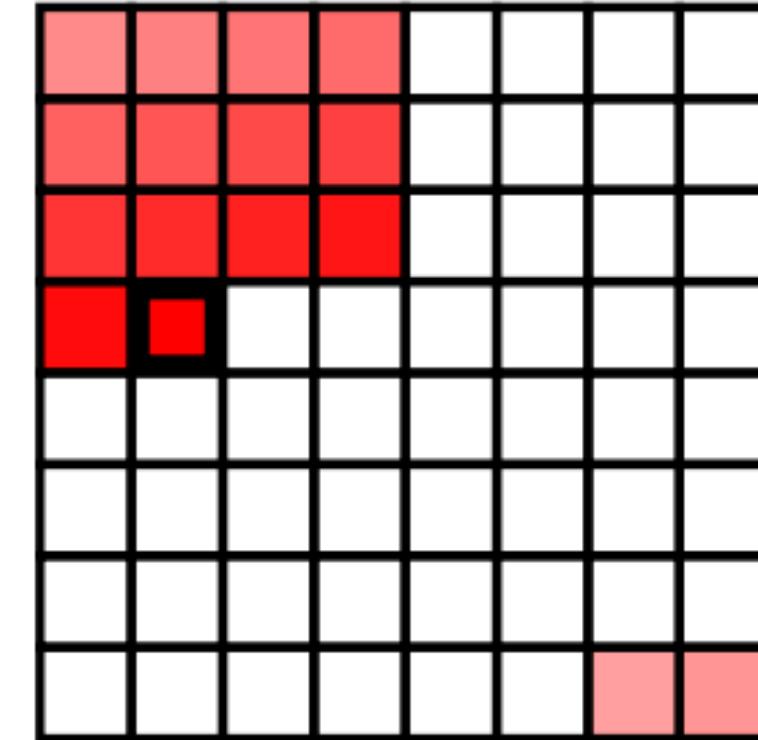
mem:927

B mem:309 cache hit:261

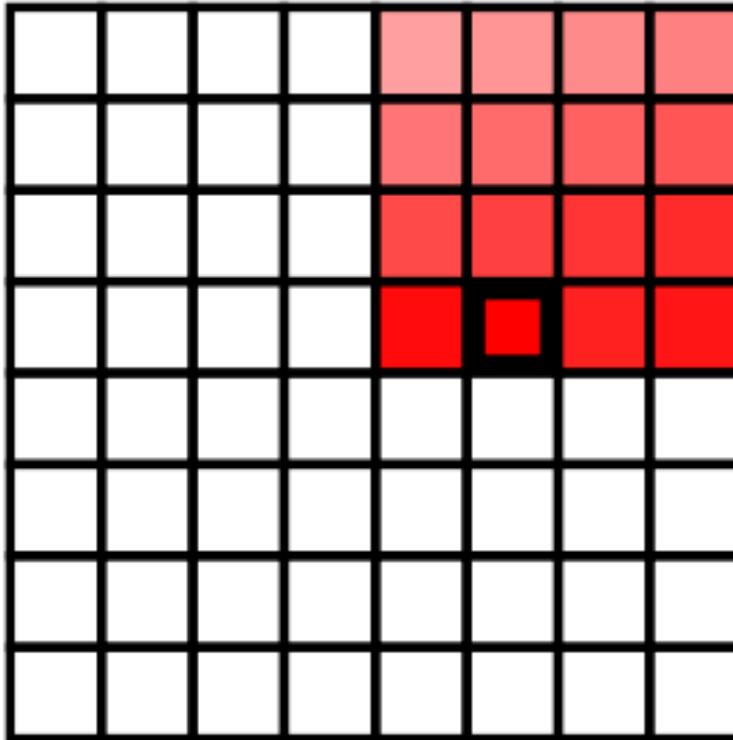


cache hits:721 $\approx 77\%$

C mem:309 cache hit:231

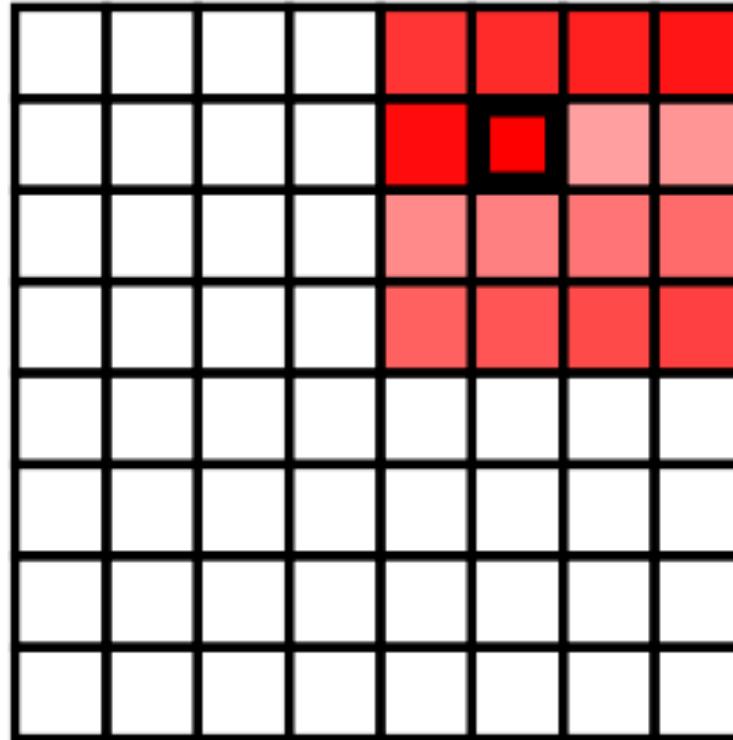


A mem:310 cache hit:230



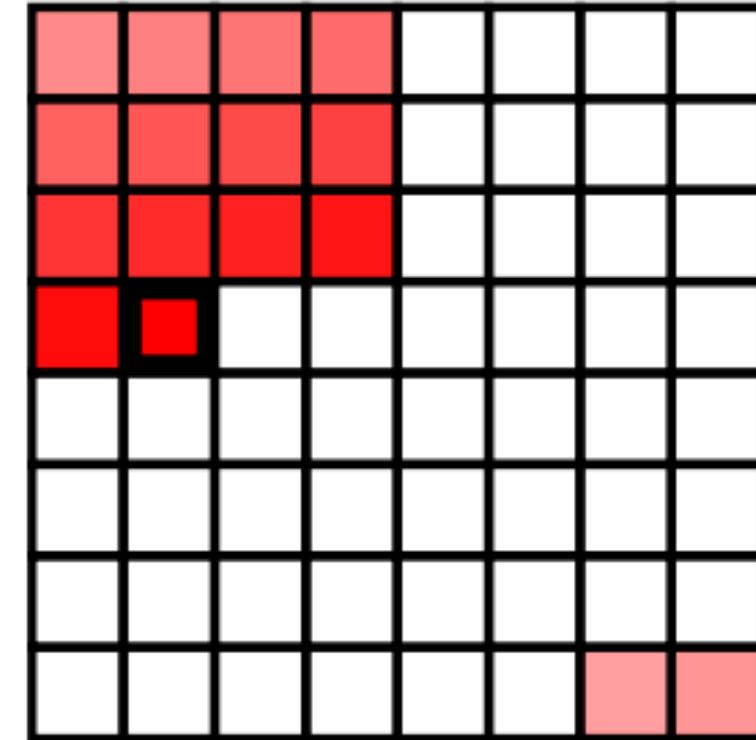
mem:930

B mem:310 cache hit:262

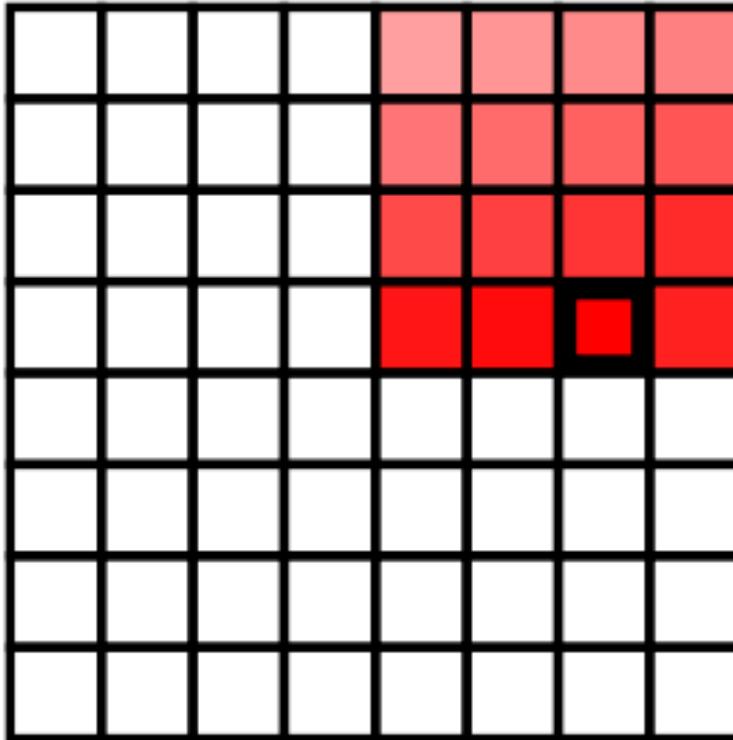


cache hits:724 ≈77%

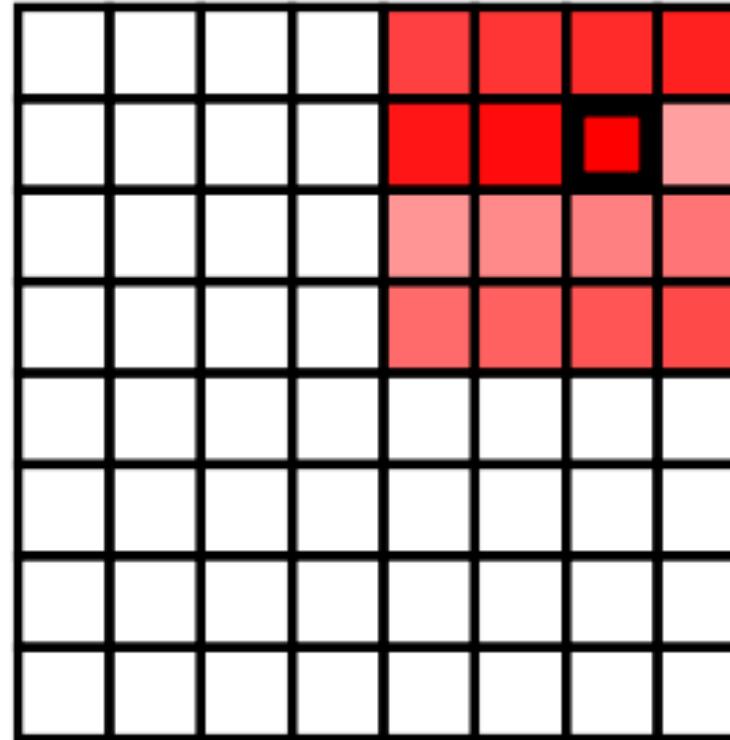
C mem:310 cache hit:232



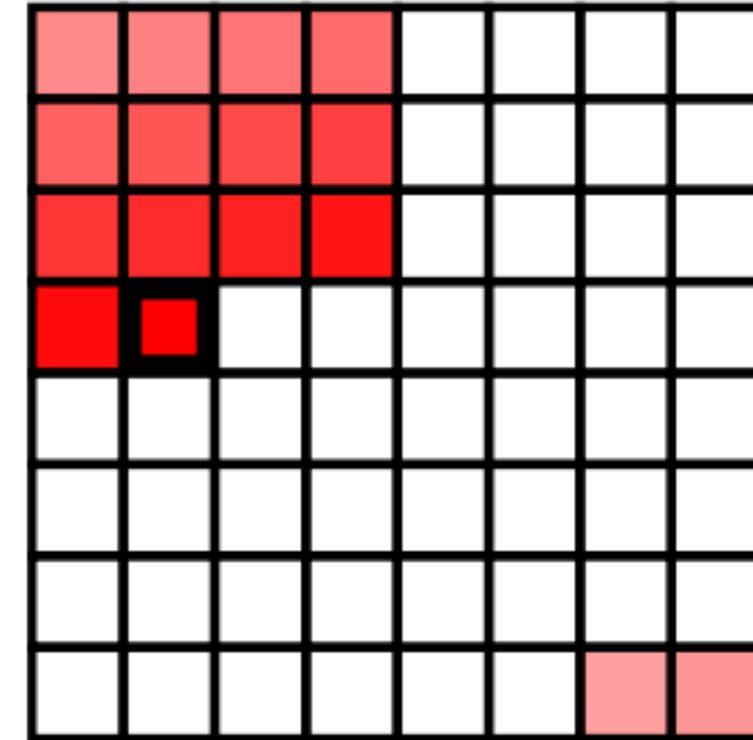
A mem:311 cache hit:231



B mem:311 cache hit:263



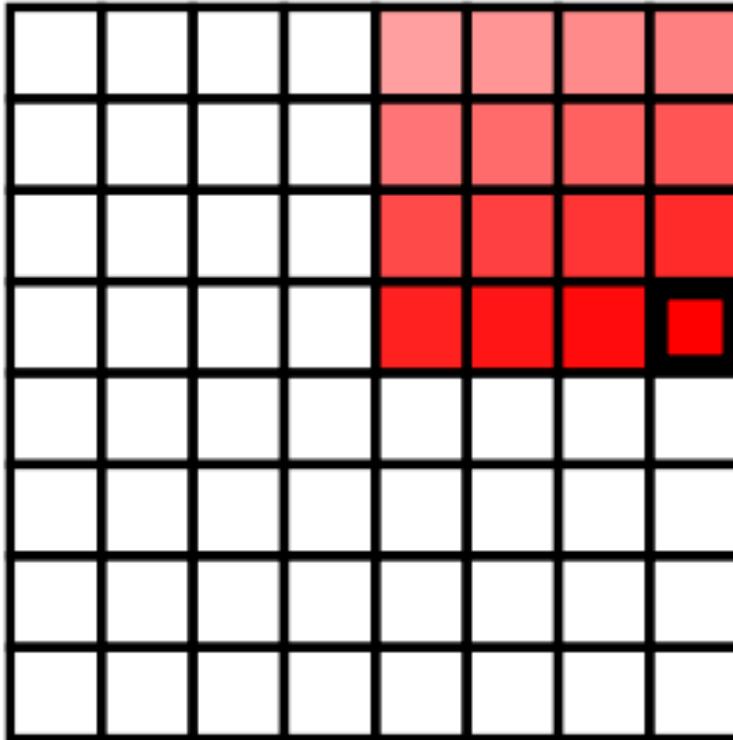
C mem:311 cache hit:233



mem:933

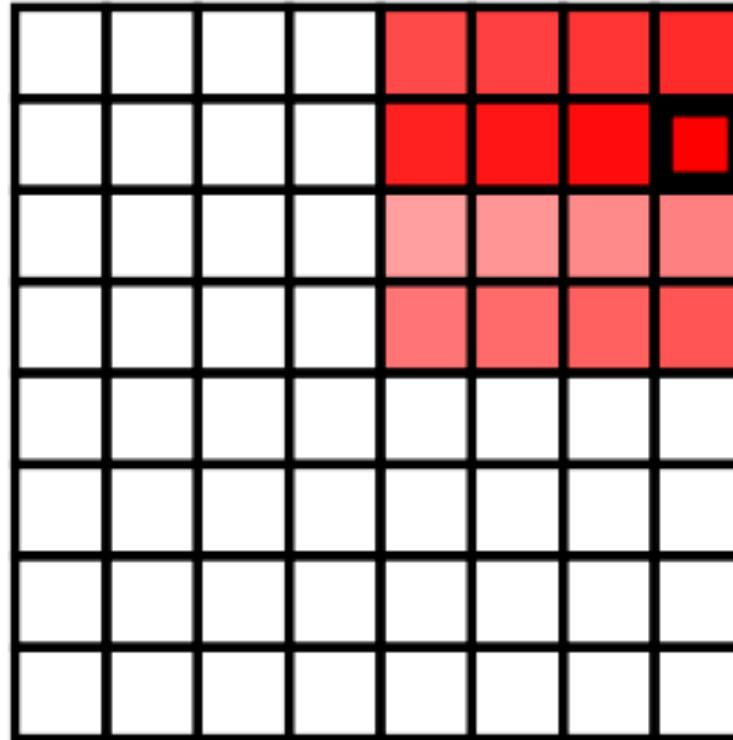
cache hits:727 $\approx 77\%$

A mem:312 cache hit:232



mem:936

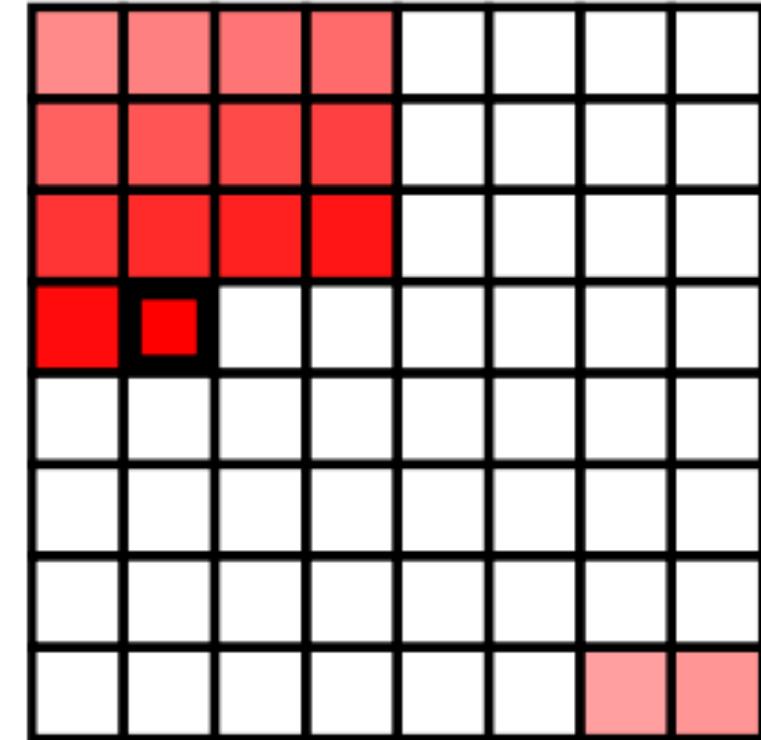
B mem:312 cache hit:264



cache hits:730 $\approx 77\%$

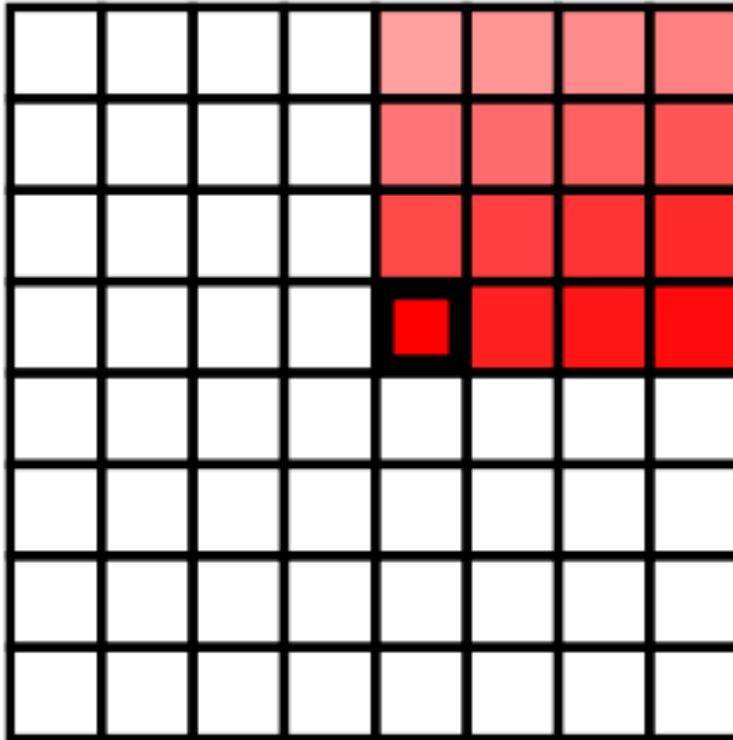
\times

C mem:312 cache hit:234



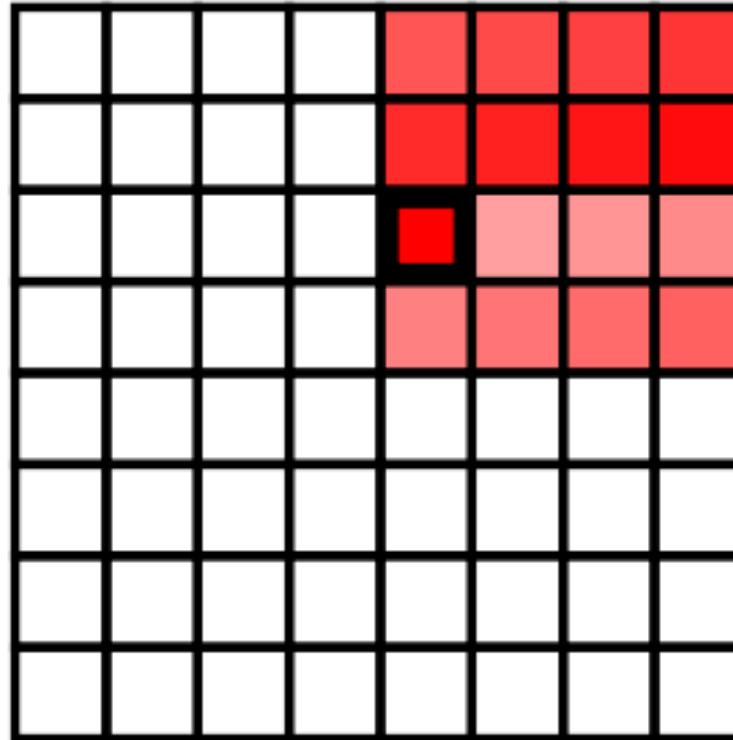
=

A mem:313 cache hit:233



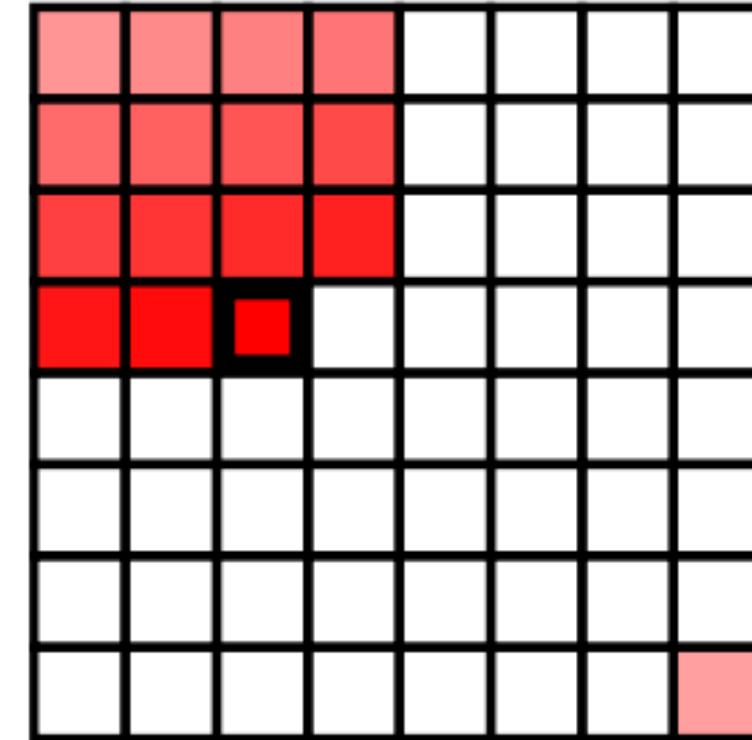
mem:939

B mem:313 cache hit:265

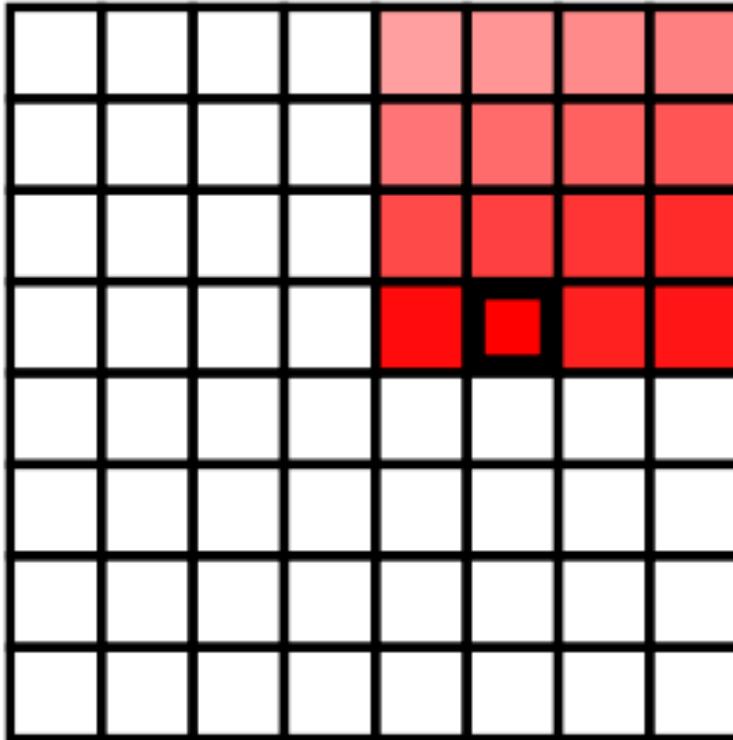


cache hits:732 $\approx 77\%$

C mem:313 cache hit:234

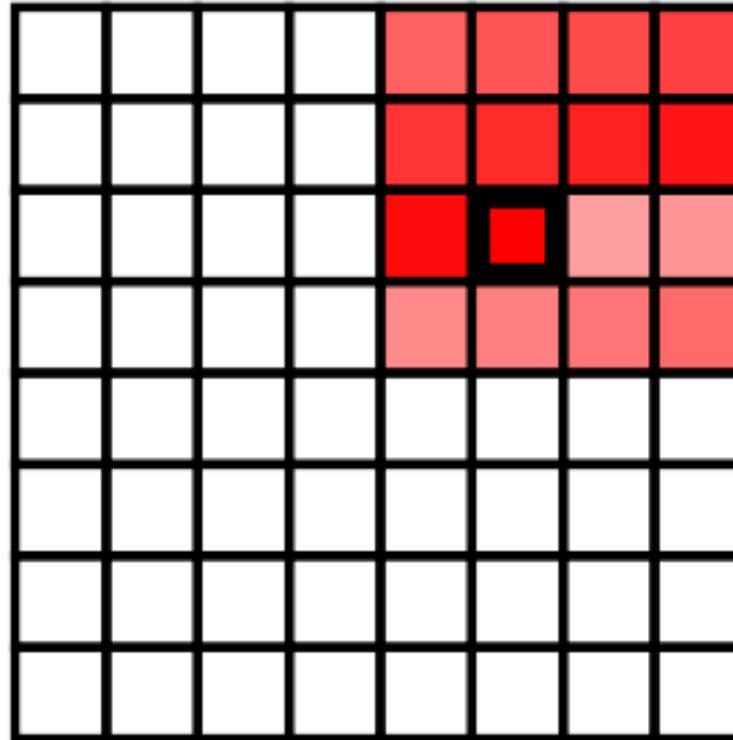


A mem:314 cache hit:234



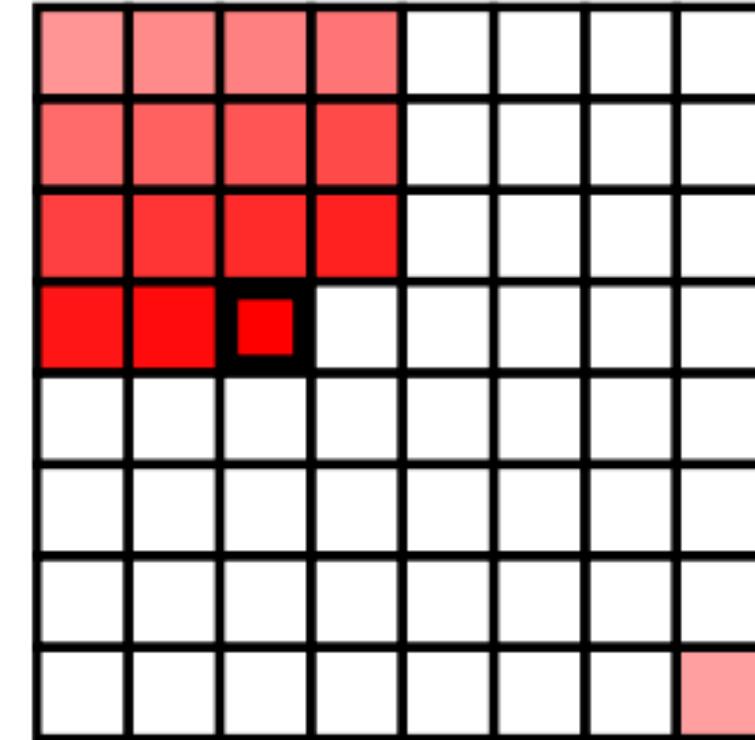
mem:942

B mem:314 cache hit:266

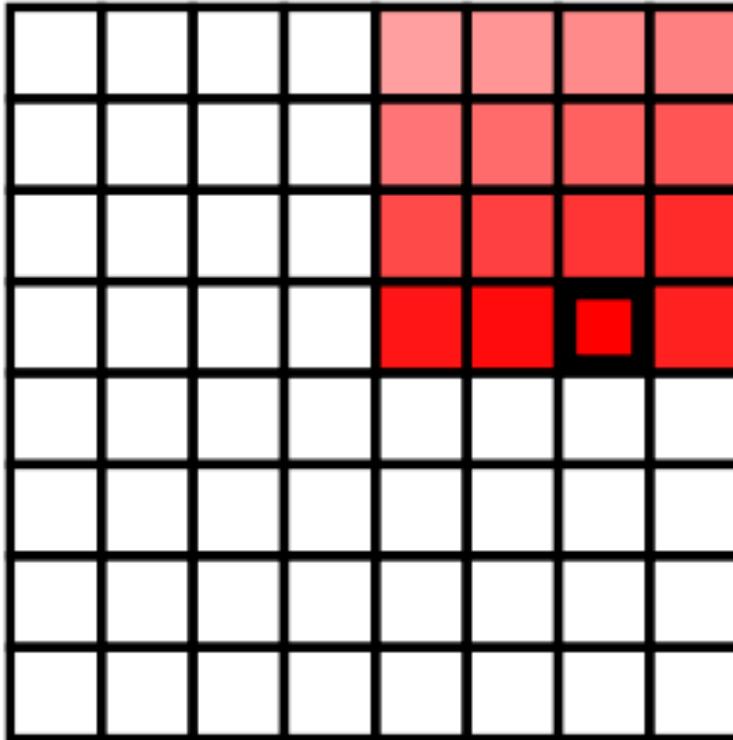


cache hits:735 ≈78%

C mem:314 cache hit:235

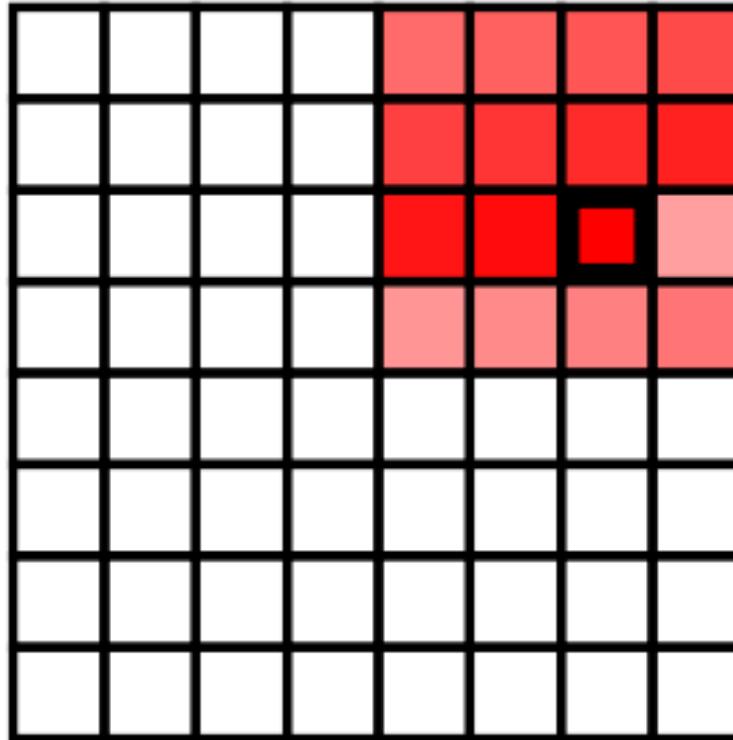


A mem:315 cache hit:235



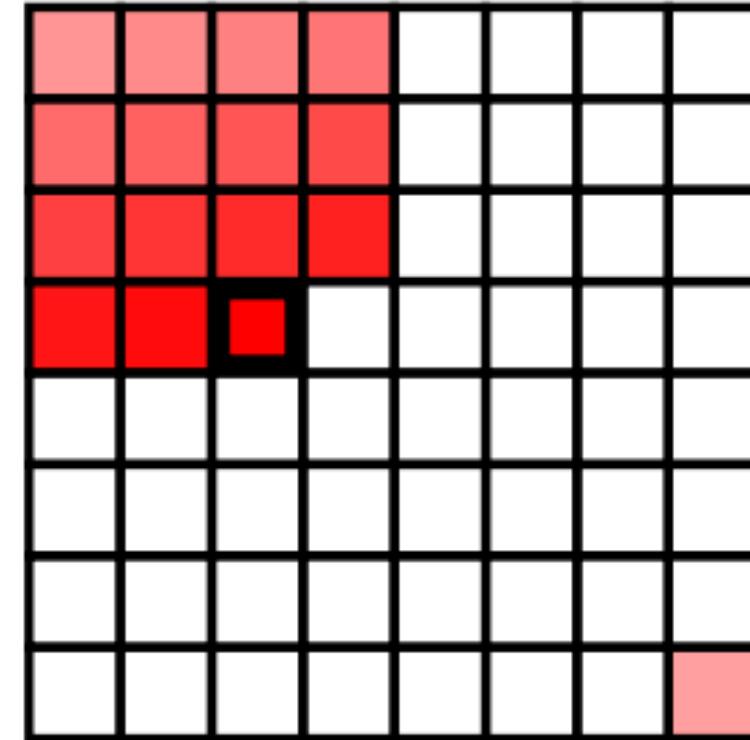
mem:945

B mem:315 cache hit:267

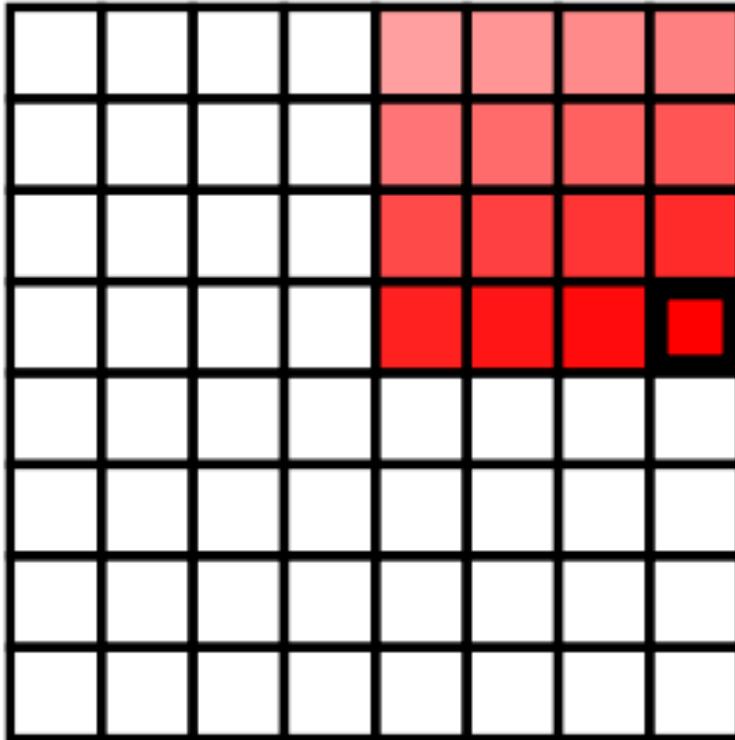


cache hits:738 $\approx 78\%$

C mem:315 cache hit:236

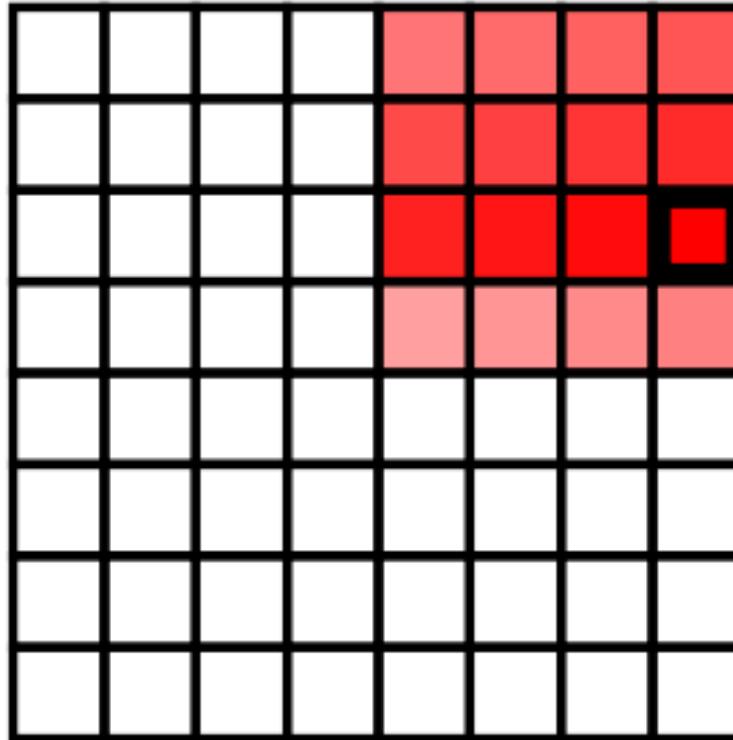


A mem:316 cache hit:236



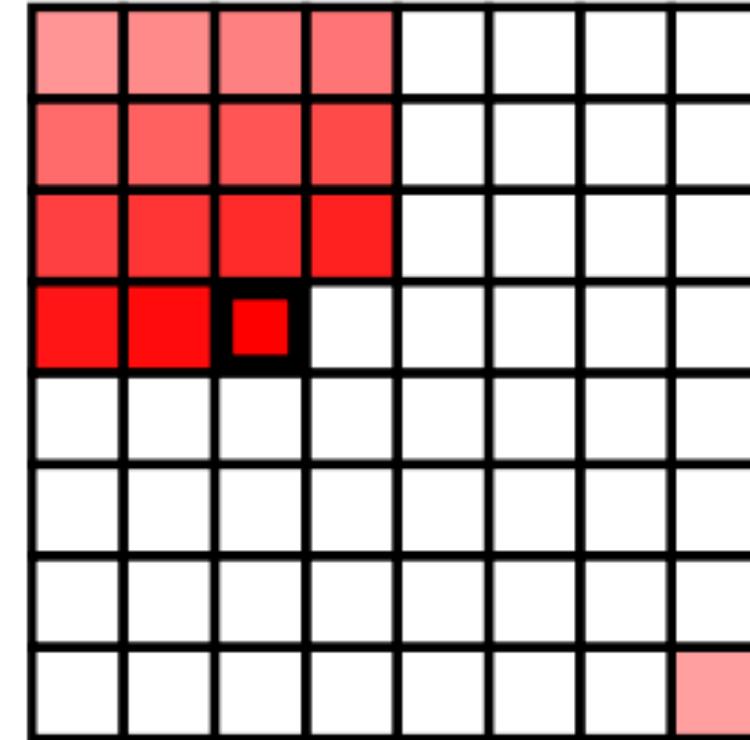
mem:948

B mem:316 cache hit:268

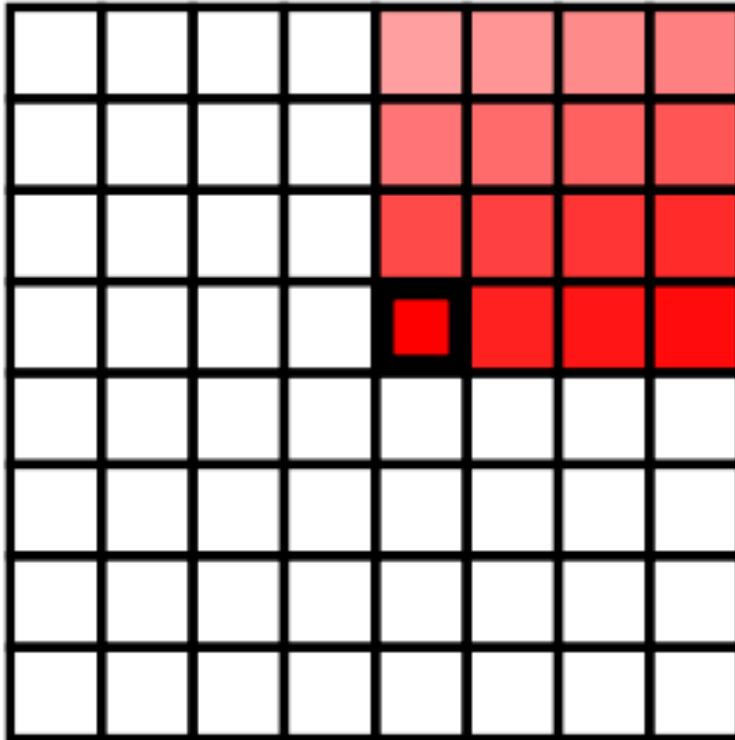


cache hits:741 $\approx 78\%$

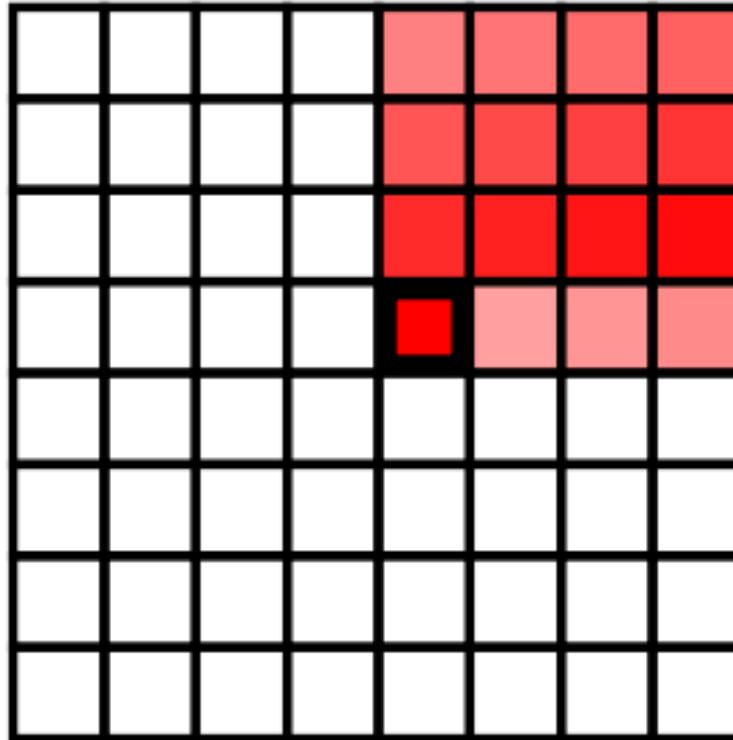
C mem:316 cache hit:237



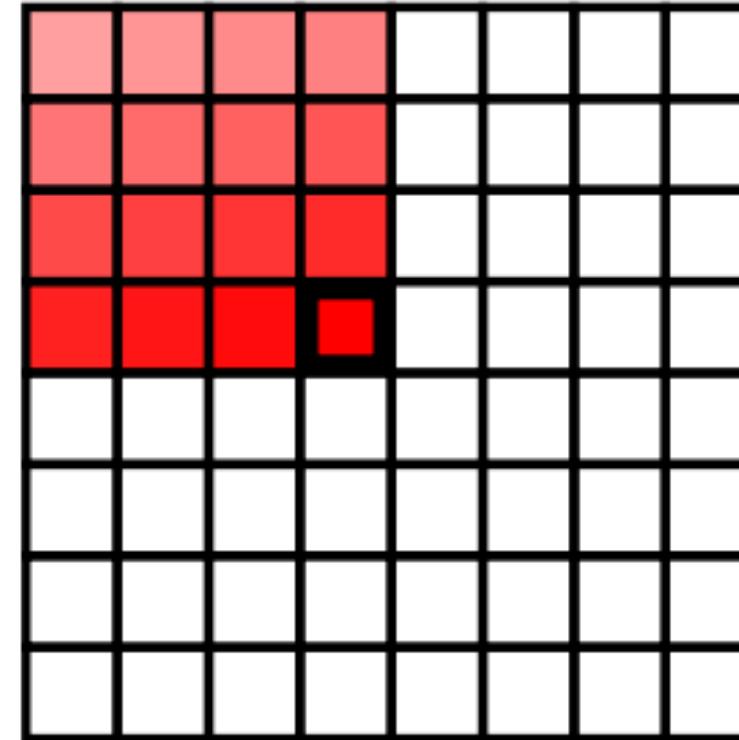
A mem:317 cache hit:237



B mem:317 cache hit:269



C mem:317 cache hit:237



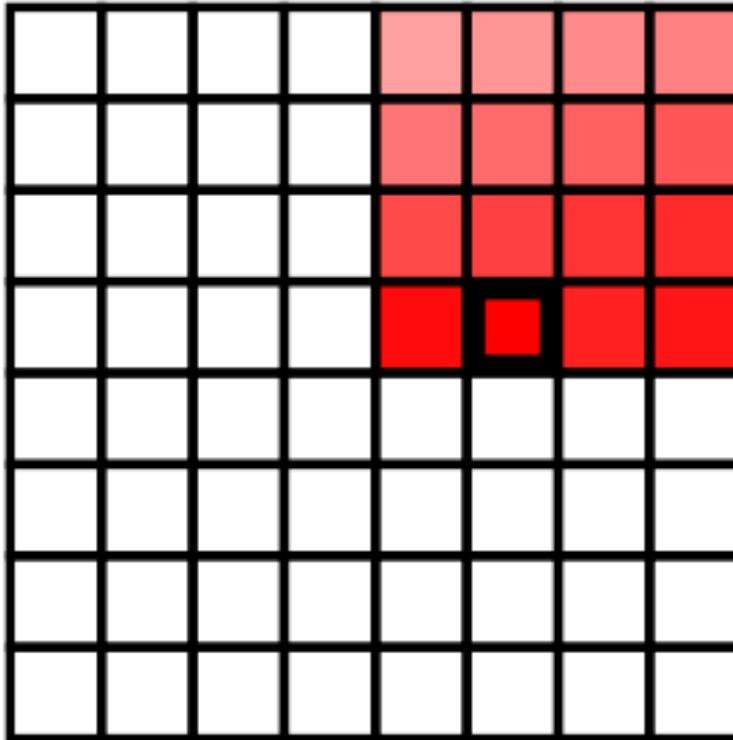
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mem:951

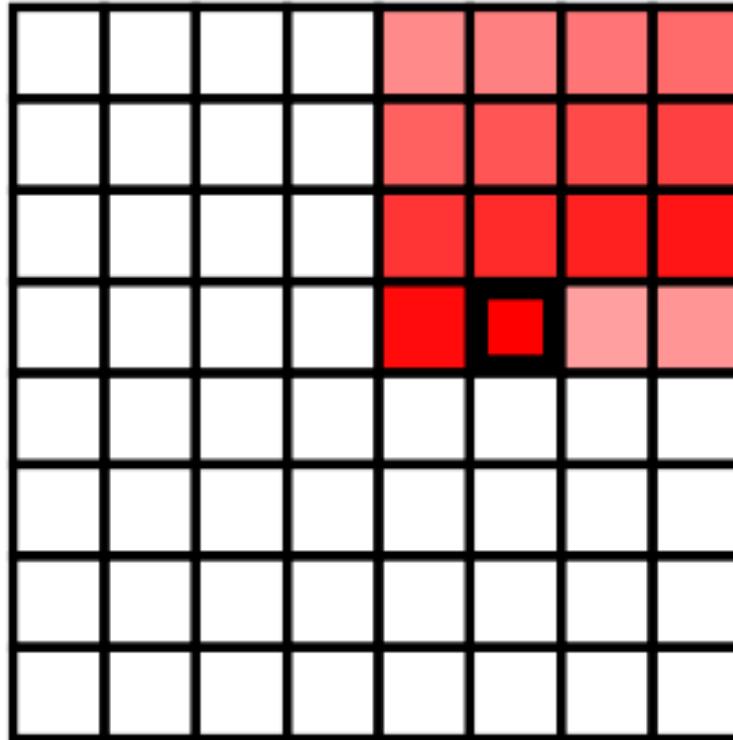
cache hits:743 ≈78%

A mem:318 cache hit:238



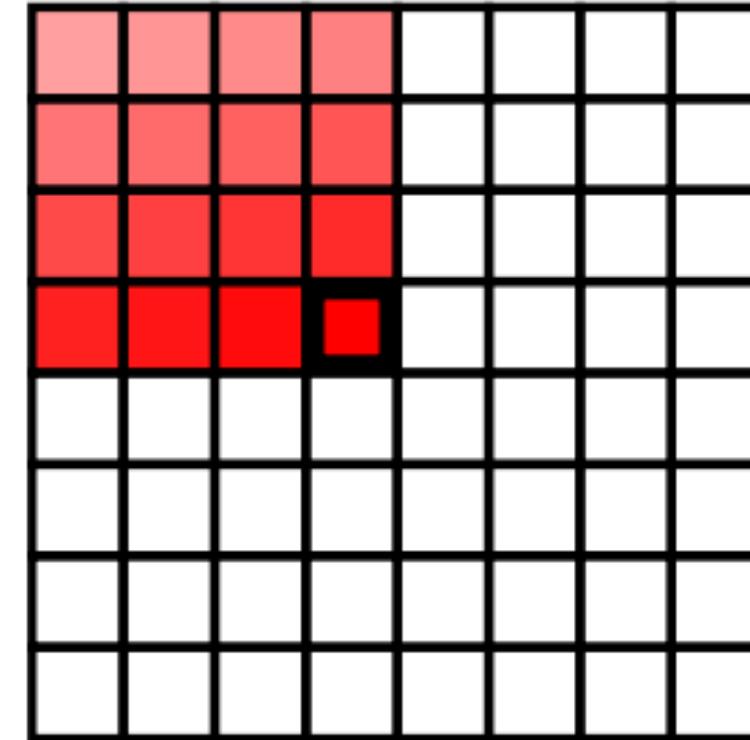
mem:954

B mem:318 cache hit:270

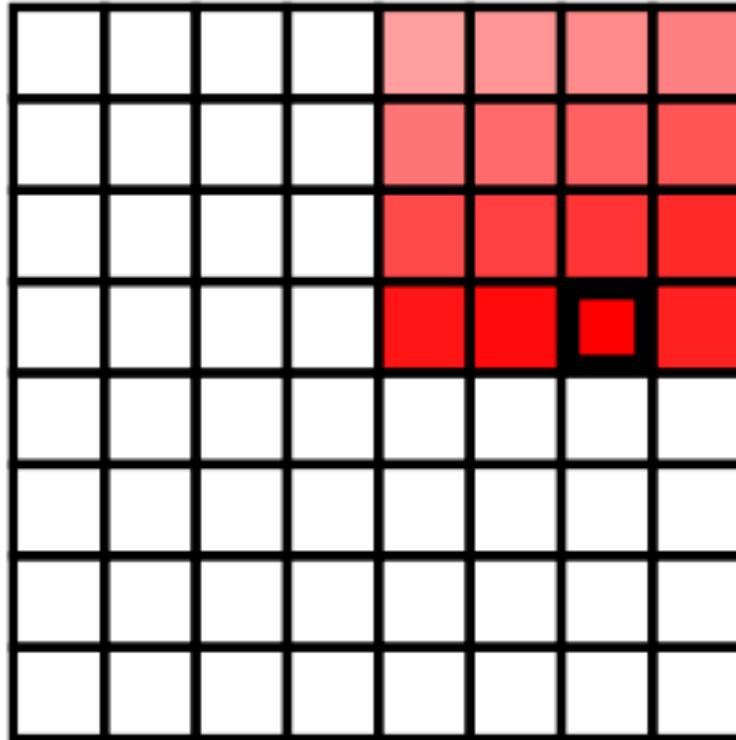


cache hits:746 ≈78%

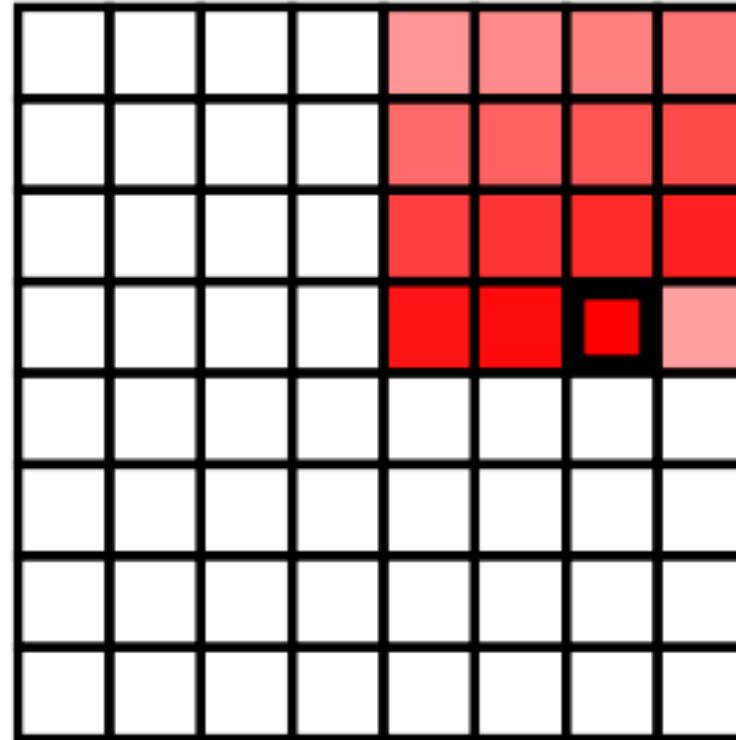
C mem:318 cache hit:238



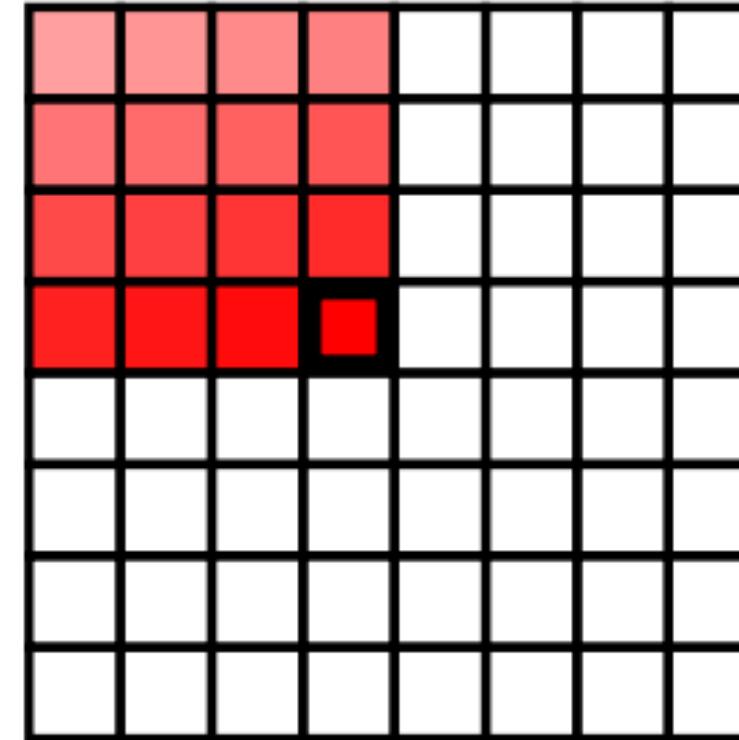
A mem:319 cache hit:239



B mem:319 cache hit:271



C mem:319 cache hit:239



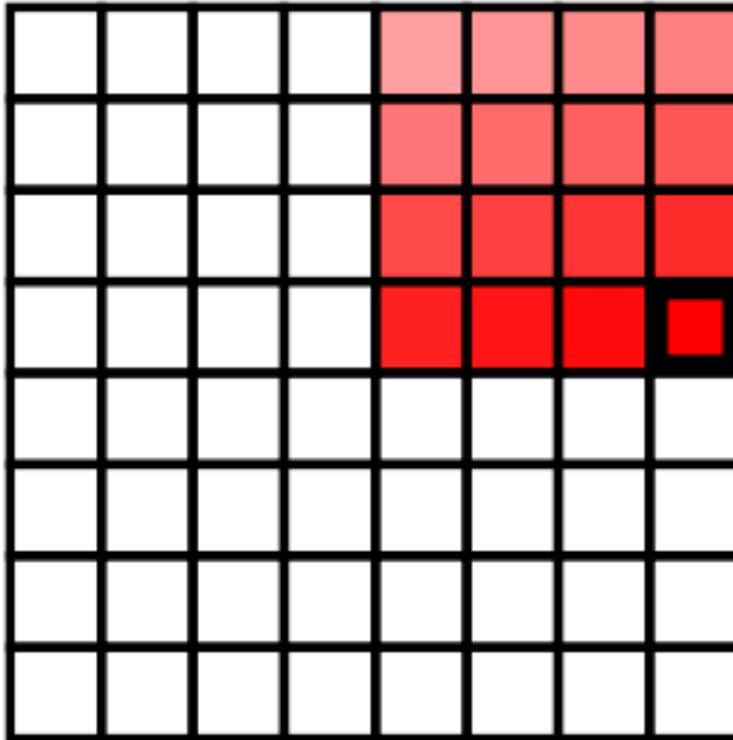
×

=

mem:957

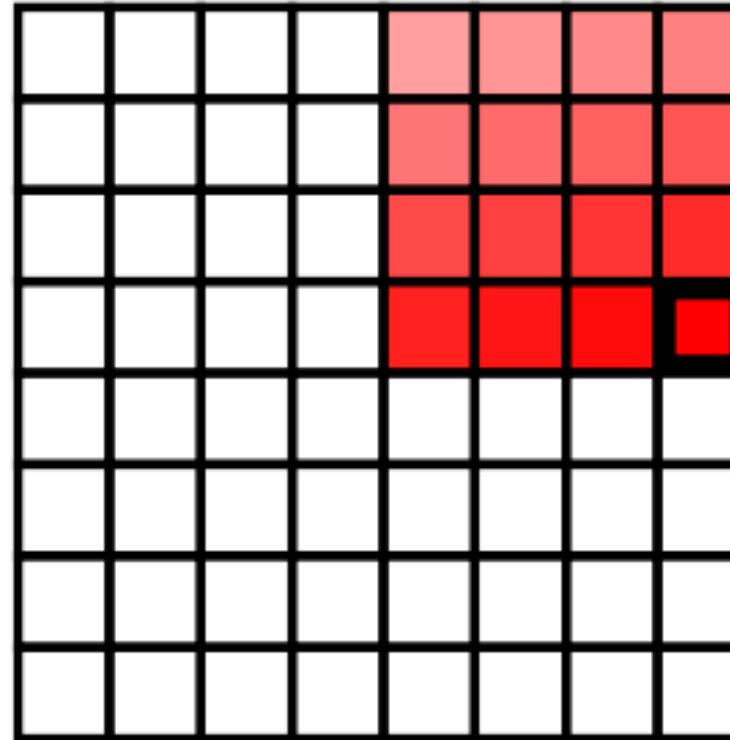
cache hits:749 \approx 78%

A mem:320 cache hit:240



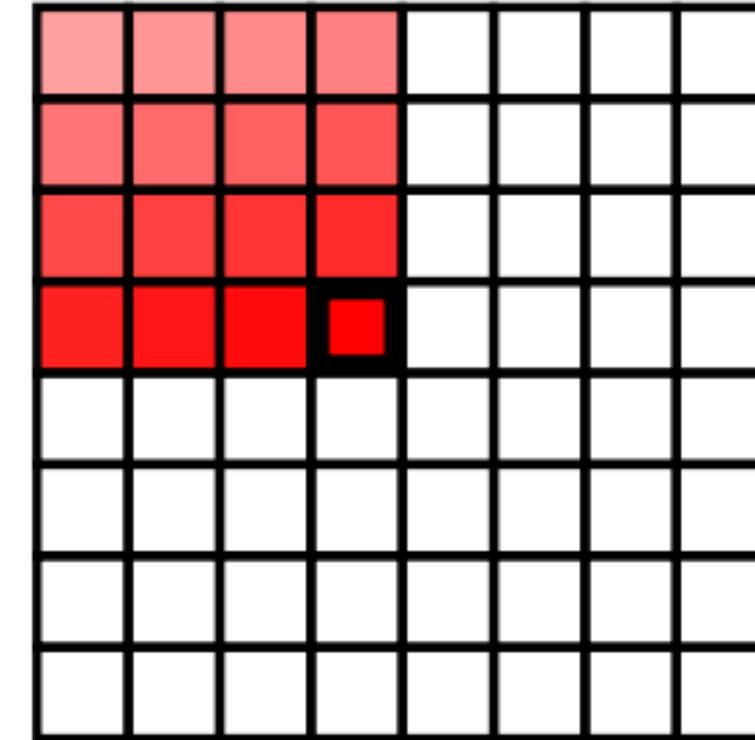
mem:960

B mem:320 cache hit:272

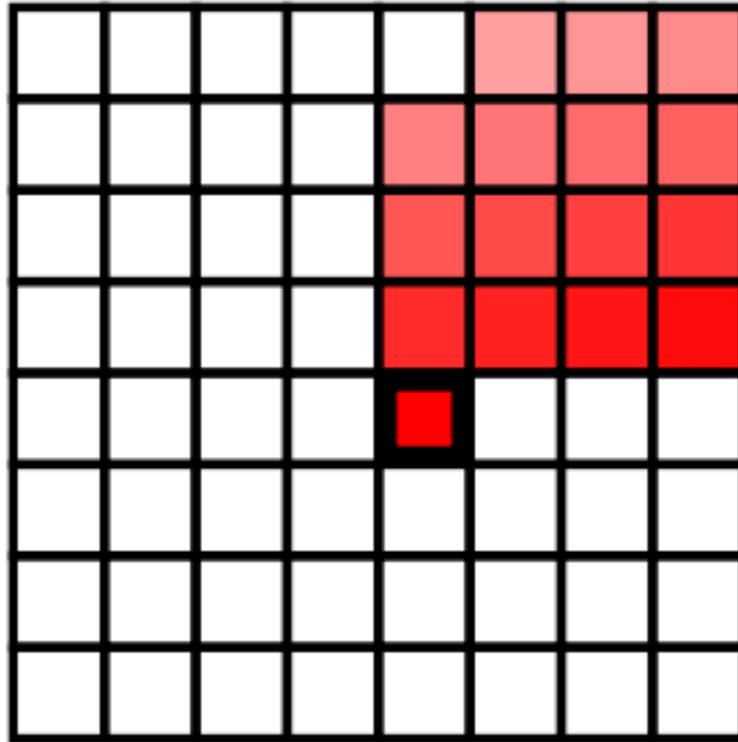


cache hits:752 ≈78%

C mem:320 cache hit:240

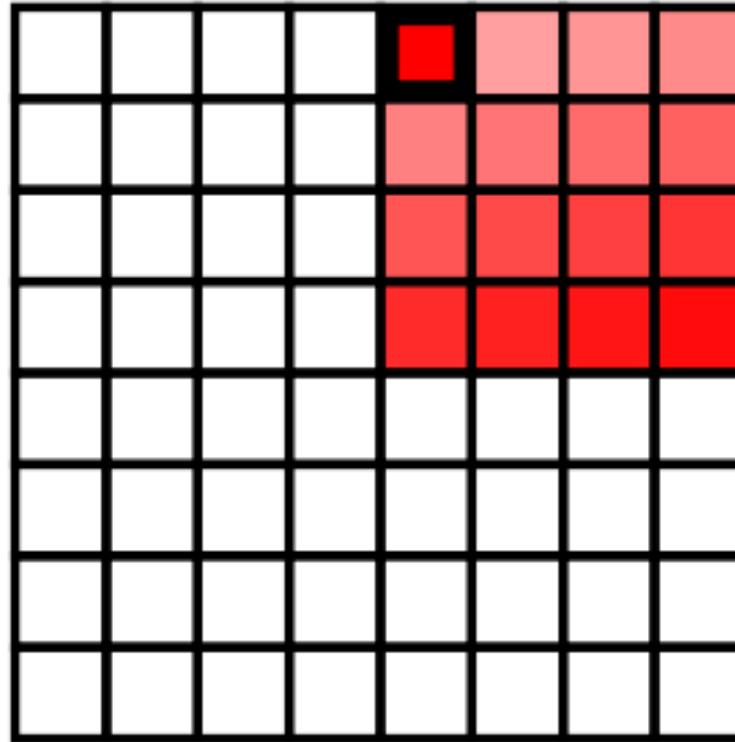


A mem:321 cache hit:240



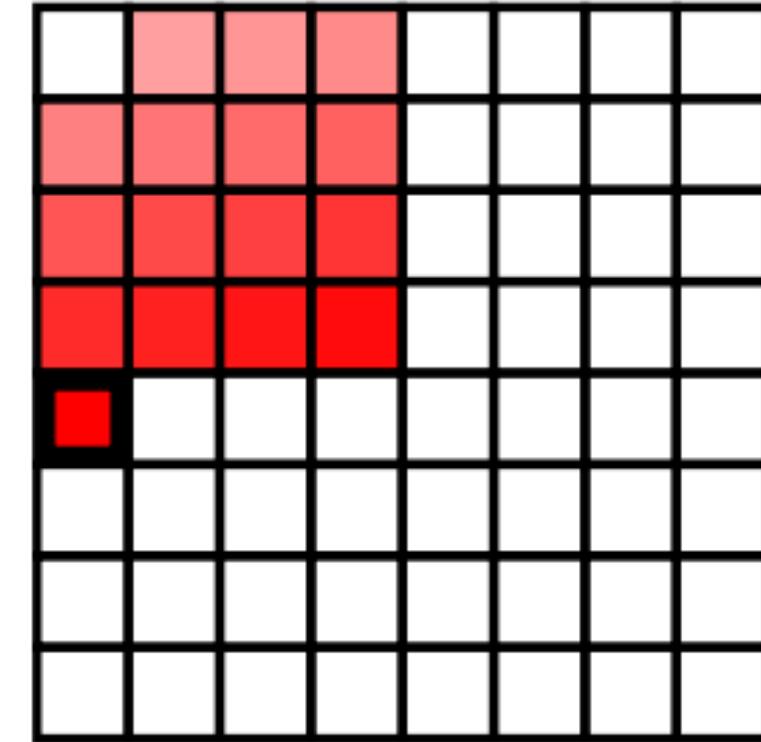
mem:963

B mem:321 cache hit:273

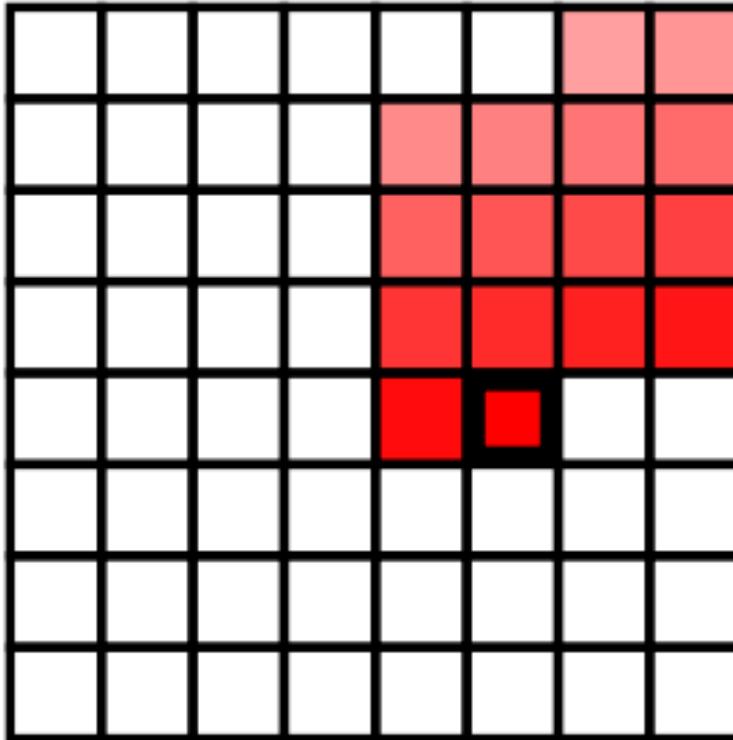


cache hits:753 ≈78%

C mem:321 cache hit:240

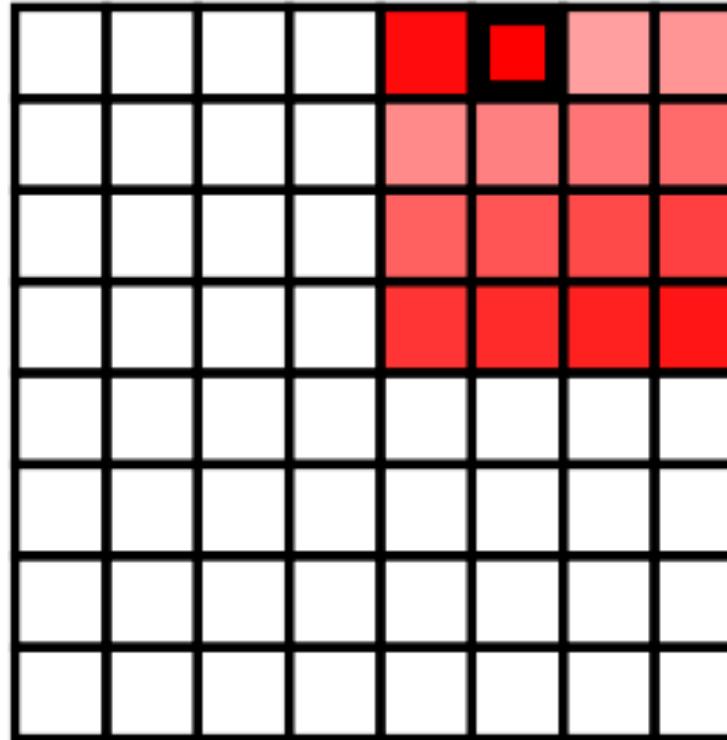


A mem:322 cache hit:240



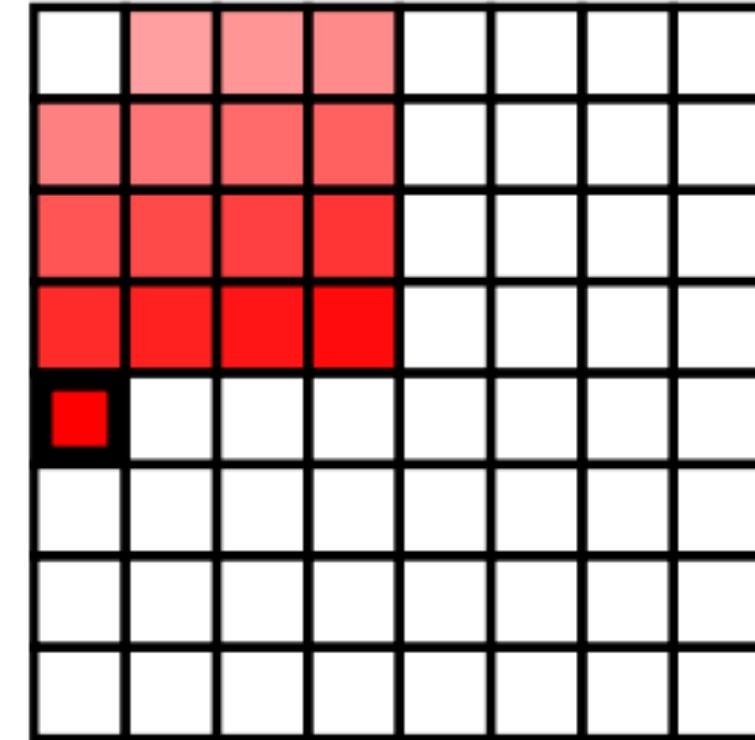
mem:966

B mem:322 cache hit:274

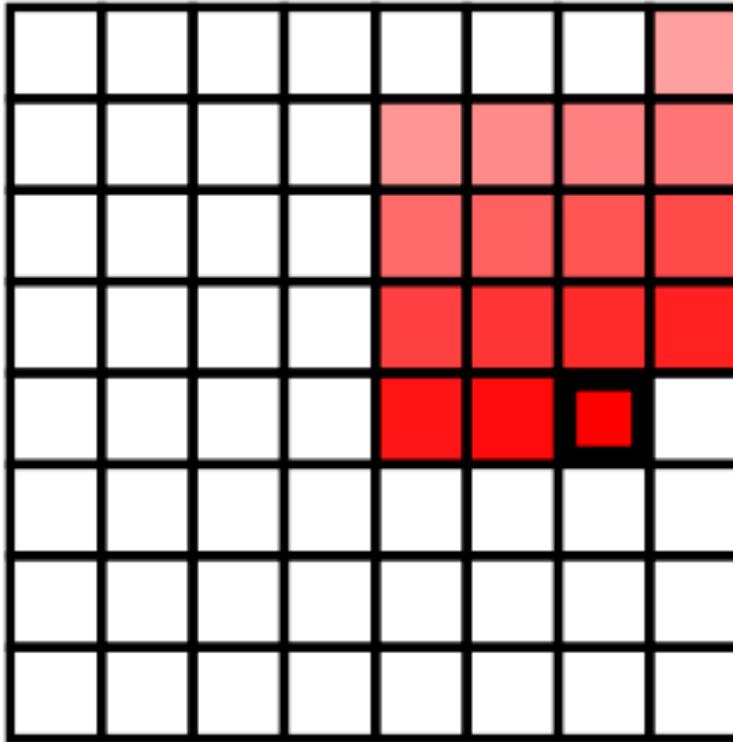


cache hits:755 ≈78%

C mem:322 cache hit:241

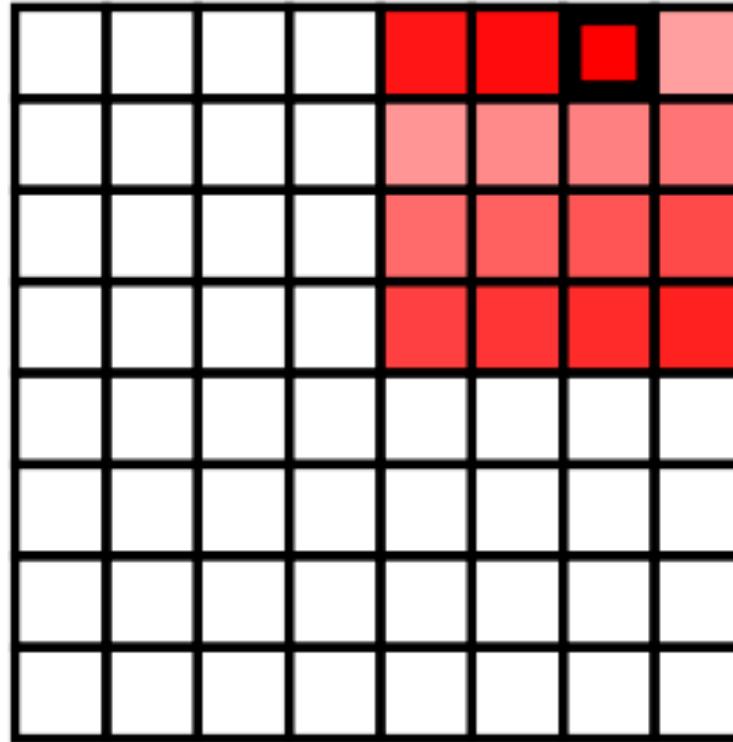


A mem:323 cache hit:240



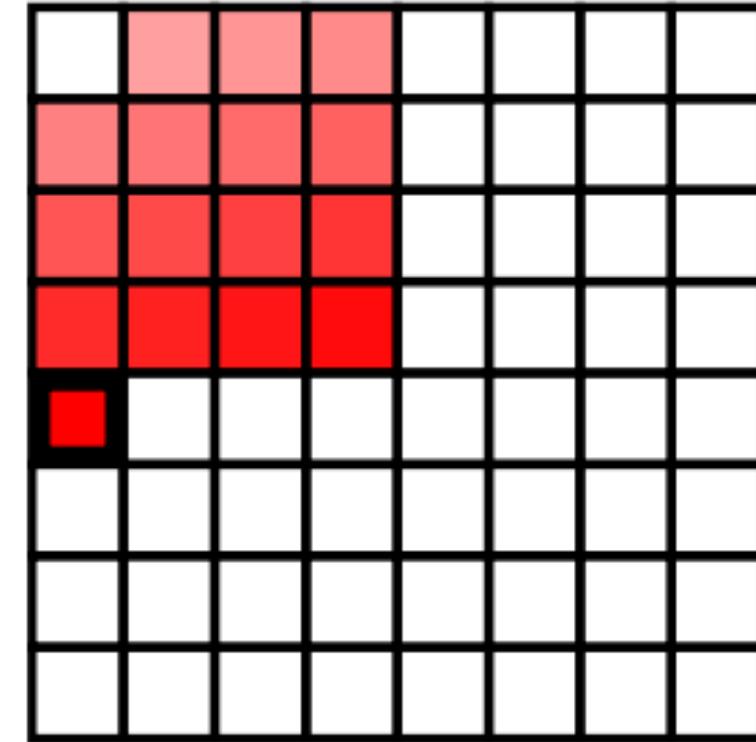
mem:969

B mem:323 cache hit:275

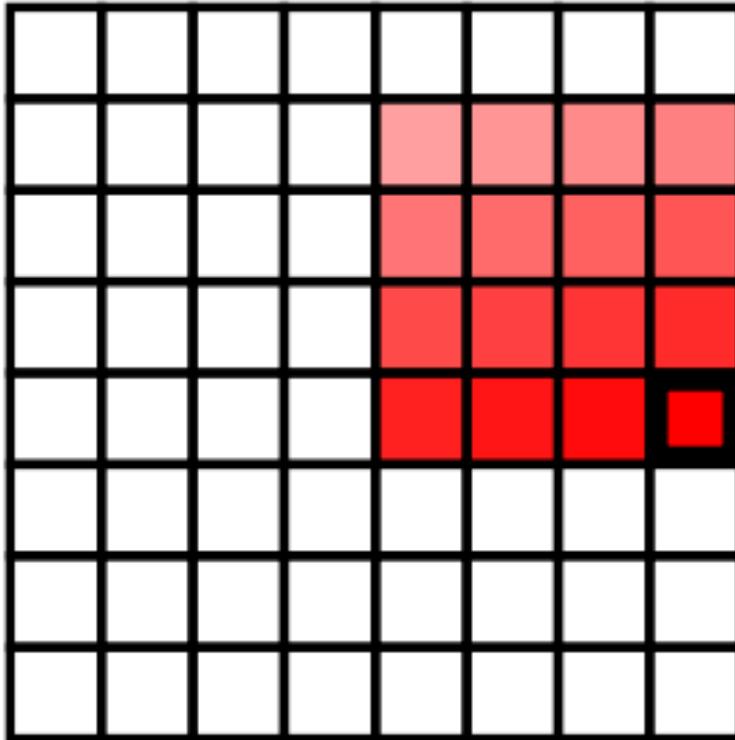


cache hits:757 ≈78%

C mem:323 cache hit:242

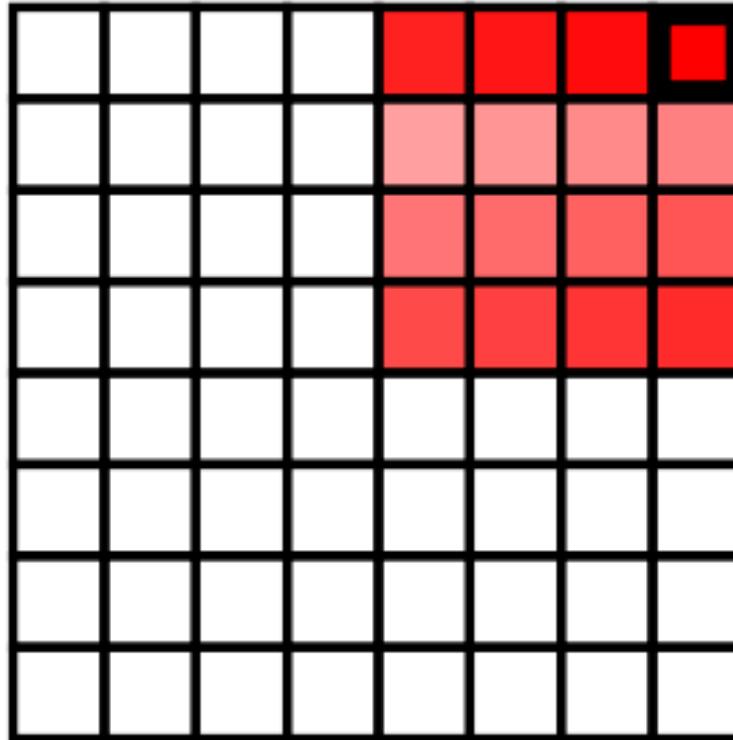


A mem:324 cache hit:240



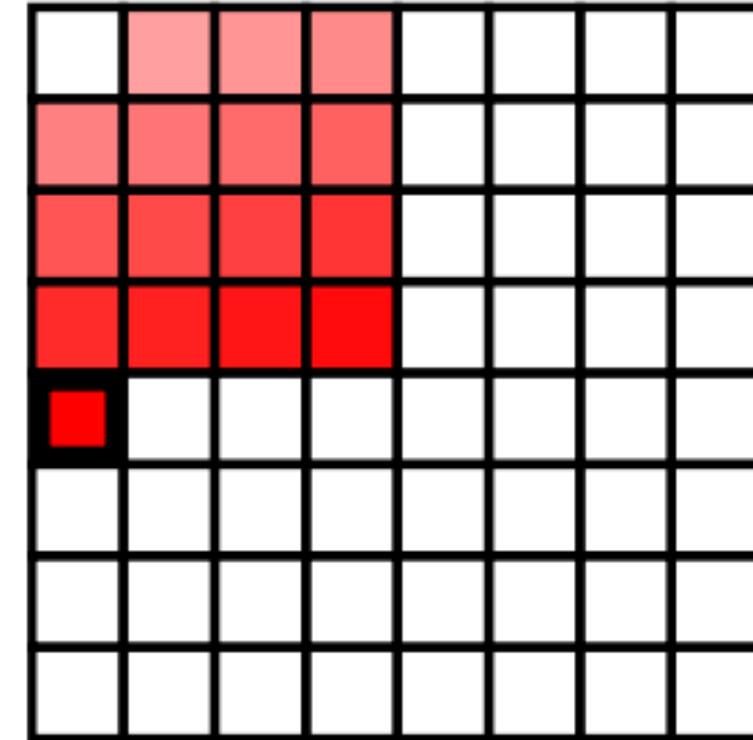
mem:972

B mem:324 cache hit:276

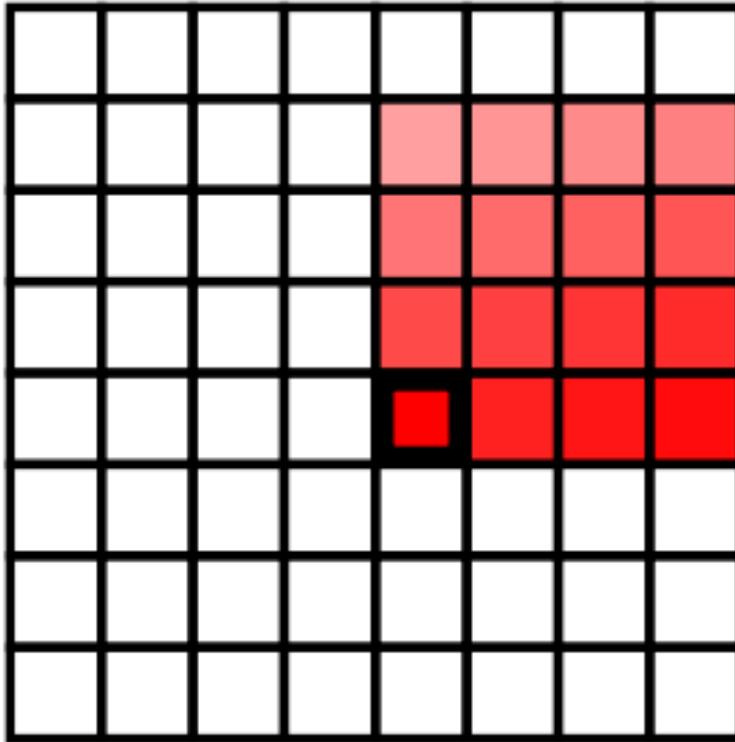


cache hits:759 ≈78%

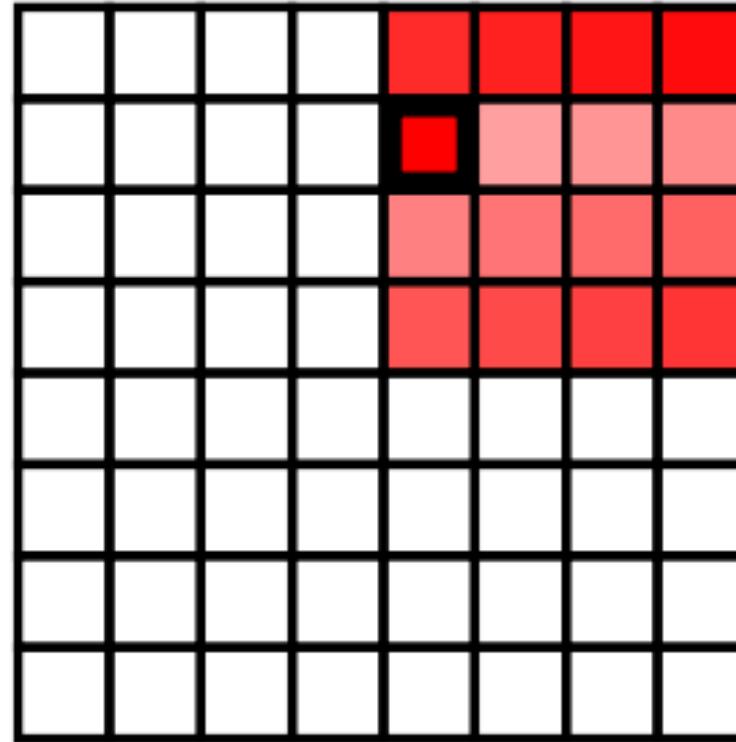
C mem:324 cache hit:243



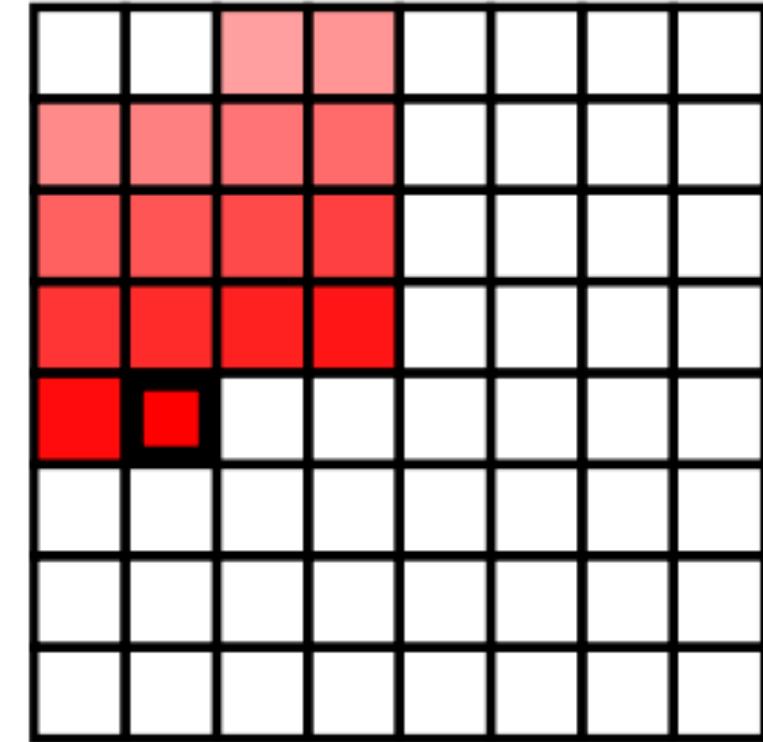
A mem:325 cache hit:241



B mem:325 cache hit:277



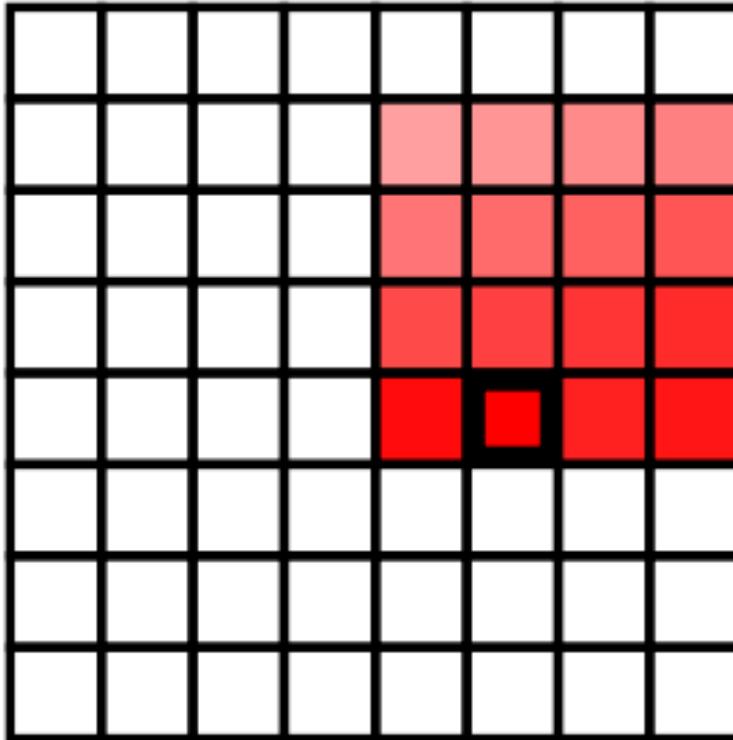
C mem:325 cache hit:243



mem:975

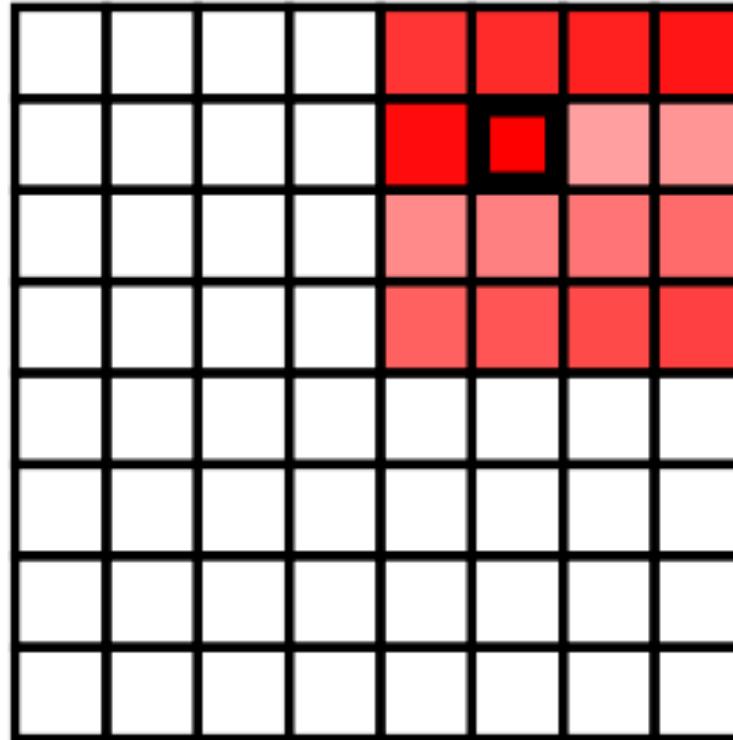
cache hits:761 $\approx 78\%$

A mem:326 cache hit:242



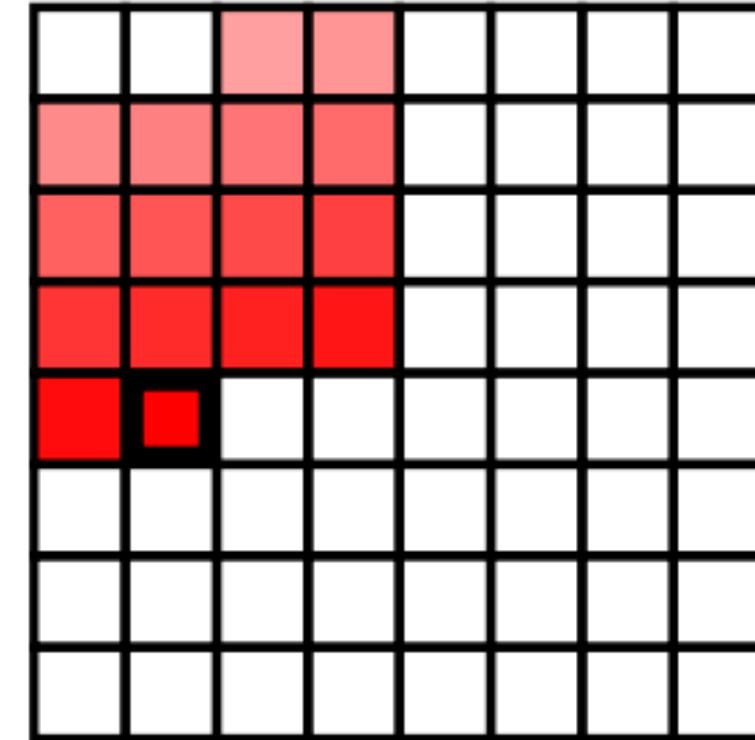
mem:978

B mem:326 cache hit:278

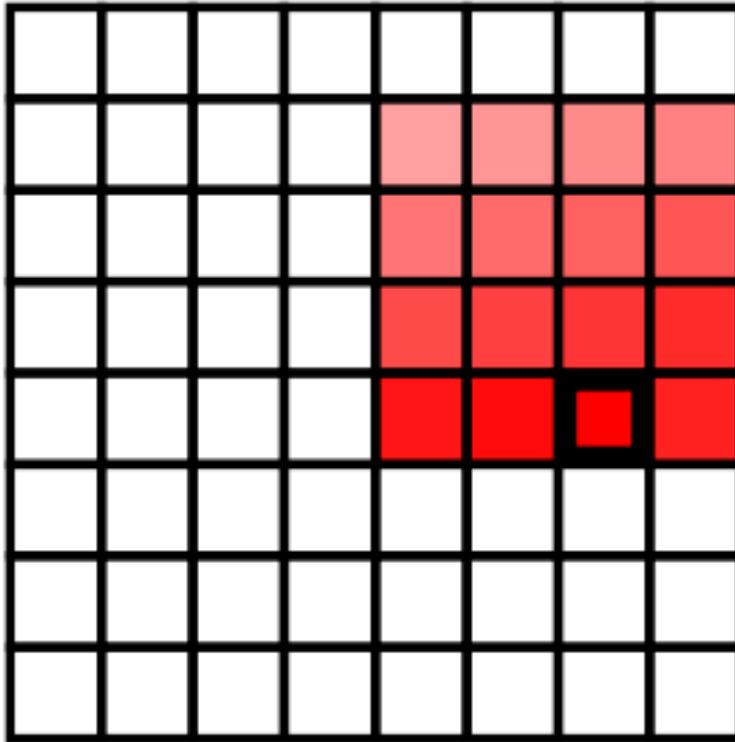


cache hits:764 $\approx 78\%$

C mem:326 cache hit:244

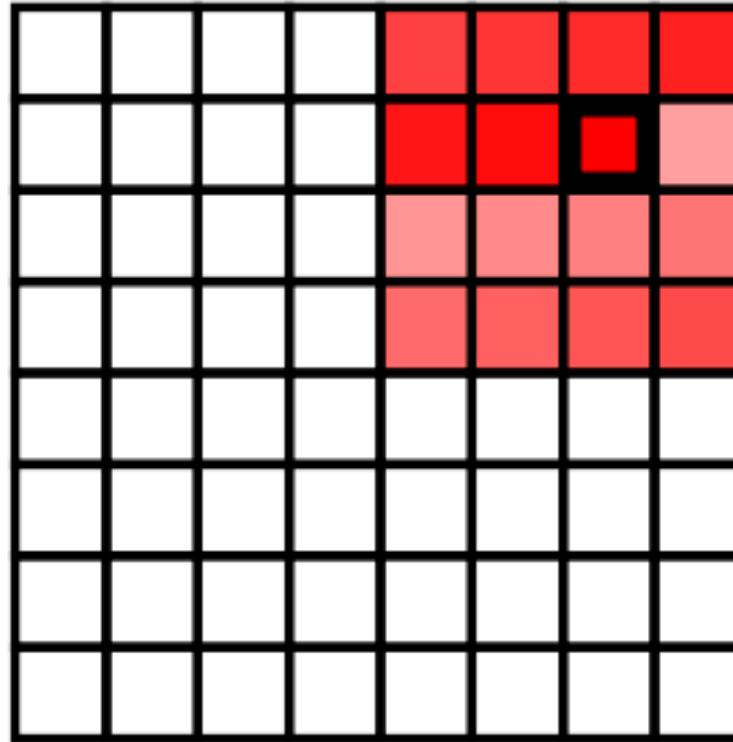


A mem:327 cache hit:243



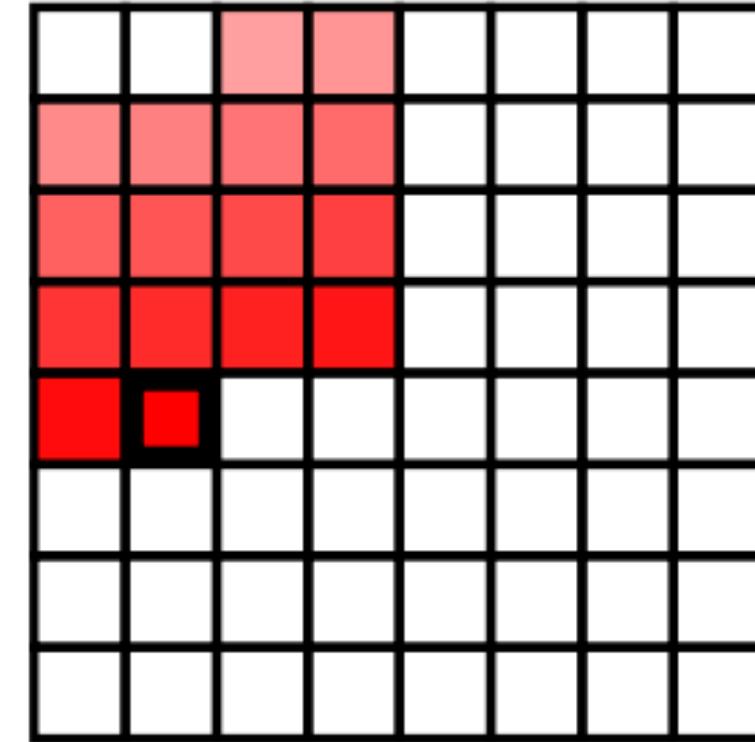
mem:981

B mem:327 cache hit:279

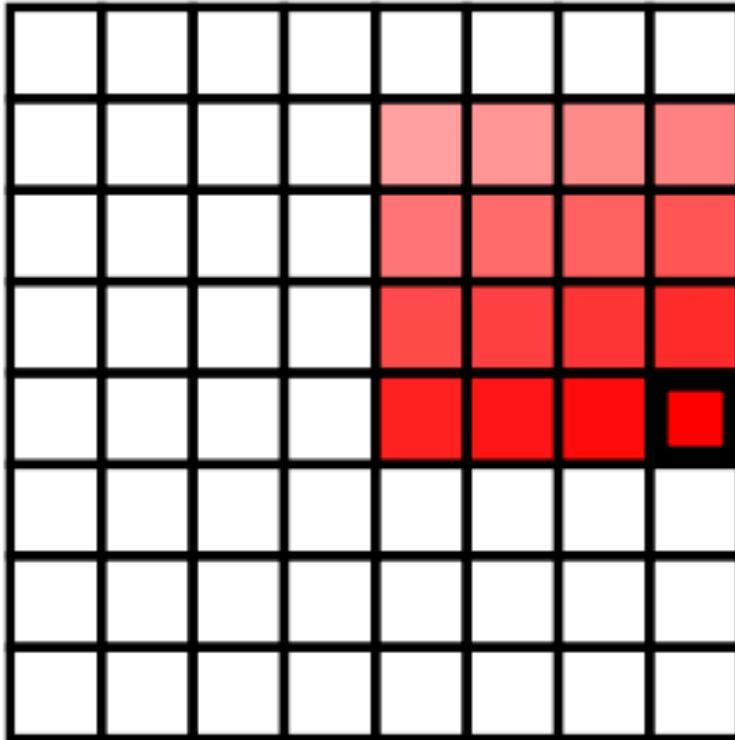


cache hits:767 ≈78%

C mem:327 cache hit:245

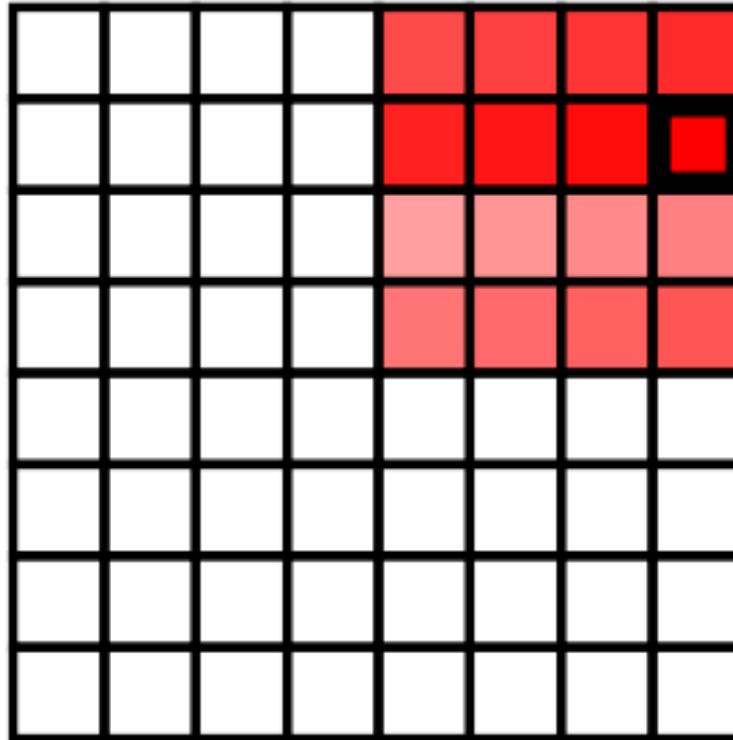


A mem:328 cache hit:244



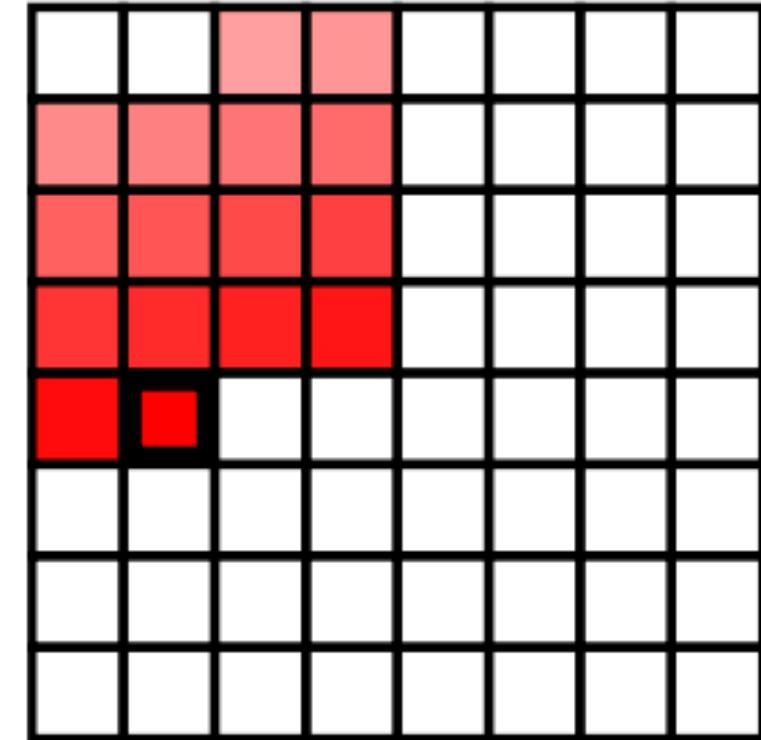
mem:984

B mem:328 cache hit:280

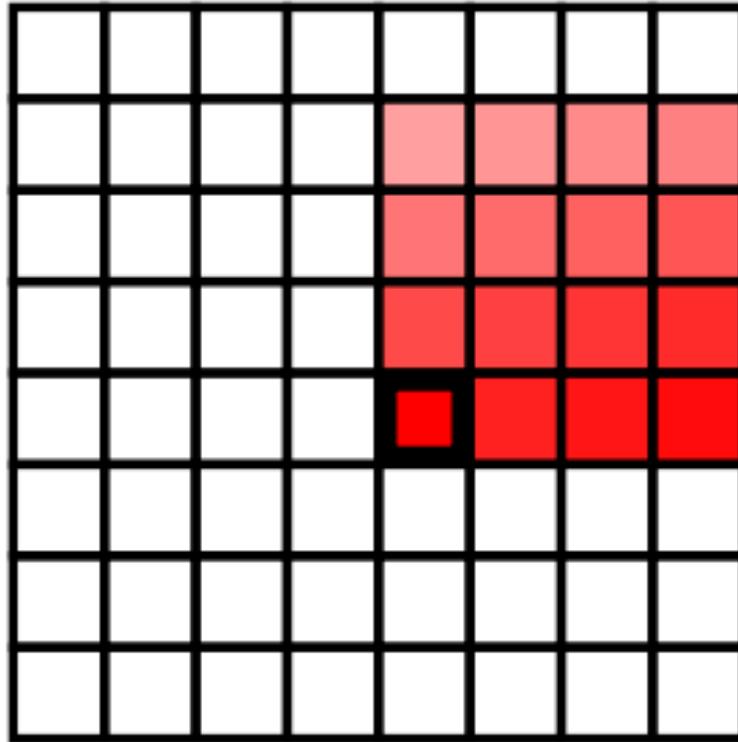


cache hits:770 $\approx 78\%$

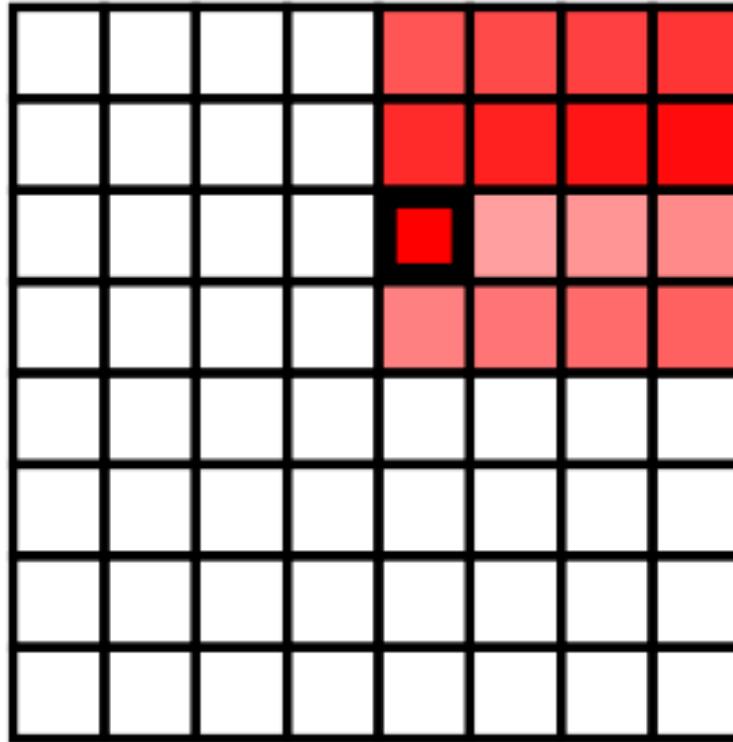
C mem:328 cache hit:246



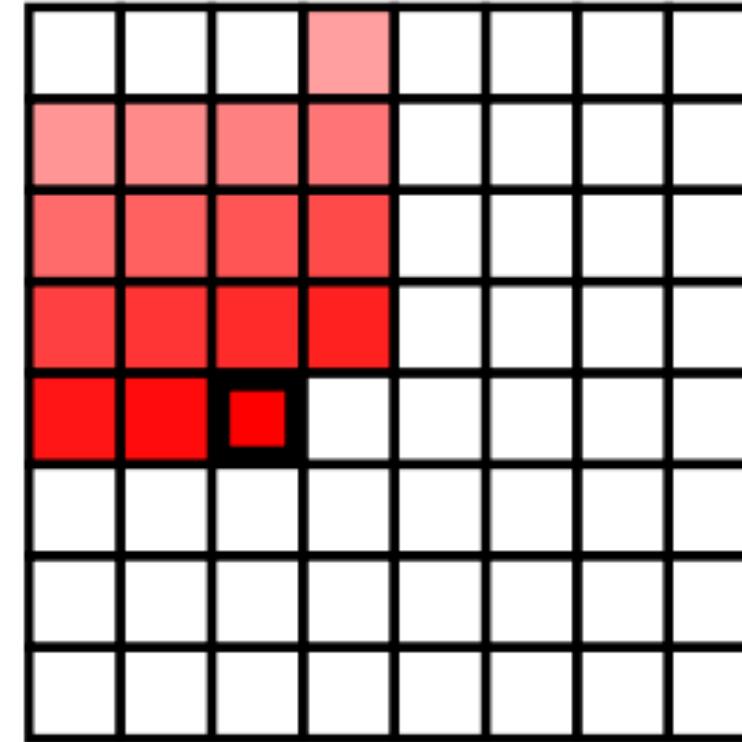
A mem:329 cache hit:245



B mem:329 cache hit:281



C mem:329 cache hit:246



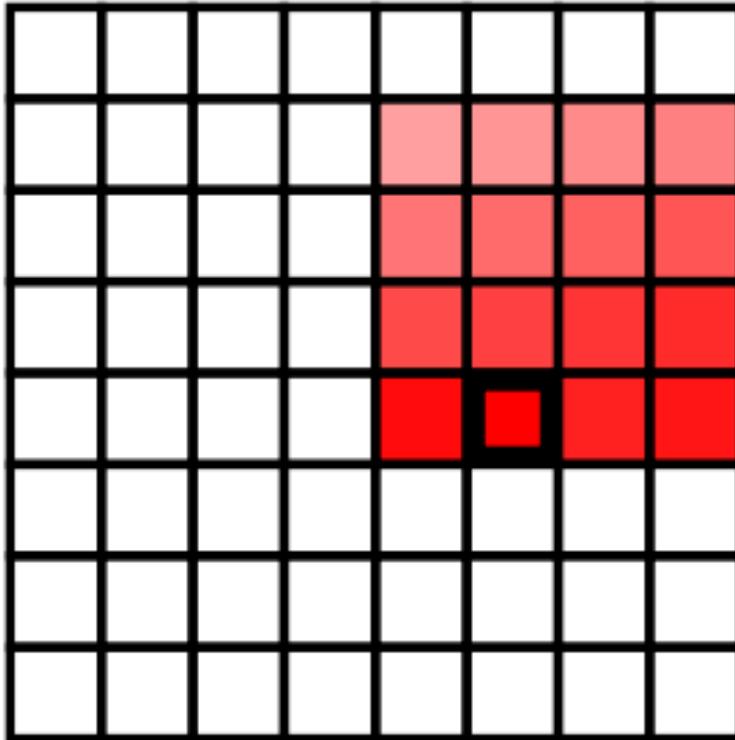
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mem:987

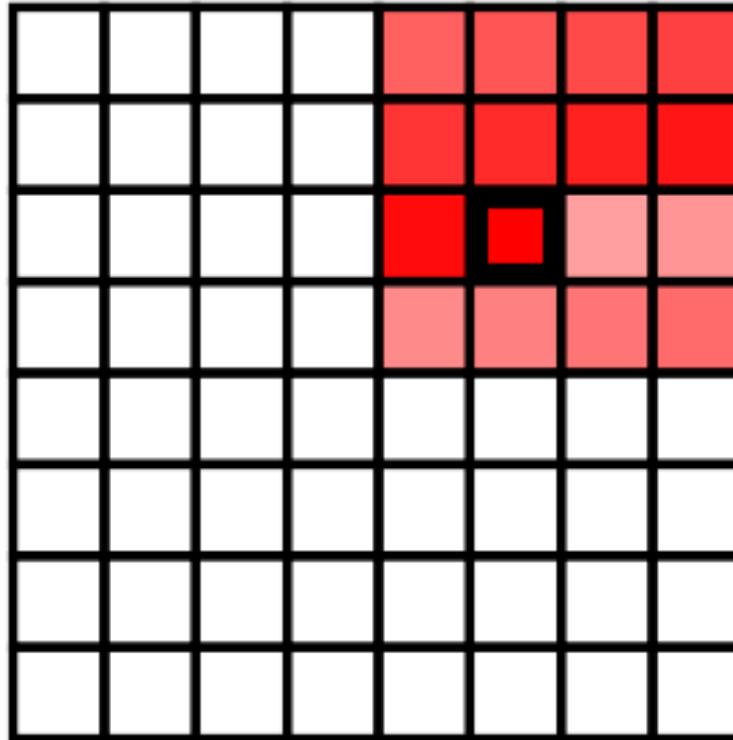
cache hits:772 \approx 78%

A mem:330 cache hit:246



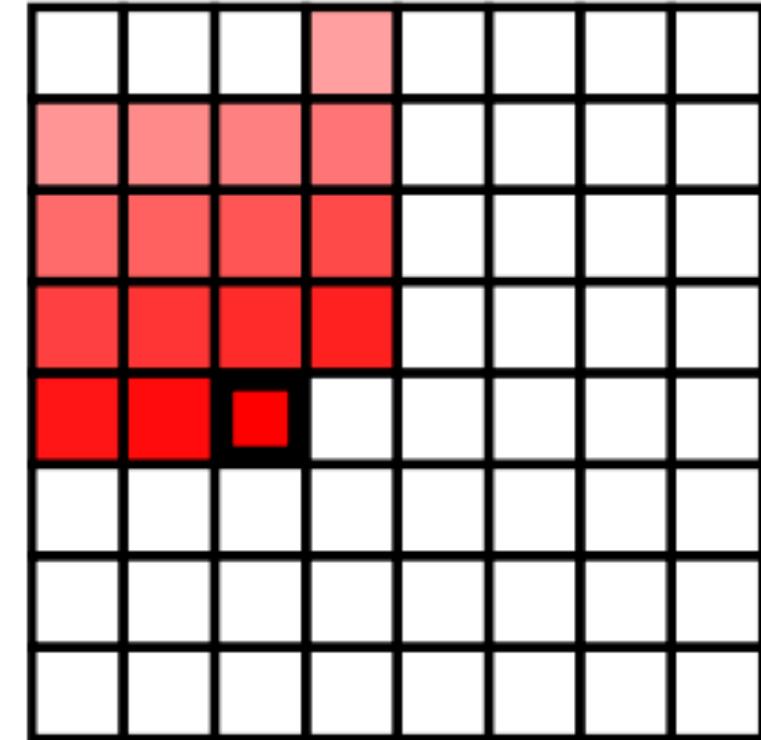
mem:990

B mem:330 cache hit:282

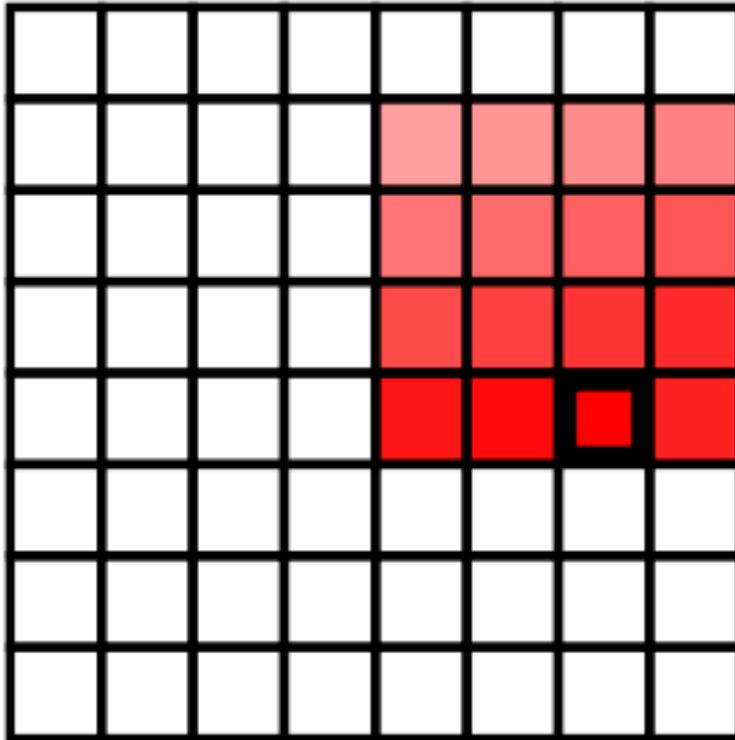


cache hits:775 ≈78%

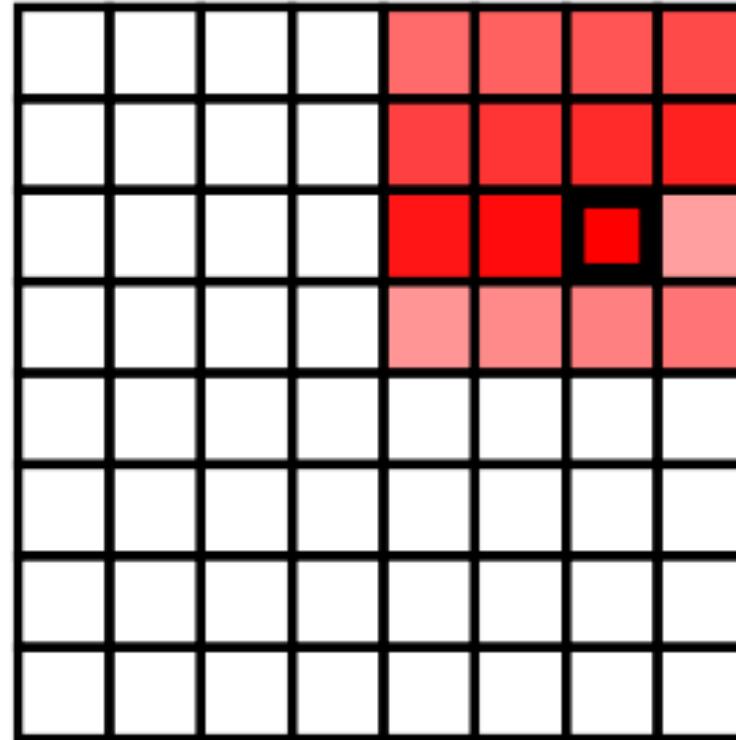
C mem:330 cache hit:247



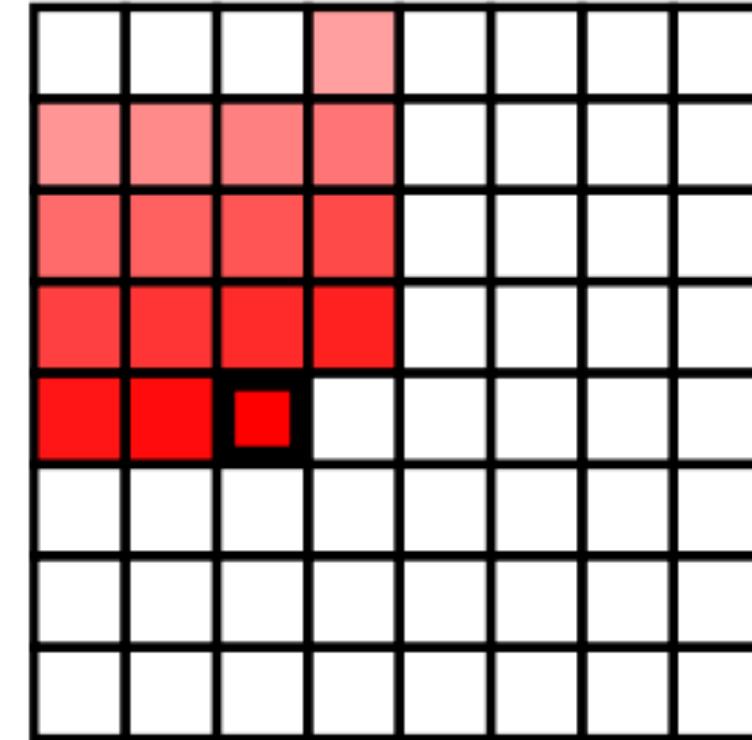
A mem:331 cache hit:247



B mem:331 cache hit:283



C mem:331 cache hit:248



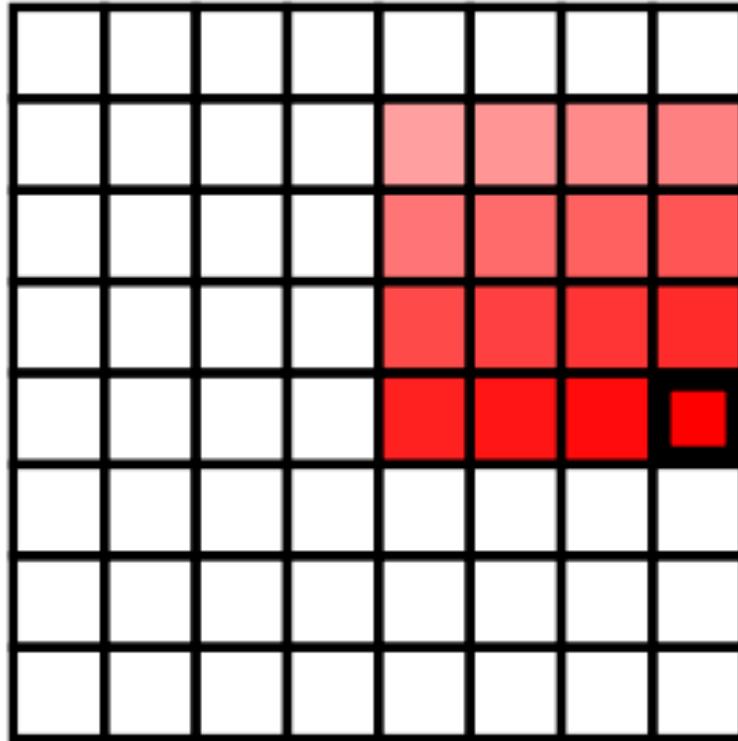
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mem:993

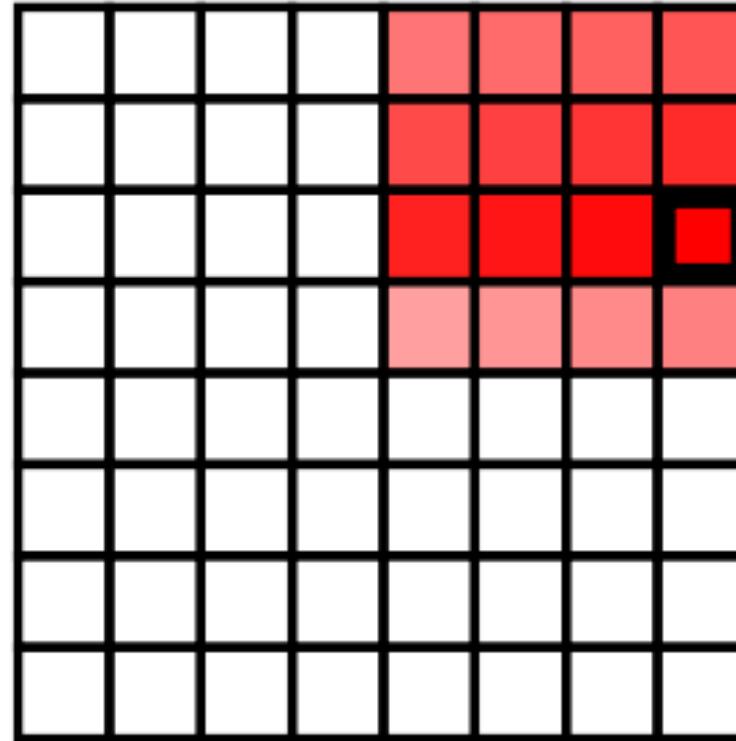
cache hits:778 \approx 78%

A mem:332 cache hit:248



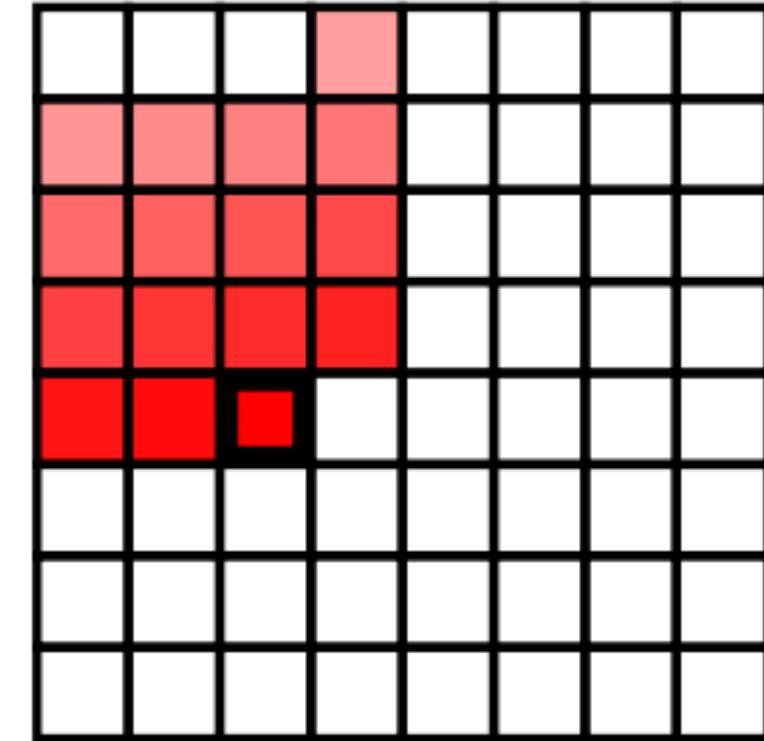
mem:996

B mem:332 cache hit:284

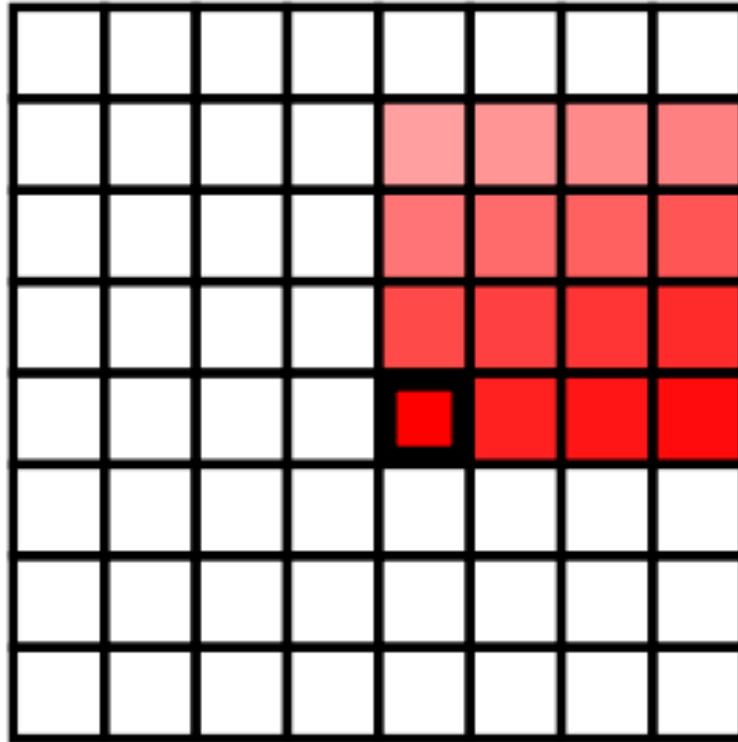


cache hits:781 $\approx 78\%$

C mem:332 cache hit:249

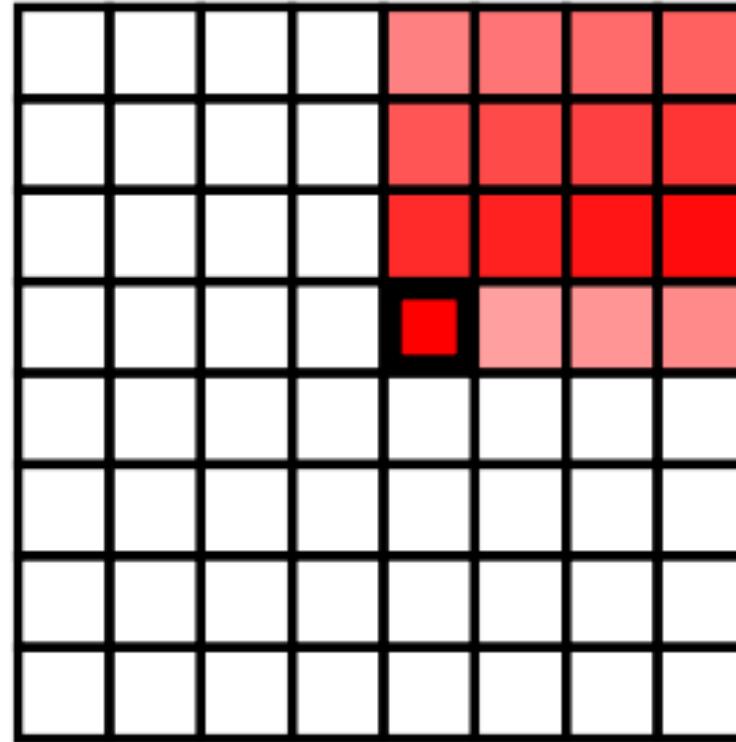


A mem:333 cache hit:249



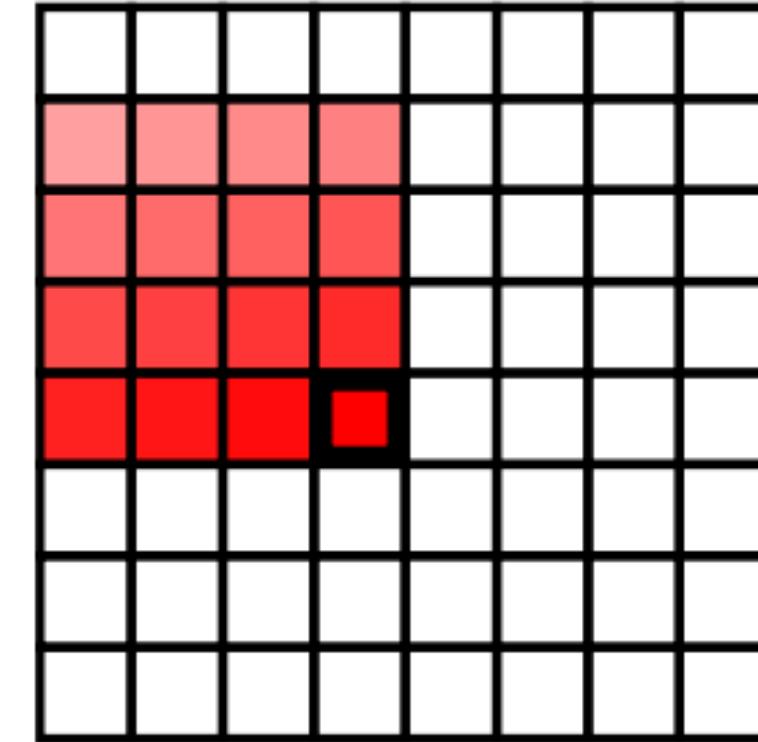
mem:999

B mem:333 cache hit:285

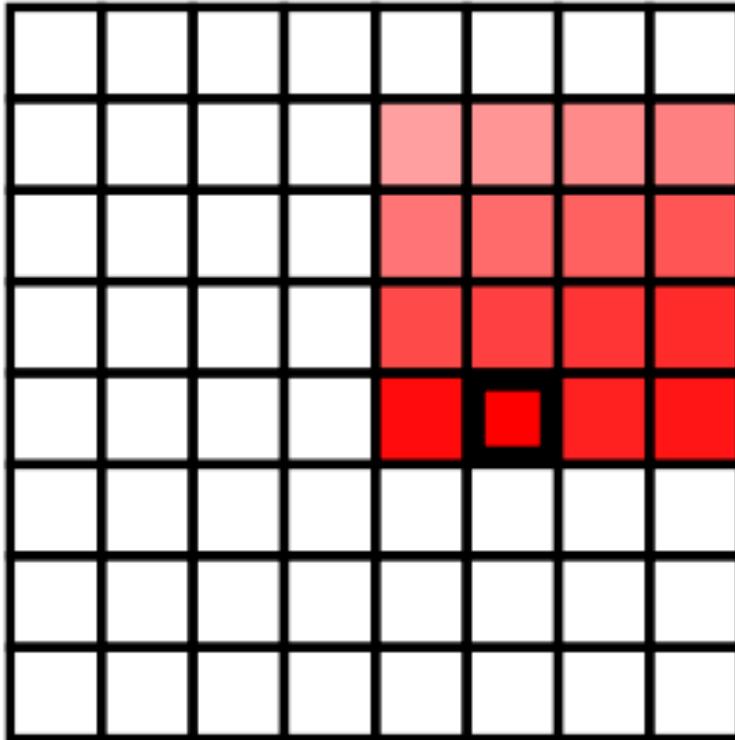


cache hits:783 ≈78%

C mem:333 cache hit:249

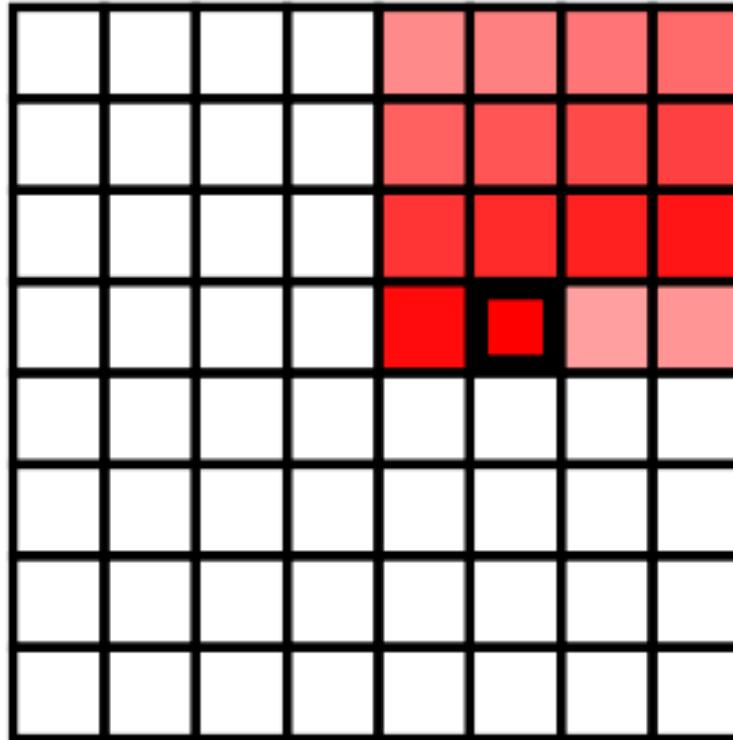


A mem:334 cache hit:250



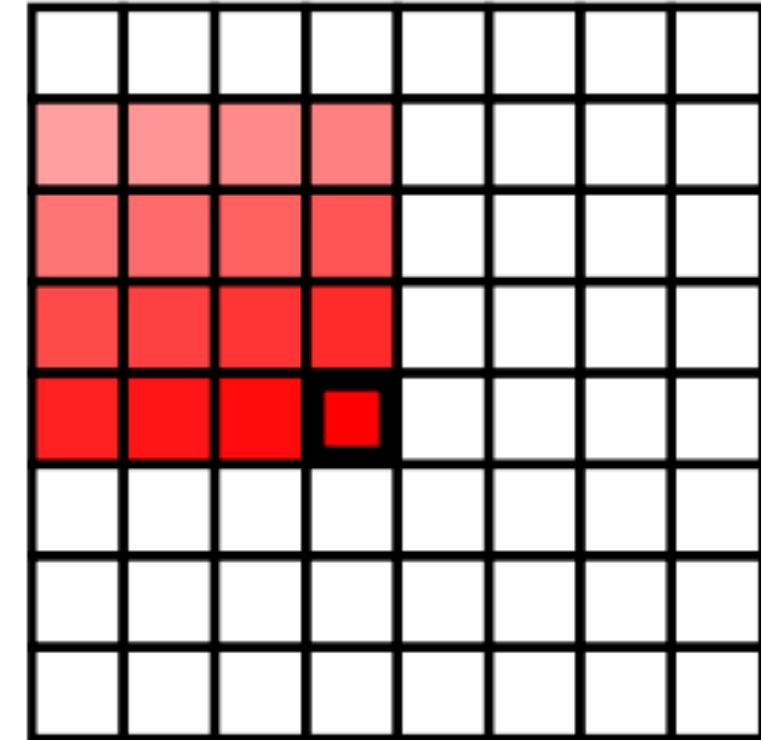
mem:1002

B mem:334 cache hit:286

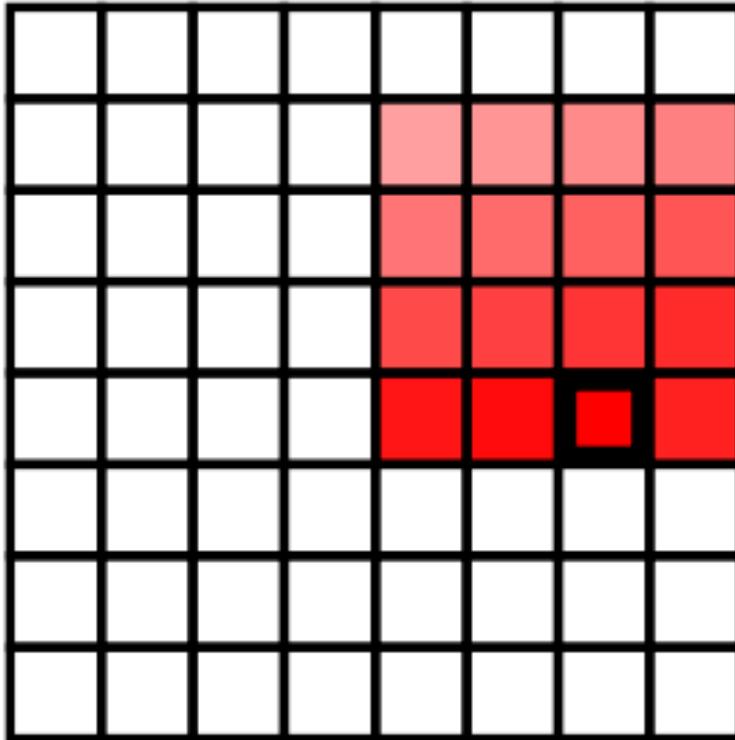


cache hits:786 $\approx 78\%$

C mem:334 cache hit:250

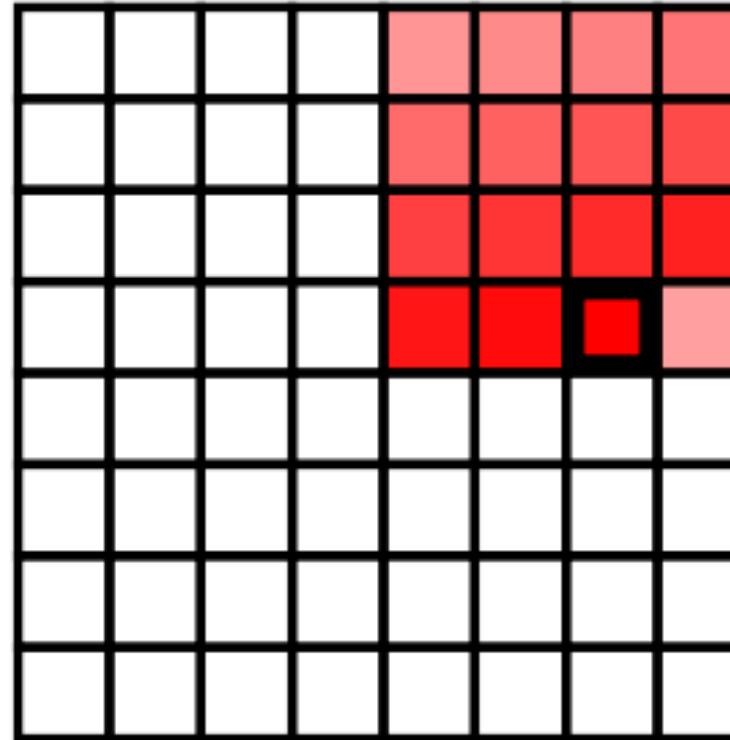


A mem:335 cache hit:251



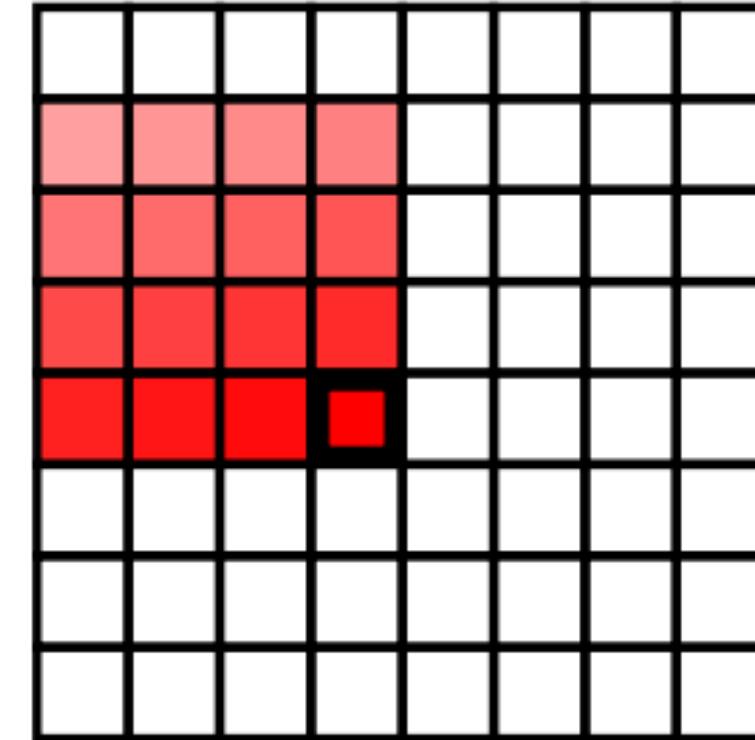
mem:1005

B mem:335 cache hit:287

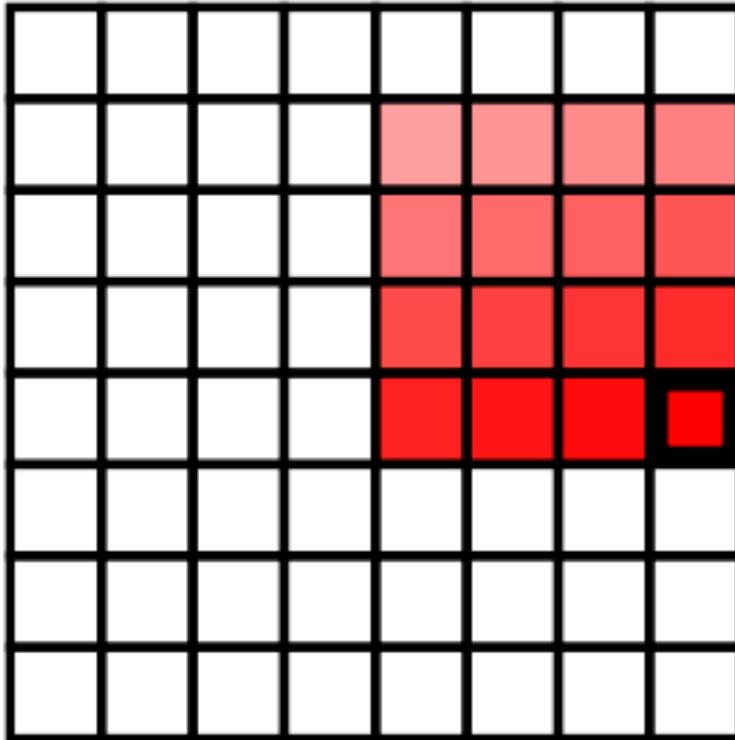


cache hits:789 $\approx 78\%$

C mem:335 cache hit:251

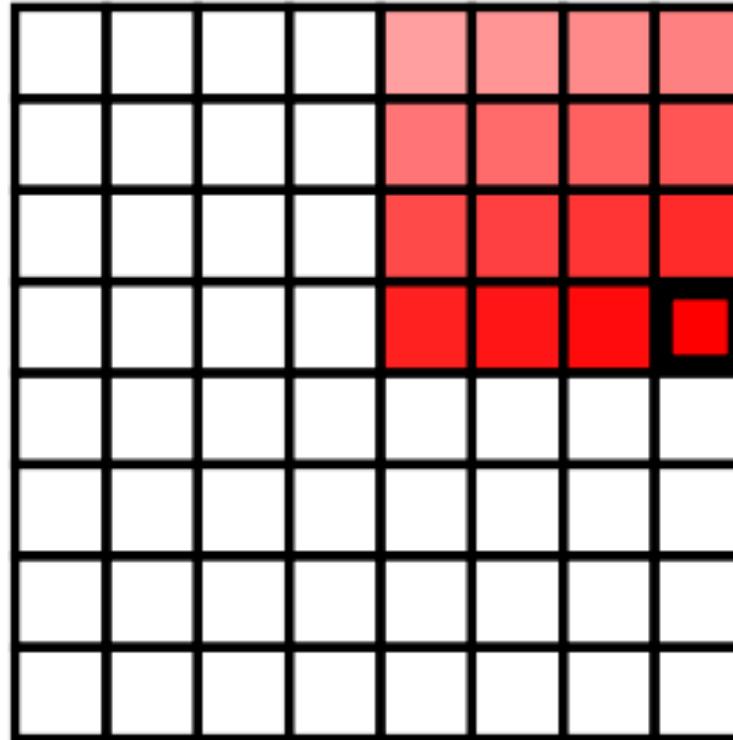


A mem:336 cache hit:252



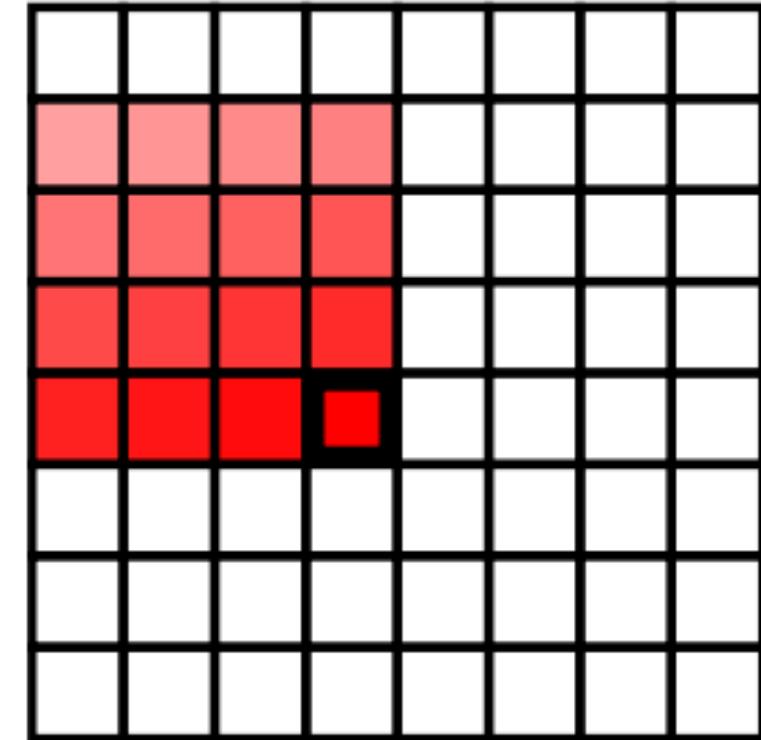
mem:1008

B mem:336 cache hit:288

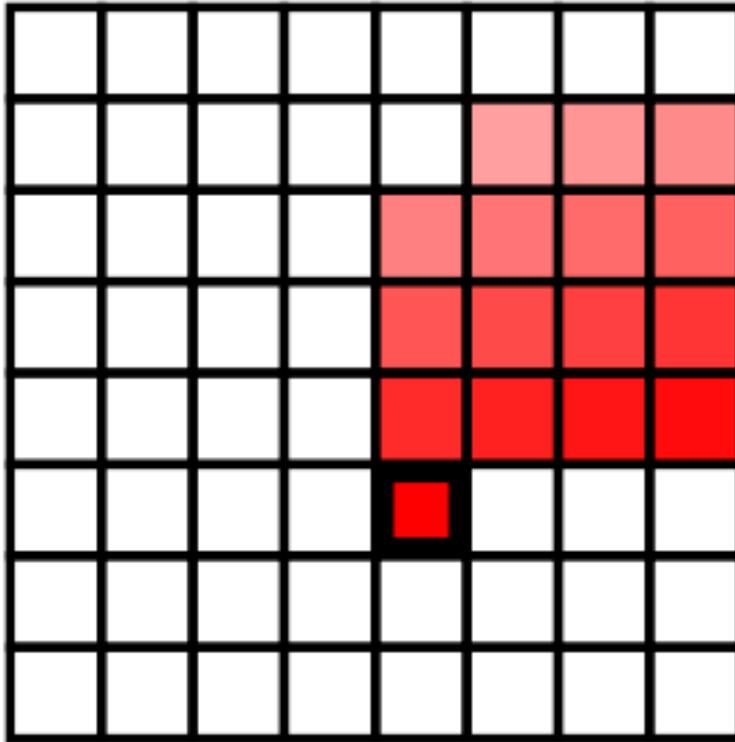


cache hits:792 $\approx 78\%$

C mem:336 cache hit:252

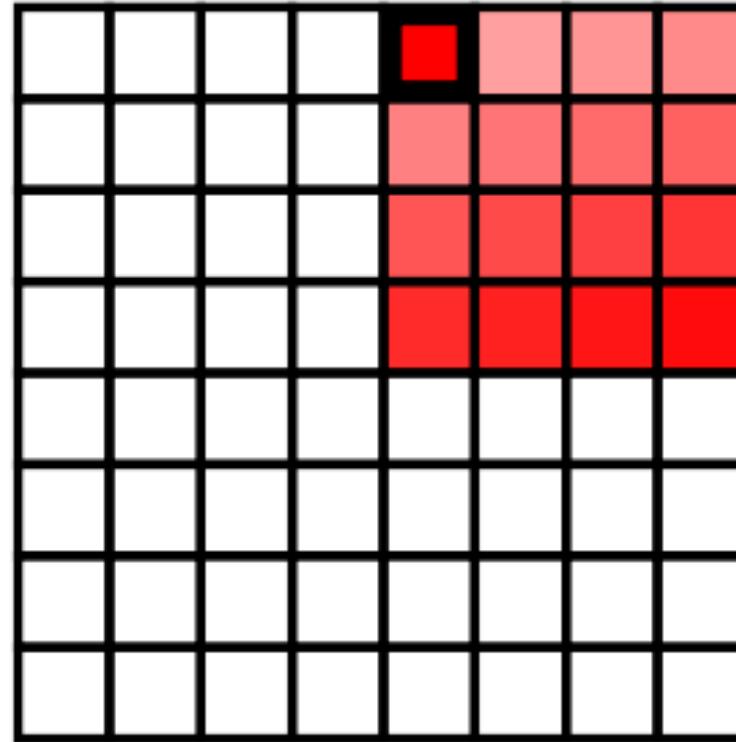


A mem:337 cache hit:252



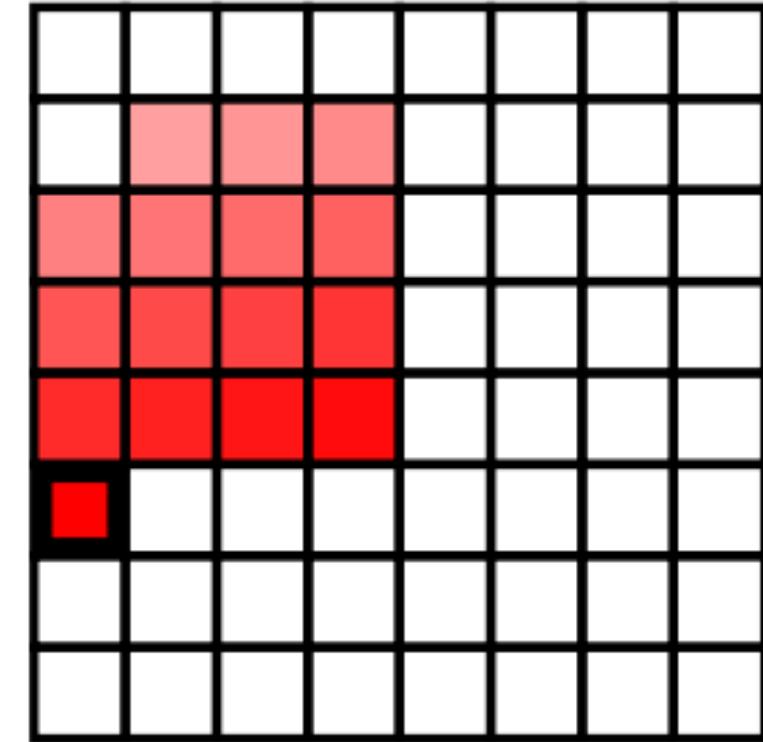
mem:1011

B mem:337 cache hit:289

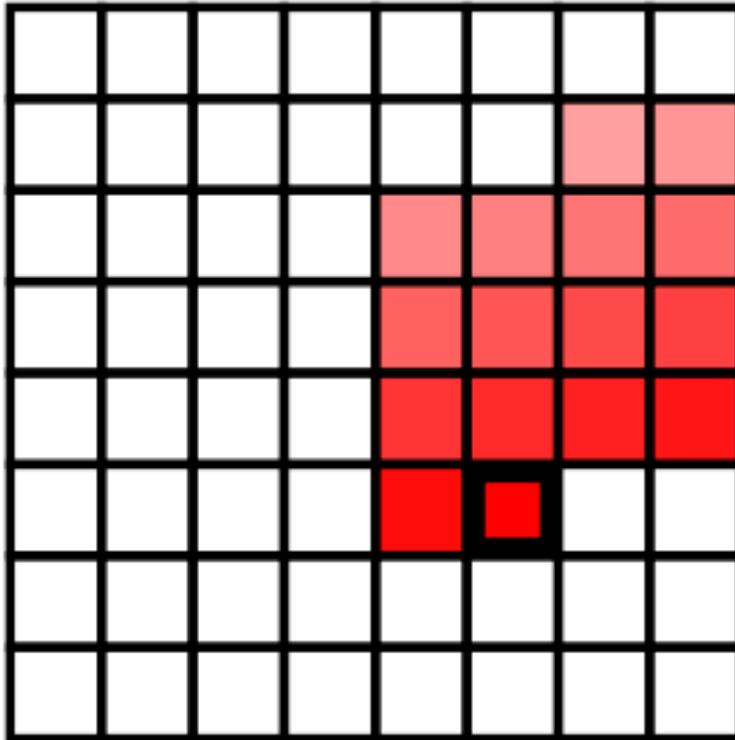


cache hits:793 ≈78%

C mem:337 cache hit:252

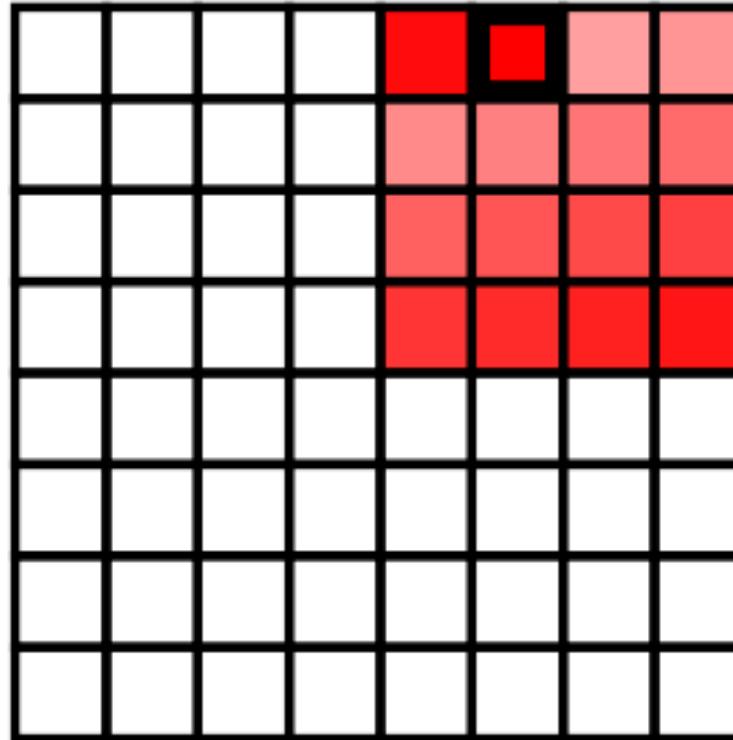


A mem:338 cache hit:252



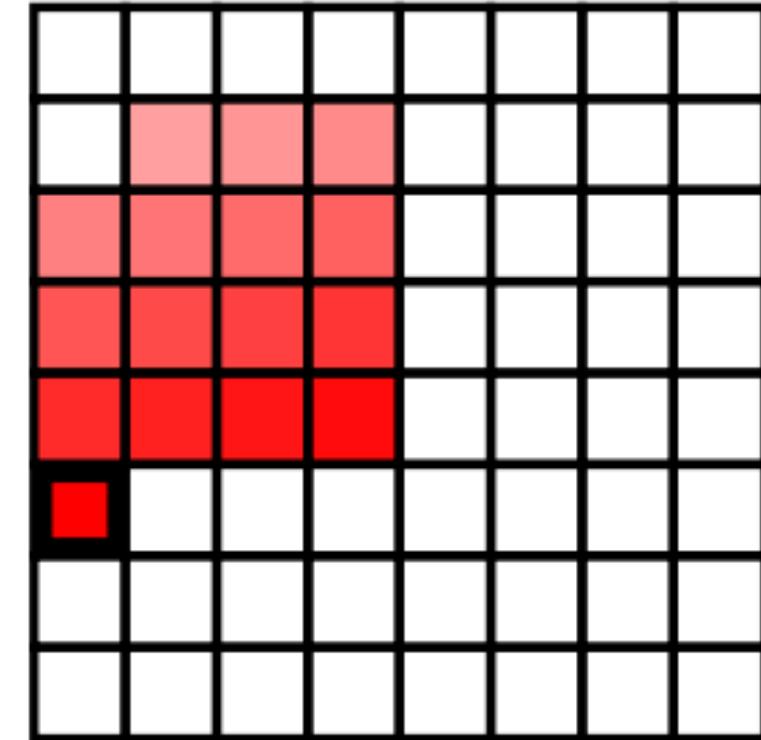
mem:1014

B mem:338 cache hit:290

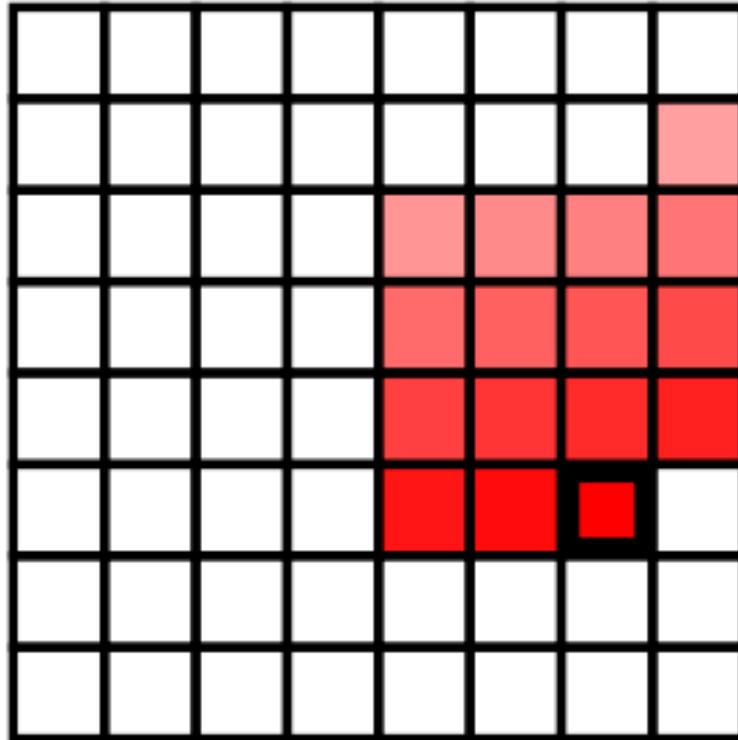


cache hits:795 ≈78%

C mem:338 cache hit:253

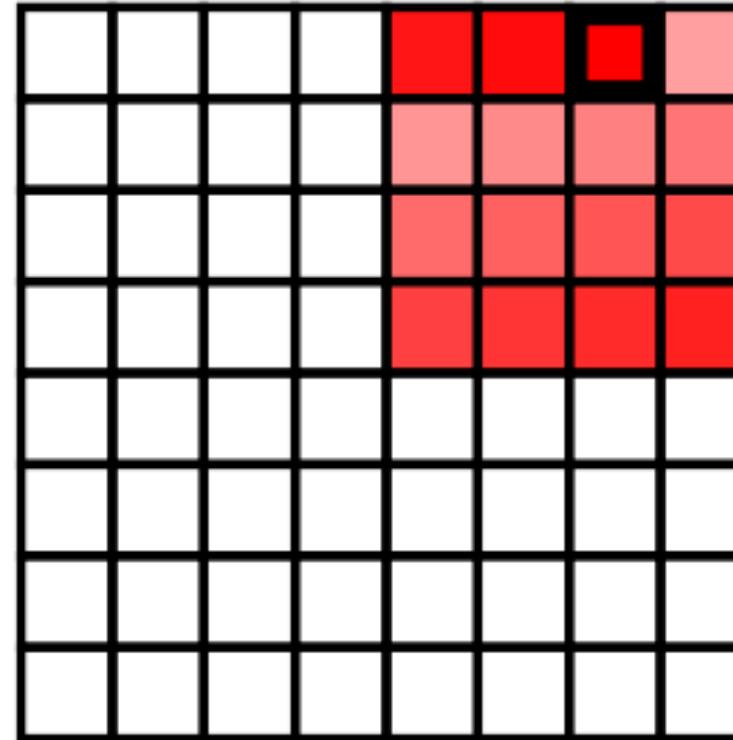


A mem:339 cache hit:252



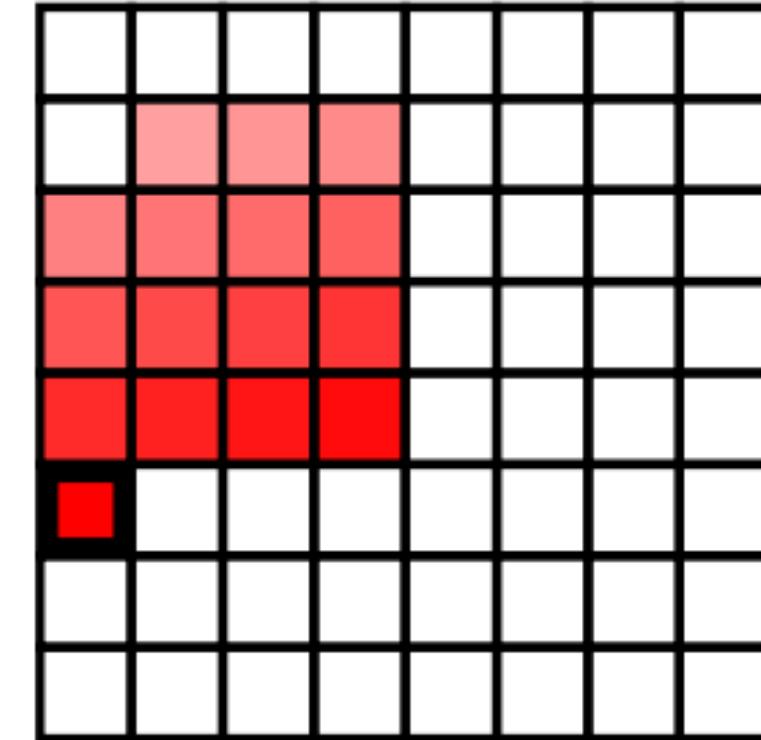
mem:1017

B mem:339 cache hit:291

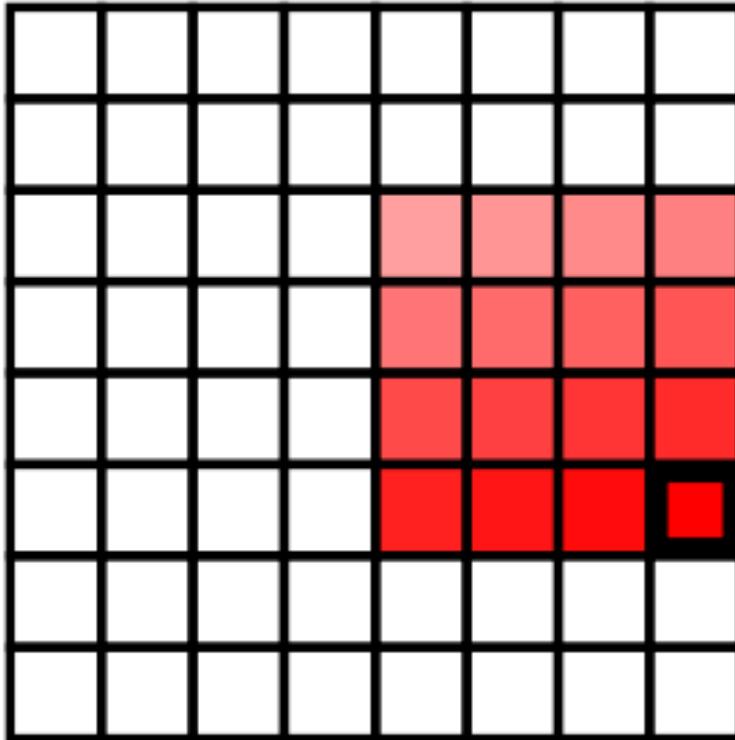


cache hits:797 $\approx 78\%$

C mem:339 cache hit:254

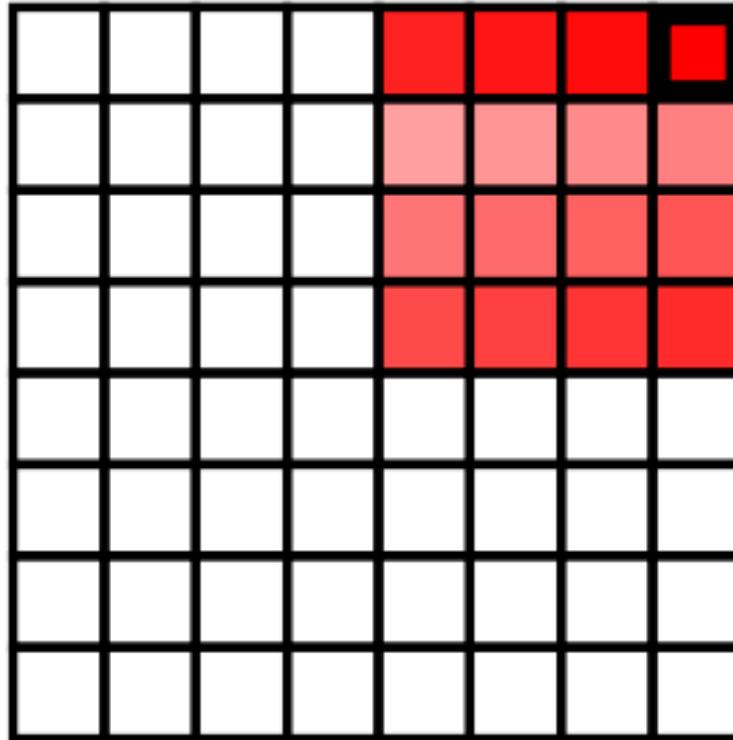


A mem:340 cache hit:252



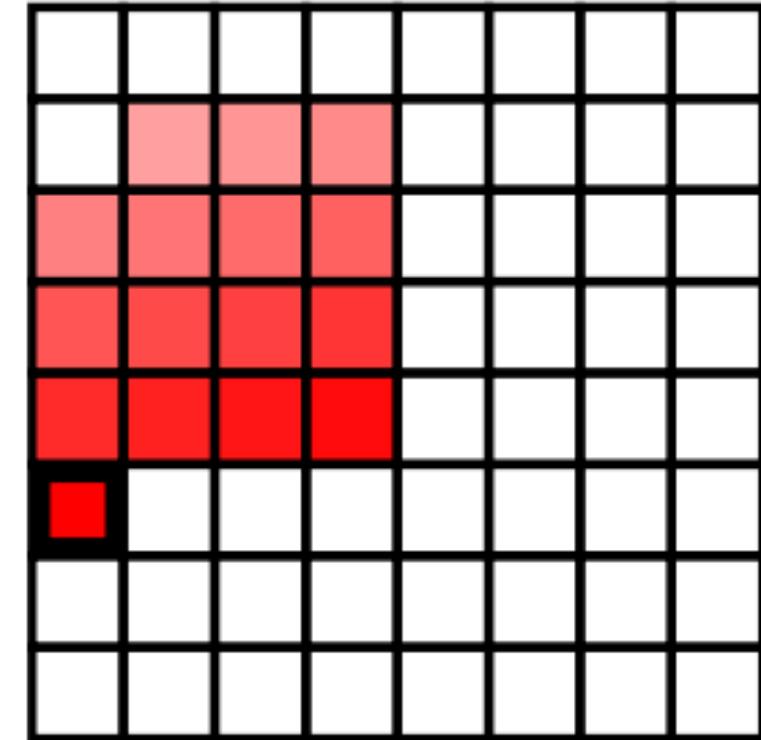
mem:1020

B mem:340 cache hit:292

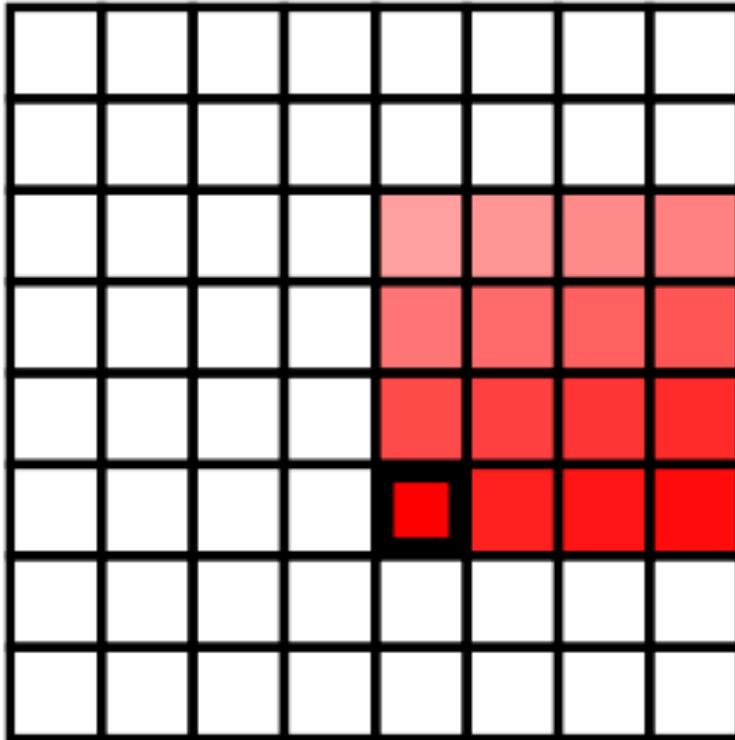


cache hits:799 $\approx 78\%$

C mem:340 cache hit:255

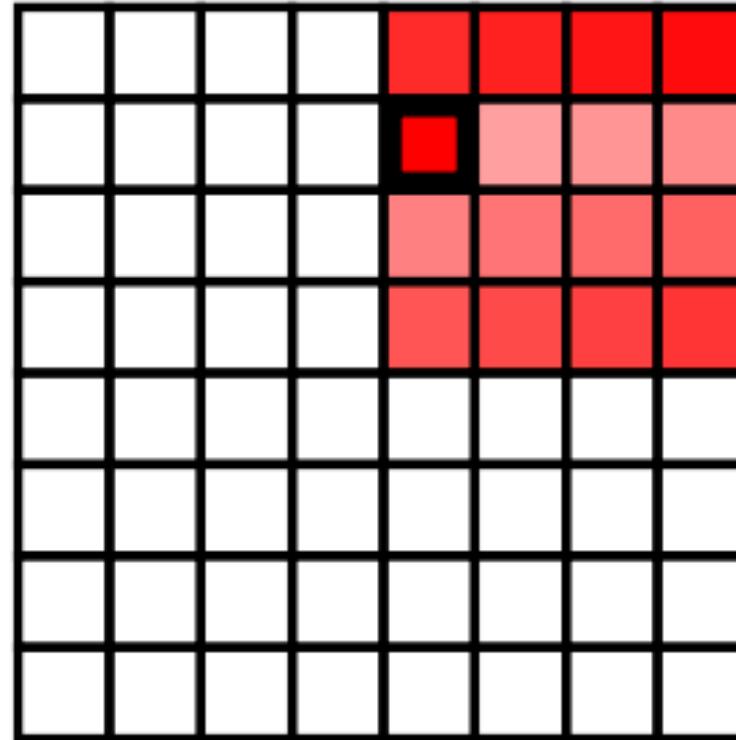


A mem:341 cache hit:253



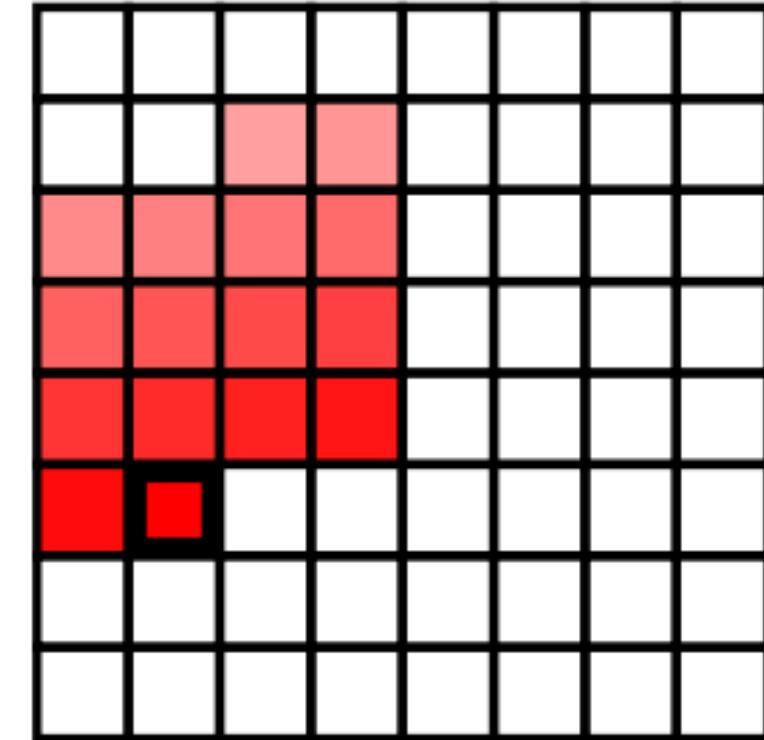
mem:1023

B mem:341 cache hit:293

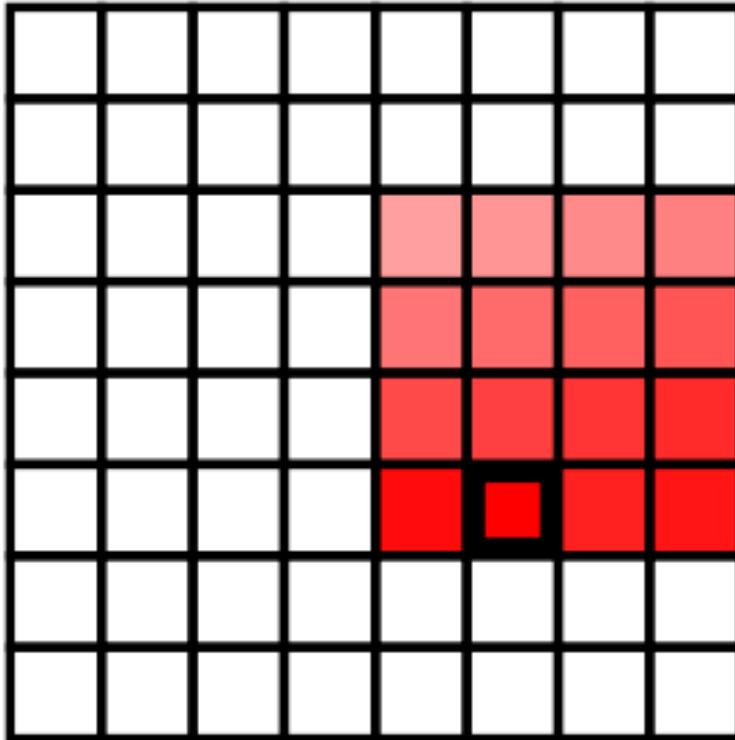


cache hits:801 $\approx 78\%$

C mem:341 cache hit:255

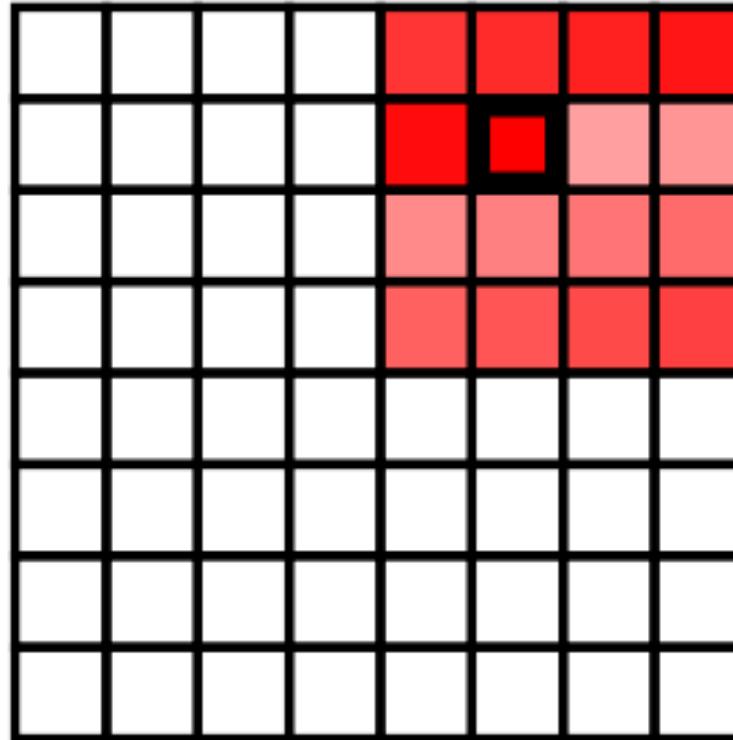


A mem:342 cache hit:254



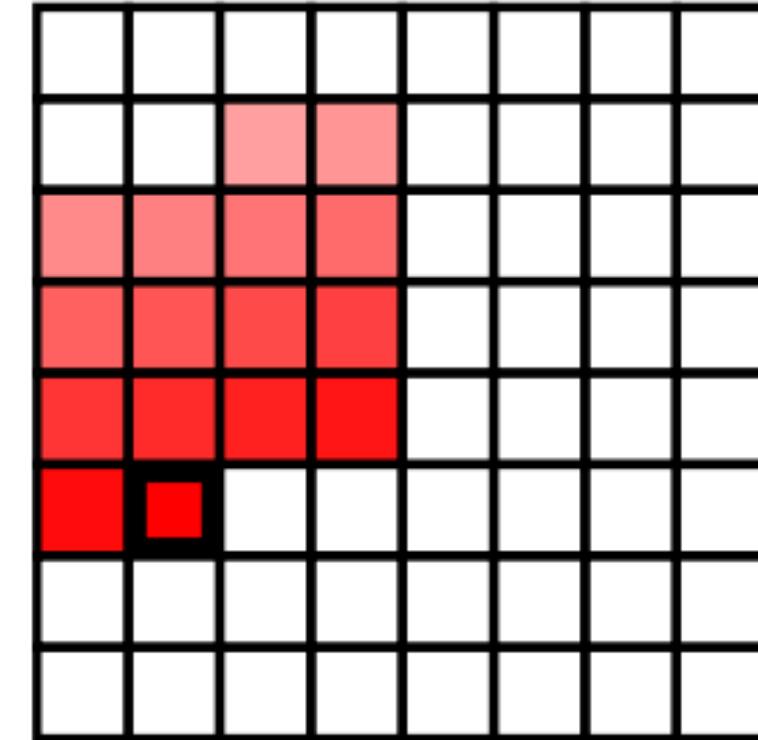
mem:1026

B mem:342 cache hit:294

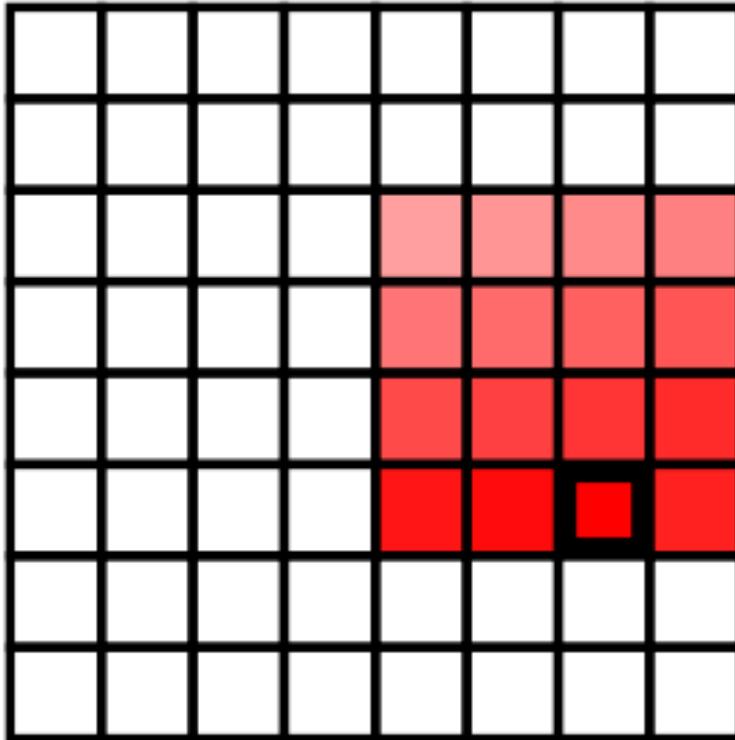


cache hits:804 ≈78%

C mem:342 cache hit:256

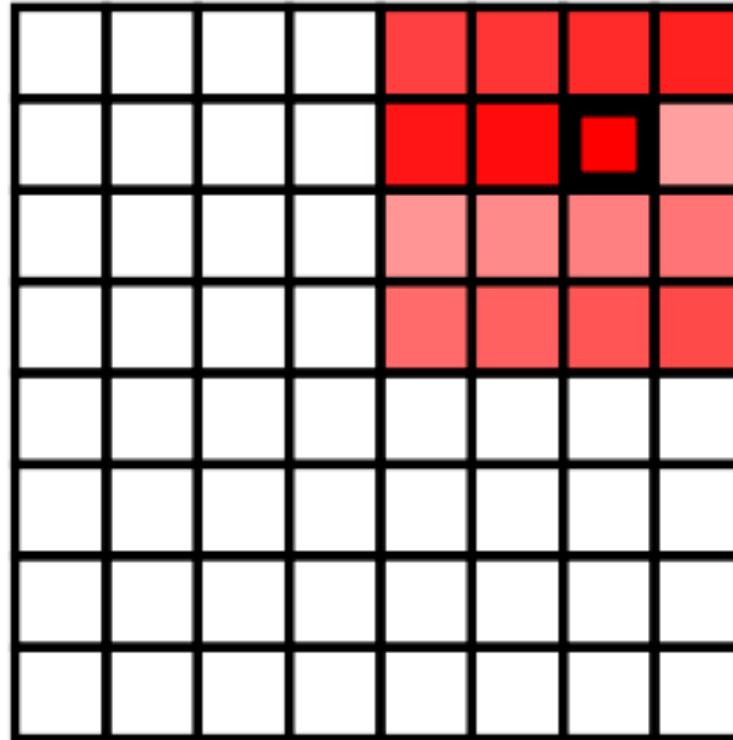


A mem:343 cache hit:255



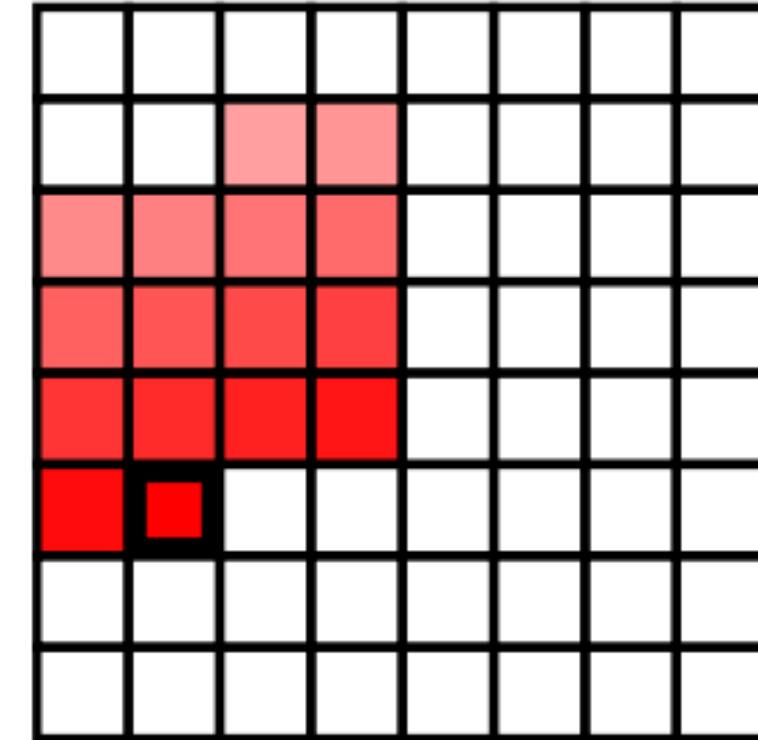
mem:1029

B mem:343 cache hit:295

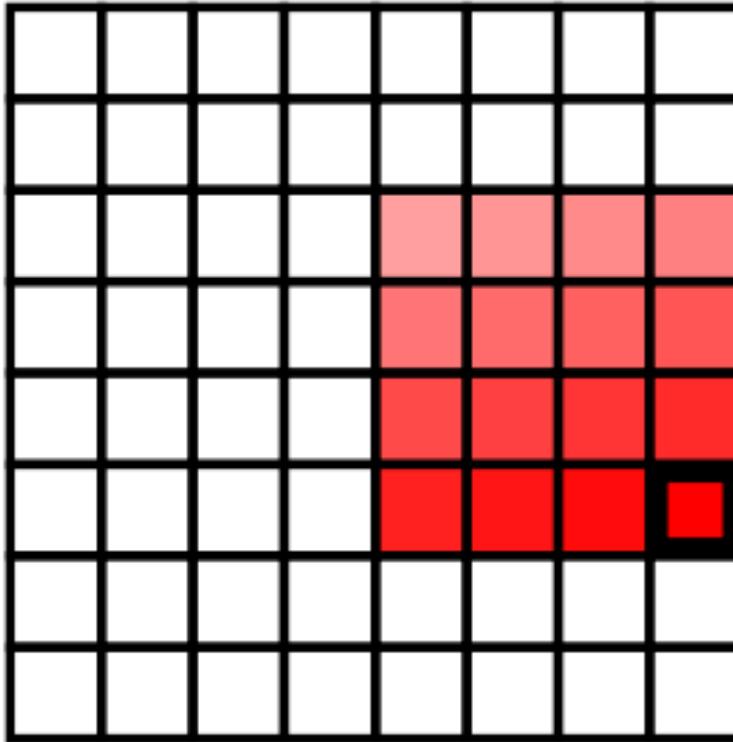


cache hits:807 ≈78%

C mem:343 cache hit:257

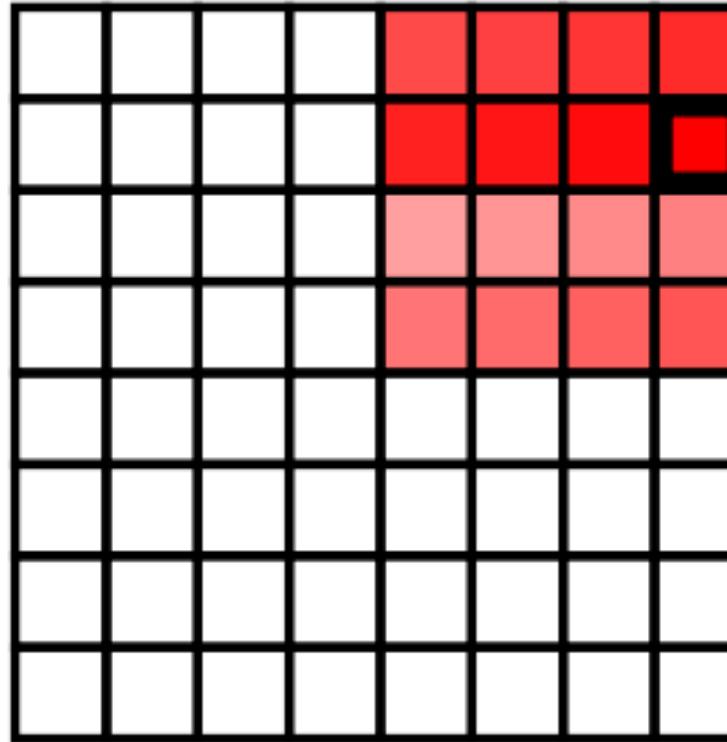


A mem:344 cache hit:256



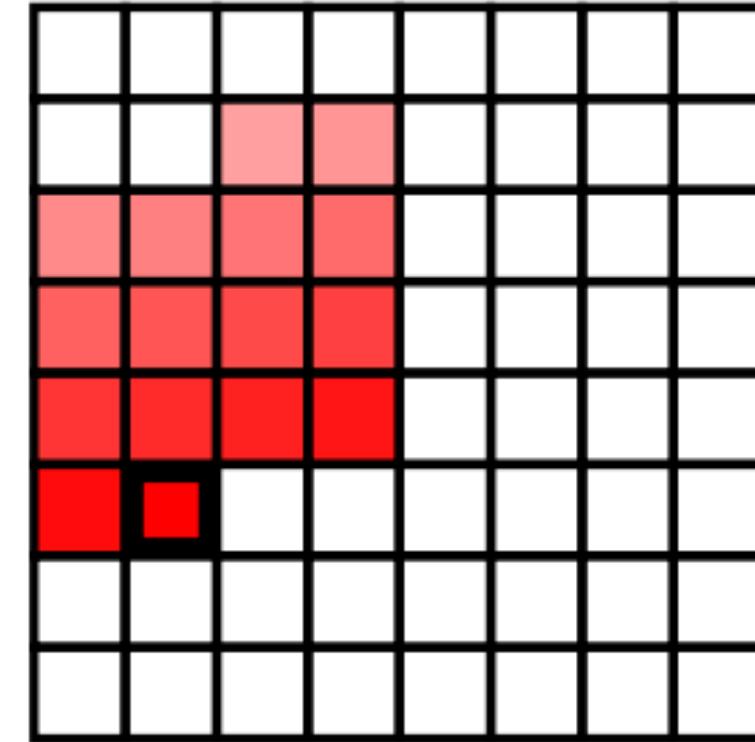
mem:1032

B mem:344 cache hit:296

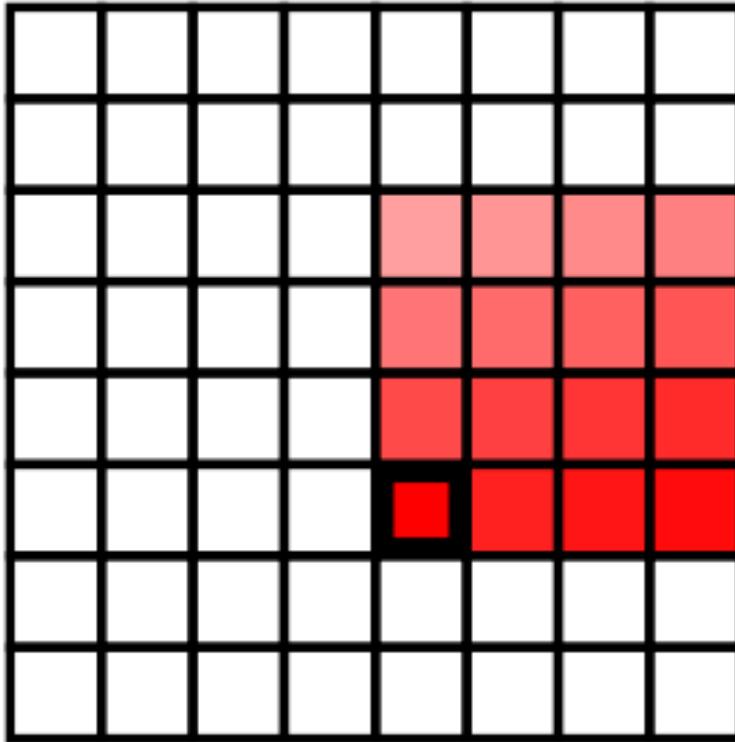


cache hits:810 $\approx 78\%$

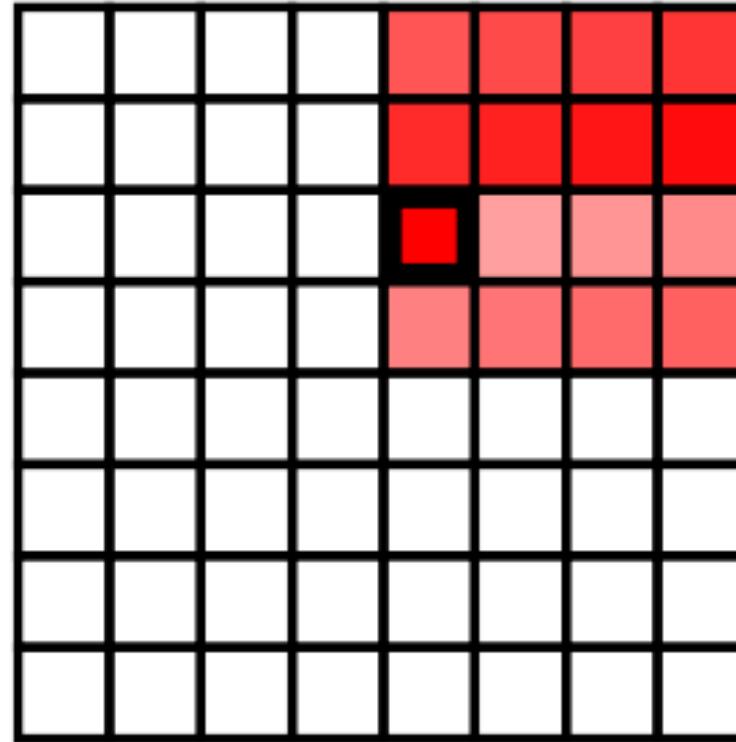
C mem:344 cache hit:258



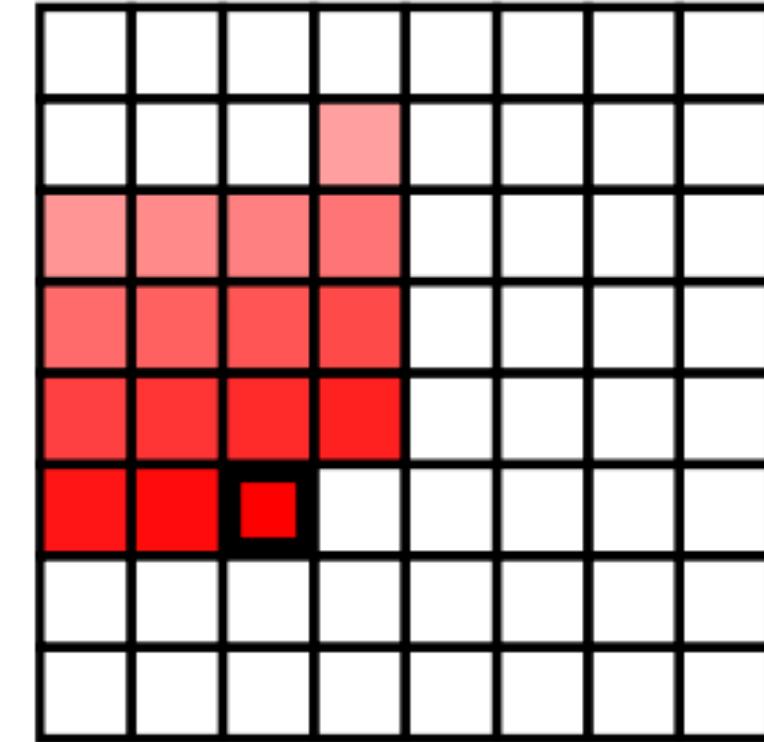
A mem:345 cache hit:257



B mem:345 cache hit:297



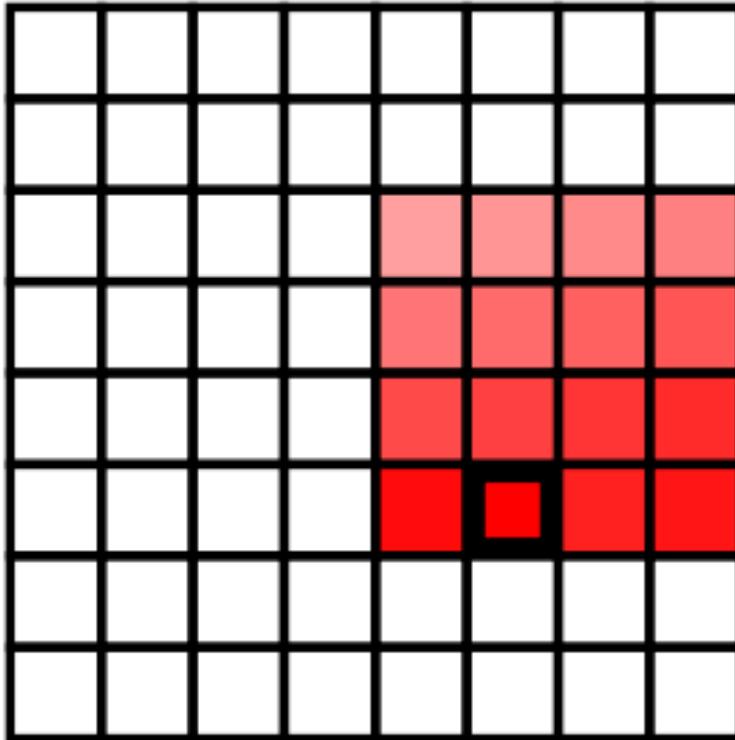
C mem:345 cache hit:258



mem:1035

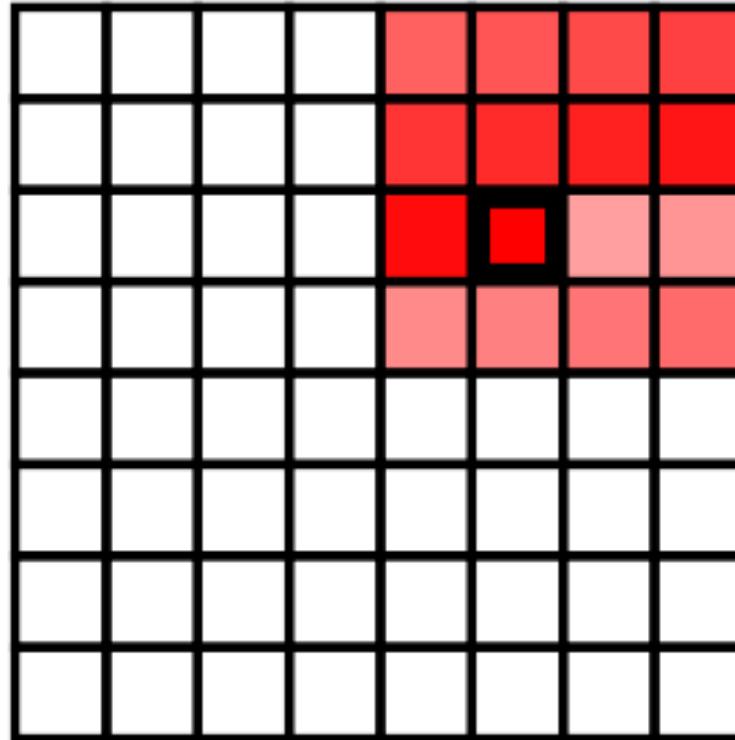
cache hits:812 $\approx 78\%$

A mem:346 cache hit:258



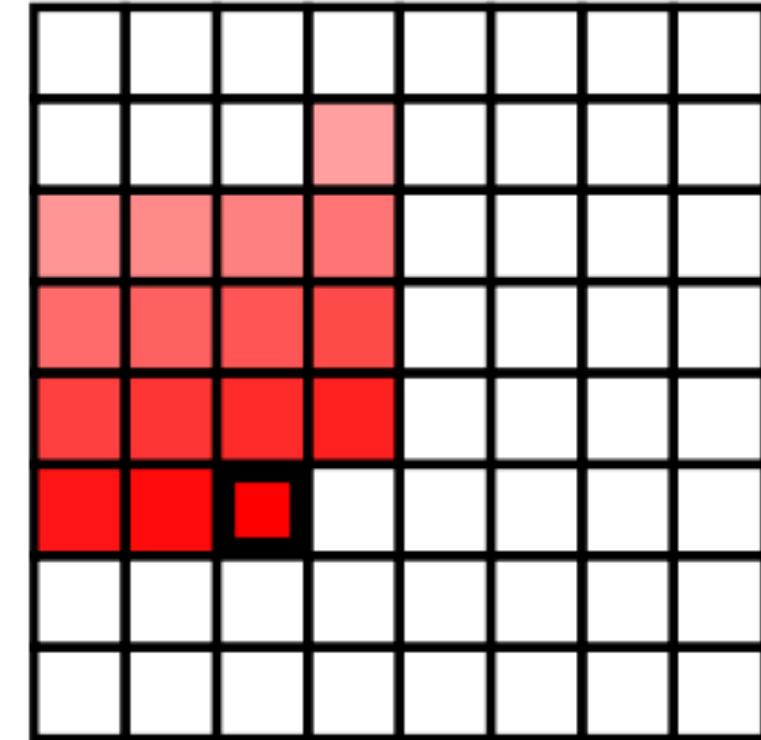
mem:1038

B mem:346 cache hit:298

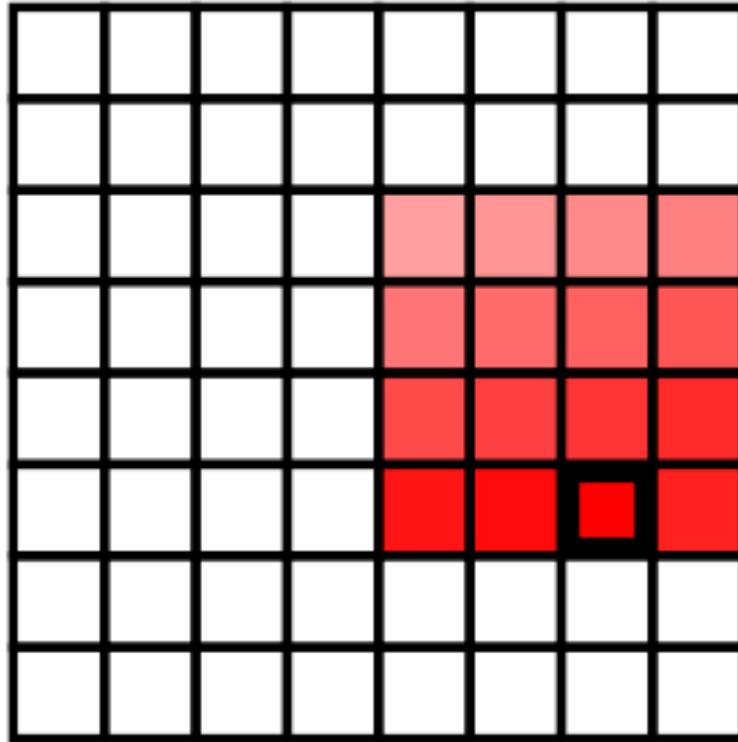


cache hits:815 ≈78%

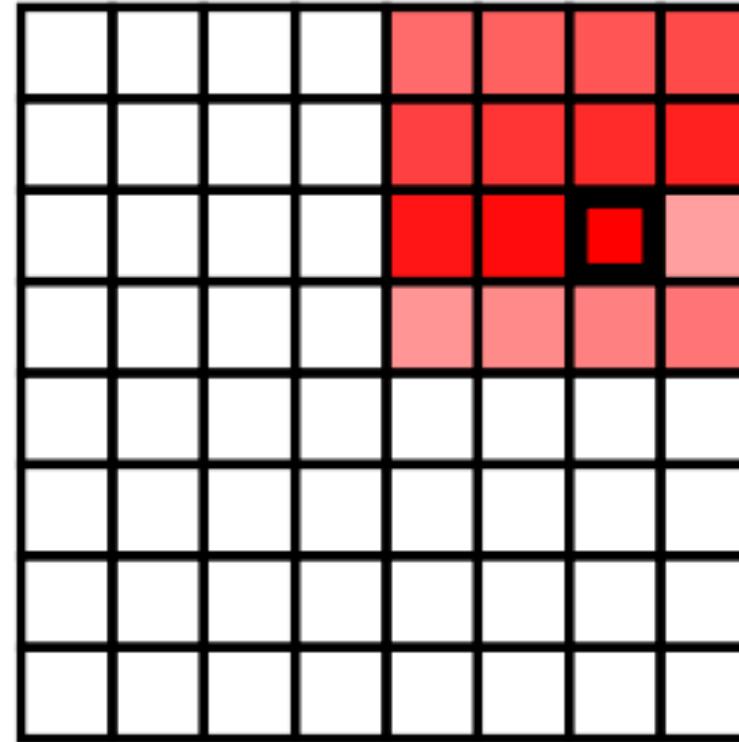
C mem:346 cache hit:259



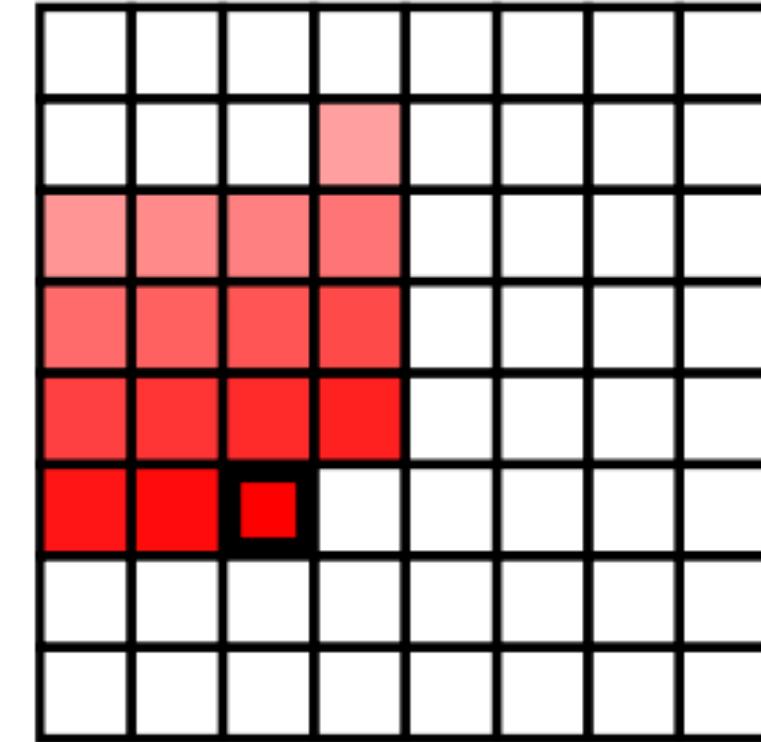
A mem:347 cache hit:259



B mem:347 cache hit:299



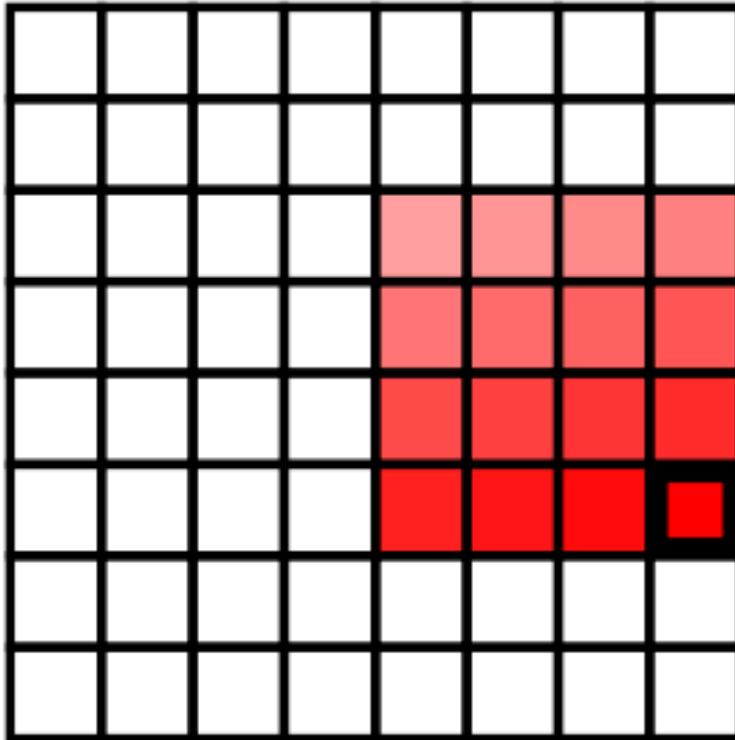
C mem:347 cache hit:260



mem:1041

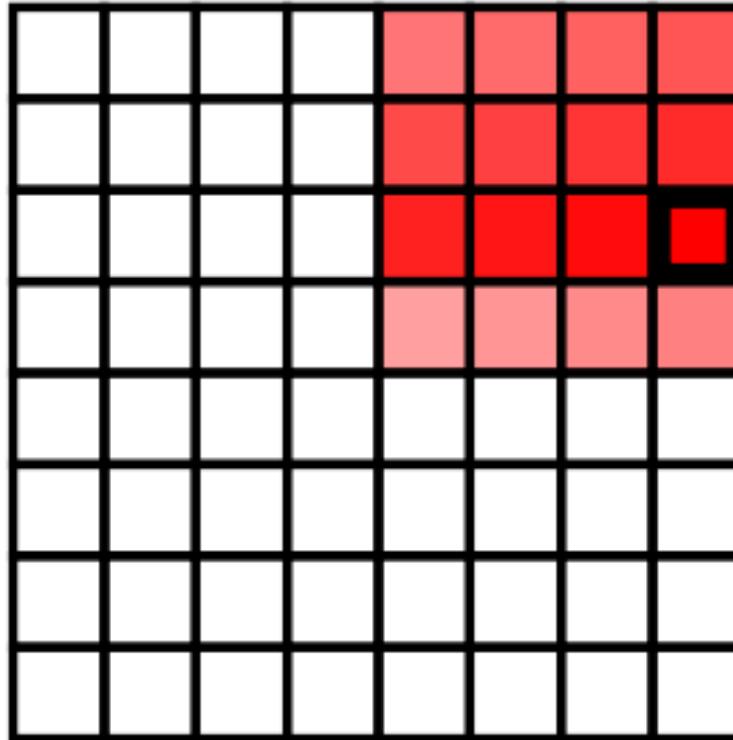
cache hits:818 $\approx 78\%$

A mem:348 cache hit:260



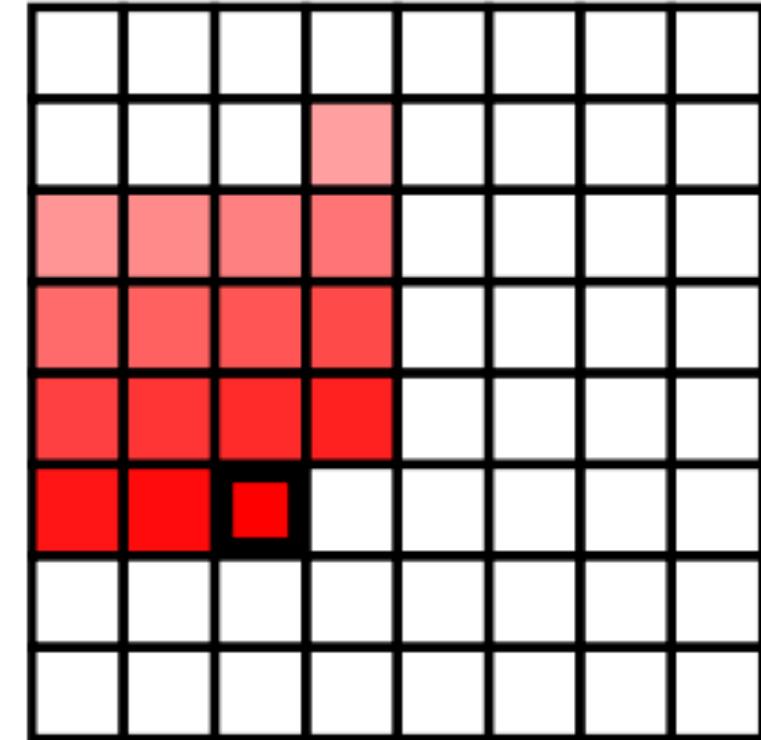
mem:1044

B mem:348 cache hit:300

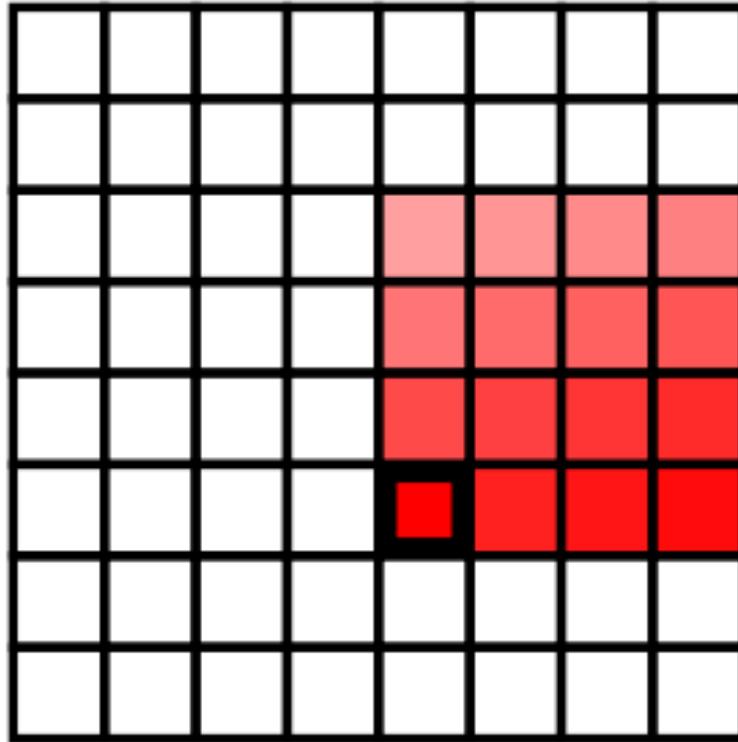


cache hits:821 $\approx 78\%$

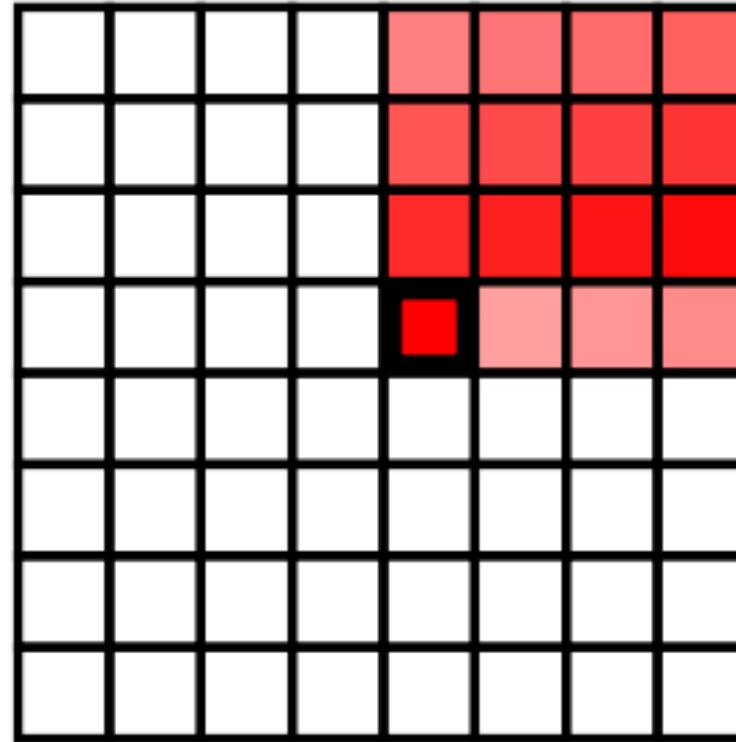
C mem:348 cache hit:261



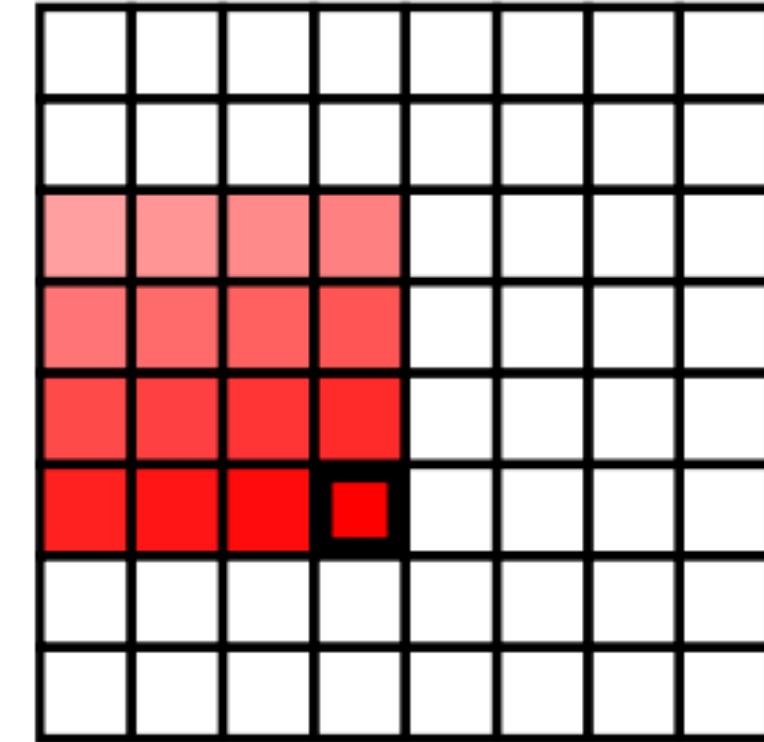
A mem:349 cache hit:261



B mem:349 cache hit:301



C mem:349 cache hit:261



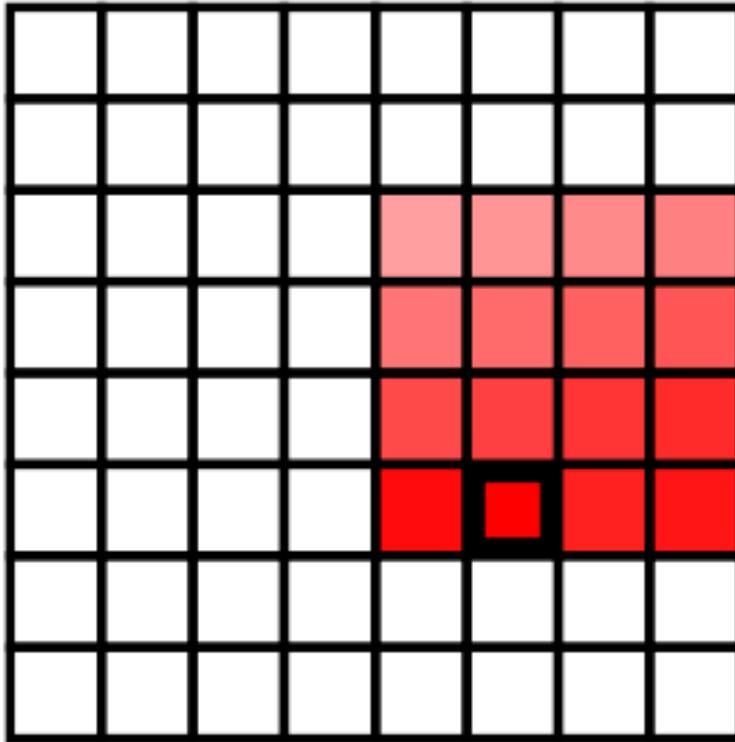
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mem:1047

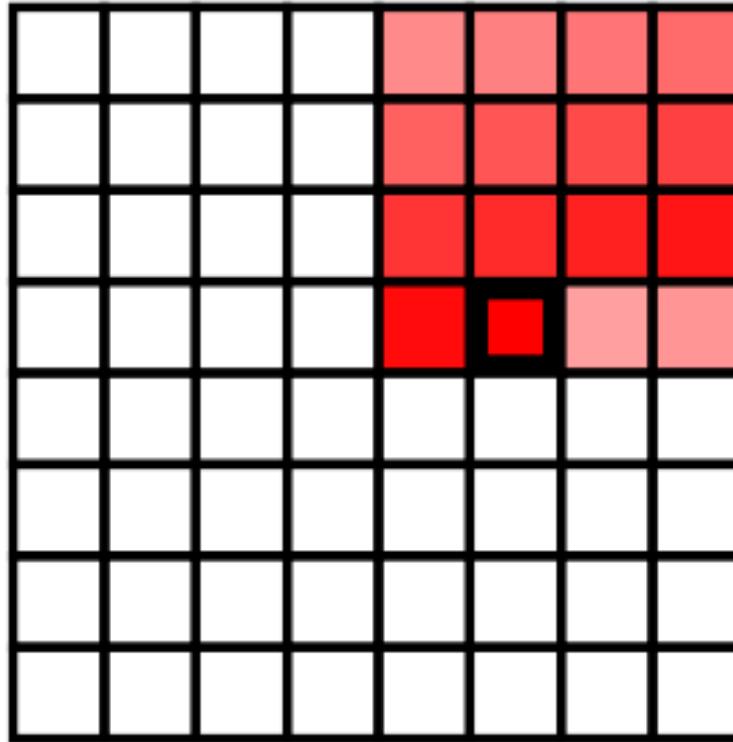
cache hits:823 $\approx 78\%$

A mem:350 cache hit:262



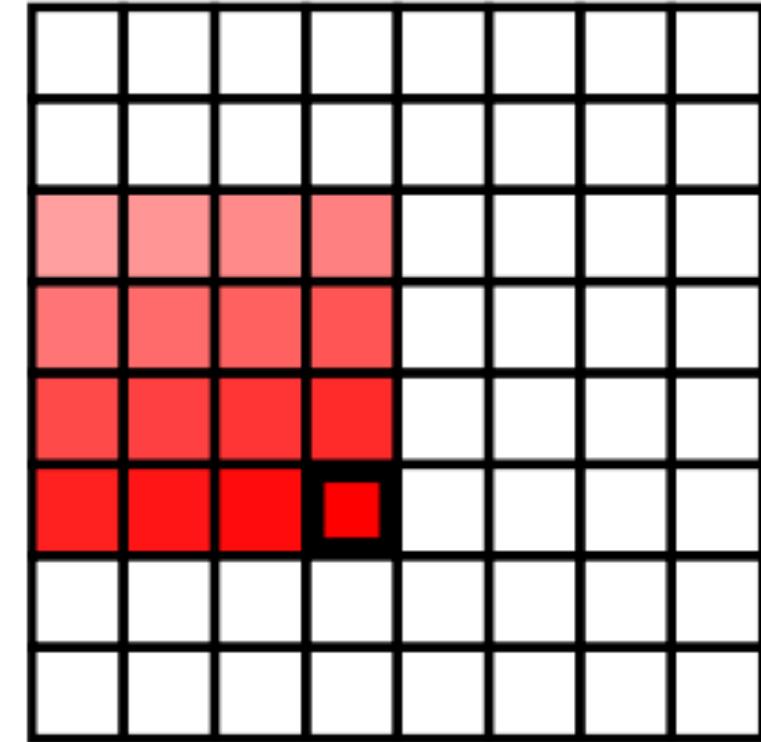
mem:1050

B mem:350 cache hit:302

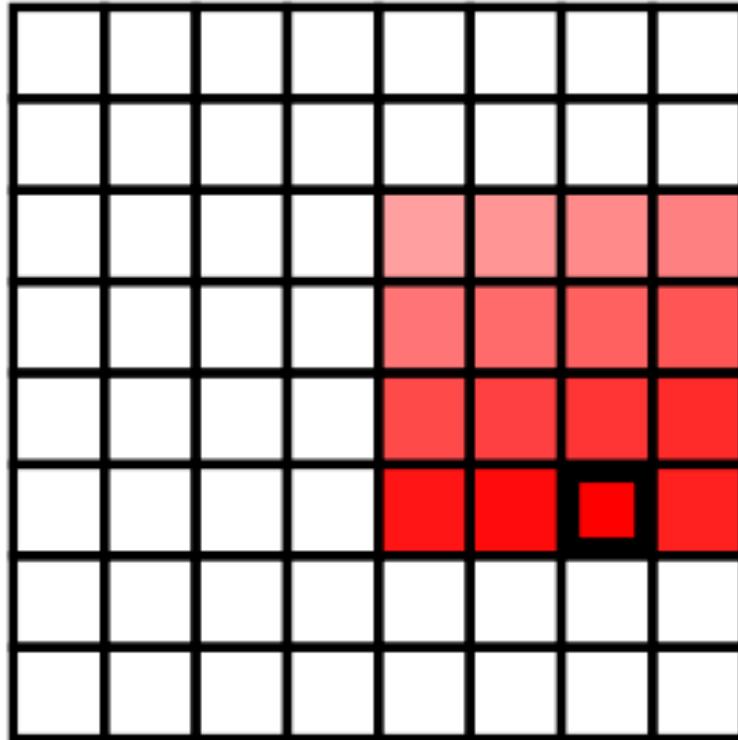


cache hits:826 ≈78%

C mem:350 cache hit:262

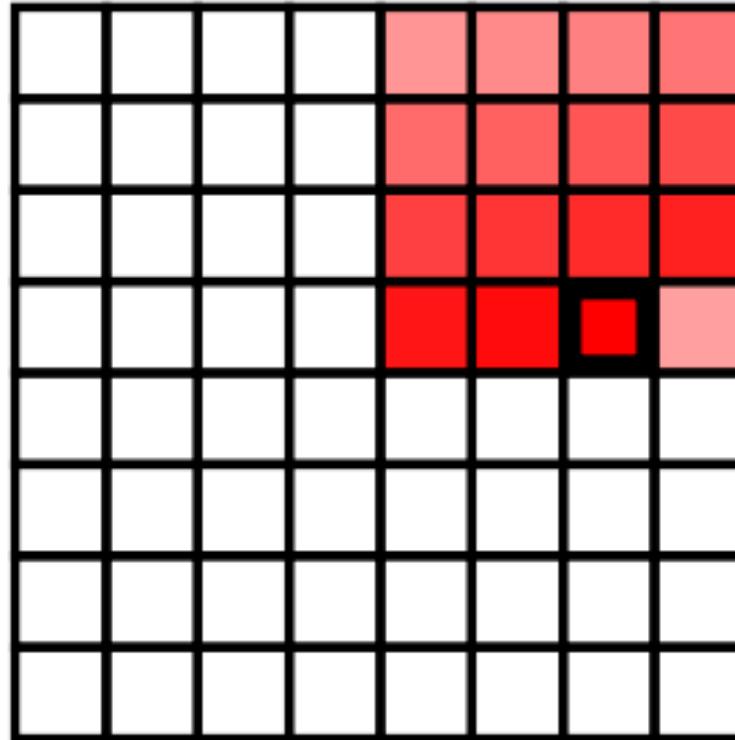


A mem:351 cache hit:263



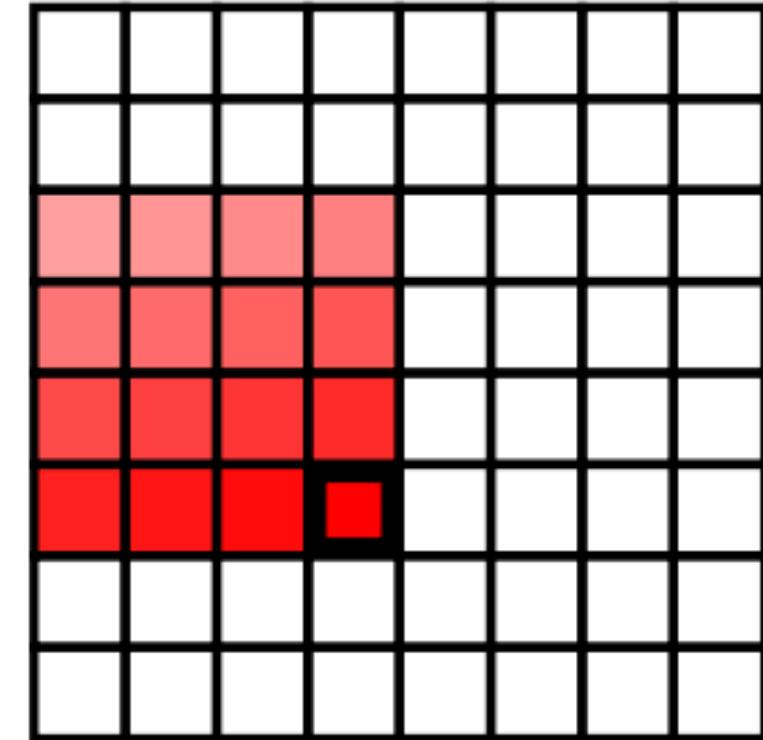
mem:1053

B mem:351 cache hit:303

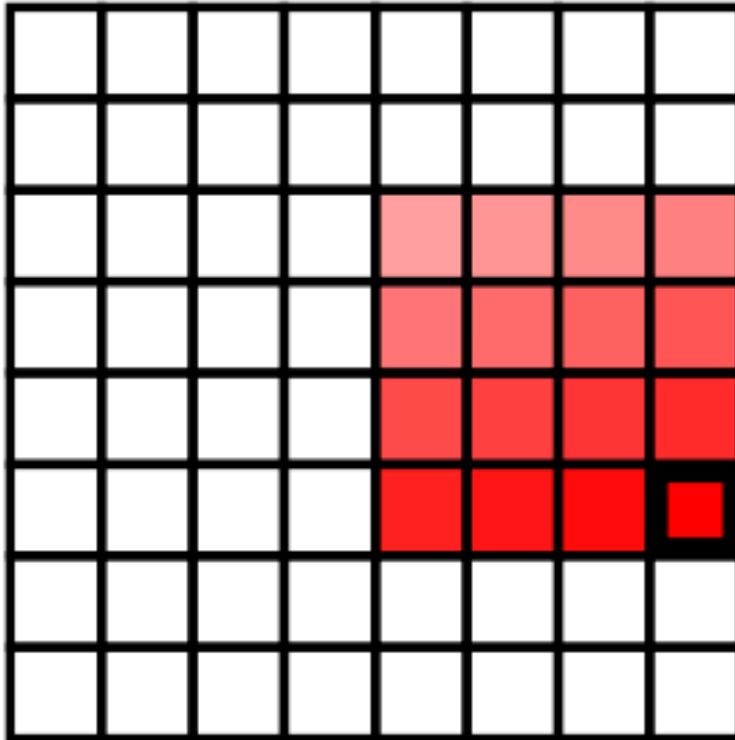


cache hits:829 ≈78%

C mem:351 cache hit:263

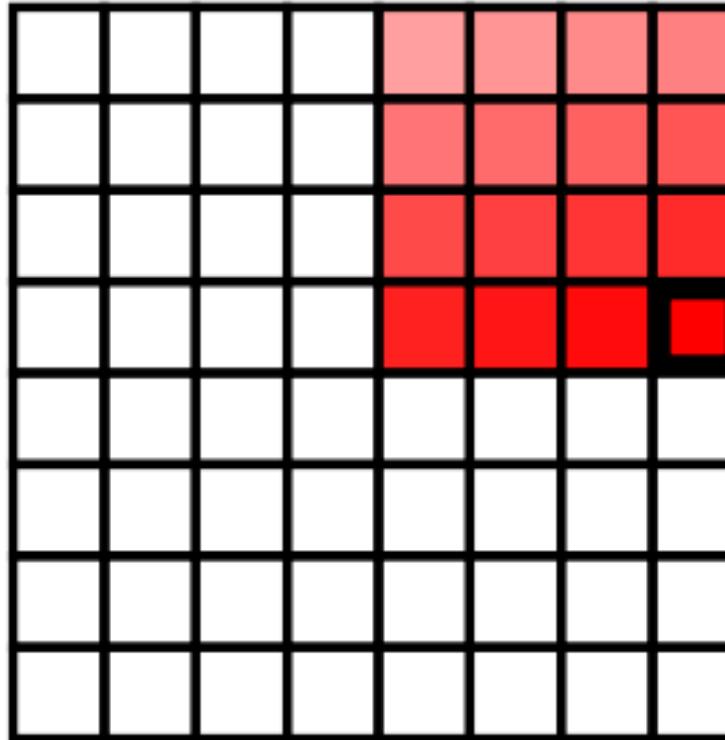


A mem:352 cache hit:264



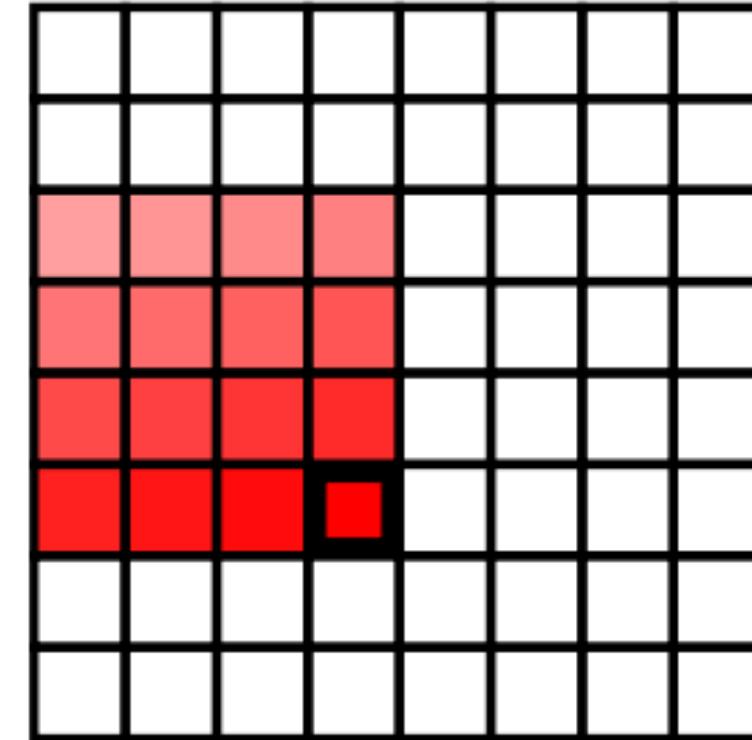
mem:1056

B mem:352 cache hit:304

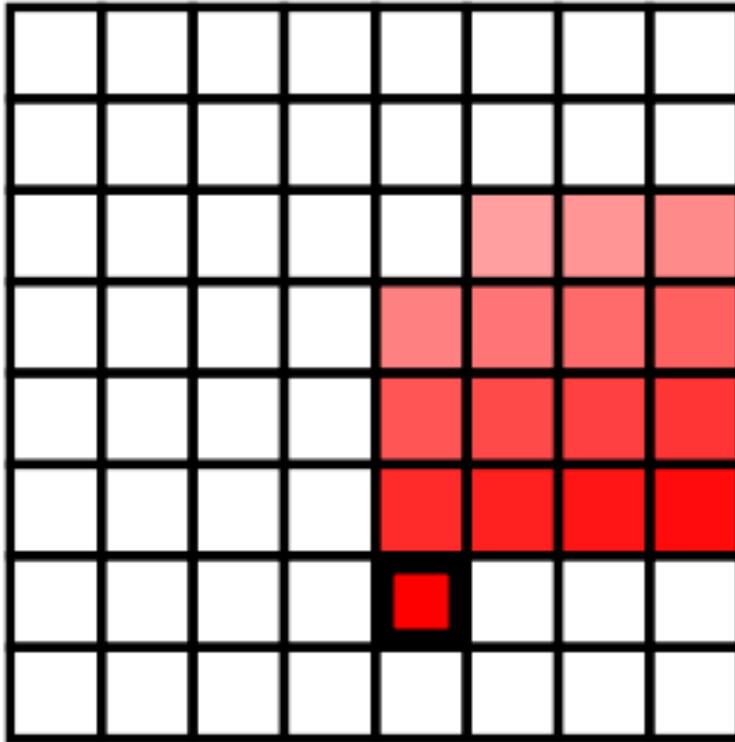


cache hits:832 $\approx 78\%$

C mem:352 cache hit:264

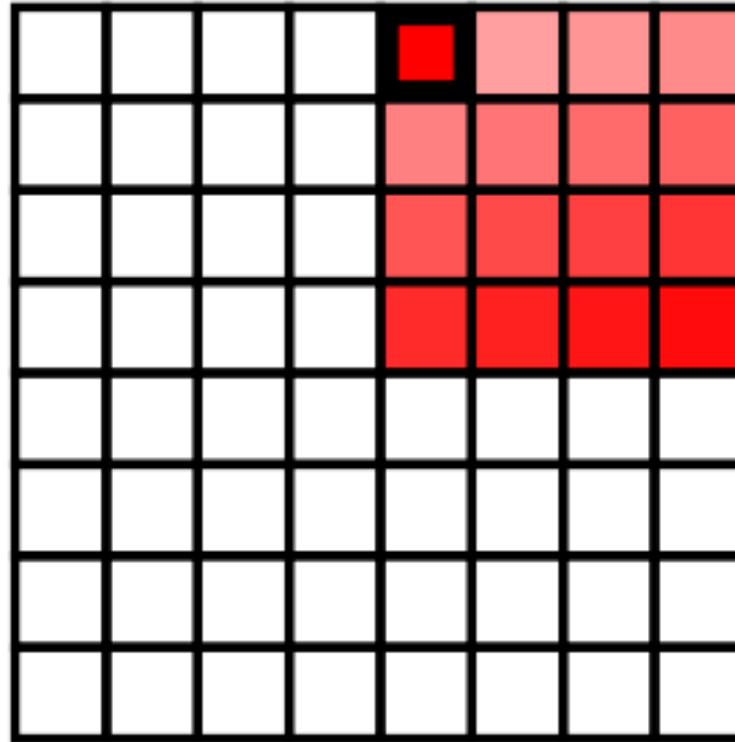


A mem:353 cache hit:264



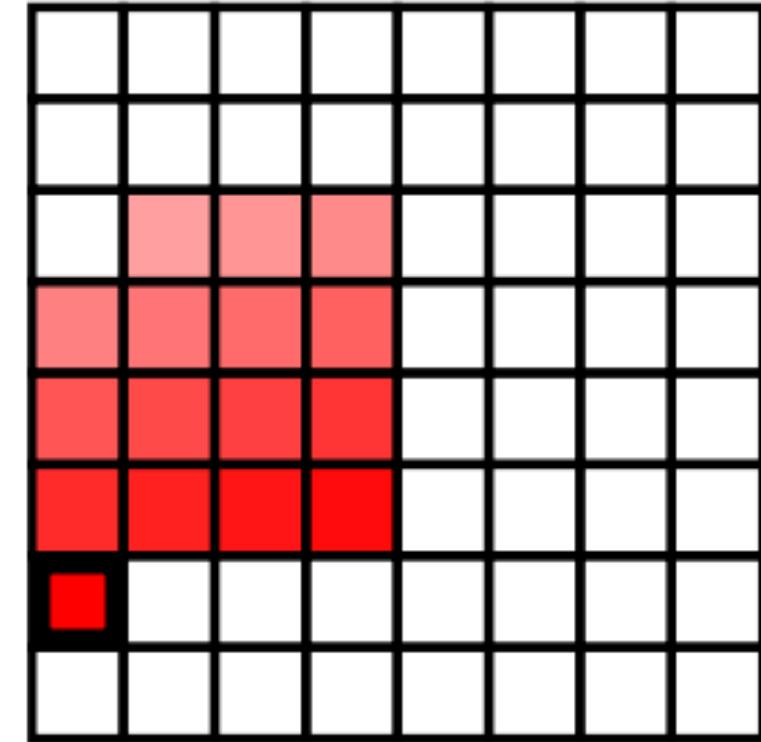
mem:1059

B mem:353 cache hit:305

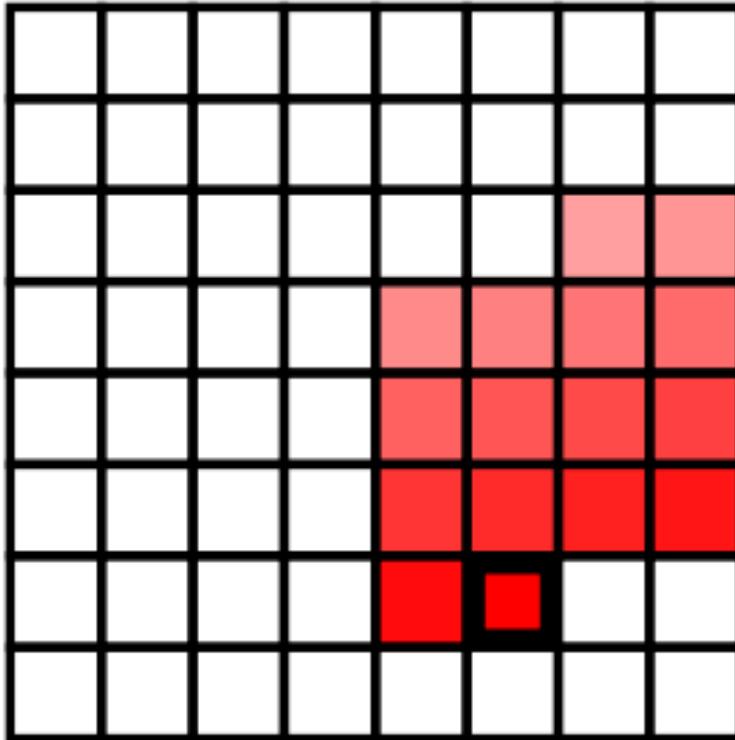


cache hits:833 ≈78%

C mem:353 cache hit:264

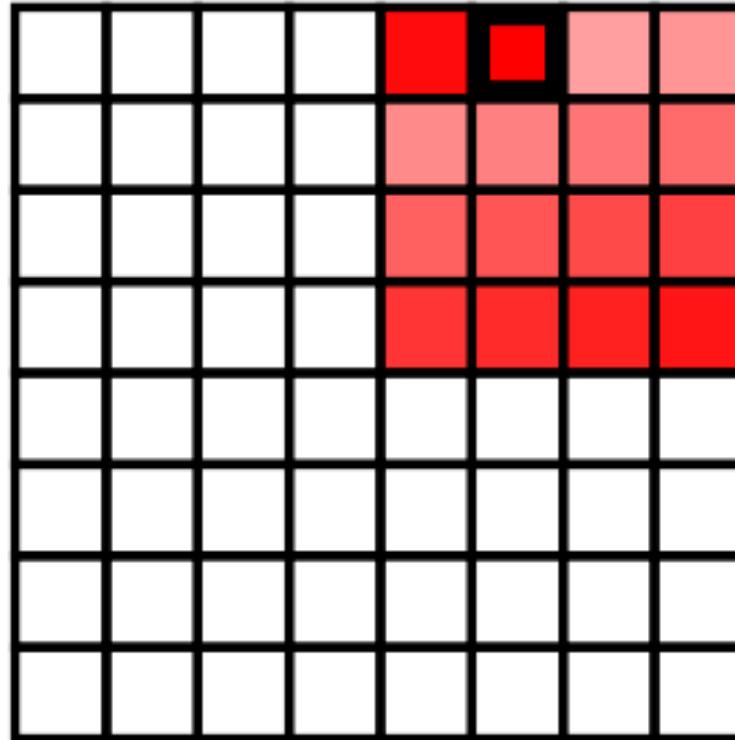


A mem:354 cache hit:264



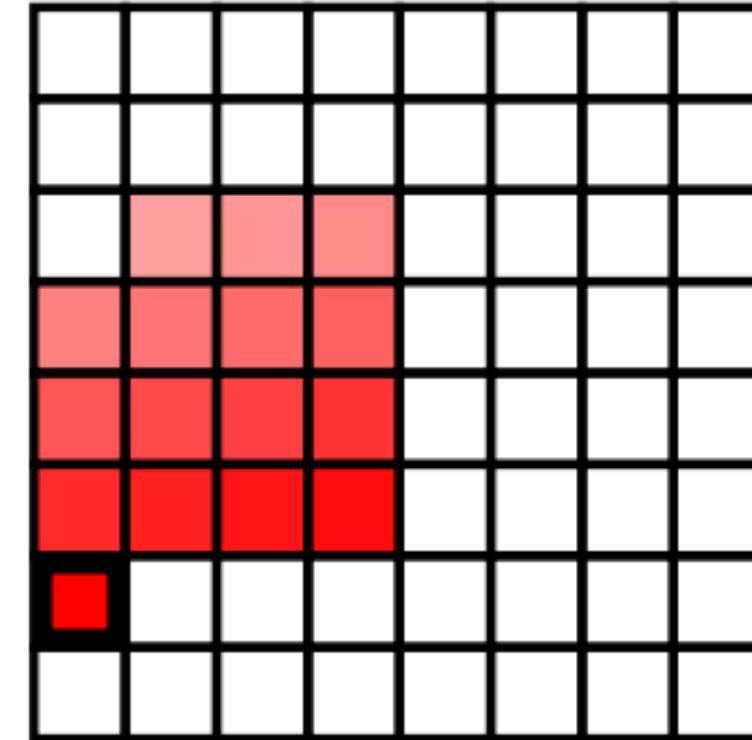
mem:1062

B mem:354 cache hit:306

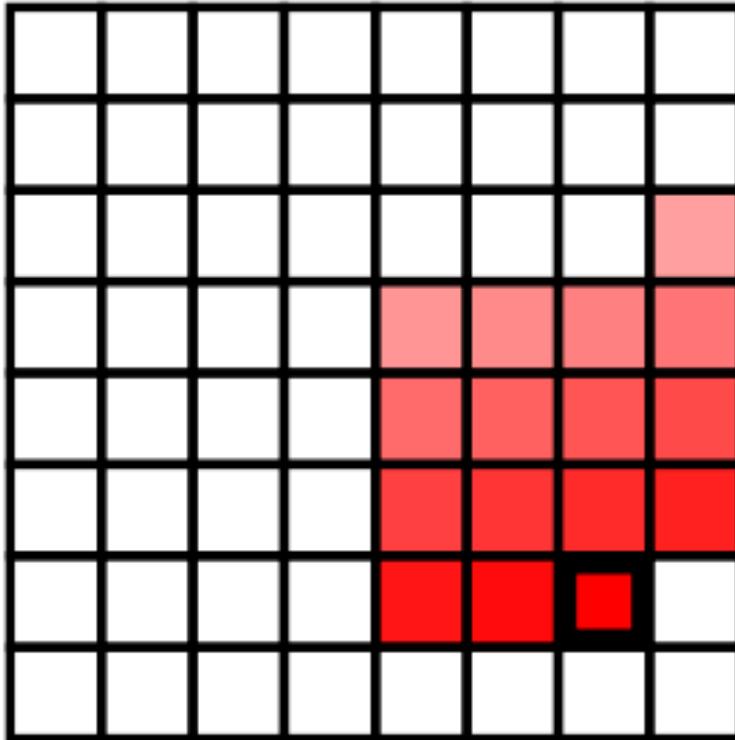


cache hits:835 $\approx 78\%$

C mem:354 cache hit:265

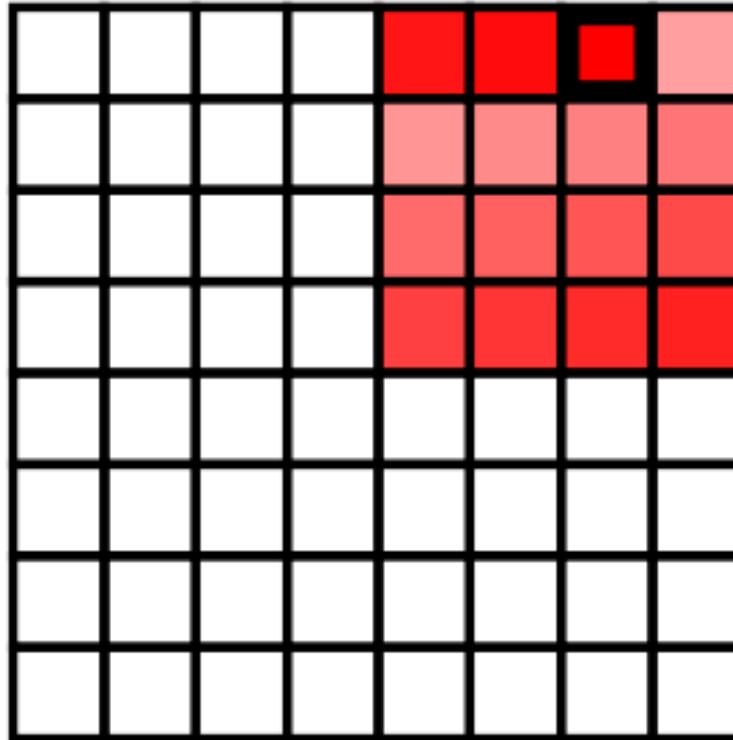


A mem:355 cache hit:264



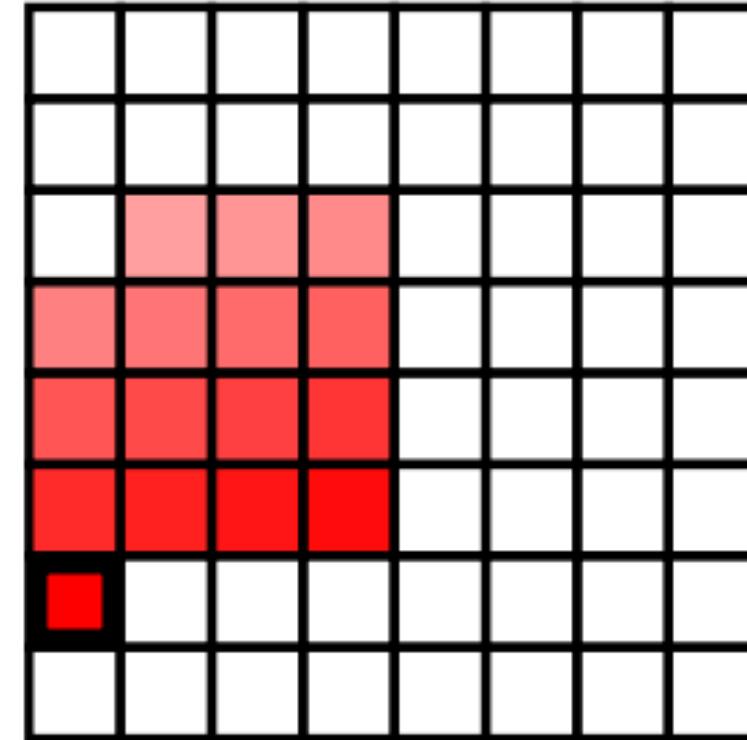
mem:1065

B mem:355 cache hit:307

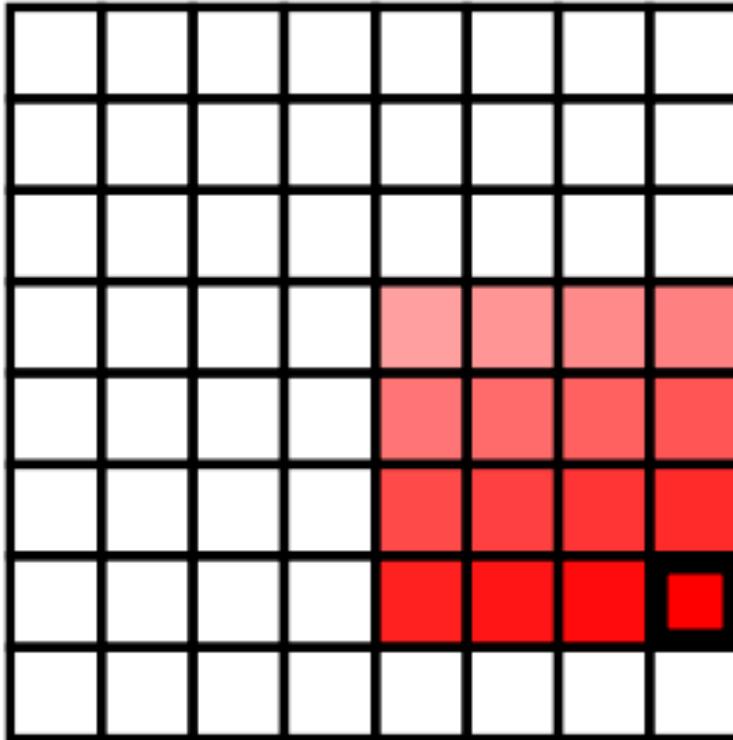


cache hits:837 $\approx 78\%$

C mem:355 cache hit:266

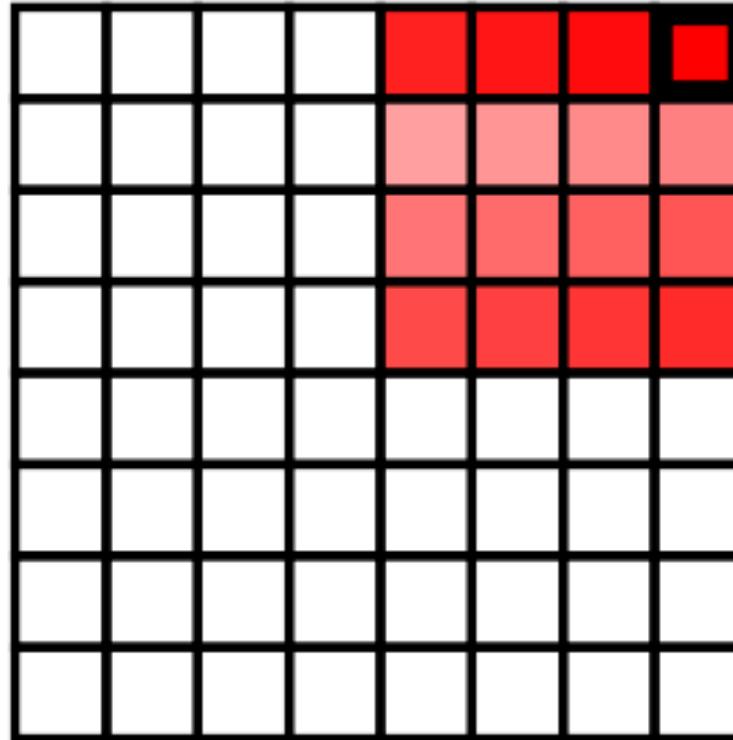


A mem:356 cache hit:264



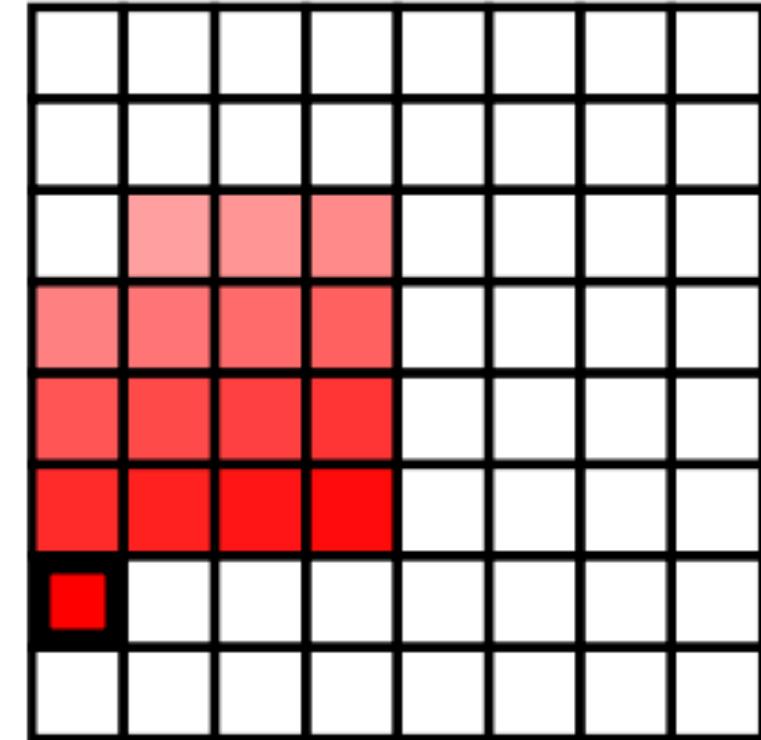
mem:1068

B mem:356 cache hit:308

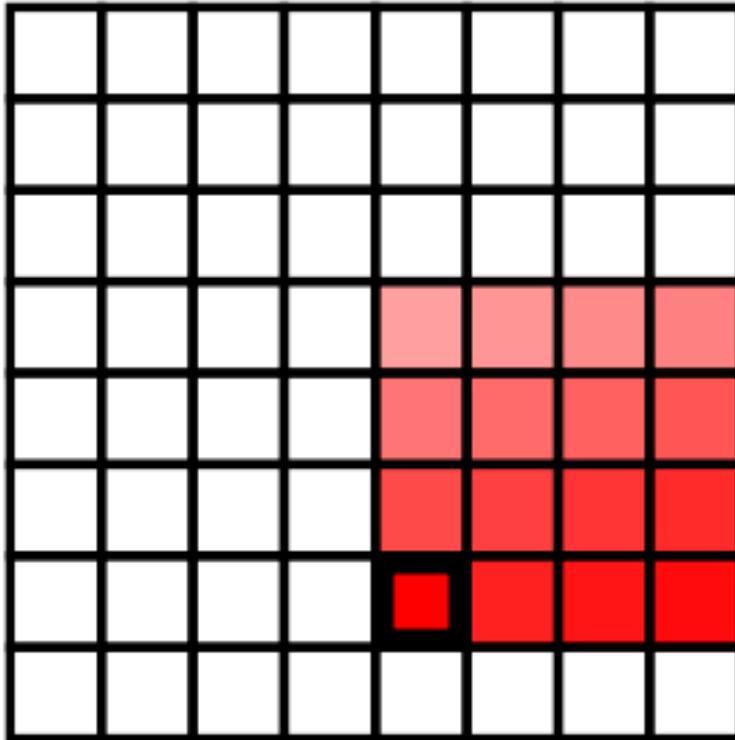


cache hits:839 $\approx 78\%$

C mem:356 cache hit:267

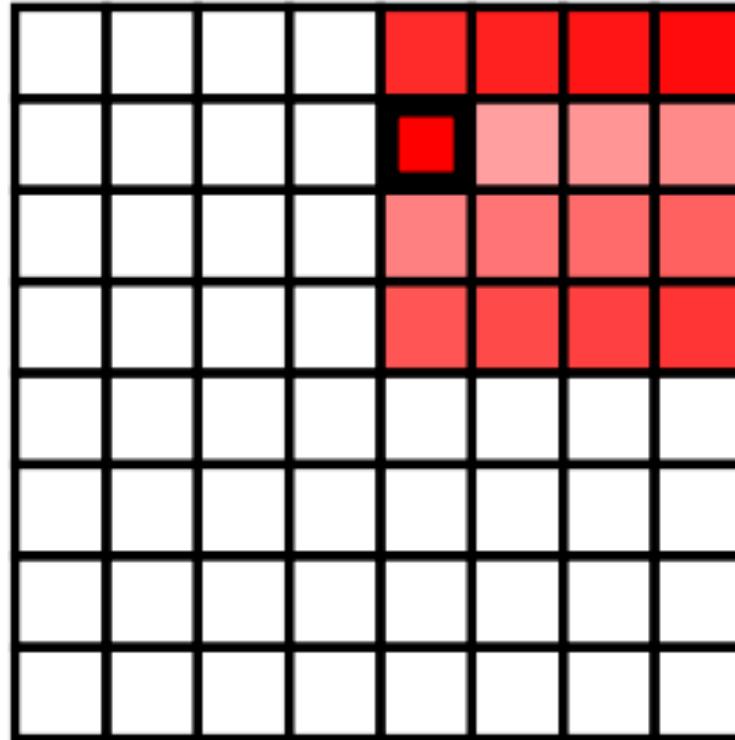


A mem:357 cache hit:265



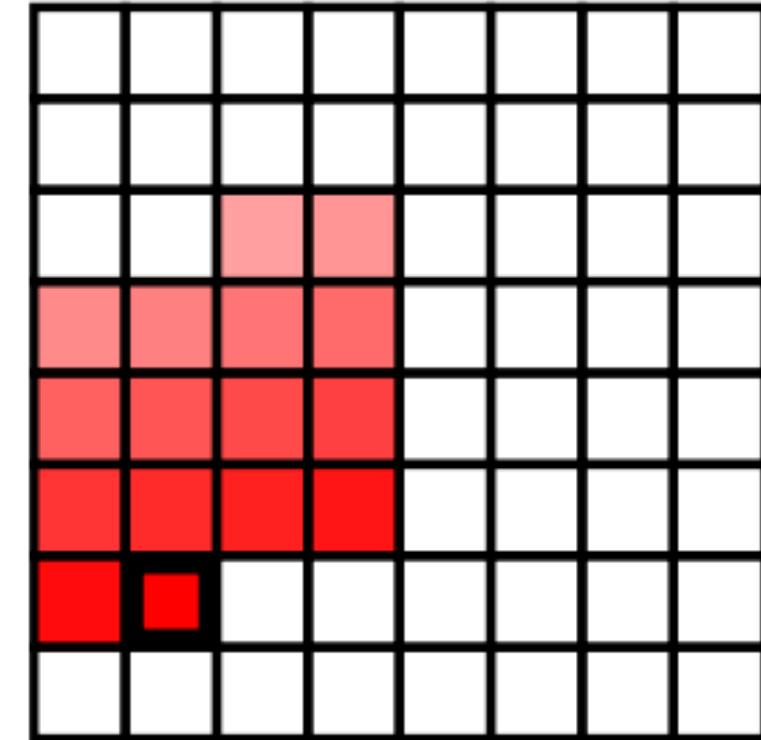
mem:1071

B mem:357 cache hit:309

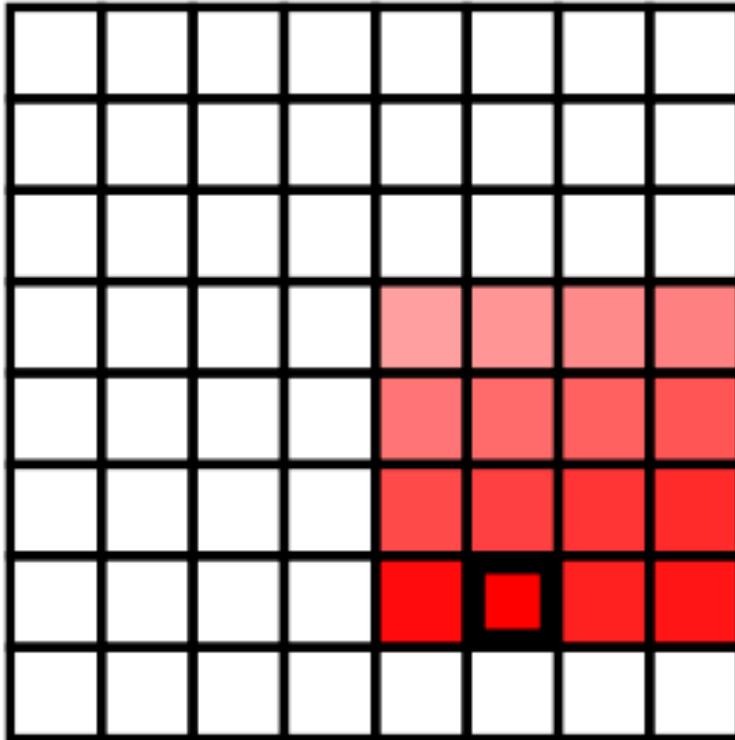


cache hits:841 ≈78%

C mem:357 cache hit:267

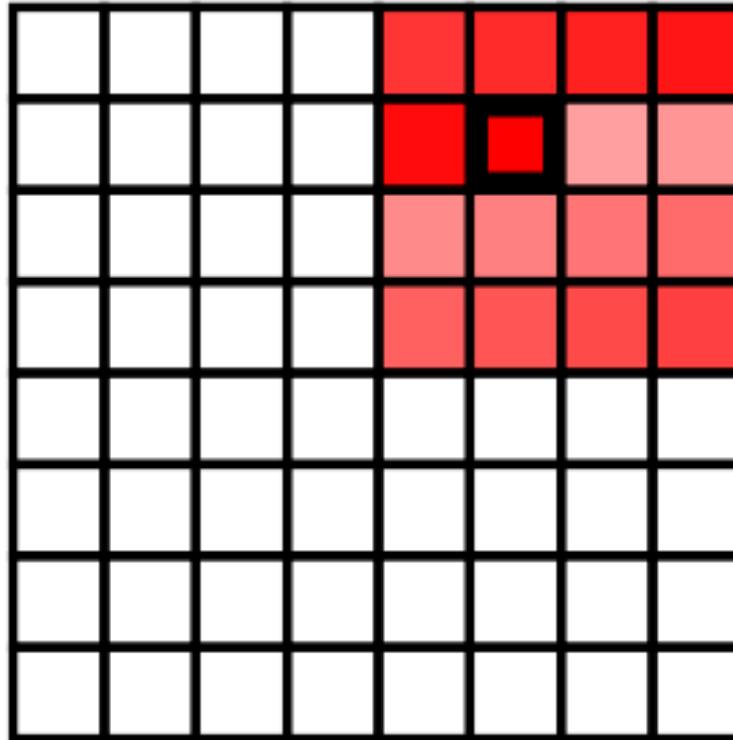


A mem:358 cache hit:266



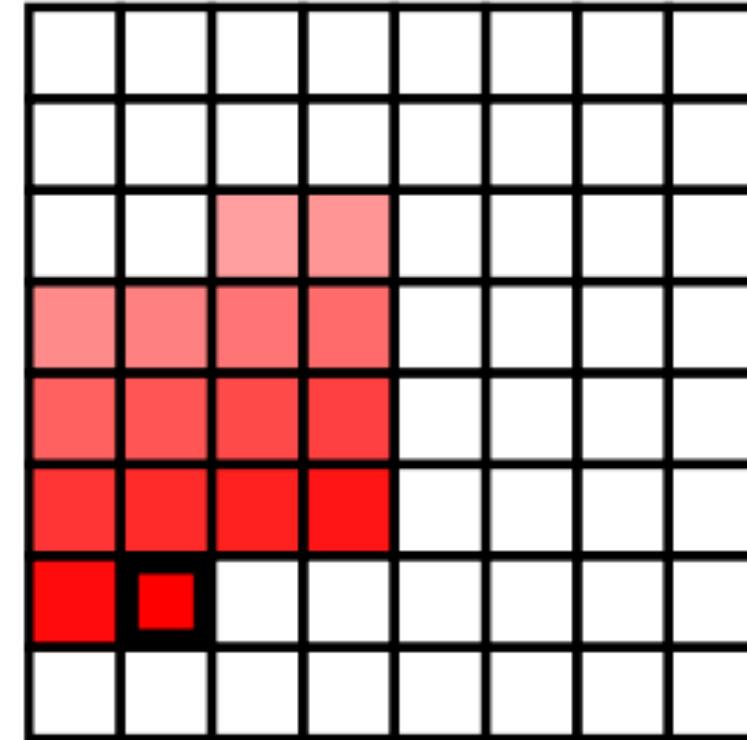
mem:1074

B mem:358 cache hit:310

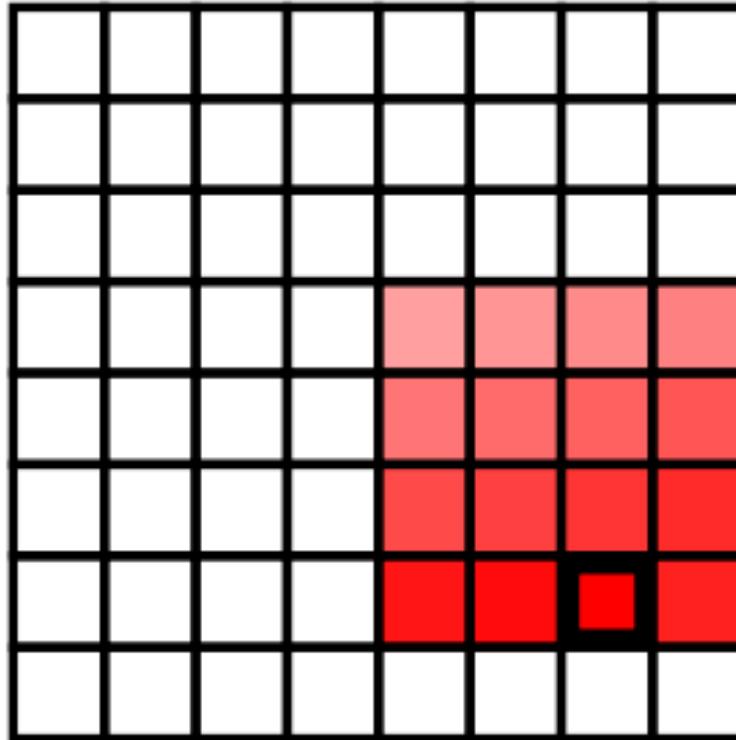


cache hits:844 ≈78%

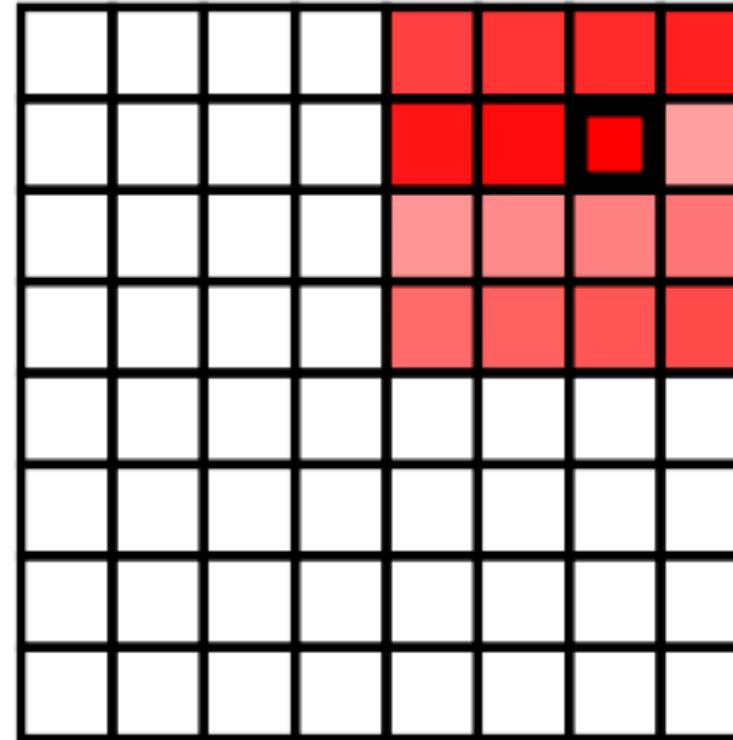
C mem:358 cache hit:268



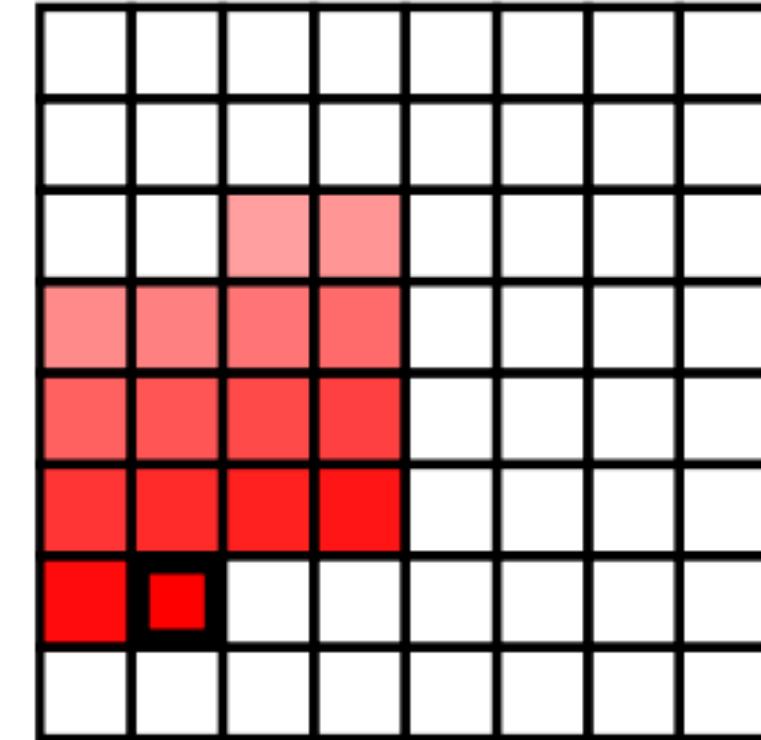
A mem:359 cache hit:267



B mem:359 cache hit:311



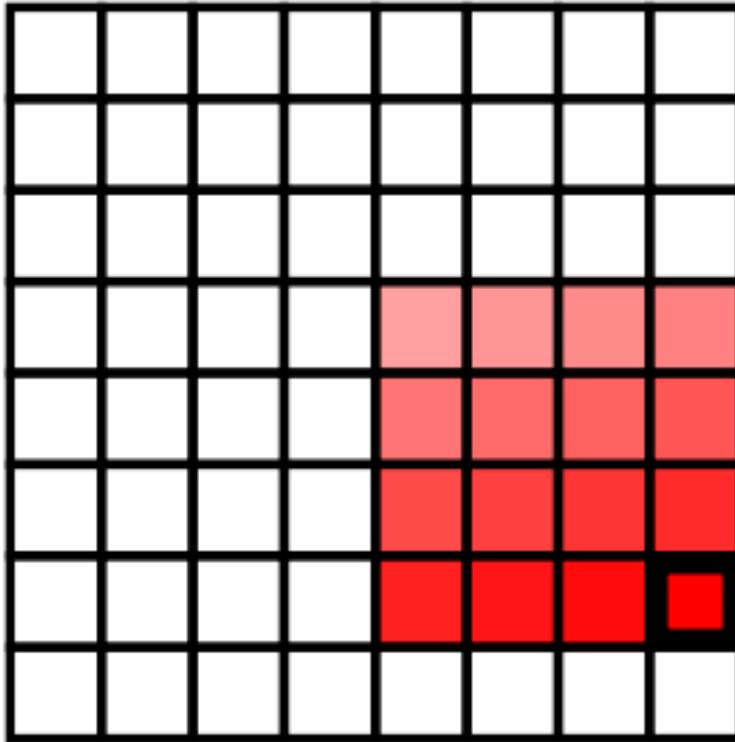
C mem:359 cache hit:269



mem:1077

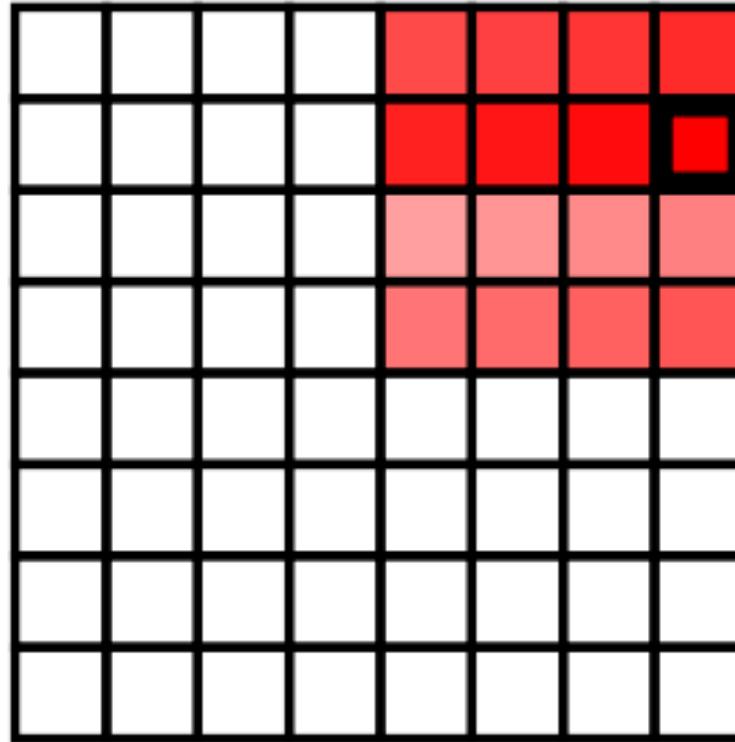
cache hits:847 $\approx 78\%$

A mem:360 cache hit:268



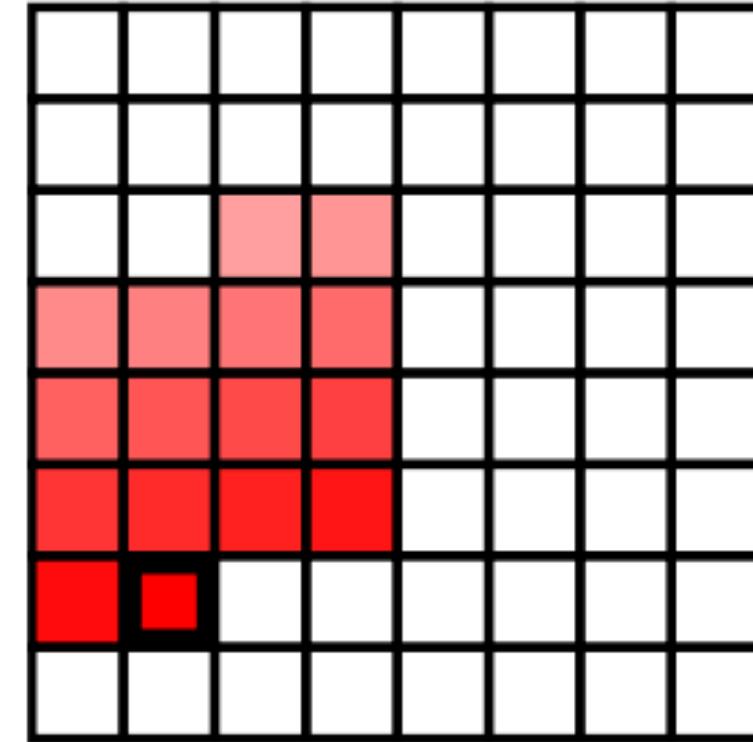
mem:1080

B mem:360 cache hit:312

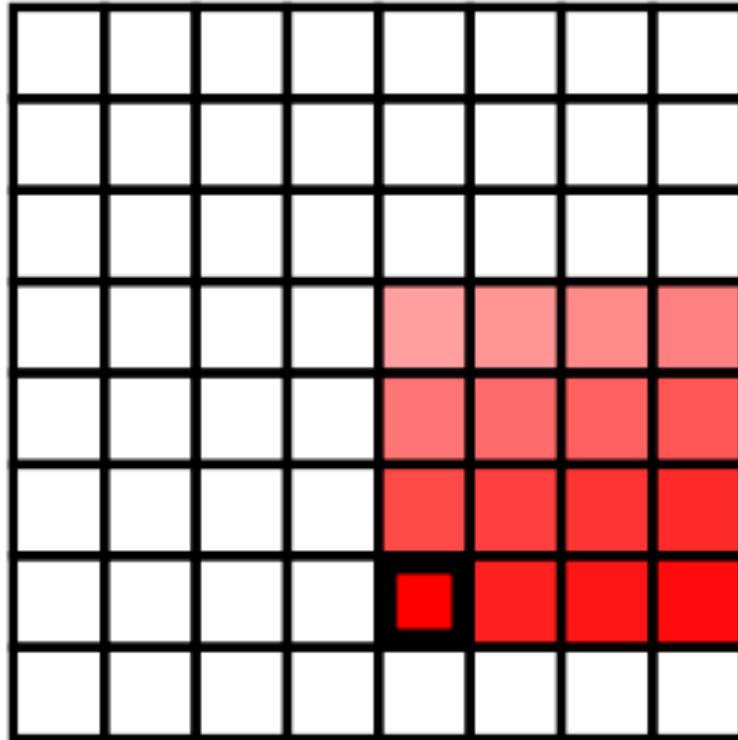


cache hits:850 $\approx 78\%$

C mem:360 cache hit:270

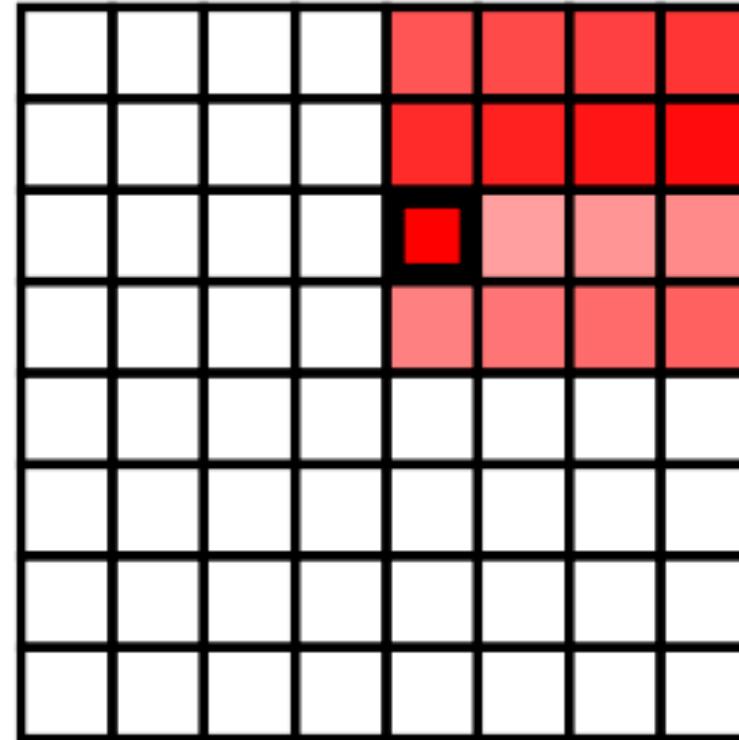


A mem:361 cache hit:269



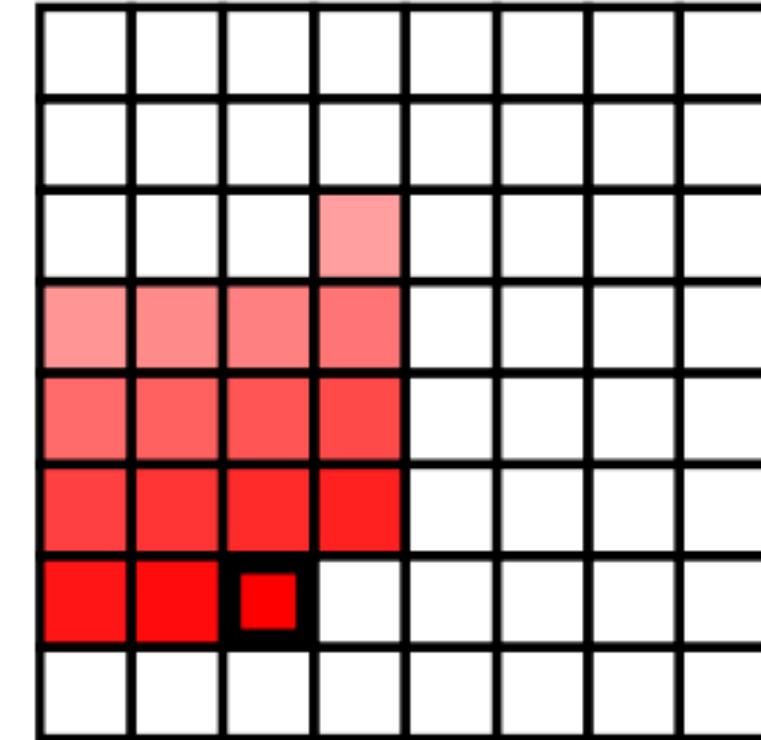
mem:1083

B mem:361 cache hit:313

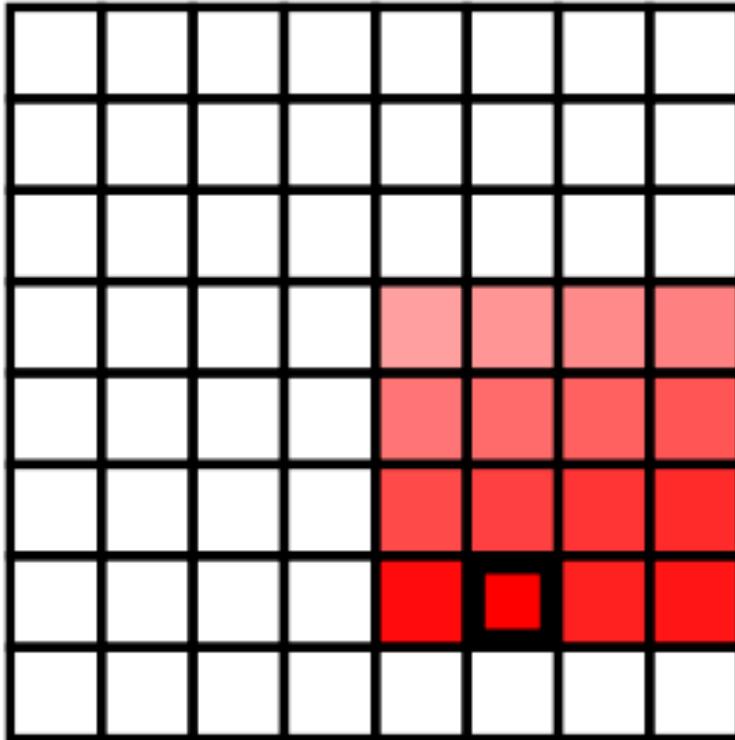


cache hits:852 ≈78%

C mem:361 cache hit:270

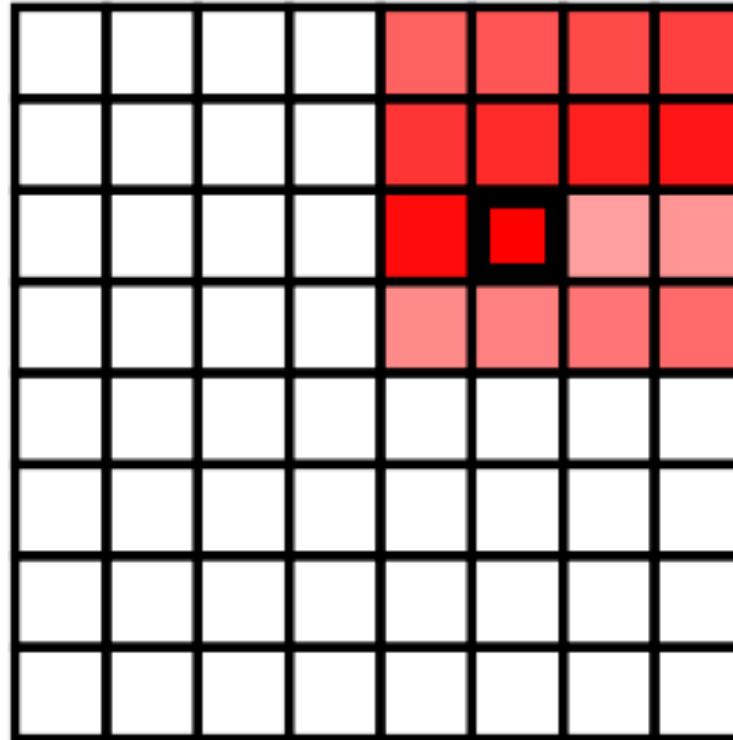


A mem:362 cache hit:270



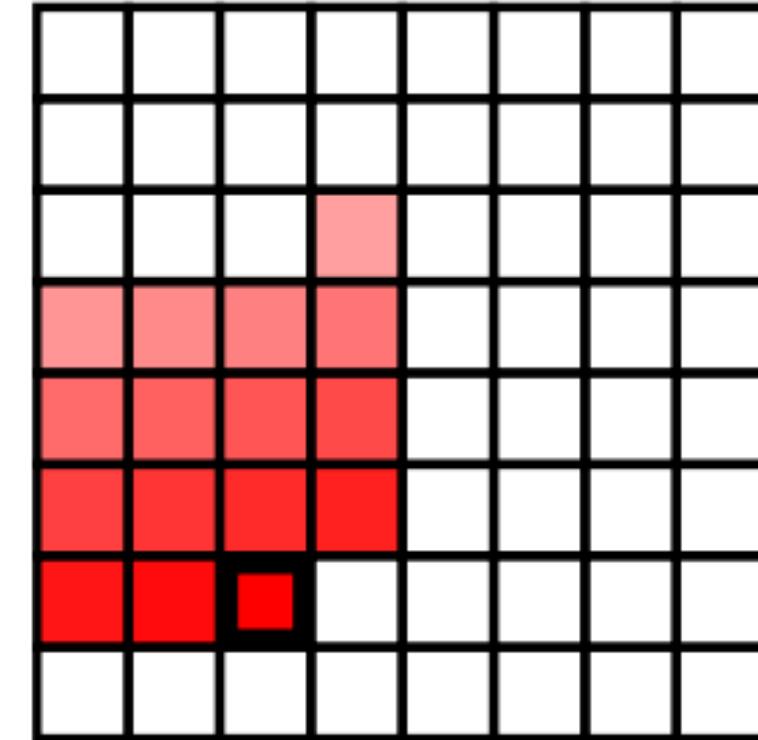
mem:1086

B mem:362 cache hit:314

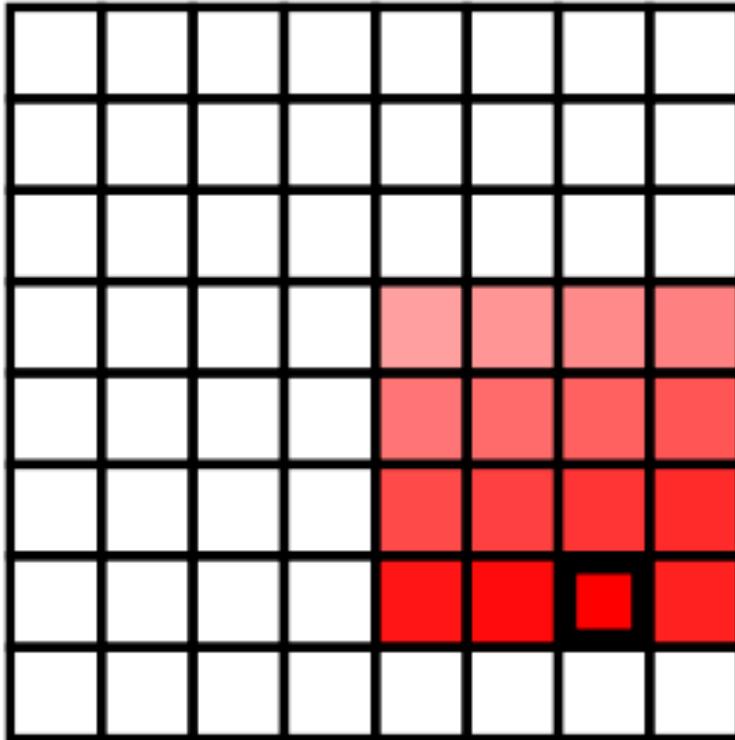


cache hits:855 ≈78%

C mem:362 cache hit:271

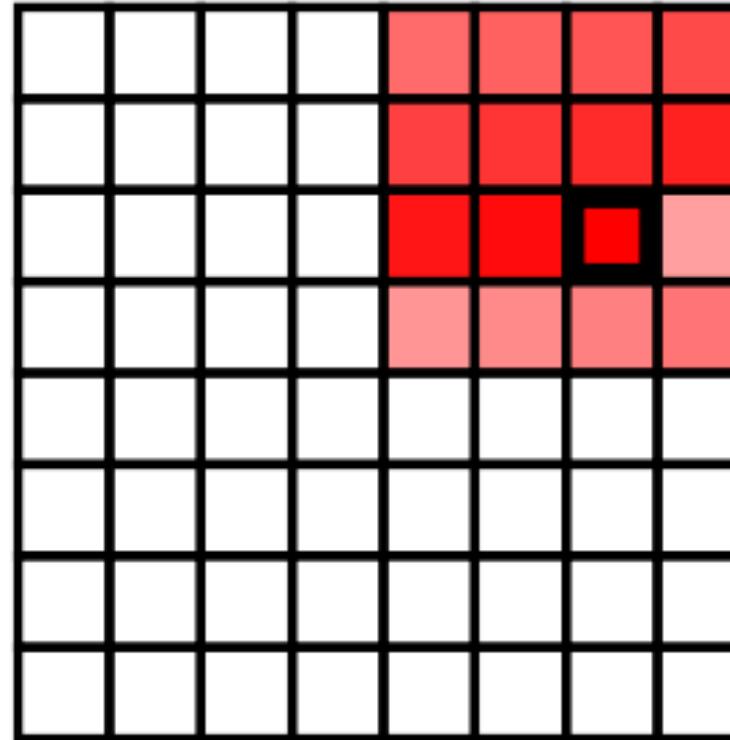


A mem:363 cache hit:271



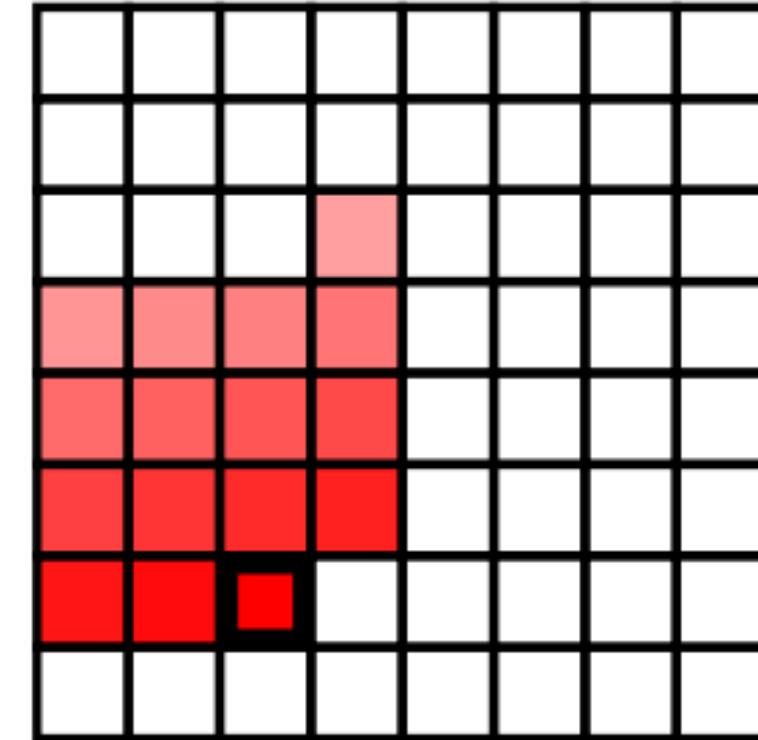
mem:1089

B mem:363 cache hit:315

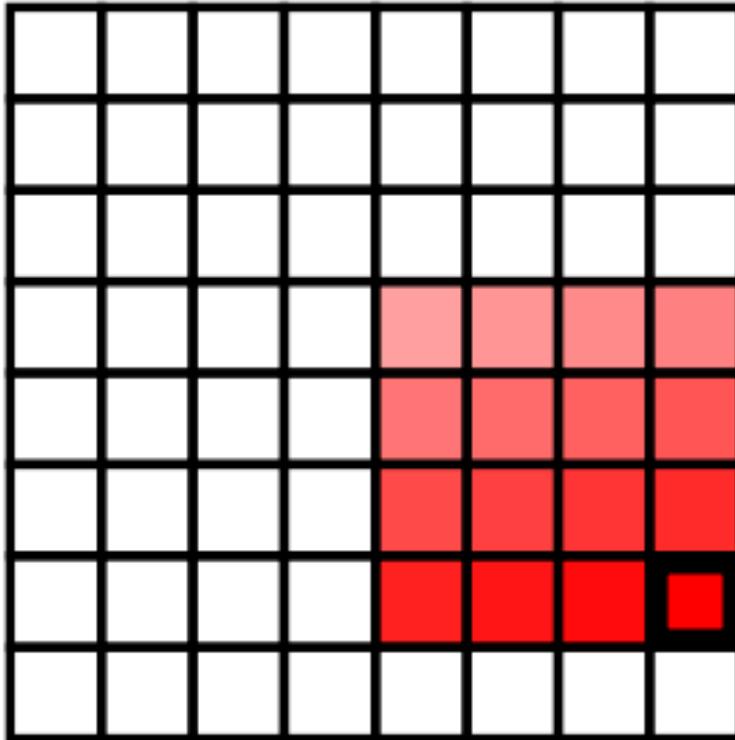


cache hits:858 ≈78%

C mem:363 cache hit:272

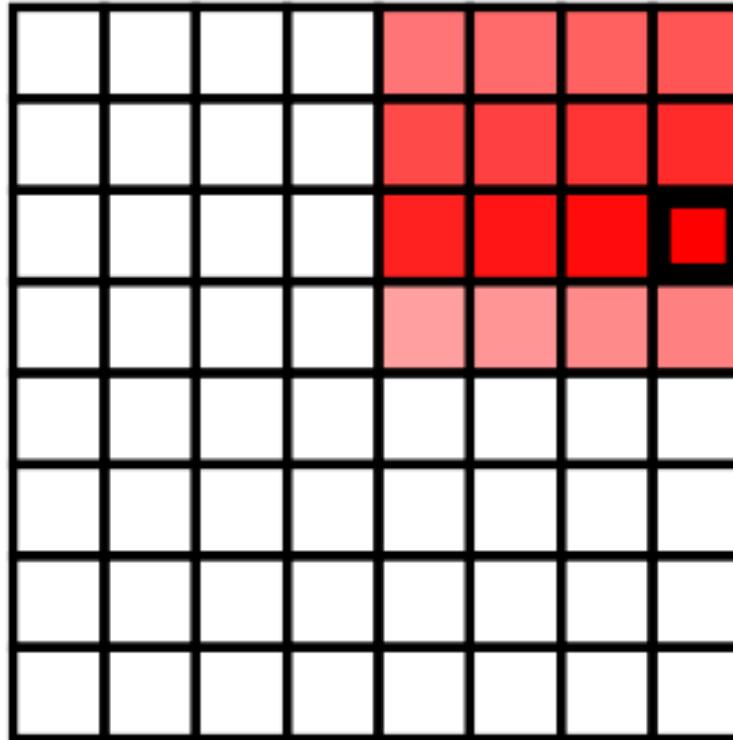


A mem:364 cache hit:272



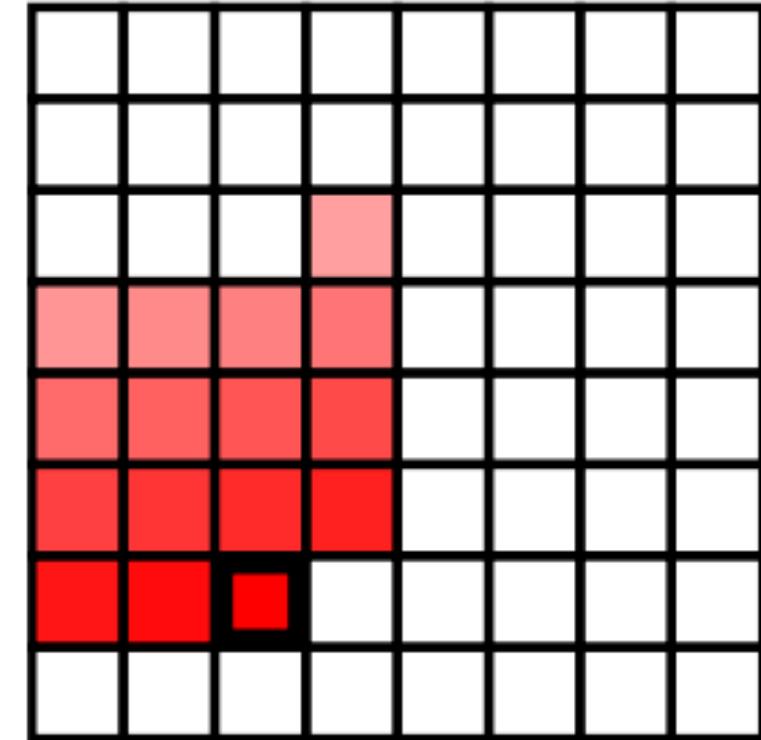
mem:1092

B mem:364 cache hit:316

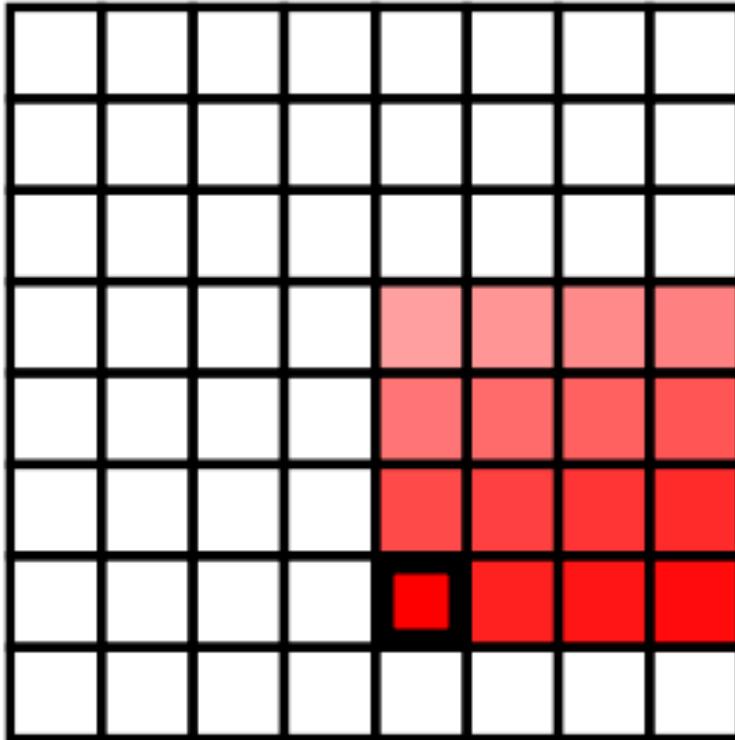


cache hits:861 $\approx 78\%$

C mem:364 cache hit:273

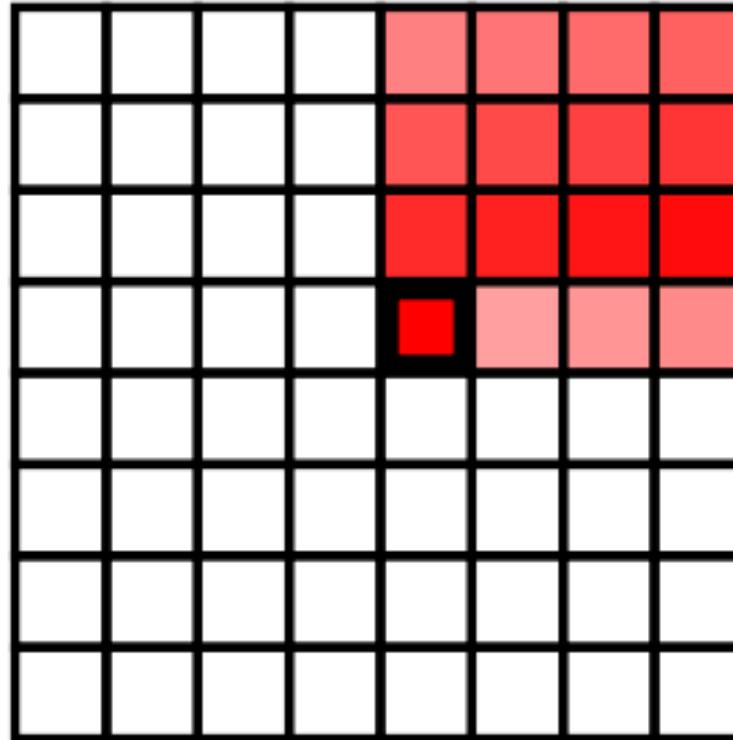


A mem:365 cache hit:273



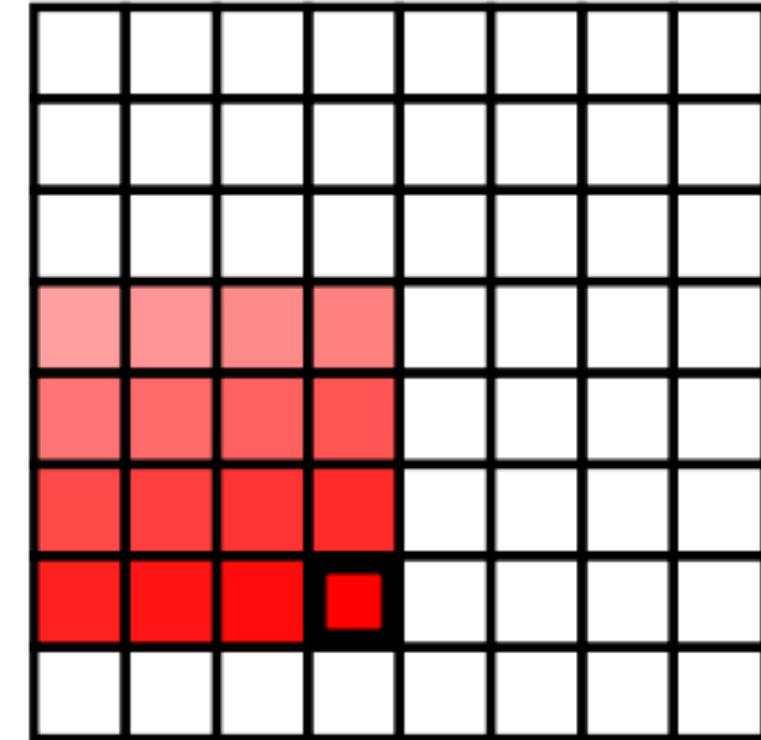
mem:1095

B mem:365 cache hit:317

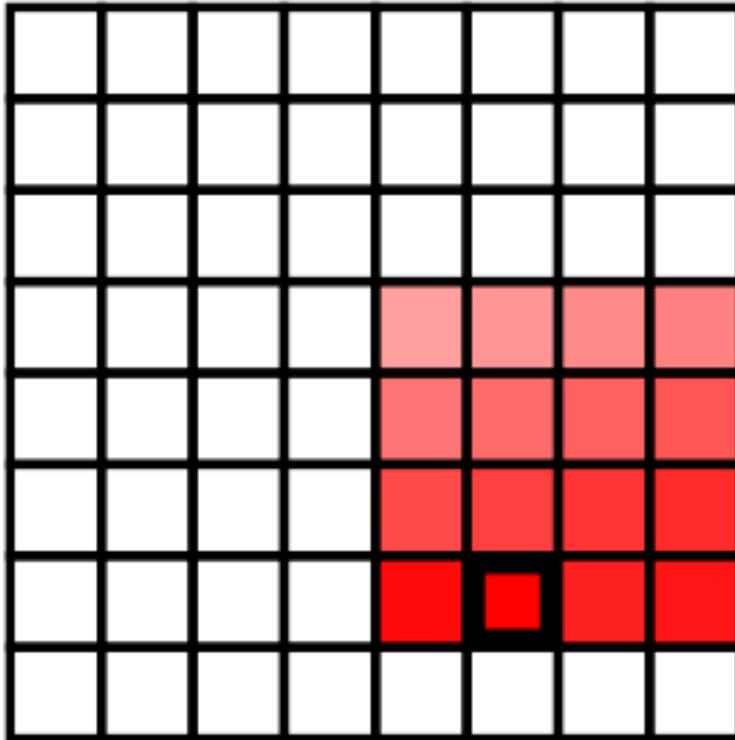


cache hits:863 ≈78%

C mem:365 cache hit:273

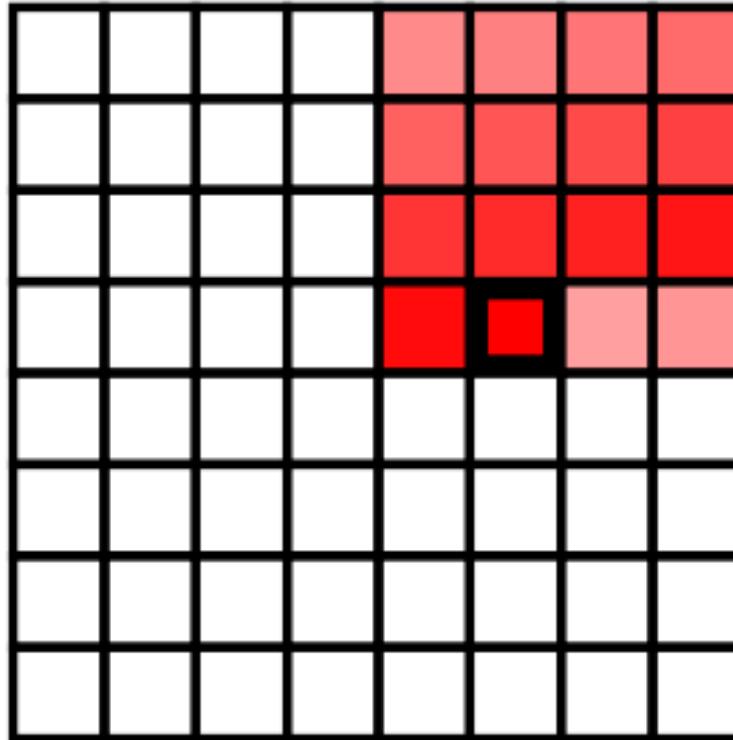


A mem:366 cache hit:274



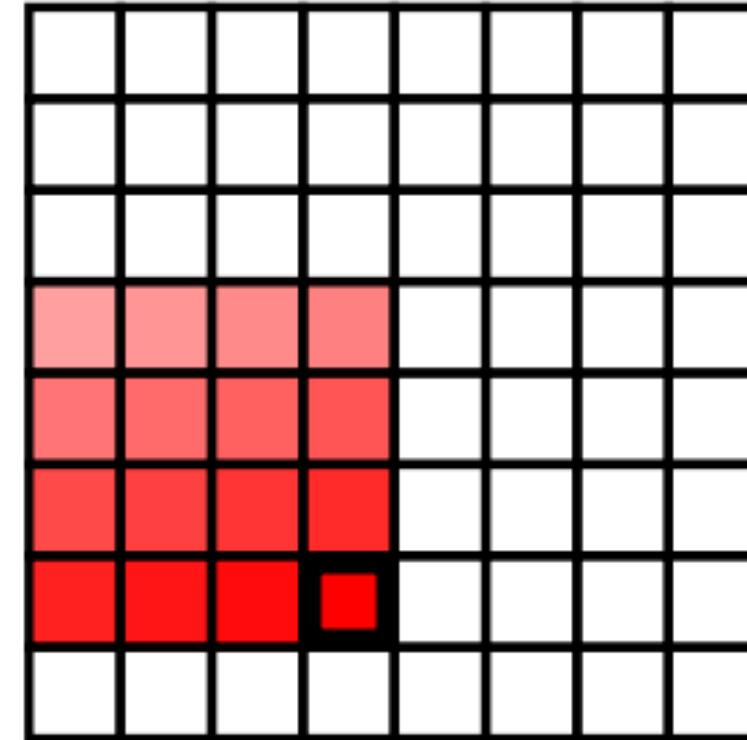
mem:1098

B mem:366 cache hit:318

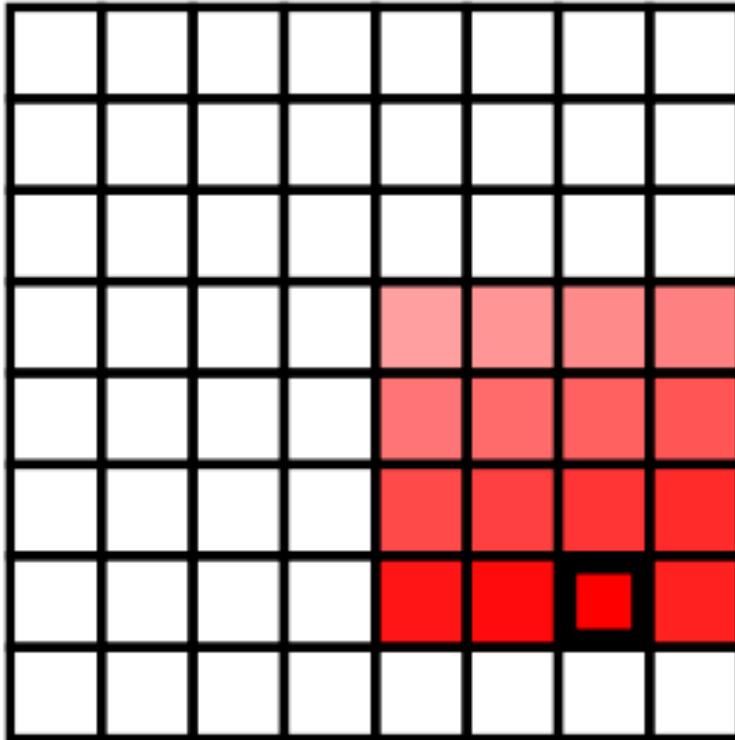


cache hits:866 ≈78%

C mem:366 cache hit:274

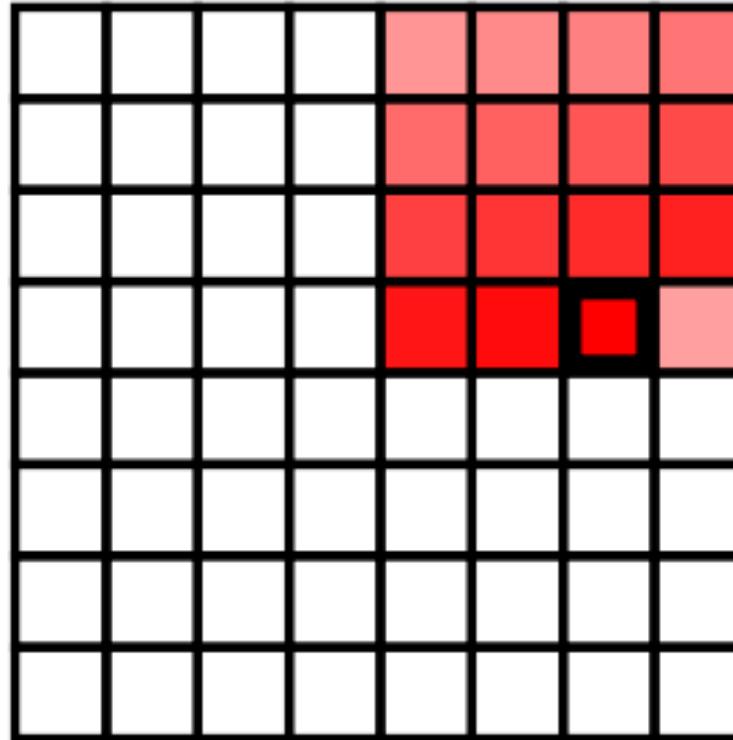


A mem:367 cache hit:275



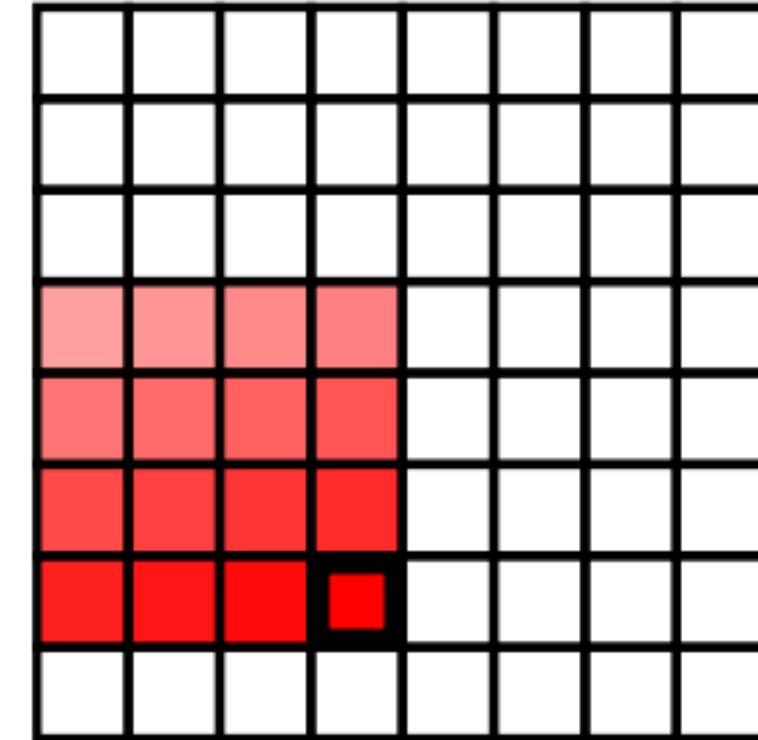
mem:1101

B mem:367 cache hit:319

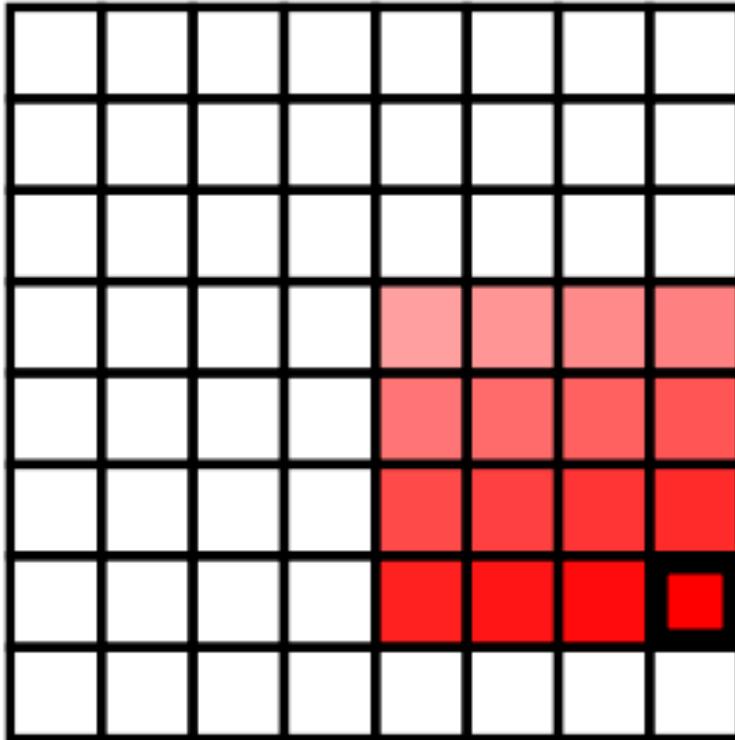


cache hits:869 ≈78%

C mem:367 cache hit:275

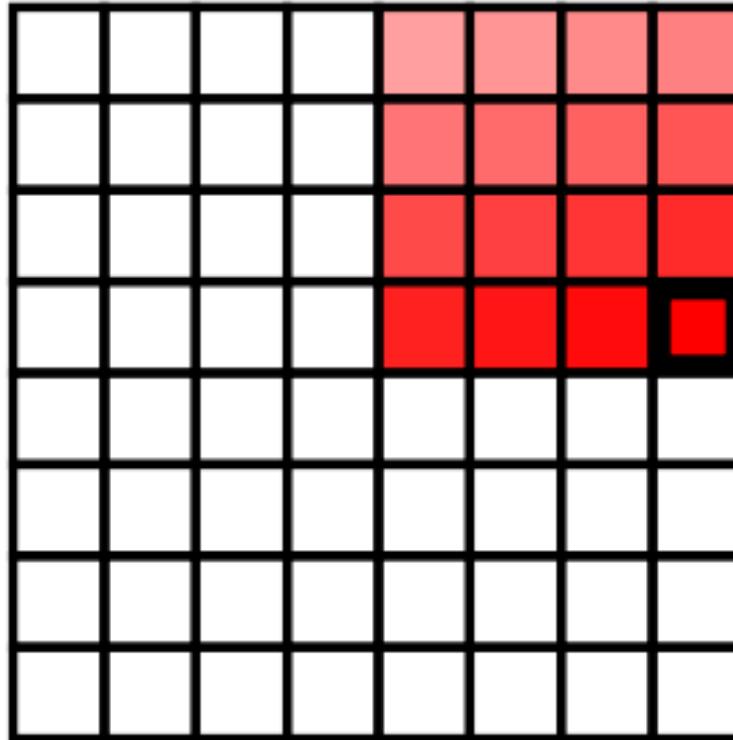


A mem:368 cache hit:276



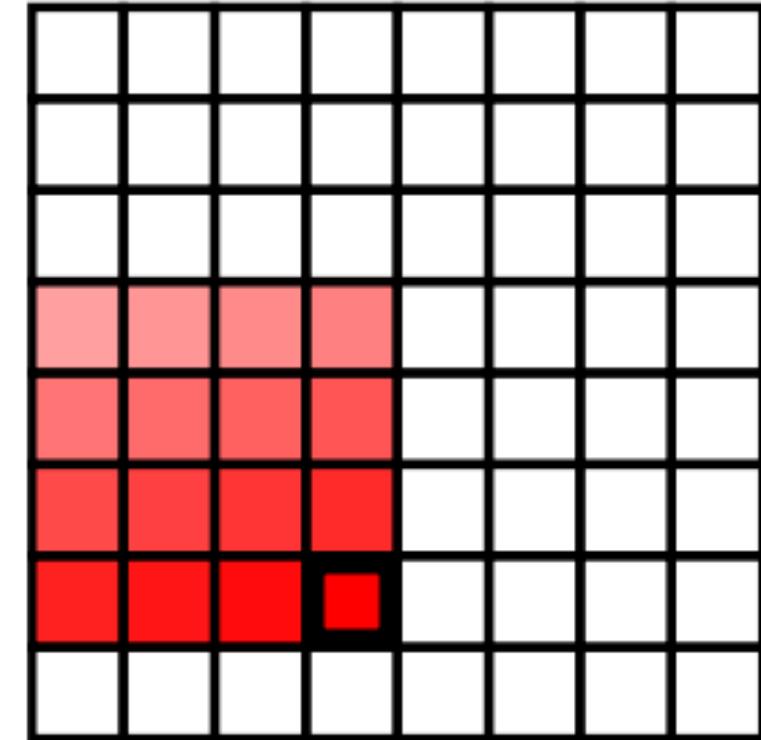
mem:1104

B mem:368 cache hit:320

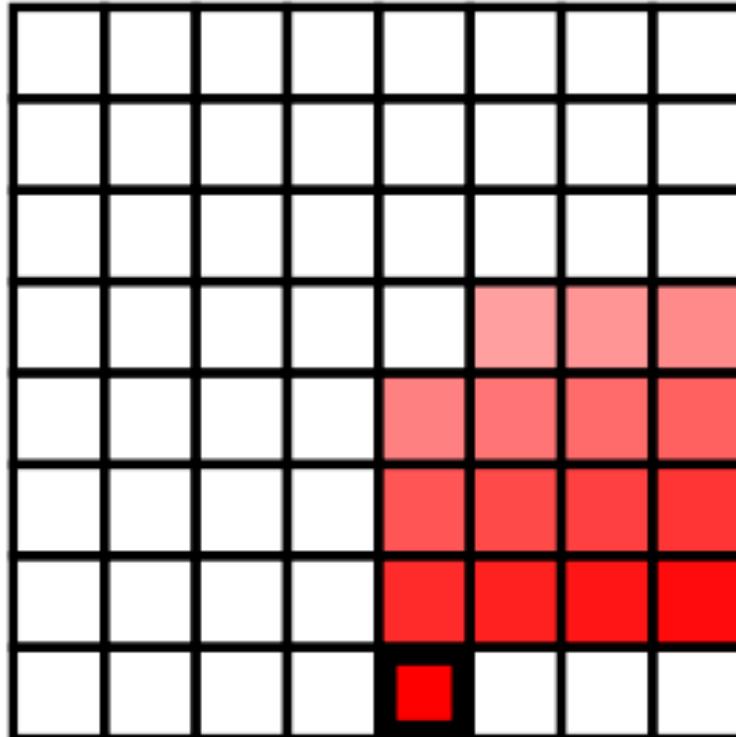


cache hits:872 ≈78%

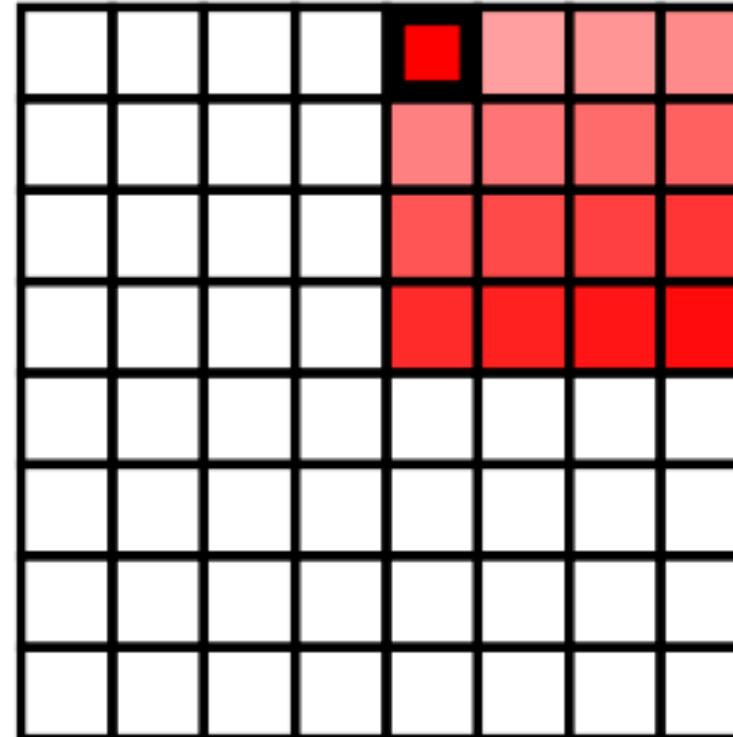
C mem:368 cache hit:276



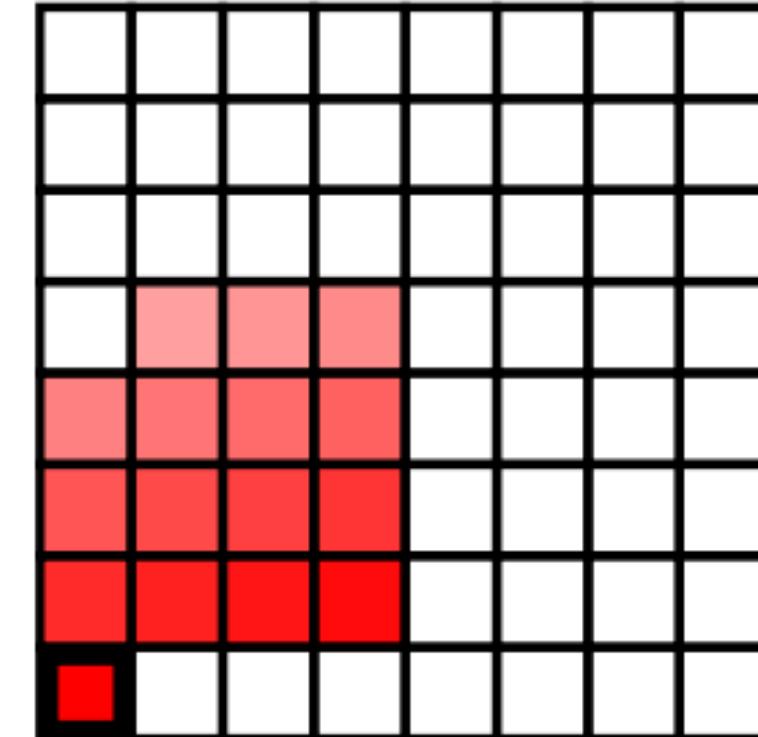
A mem:369 cache hit:276



B mem:369 cache hit:321



C mem:369 cache hit:276



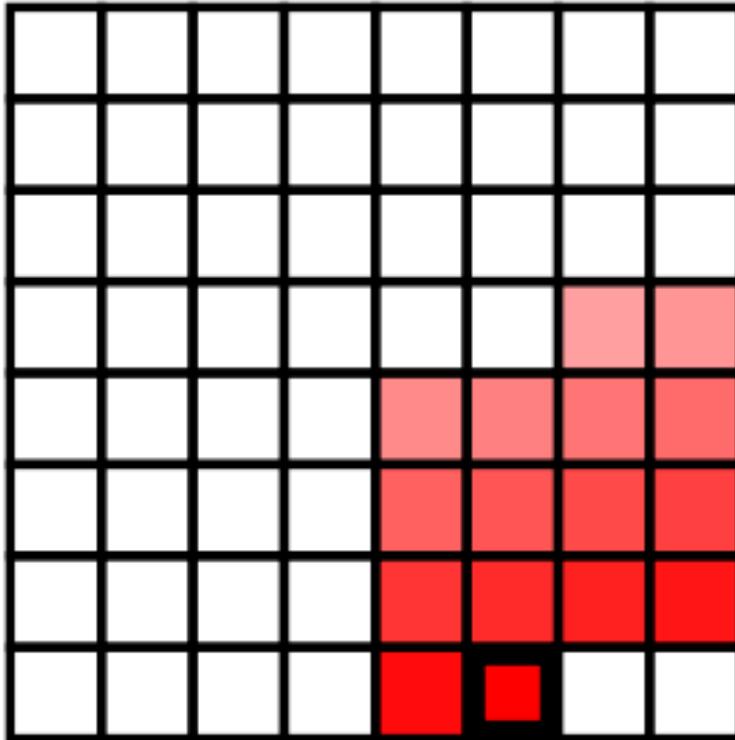
×

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mem:1107

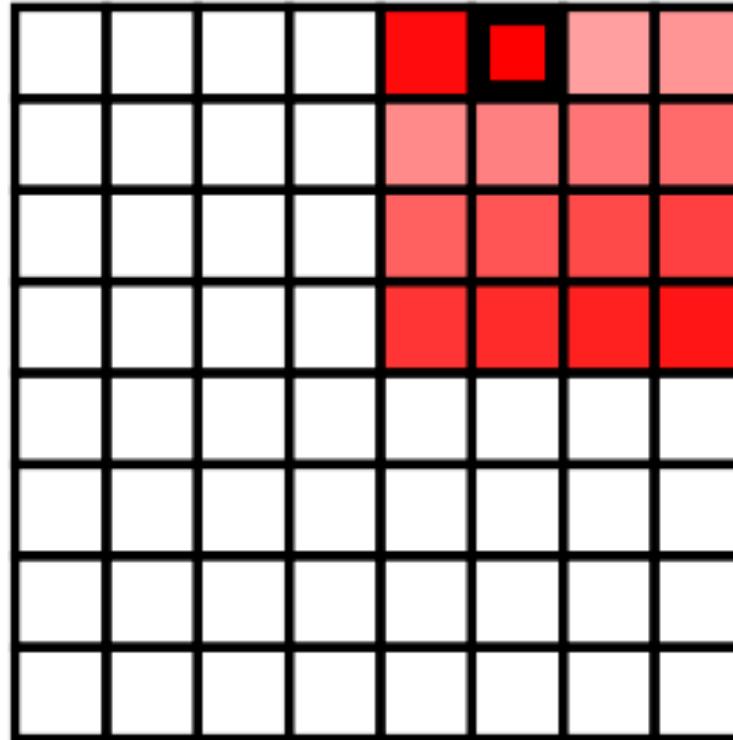
cache hits:873 \approx 78%

A mem:370 cache hit:276



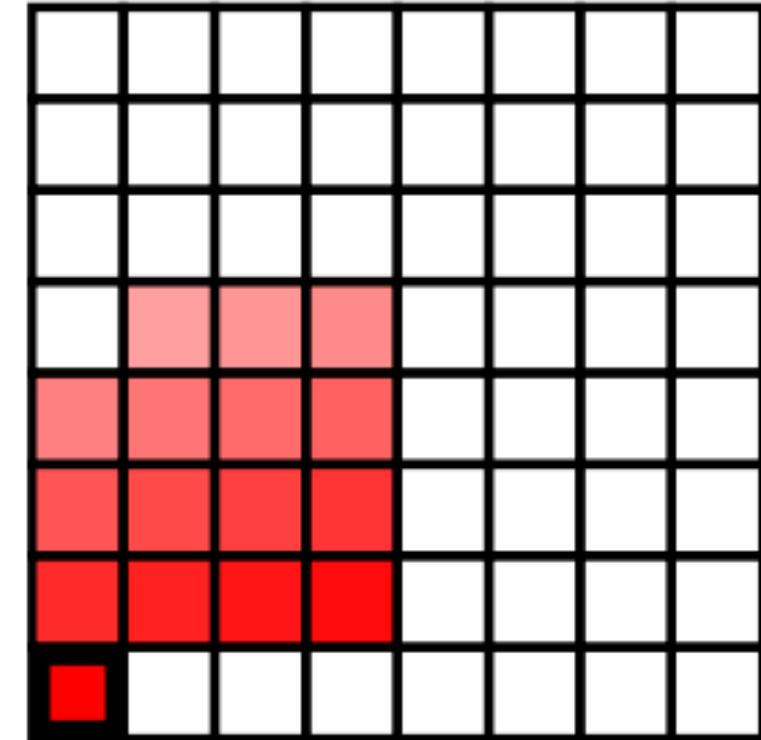
mem:1110

B mem:370 cache hit:322

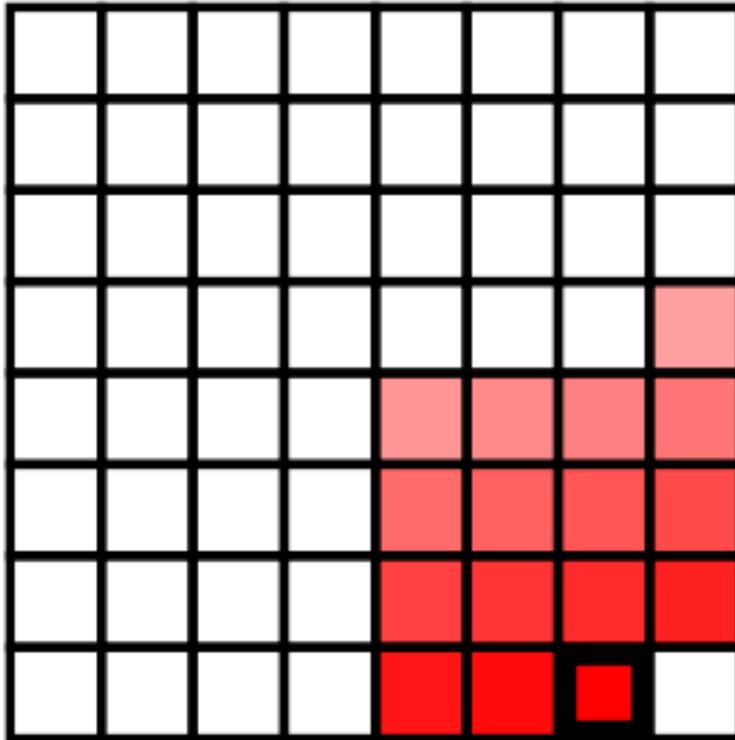


cache hits:875 $\approx 78\%$

C mem:370 cache hit:277

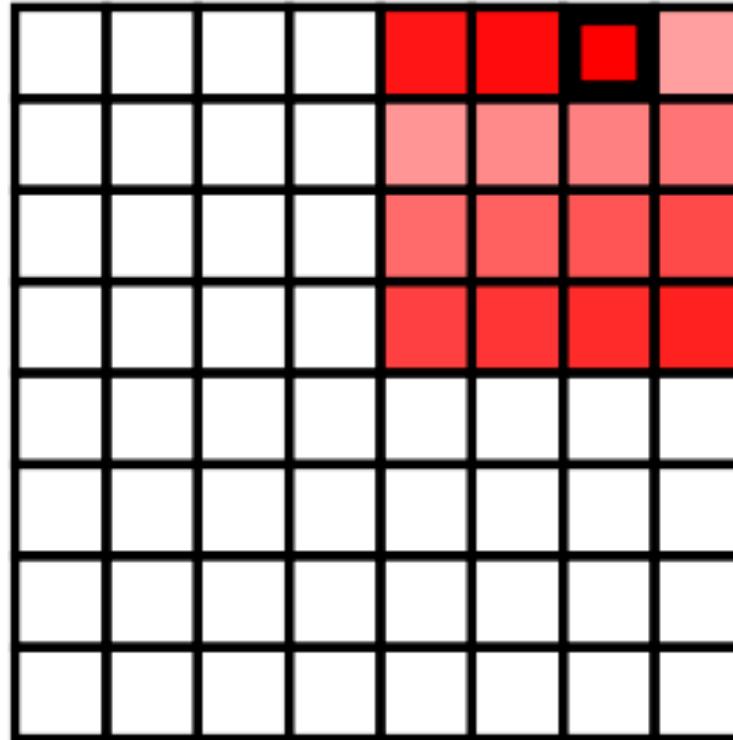


A mem:371 cache hit:276



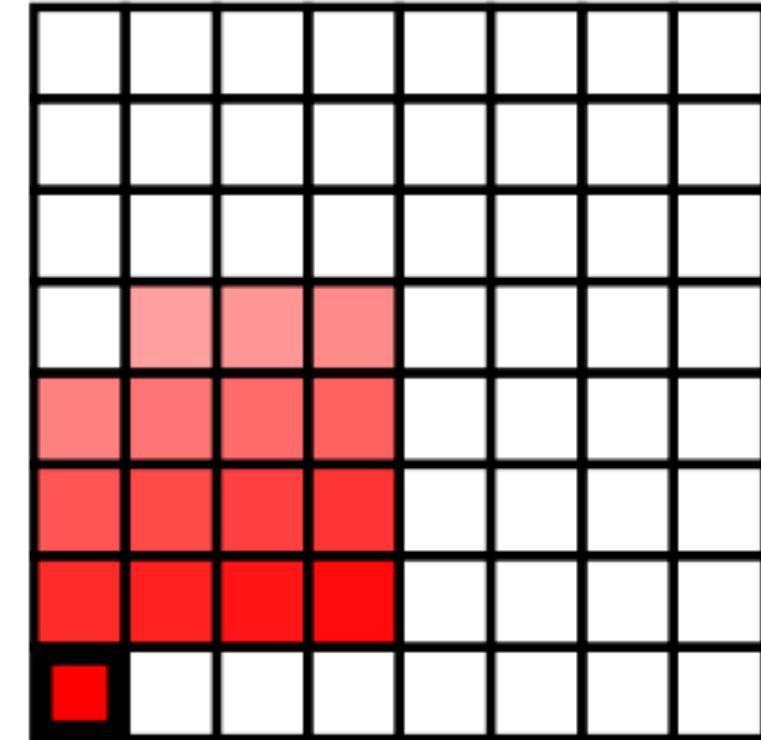
mem:1113

B mem:371 cache hit:323

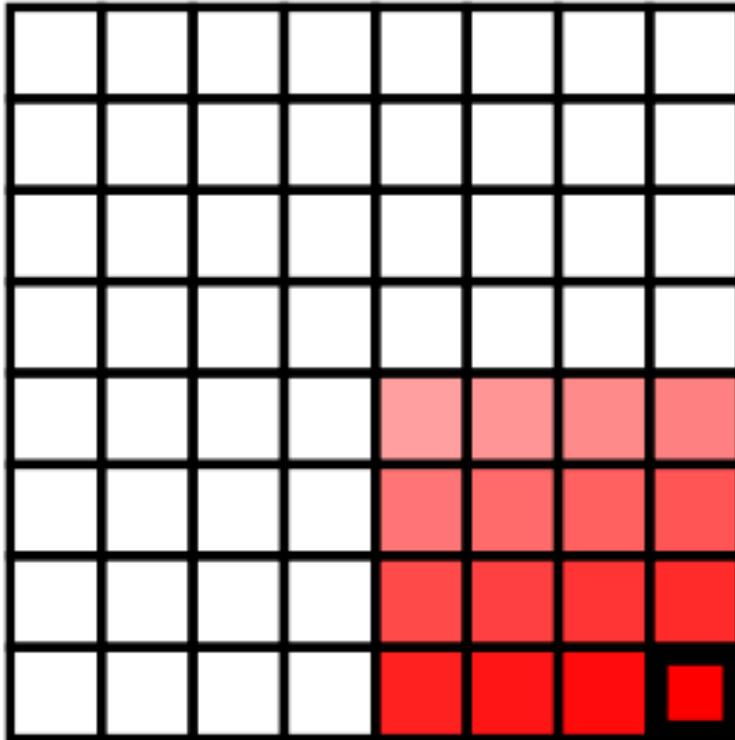


cache hits:877 ≈78%

C mem:371 cache hit:278

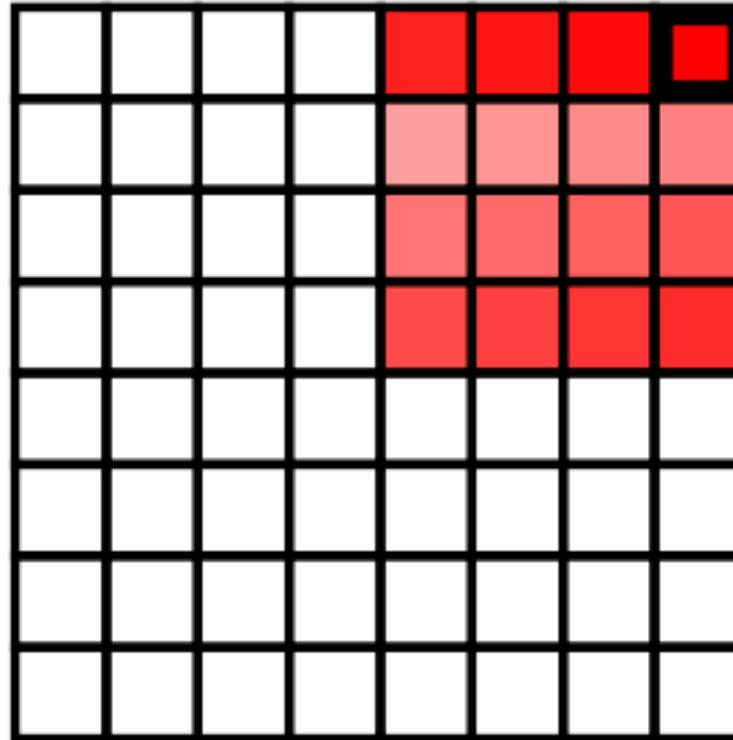


A mem:372 cache hit:276



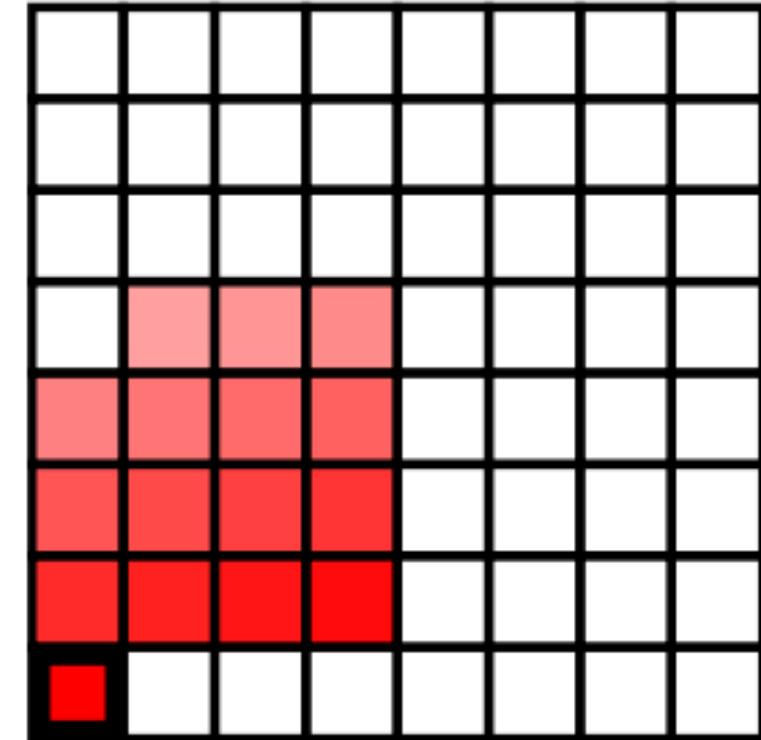
mem:1116

B mem:372 cache hit:324

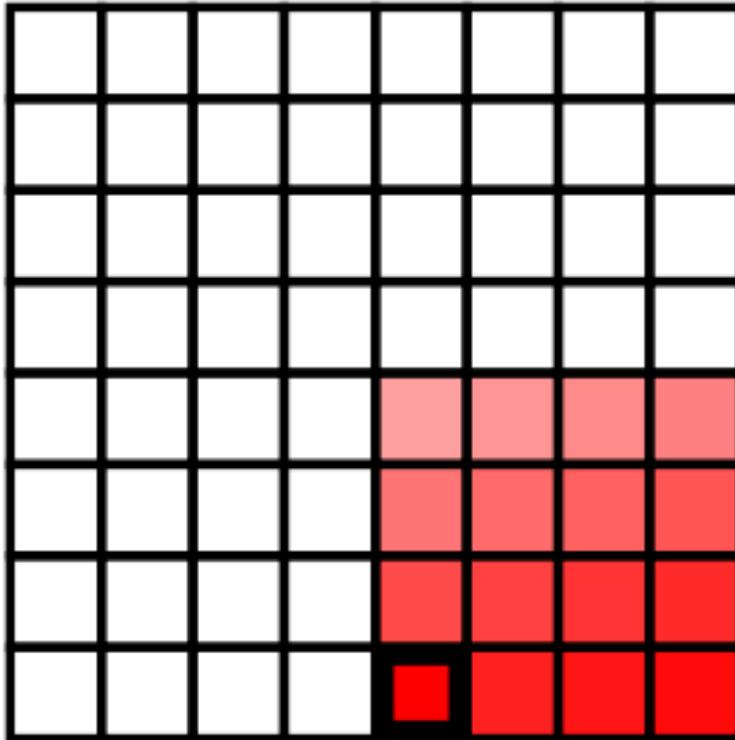


cache hits:879 ≈78%

C mem:372 cache hit:279

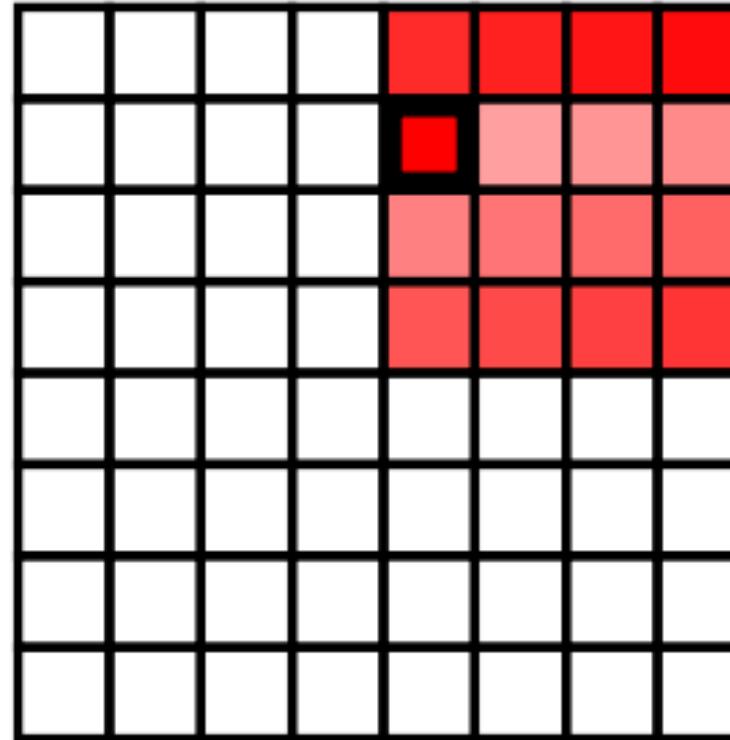


A mem:373 cache hit:277



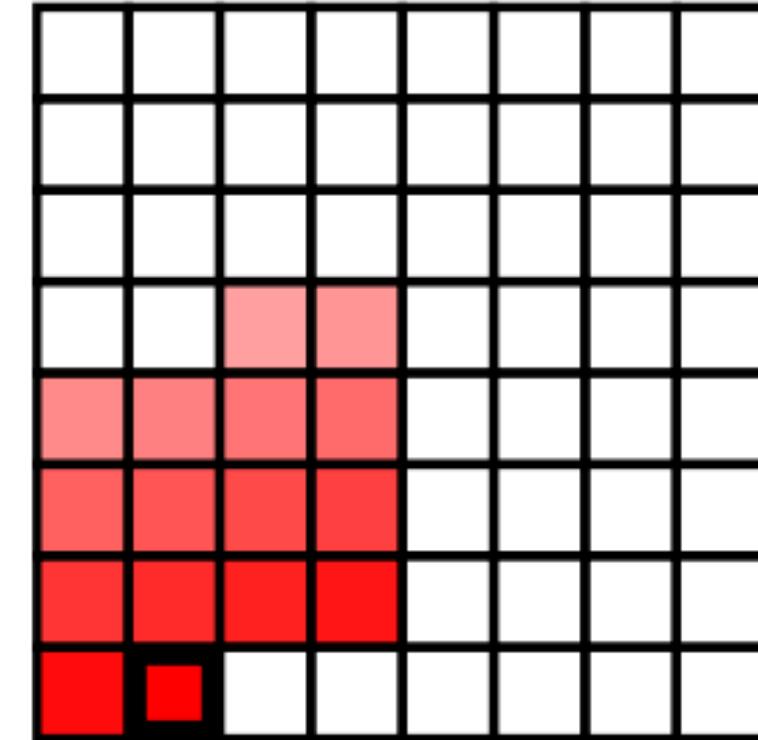
mem:1119

B mem:373 cache hit:325

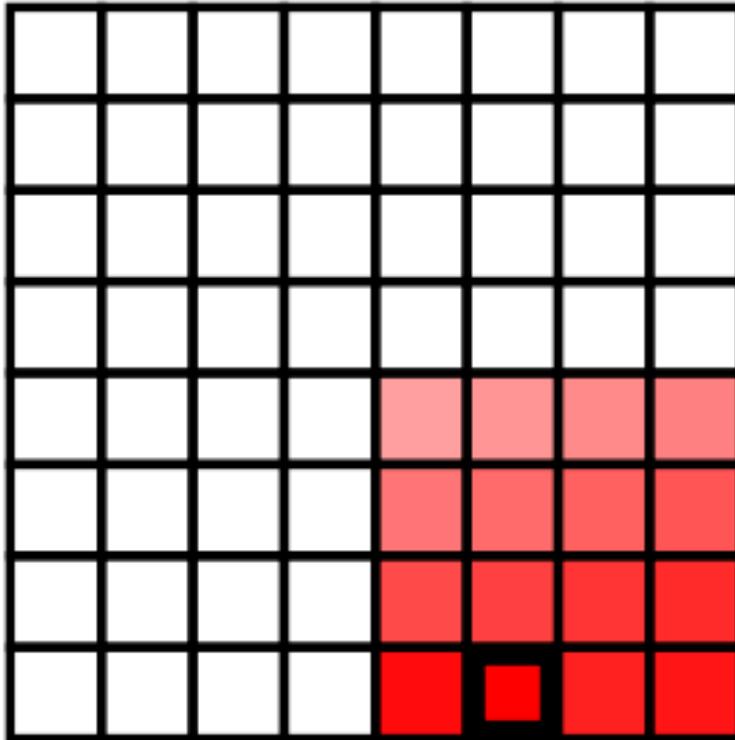


cache hits:881 ≈78%

C mem:373 cache hit:279

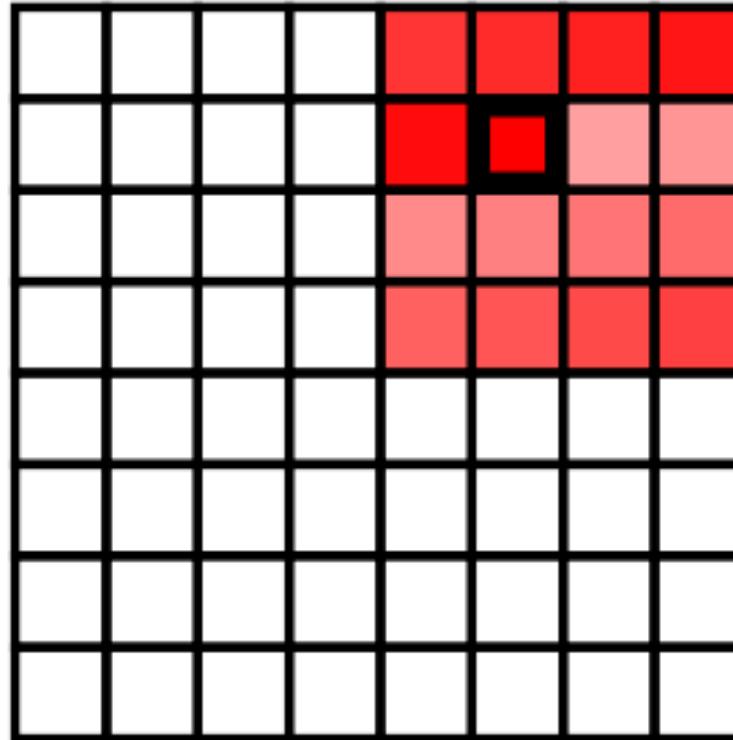


A mem:374 cache hit:278



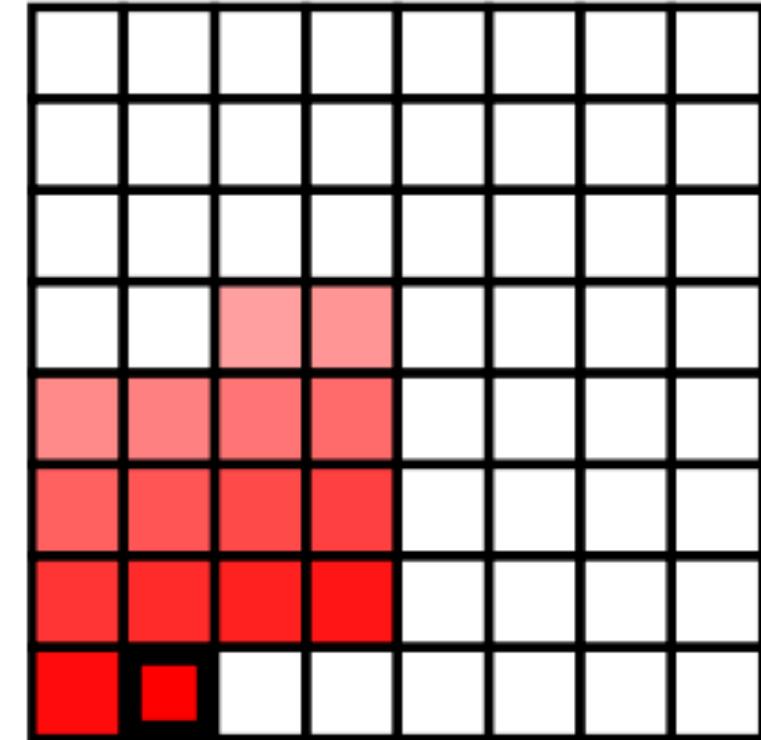
mem:1122

B mem:374 cache hit:326

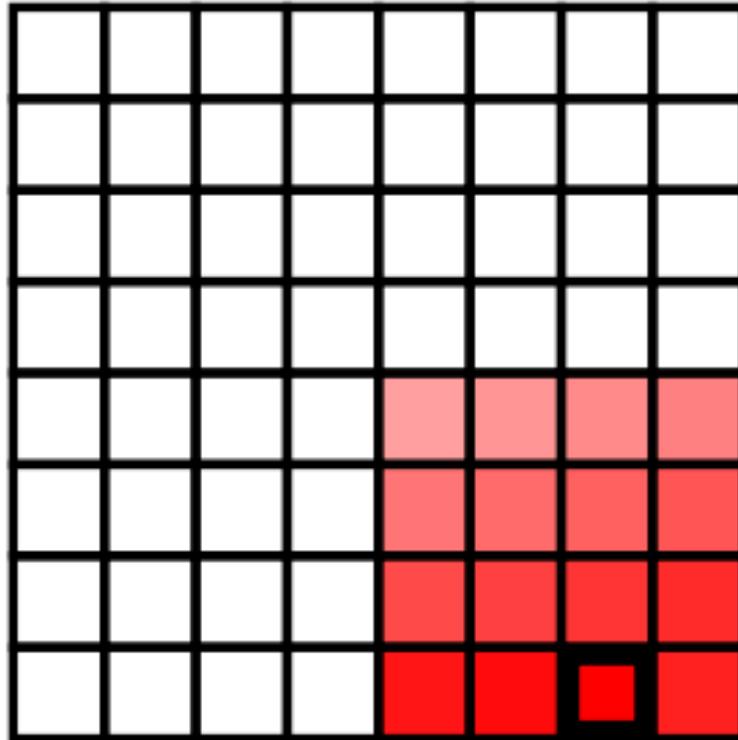


cache hits:884 ≈78%

C mem:374 cache hit:280

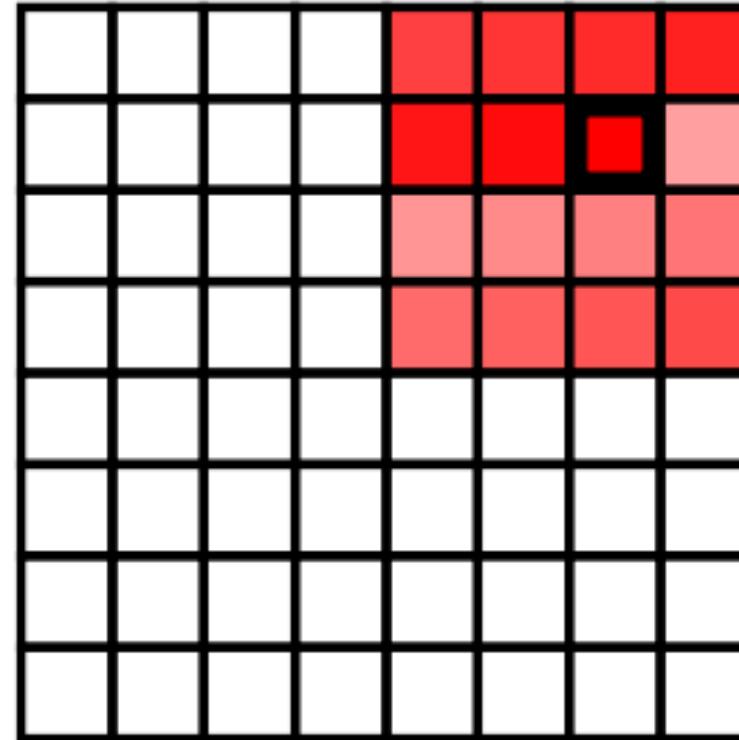


A mem:375 cache hit:279



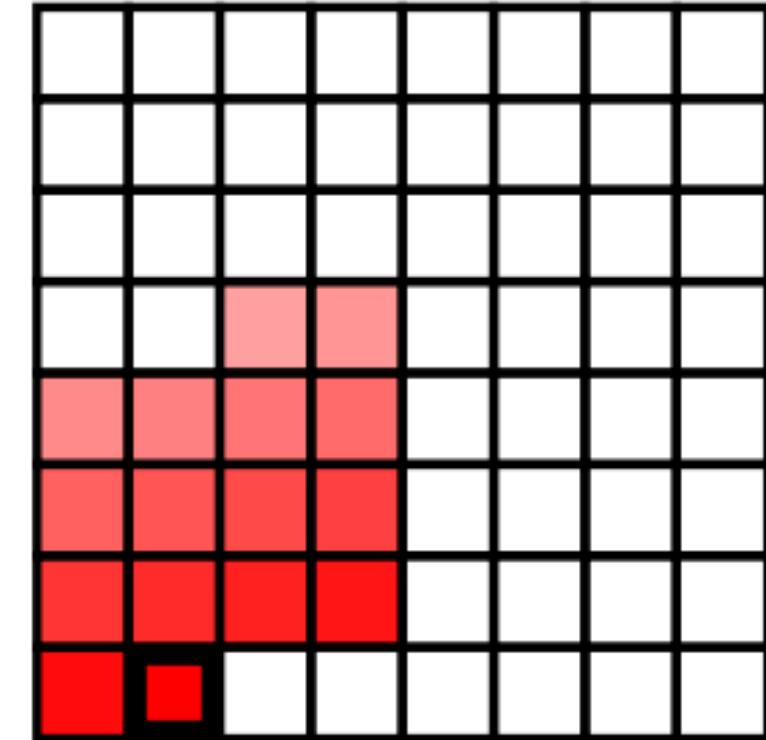
mem:1125

B mem:375 cache hit:327

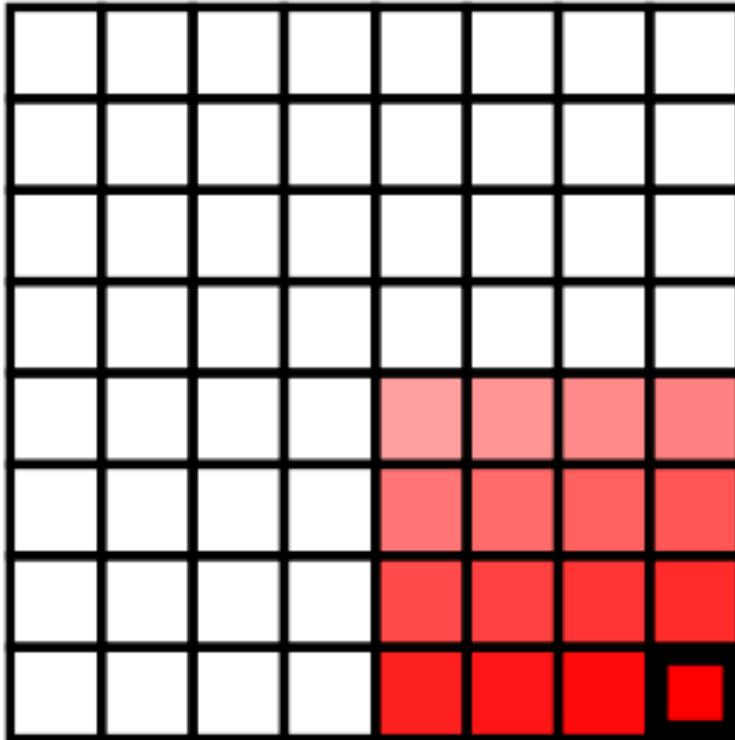


cache hits:887 $\approx 78\%$

C mem:375 cache hit:281

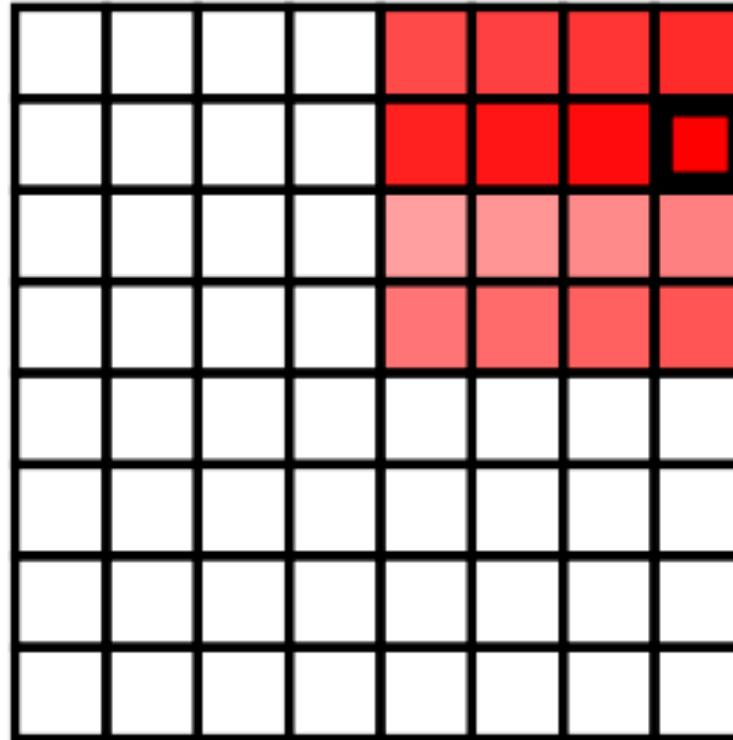


A mem:376 cache hit:280



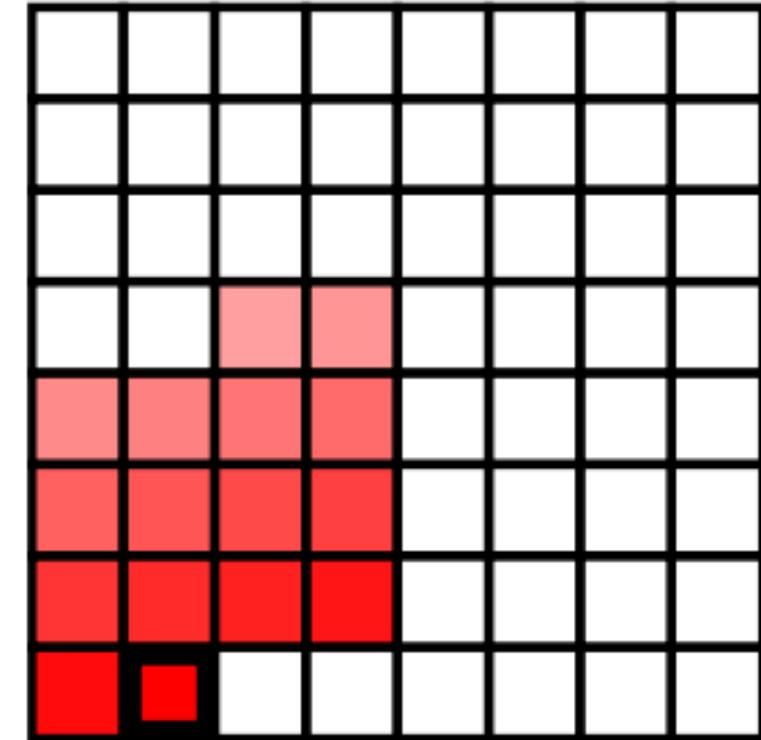
mem:1128

B mem:376 cache hit:328

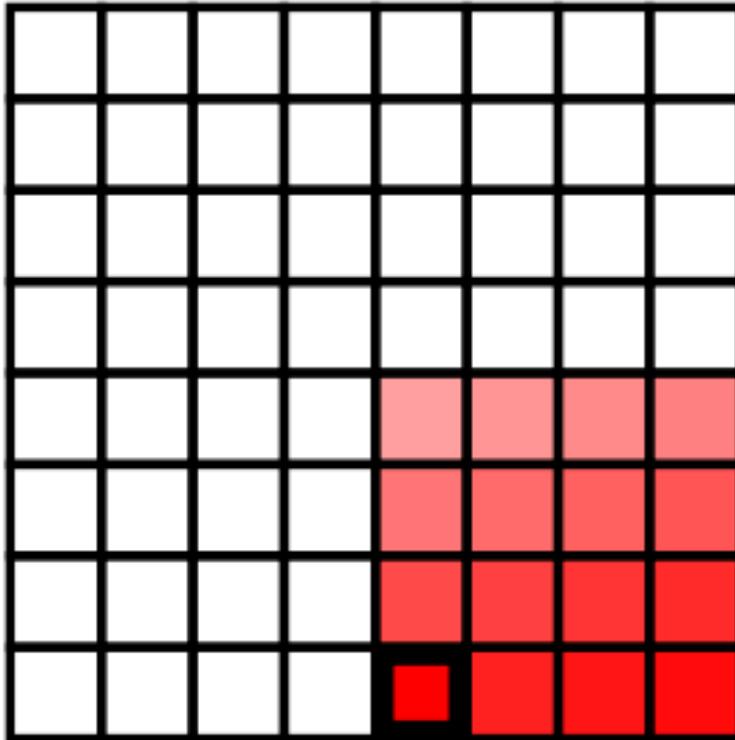


cache hits:890 $\approx 78\%$

C mem:376 cache hit:282

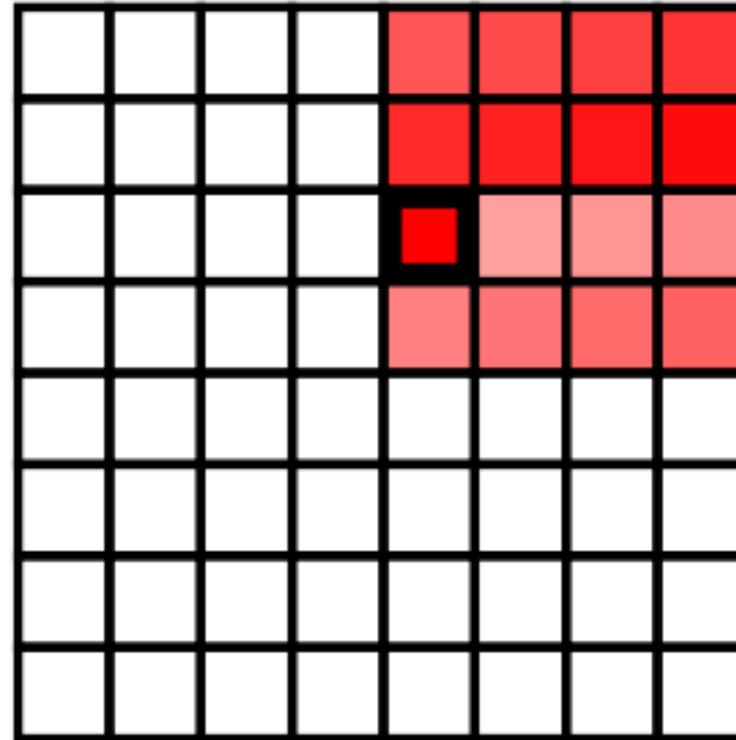


A mem:377 cache hit:281



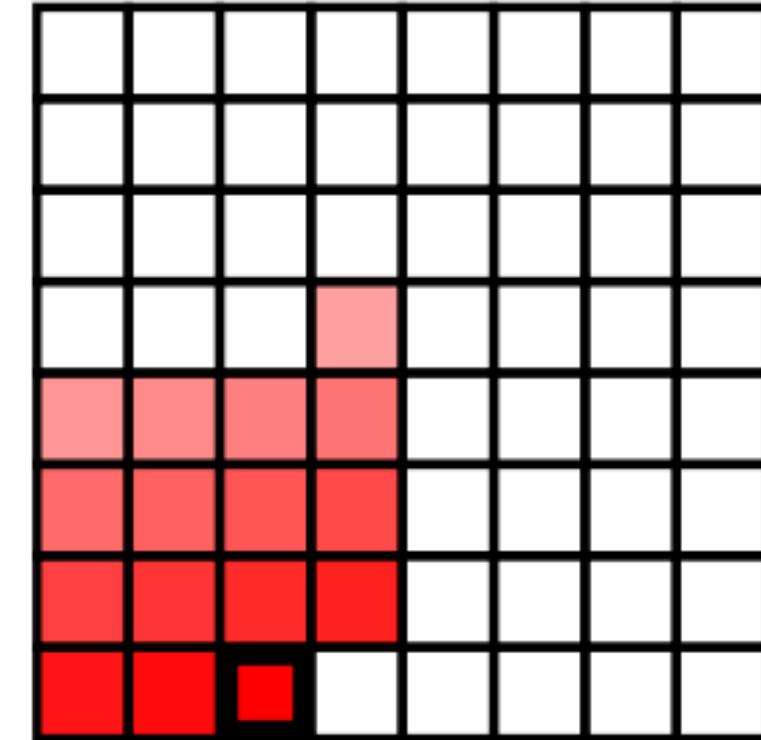
mem:1131

B mem:377 cache hit:329

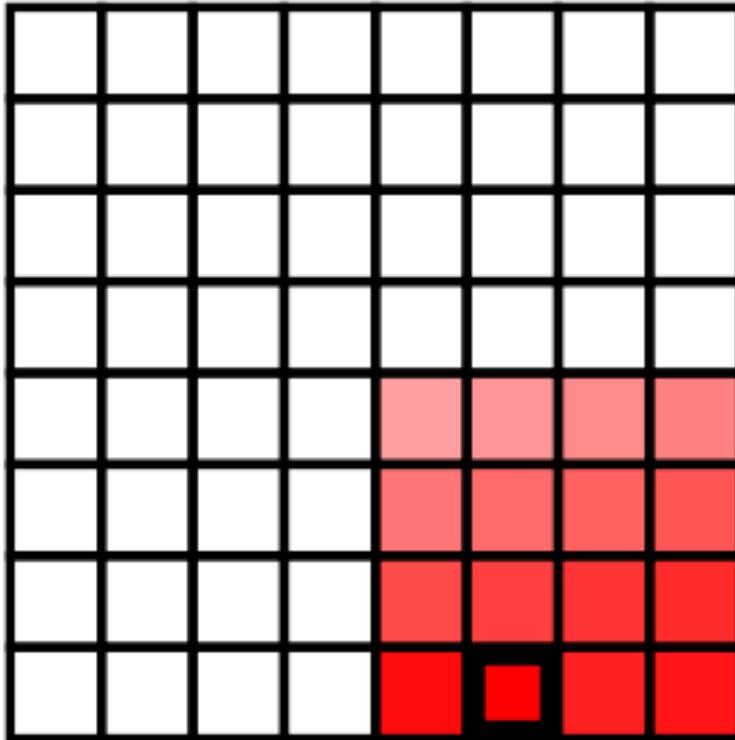


cache hits:892 ≈78%

C mem:377 cache hit:282

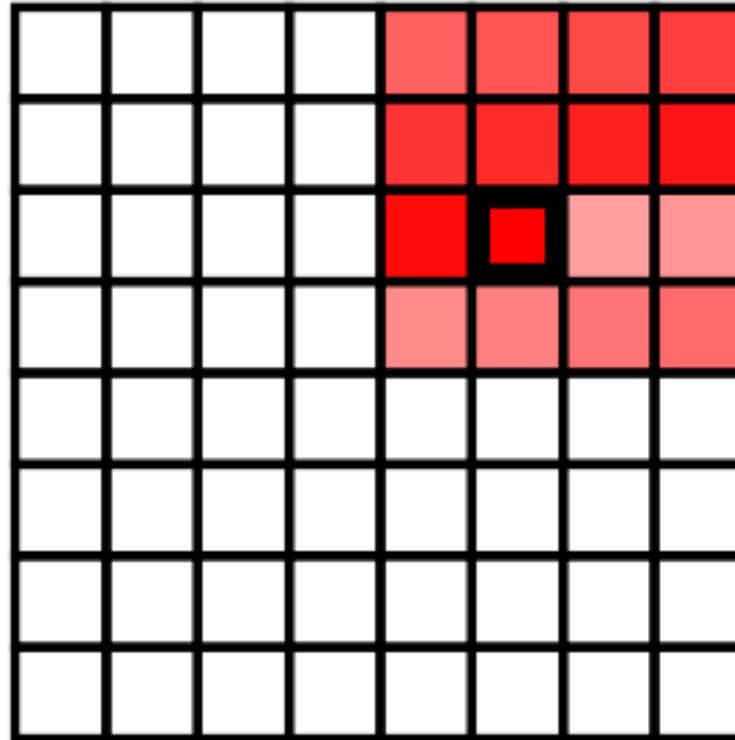


A mem:378 cache hit:282



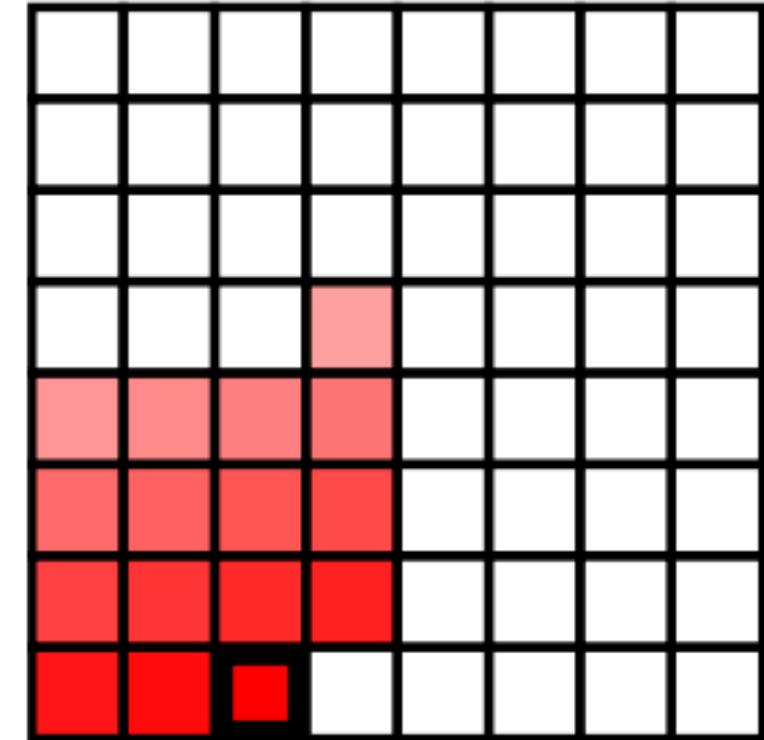
mem:1134

B mem:378 cache hit:330

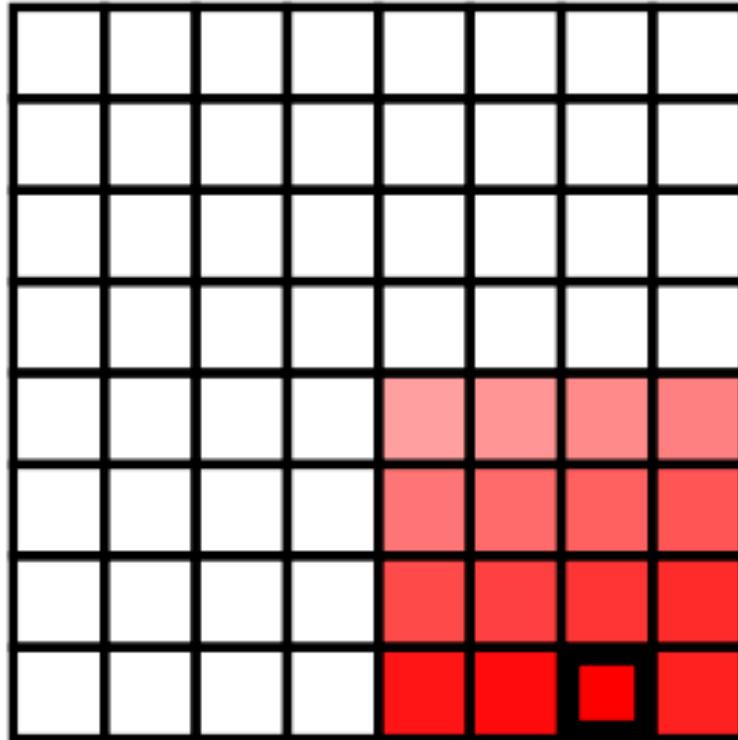


cache hits:895 ≈78%

C mem:378 cache hit:283

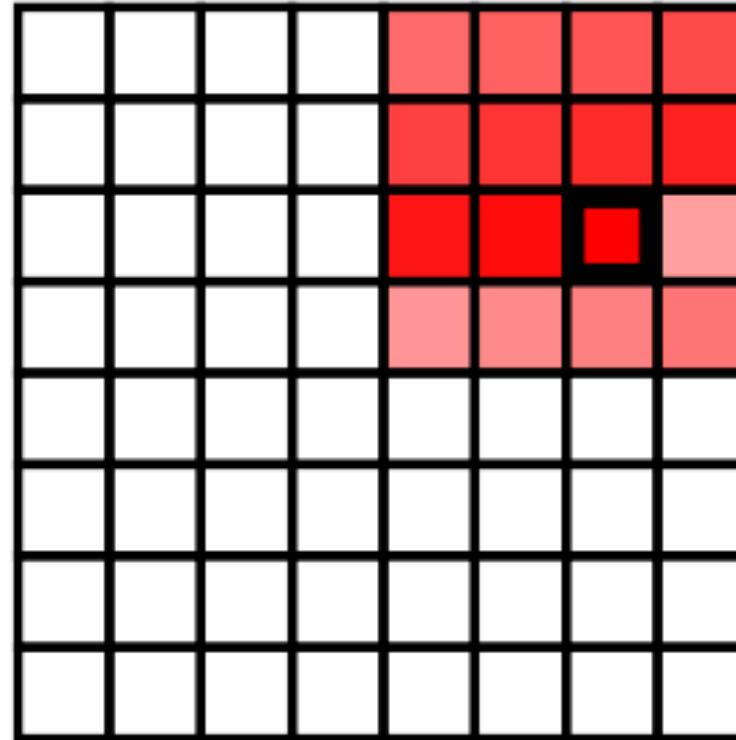


A mem:379 cache hit:283



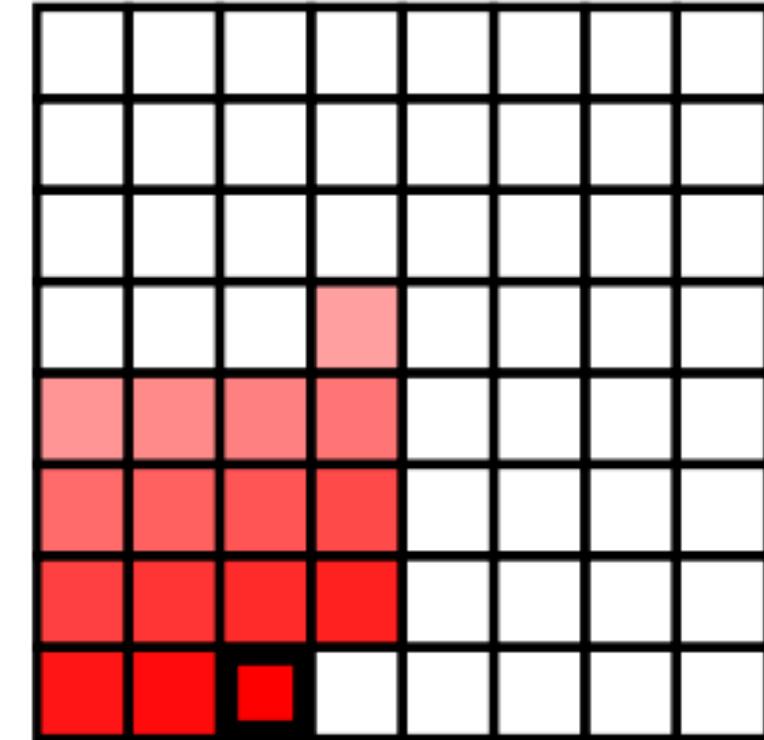
mem:1137

B mem:379 cache hit:331

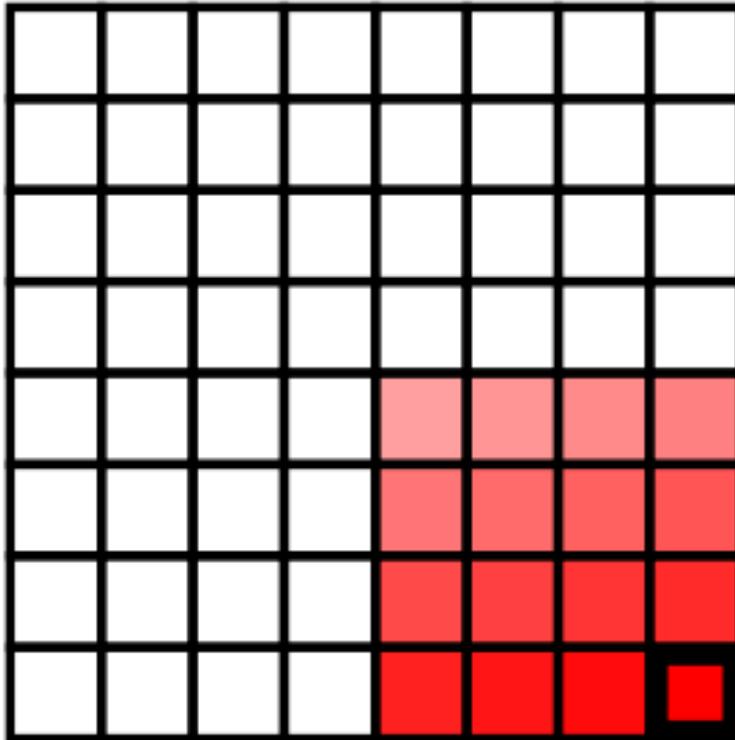


cache hits:898 ≈78%

C mem:379 cache hit:284

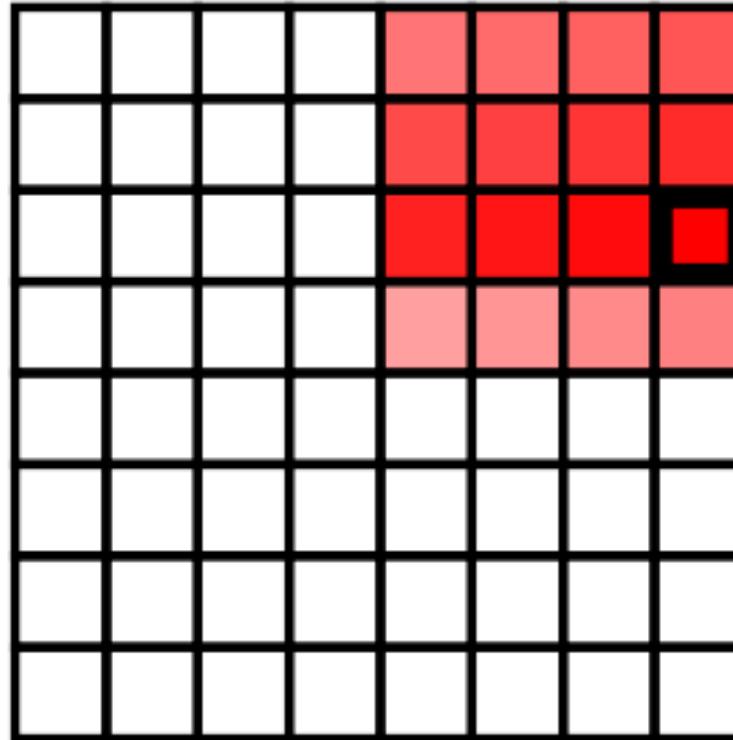


A mem:380 cache hit:284



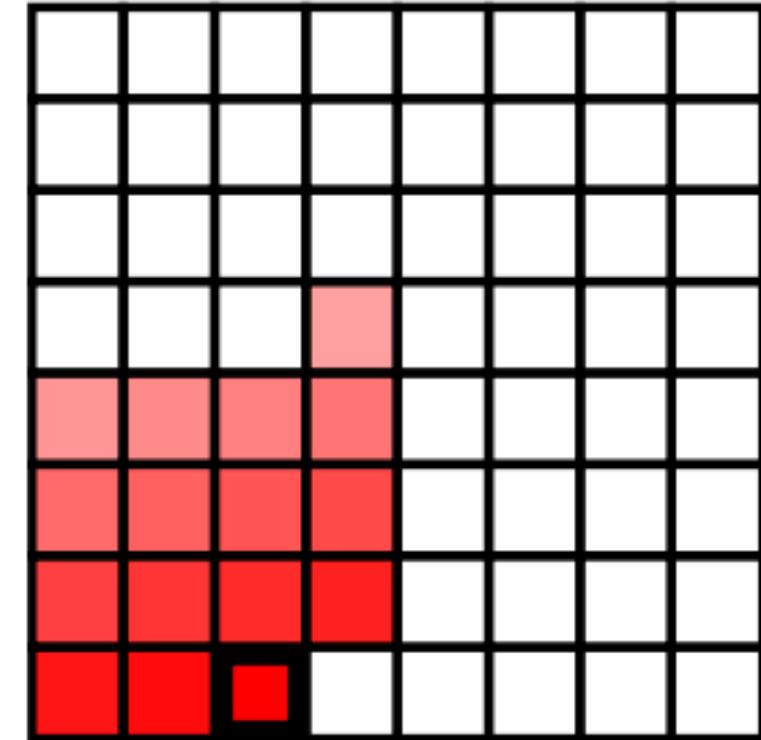
mem:1140

B mem:380 cache hit:332

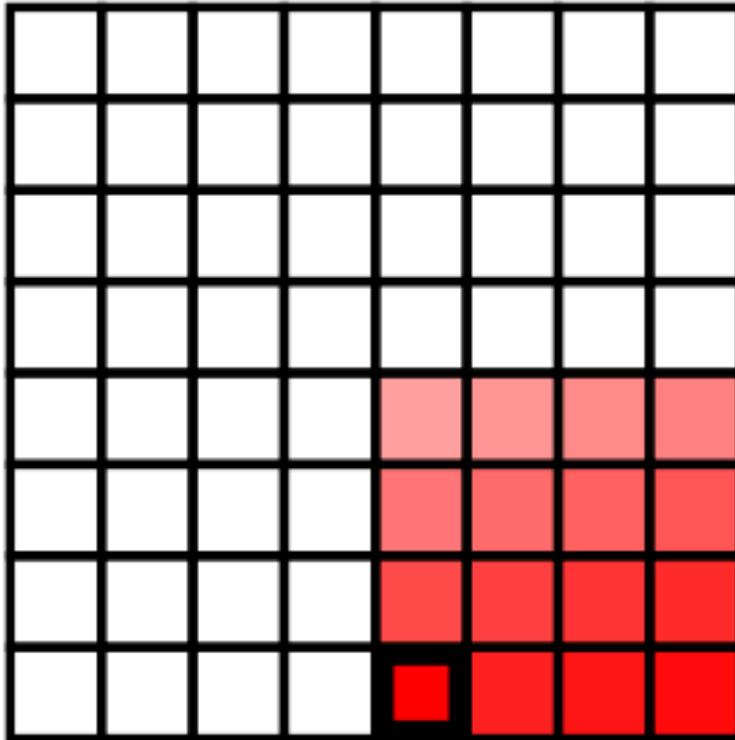


cache hits:901 ≈79%

C mem:380 cache hit:285

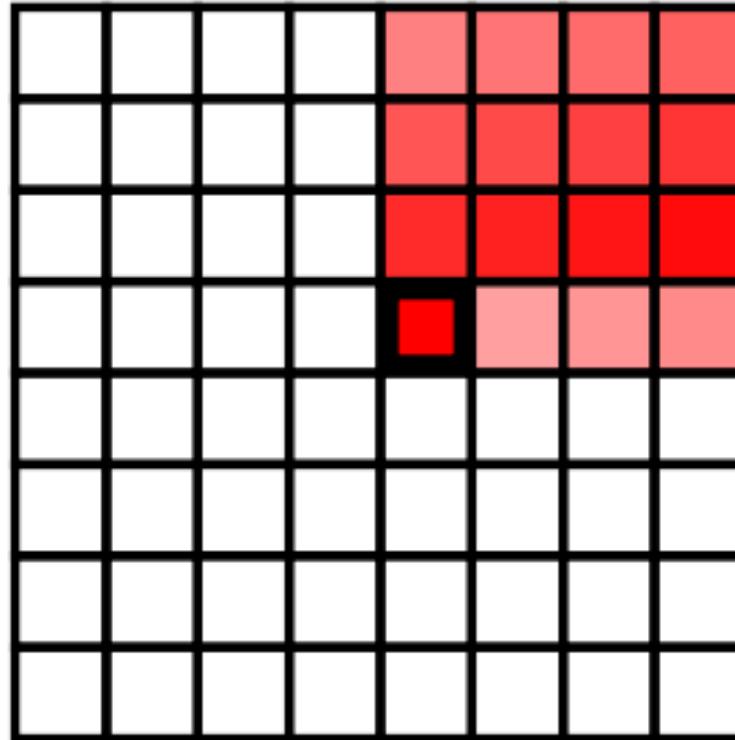


A mem:381 cache hit:285



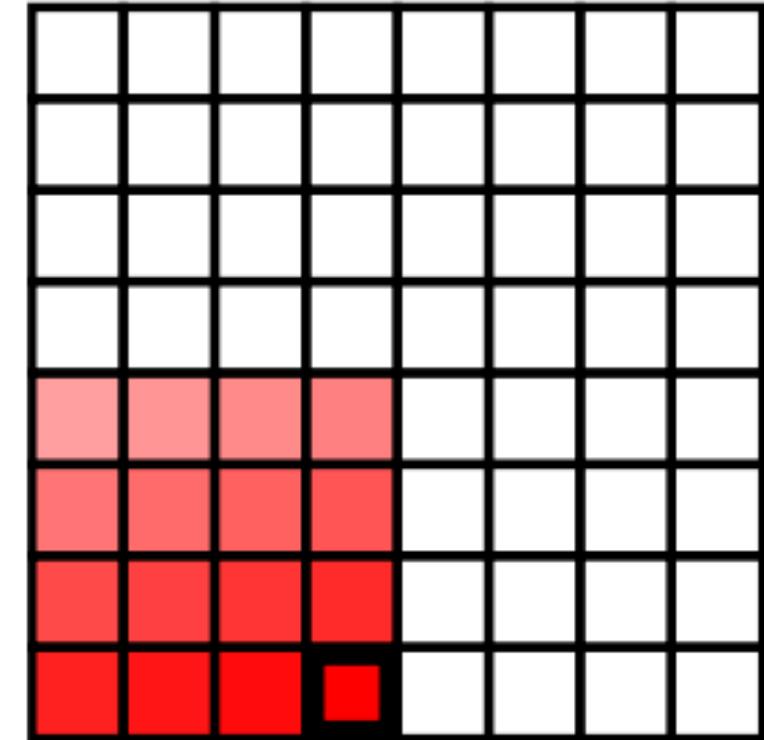
mem:1143

B mem:381 cache hit:333

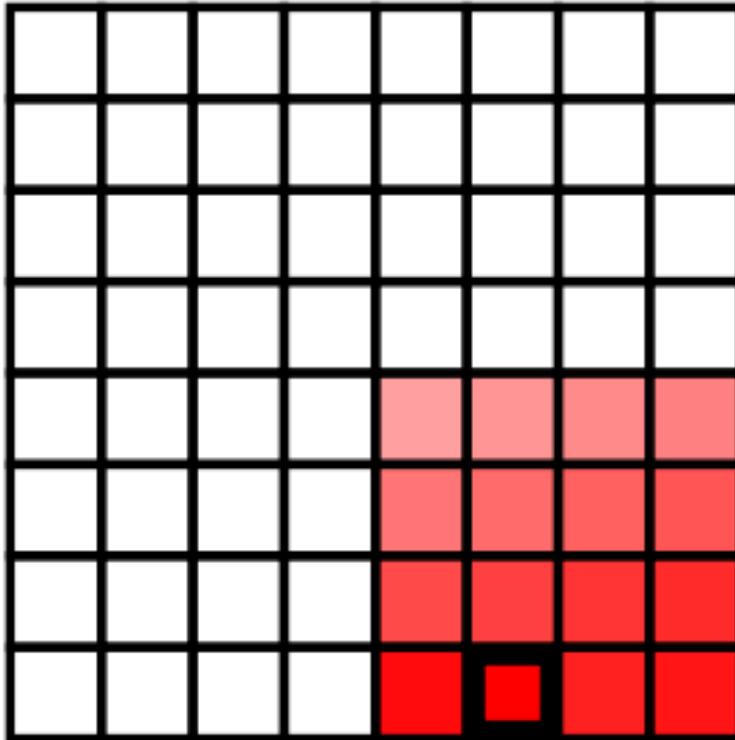


cache hits:903 ≈79%

C mem:381 cache hit:285

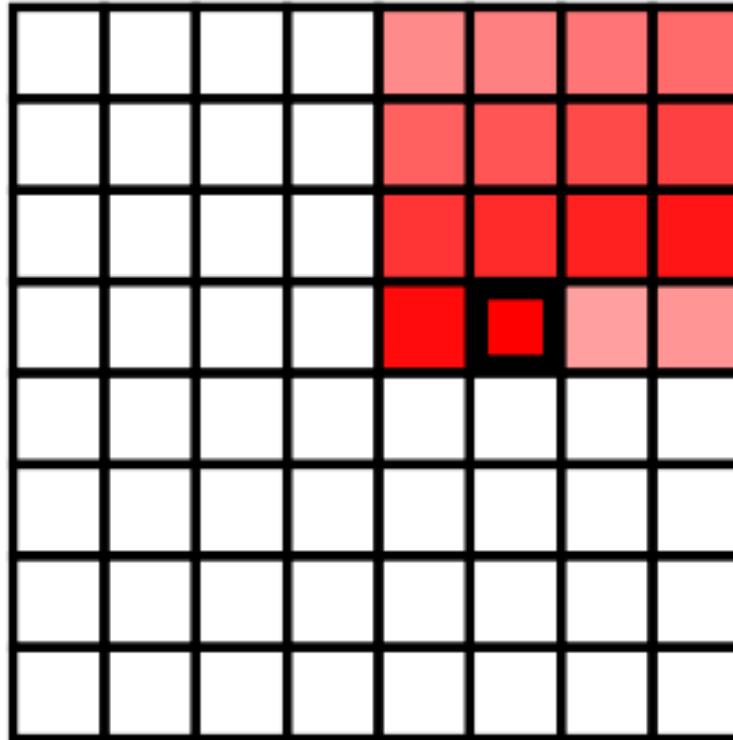


A mem:382 cache hit:286



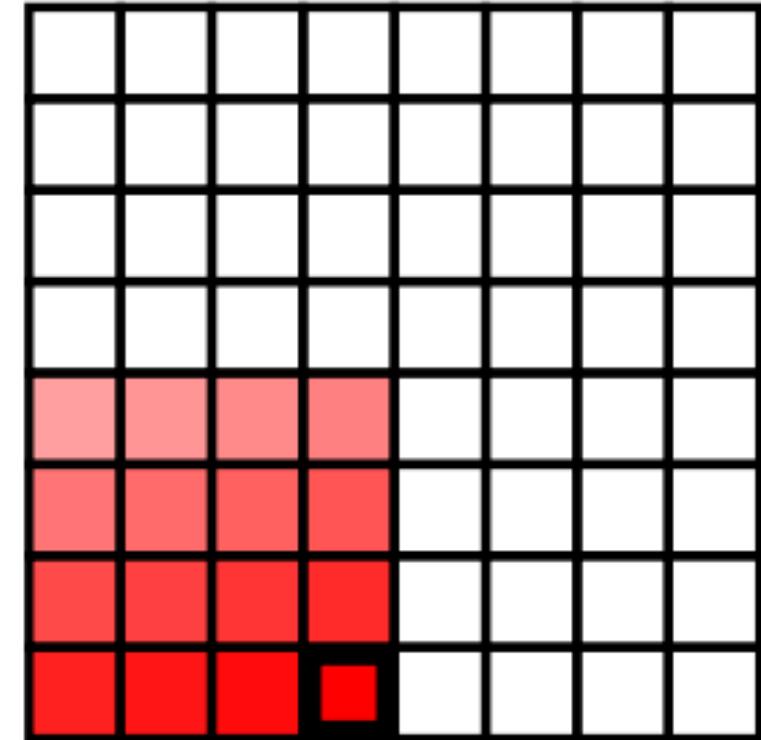
mem:1146

B mem:382 cache hit:334

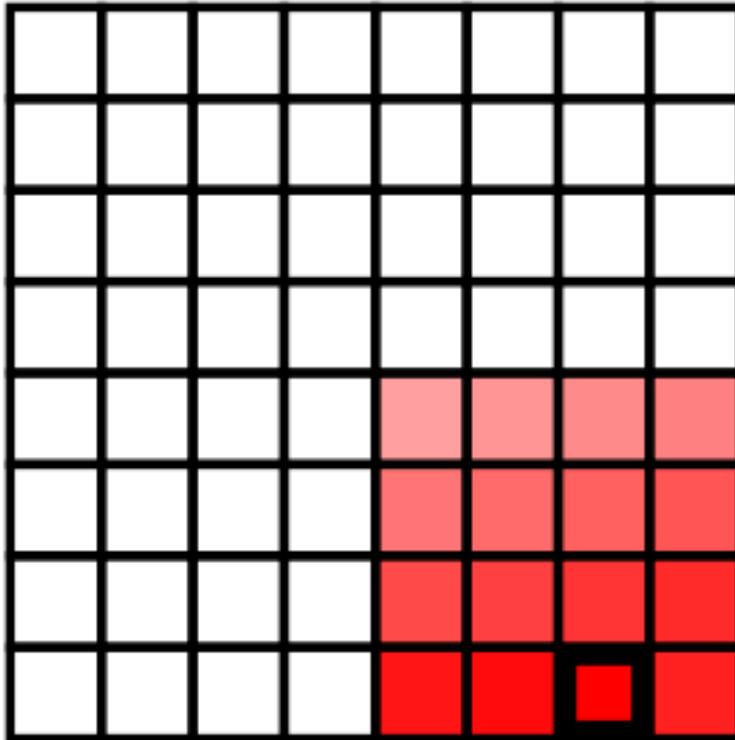


cache hits:906 ≈79%

C mem:382 cache hit:286

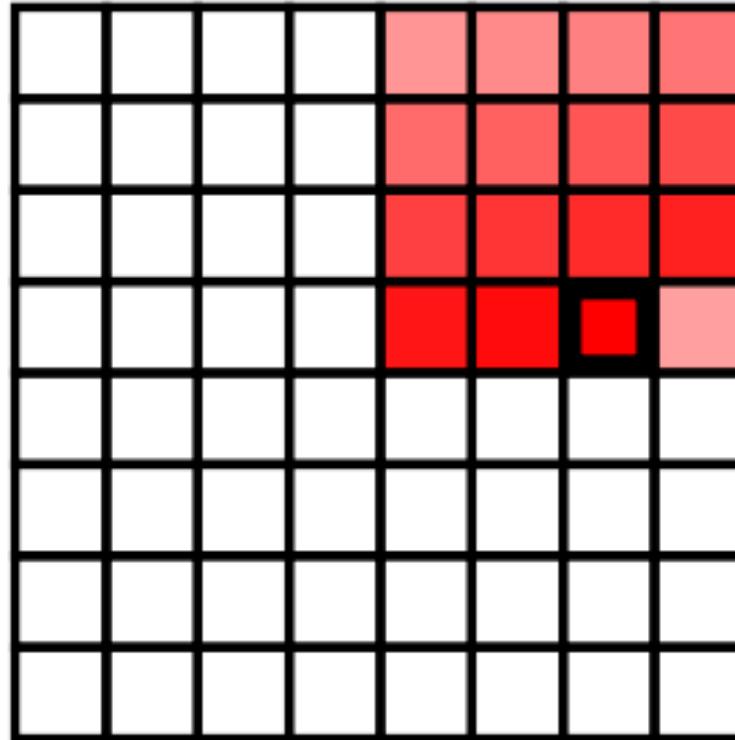


A mem:383 cache hit:287



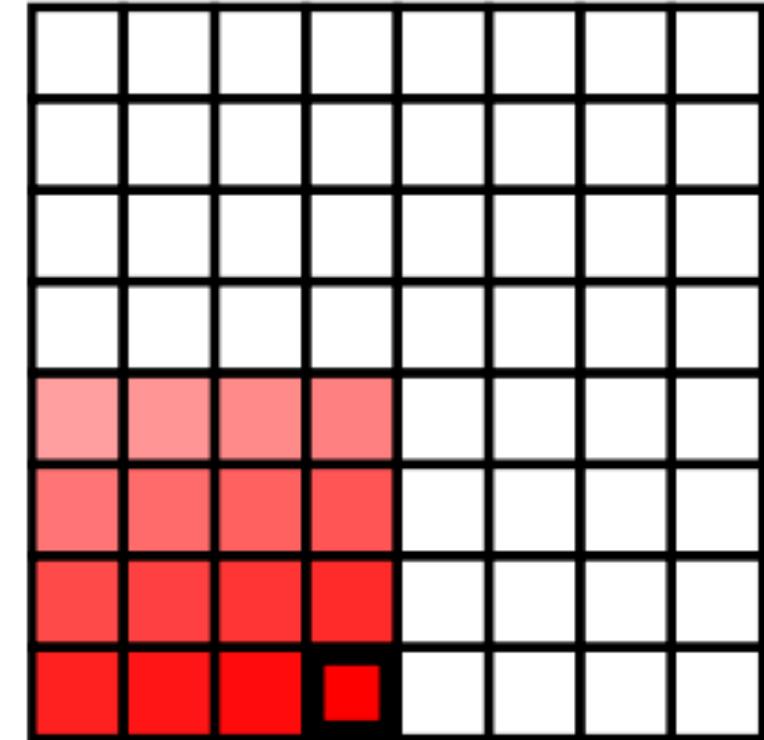
mem:1149

B mem:383 cache hit:335

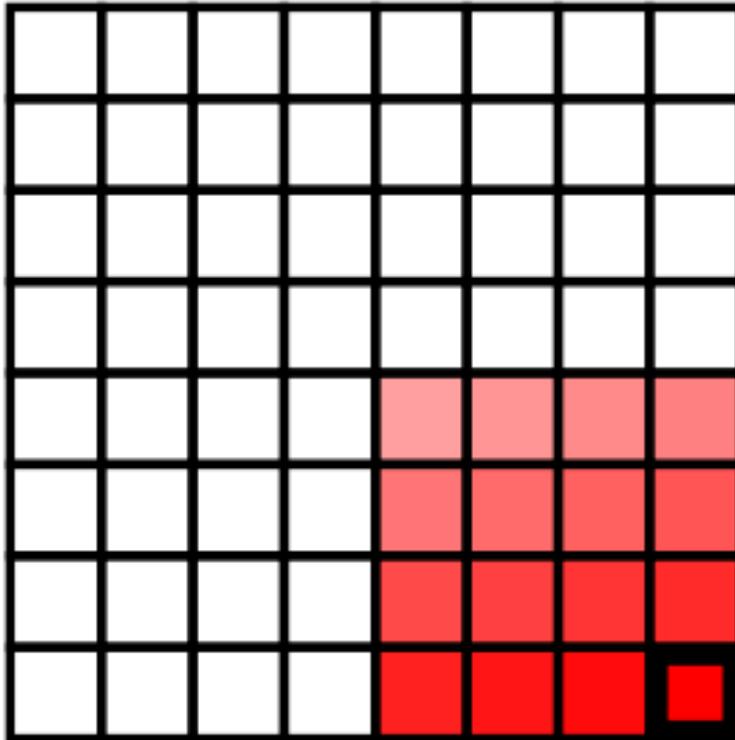


cache hits:909 ≈79%

C mem:383 cache hit:287

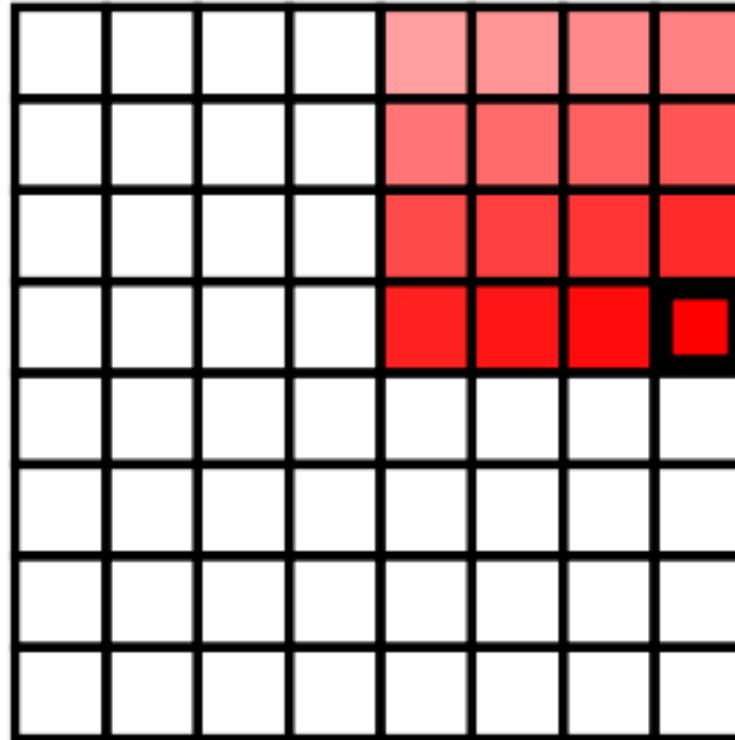


A mem:384 cache hit:288



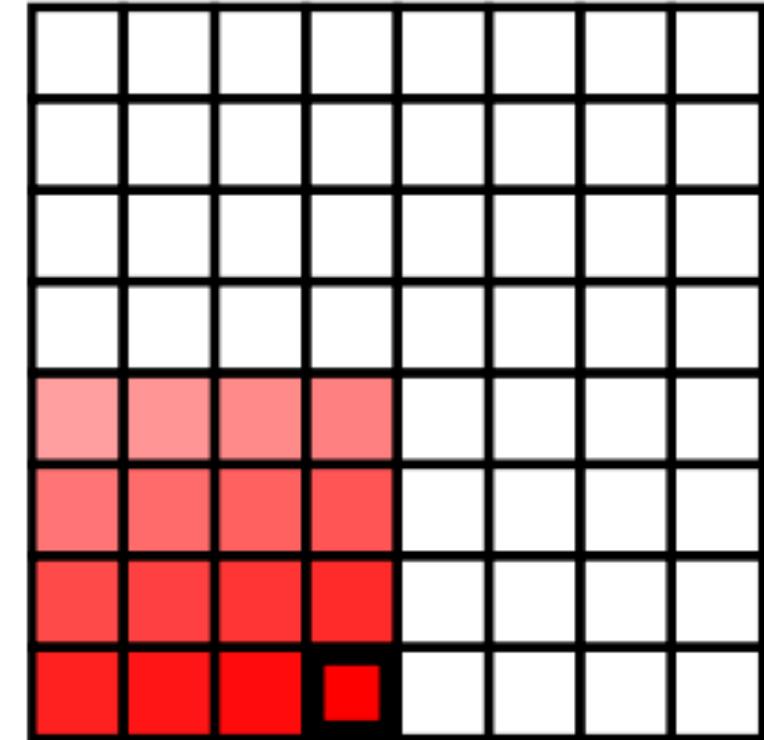
mem:1152

B mem:384 cache hit:336

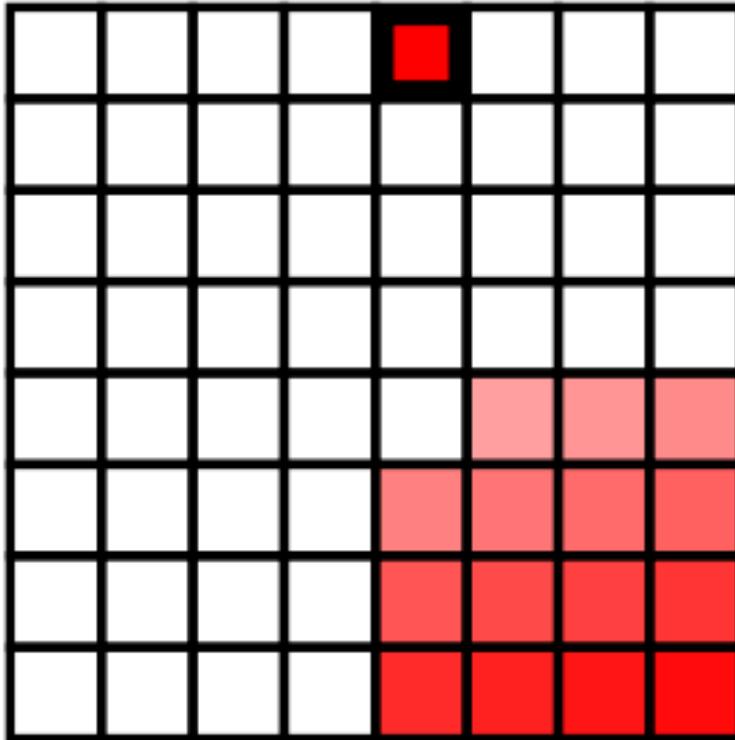


cache hits:912 $\approx 79\%$

C mem:384 cache hit:288

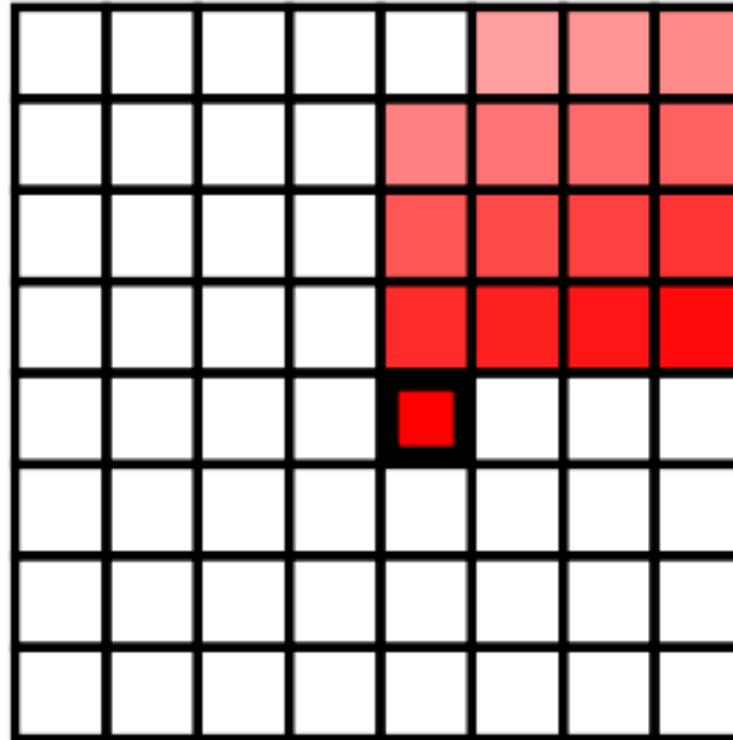


A mem:385 cache hit:288



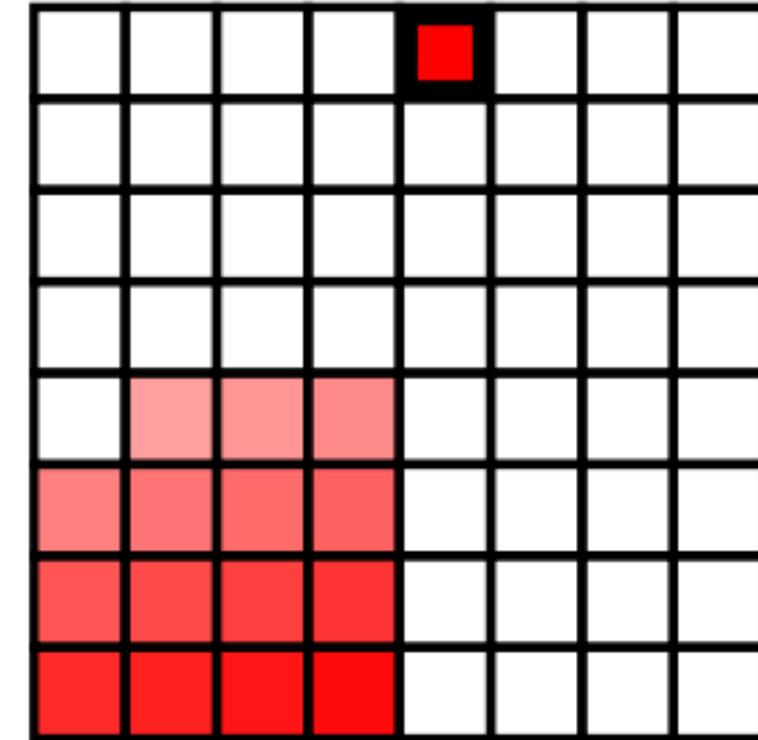
mem:1155

B mem:385 cache hit:336

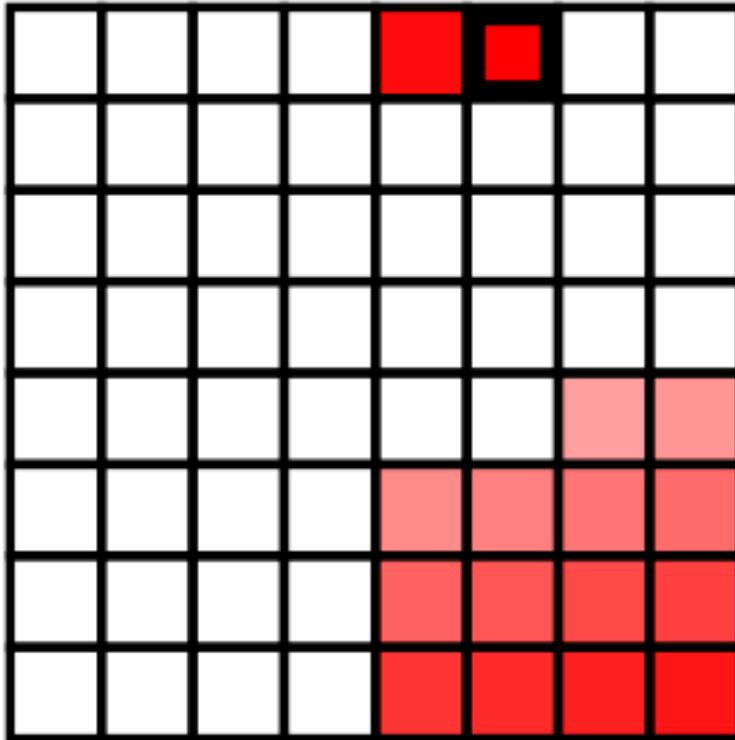


cache hits:912 ≈78%

C mem:385 cache hit:288

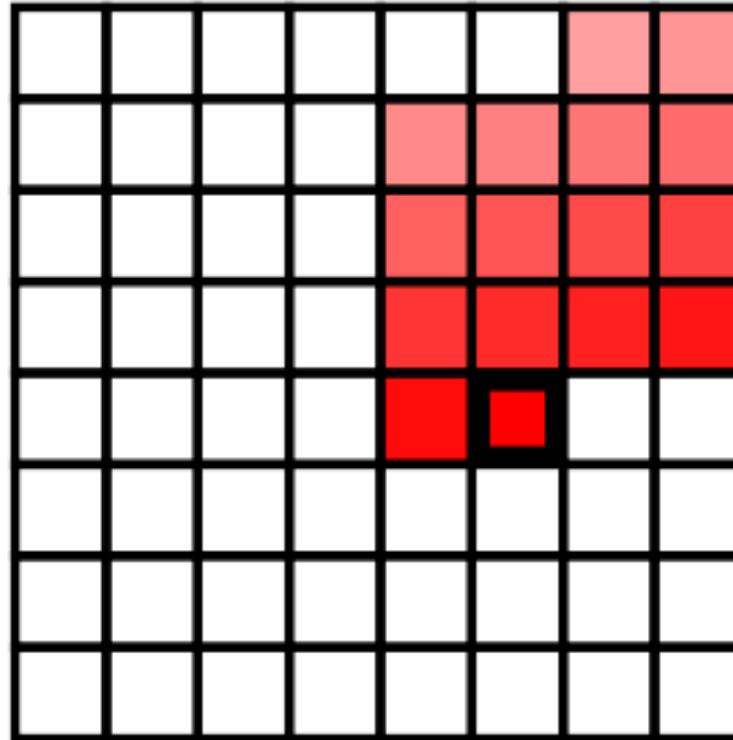


A mem:386 cache hit:288



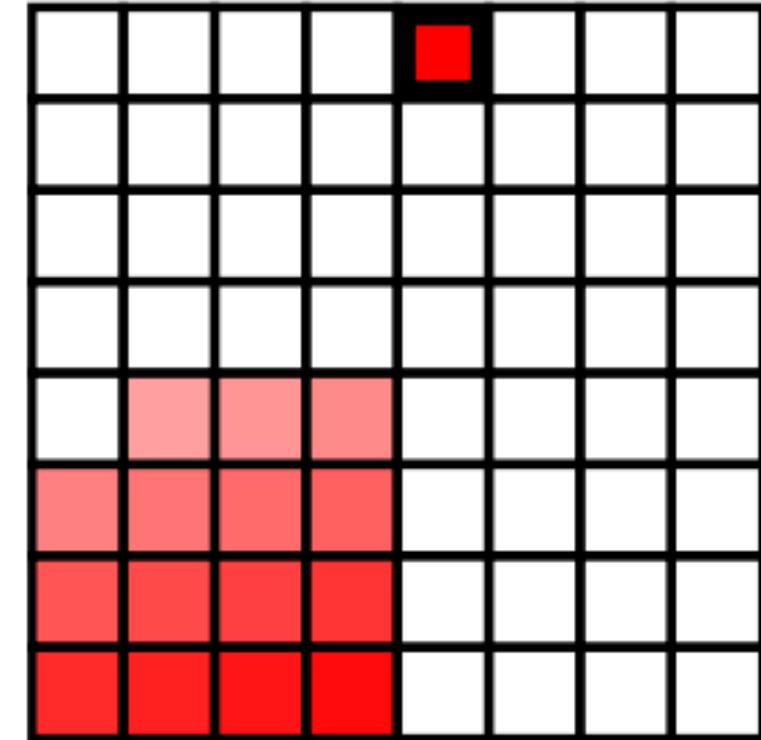
mem:1158

B mem:386 cache hit:336

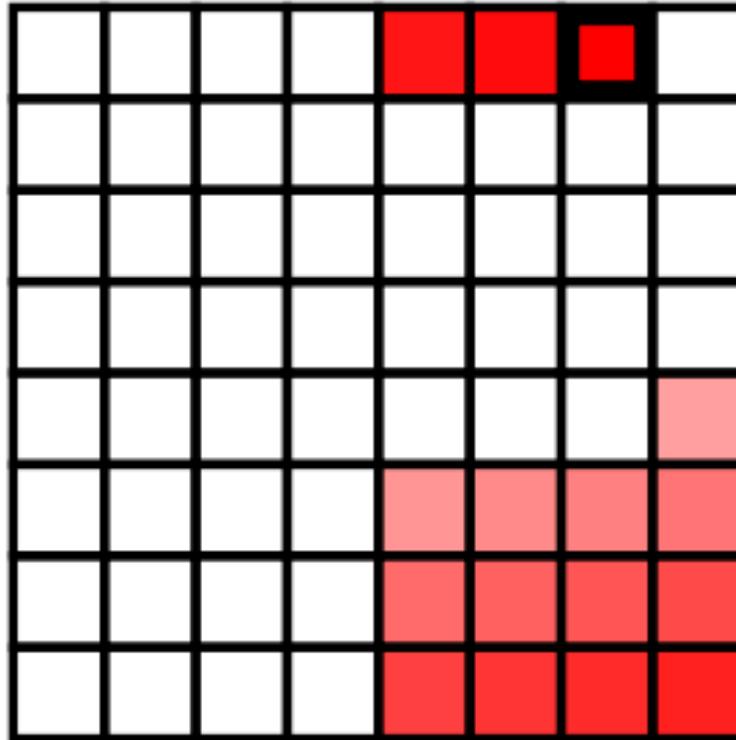


cache hits:913 ≈78%

C mem:386 cache hit:289

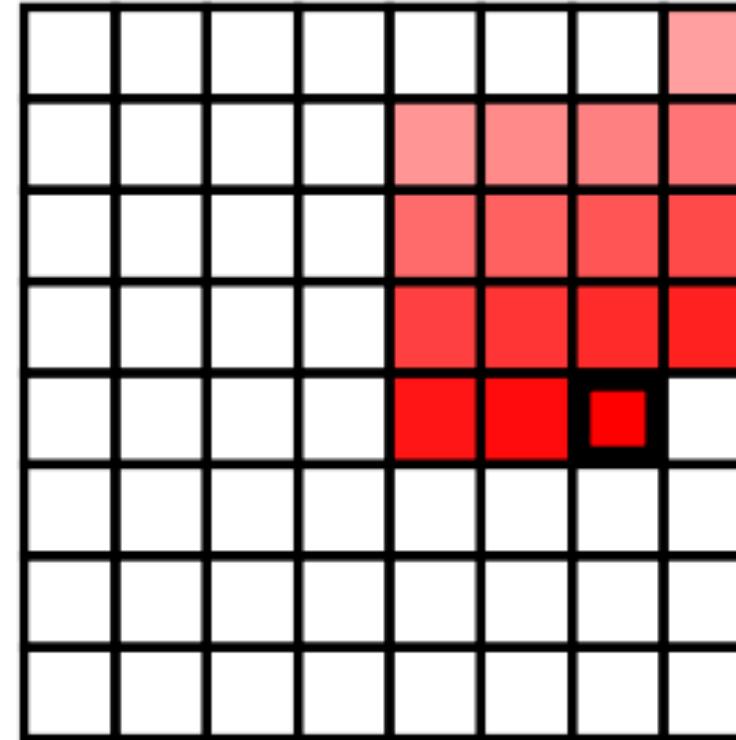


A mem:387 cache hit:288



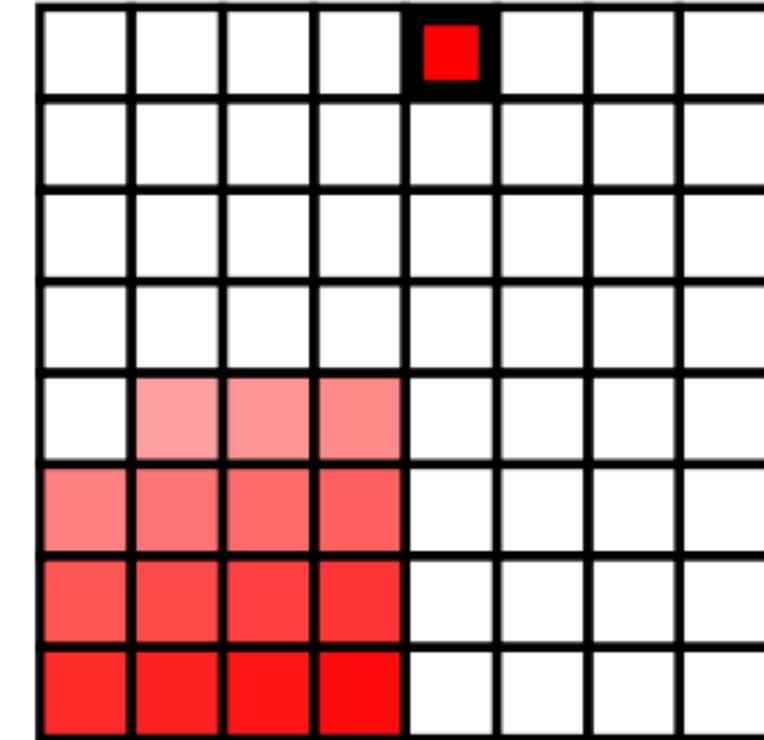
mem:1161

B mem:387 cache hit:336

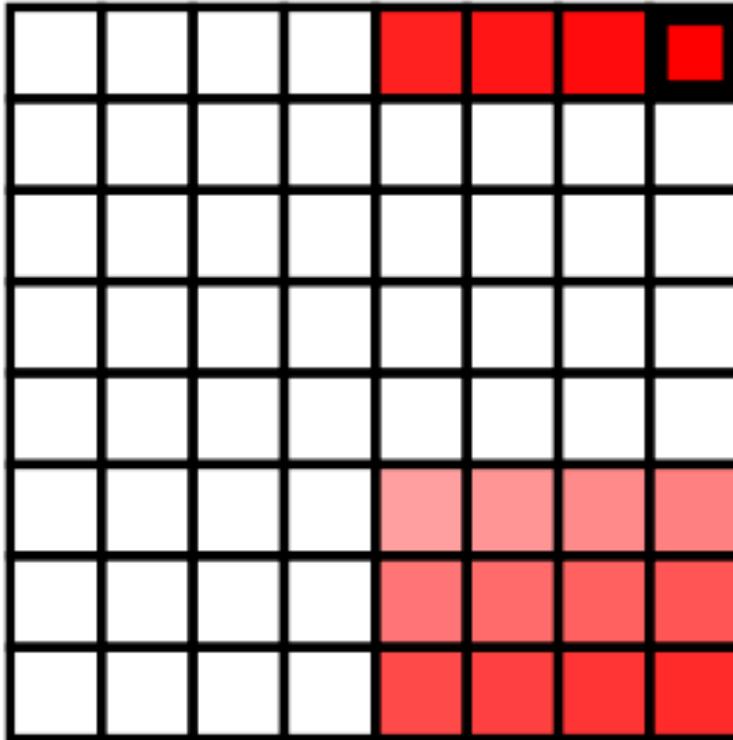


cache hits:914 ≈78%

C mem:387 cache hit:290

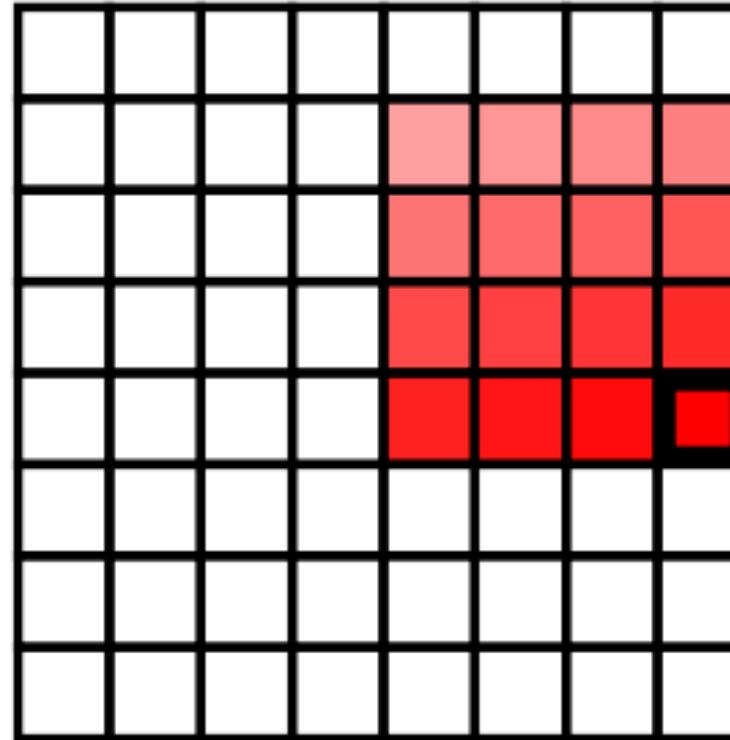


A mem:388 cache hit:288



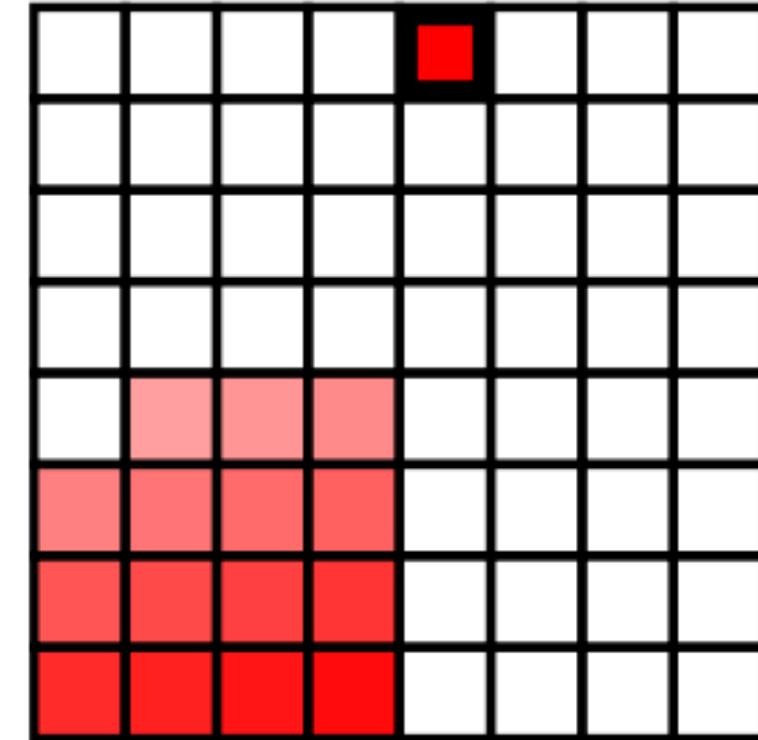
mem:1164

B mem:388 cache hit:336

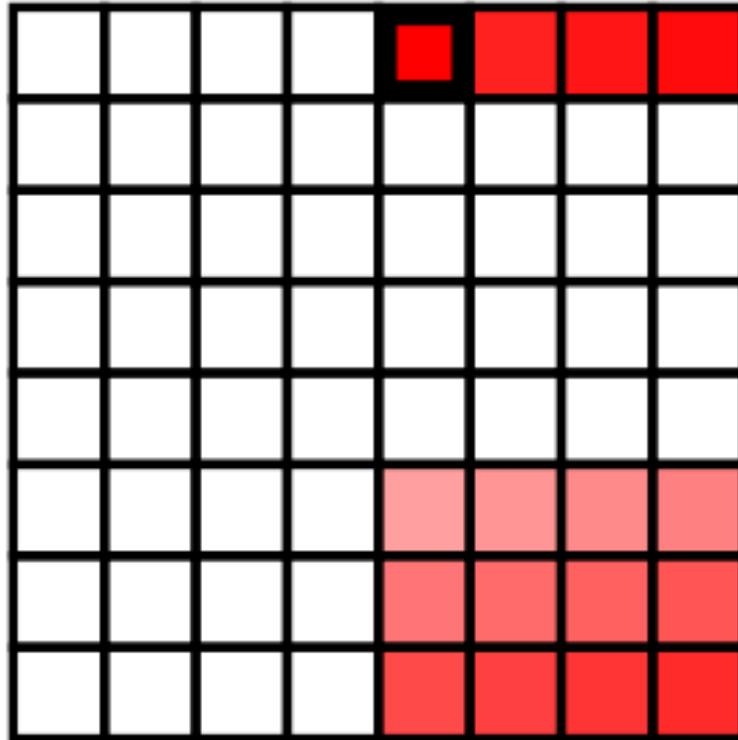


cache hits:915 ≈78%

C mem:388 cache hit:291

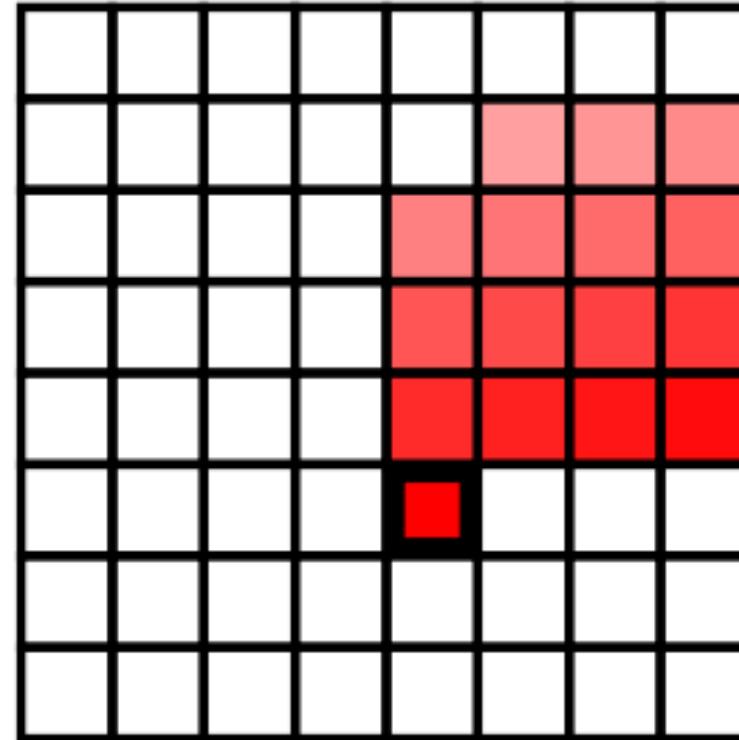


A mem:389 cache hit:289



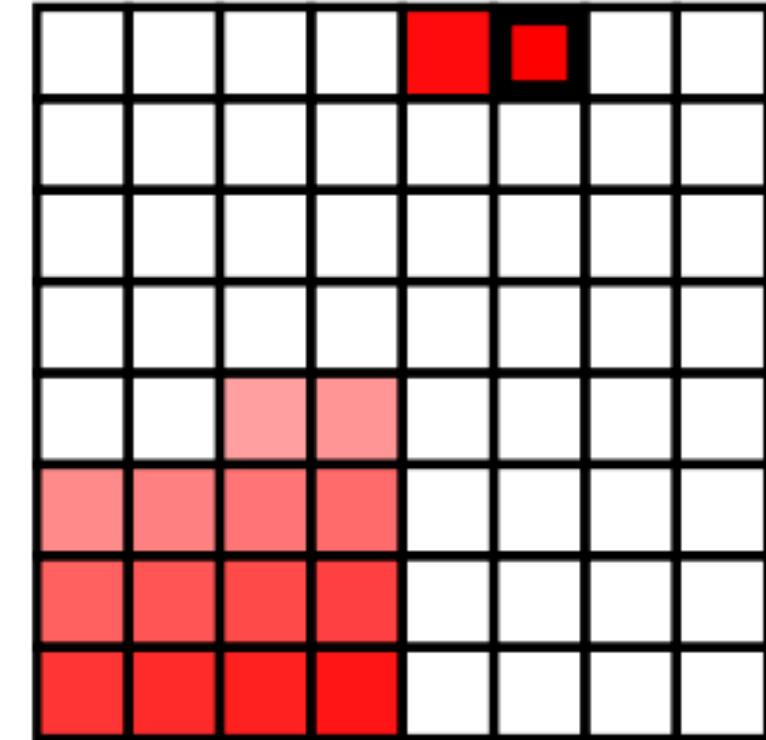
mem:1167

B mem:389 cache hit:336

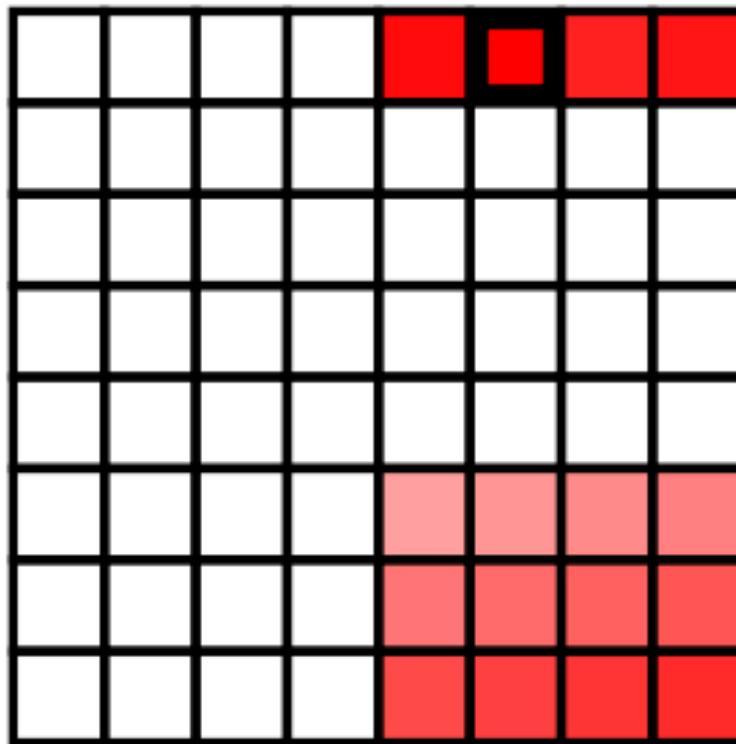


cache hits:916 ≈78%

C mem:389 cache hit:291

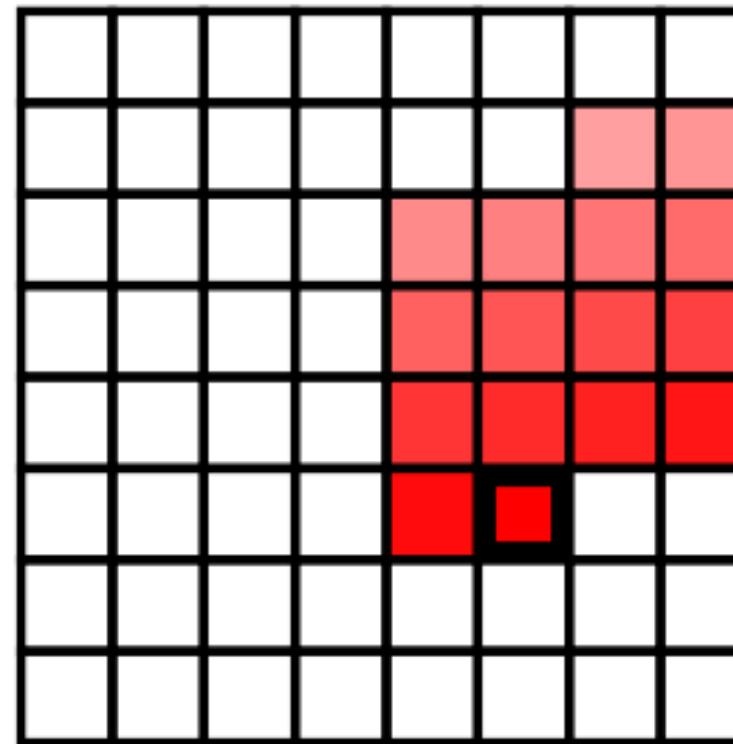


A mem:390 cache hit:290



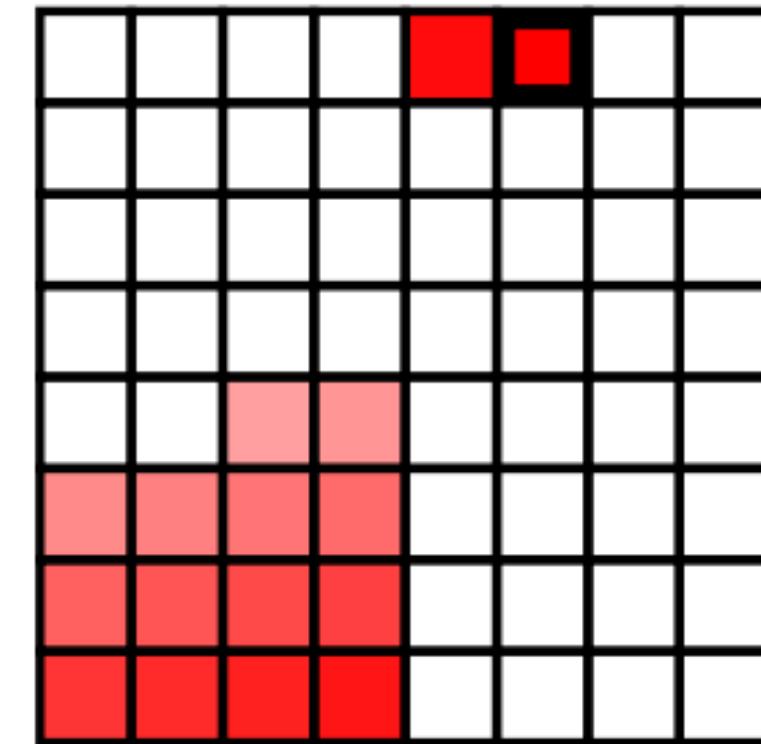
mem:1170

B mem:390 cache hit:336

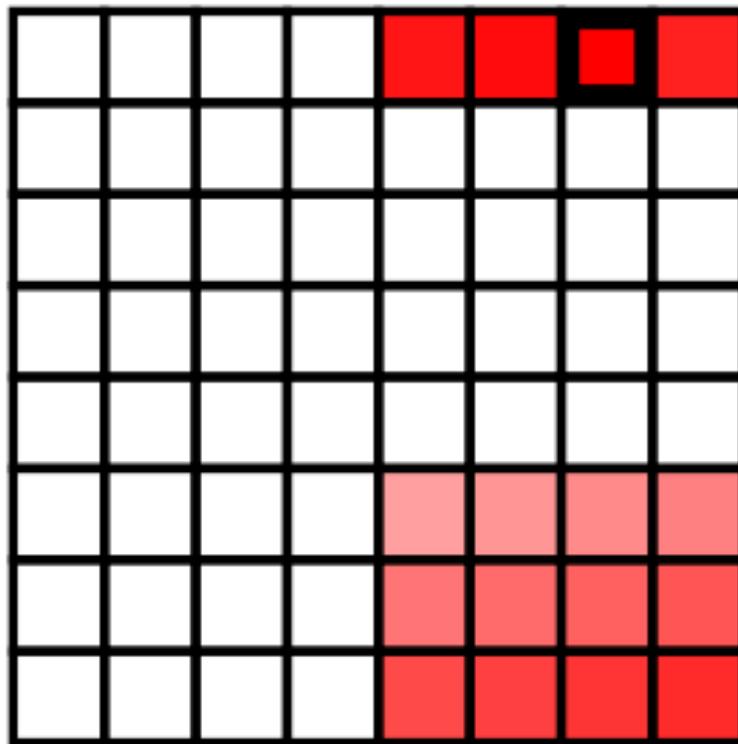


cache hits:918 ≈78%

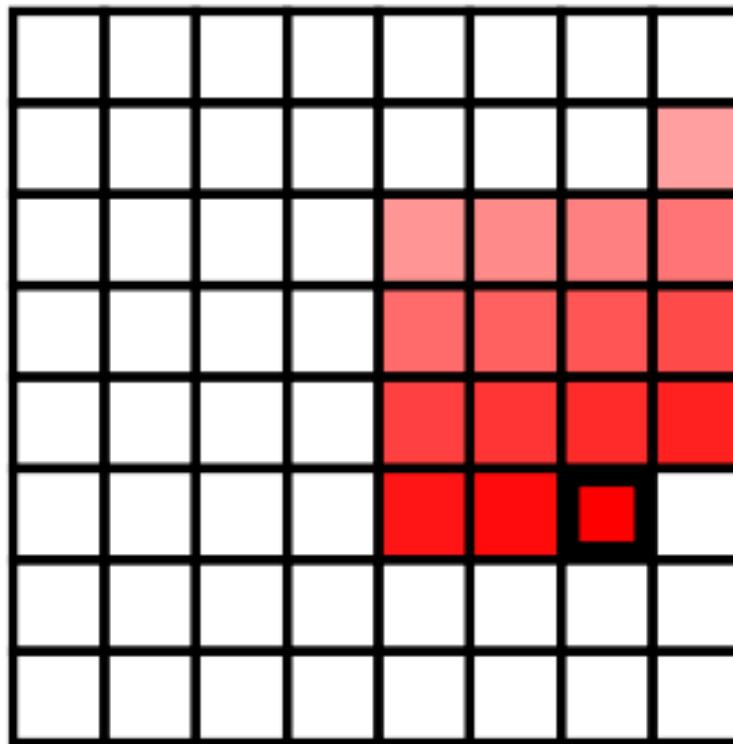
C mem:390 cache hit:292



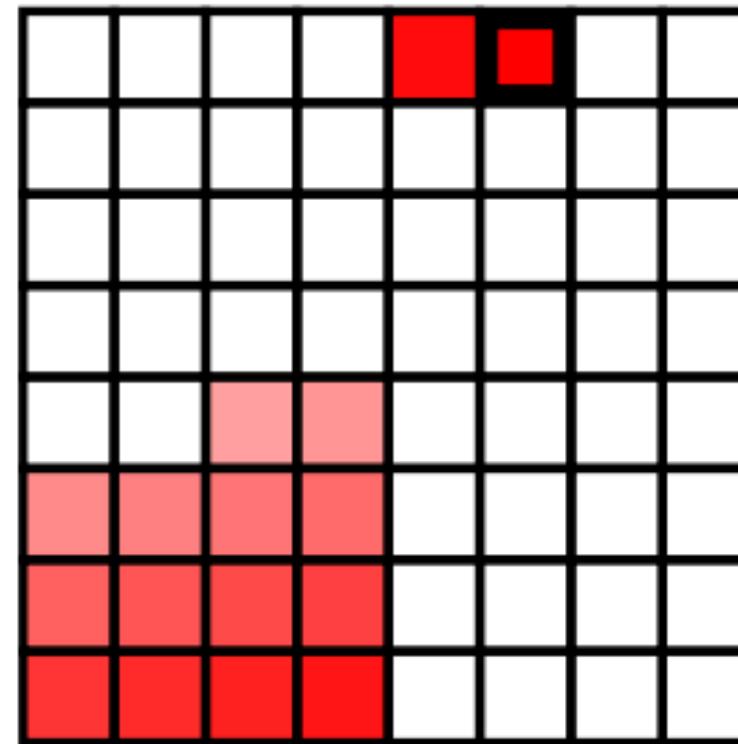
A mem:391 cache hit:291



B mem:391 cache hit:336



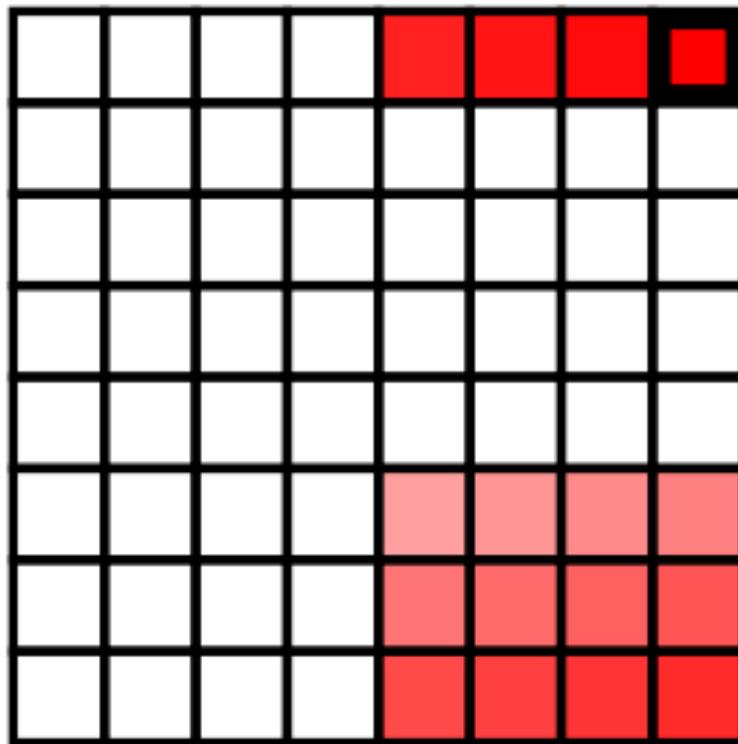
C mem:391 cache hit:293



mem:1173

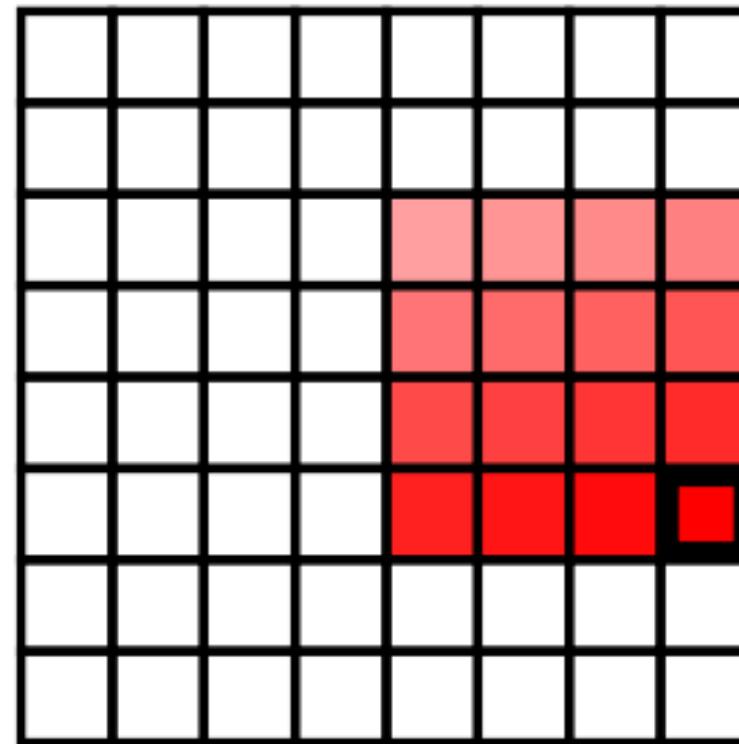
cache hits:920 $\approx 78\%$

A mem:392 cache hit:292



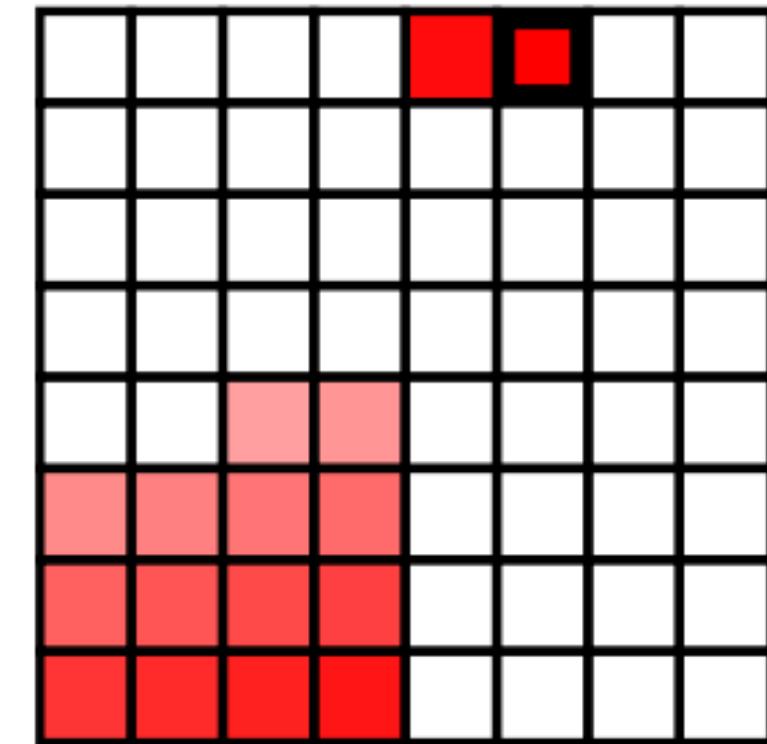
mem:1176

B mem:392 cache hit:336

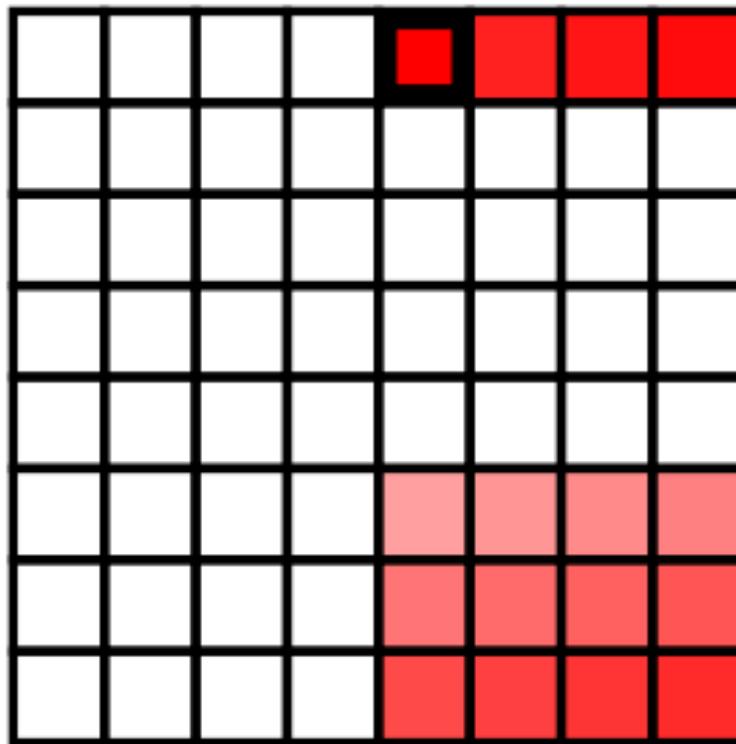


cache hits:922 ≈78%

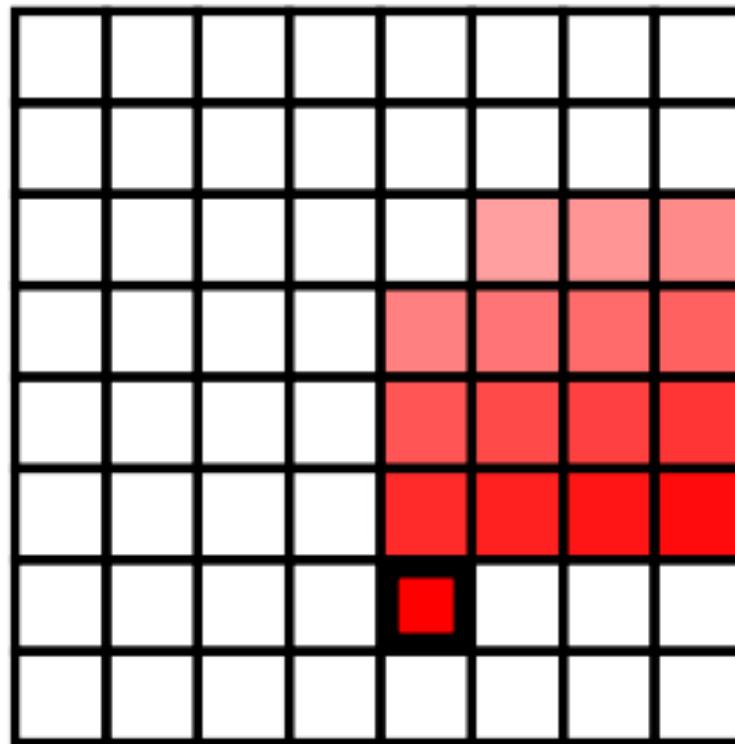
C mem:392 cache hit:294



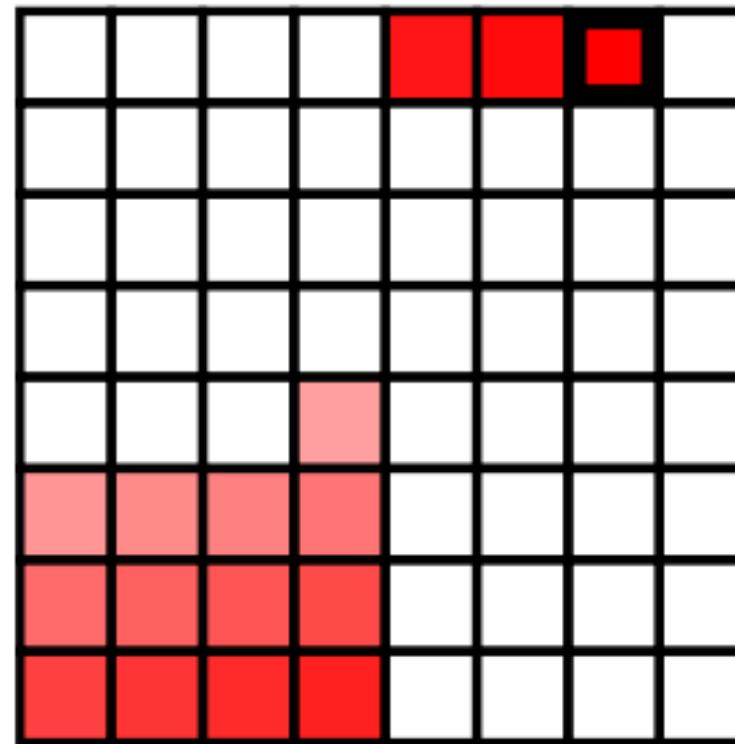
A mem:393 cache hit:293



B mem:393 cache hit:336



C mem:393 cache hit:294



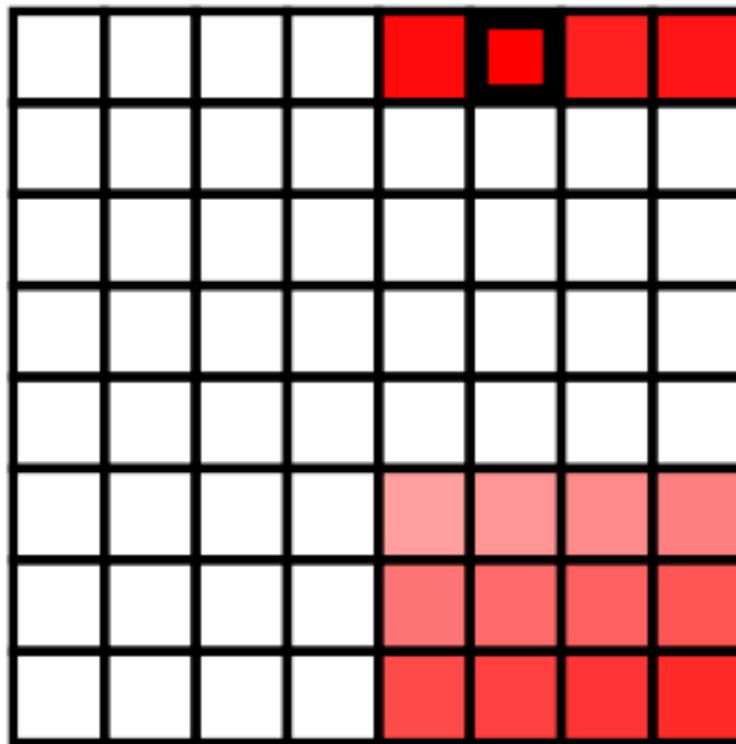
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mem:1179

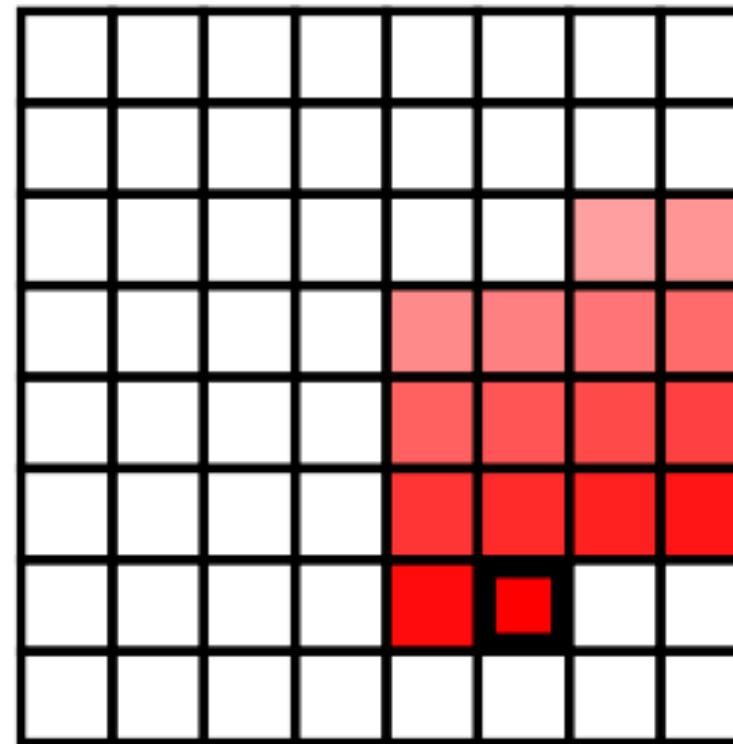
cache hits:923 $\approx 78\%$

A mem:394 cache hit:294



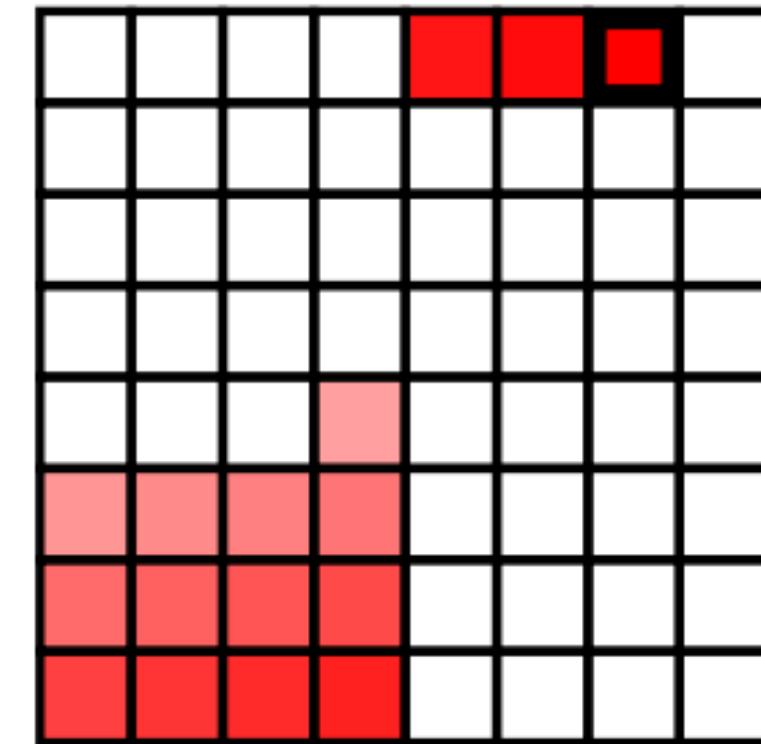
mem:1182

B mem:394 cache hit:336

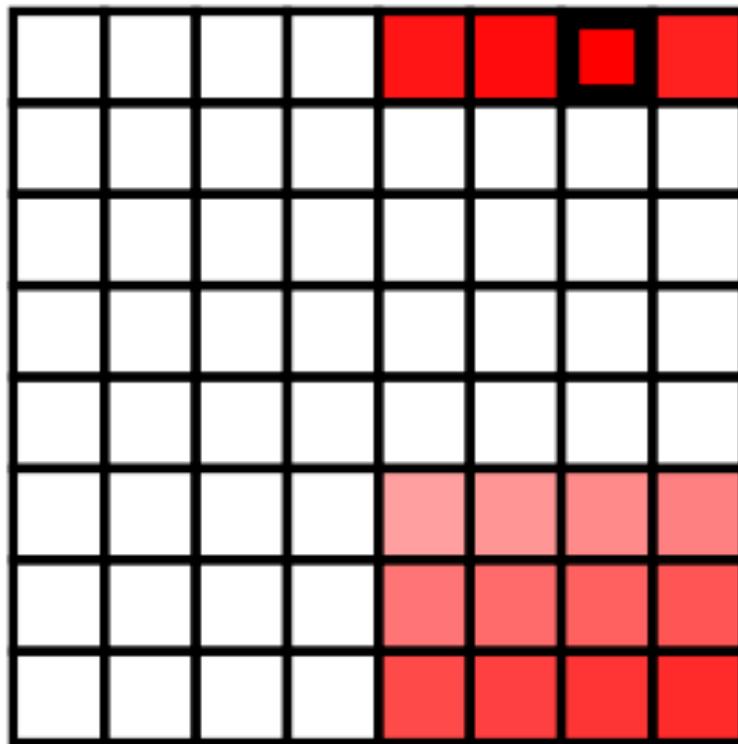


cache hits:925 $\approx 78\%$

C mem:394 cache hit:295

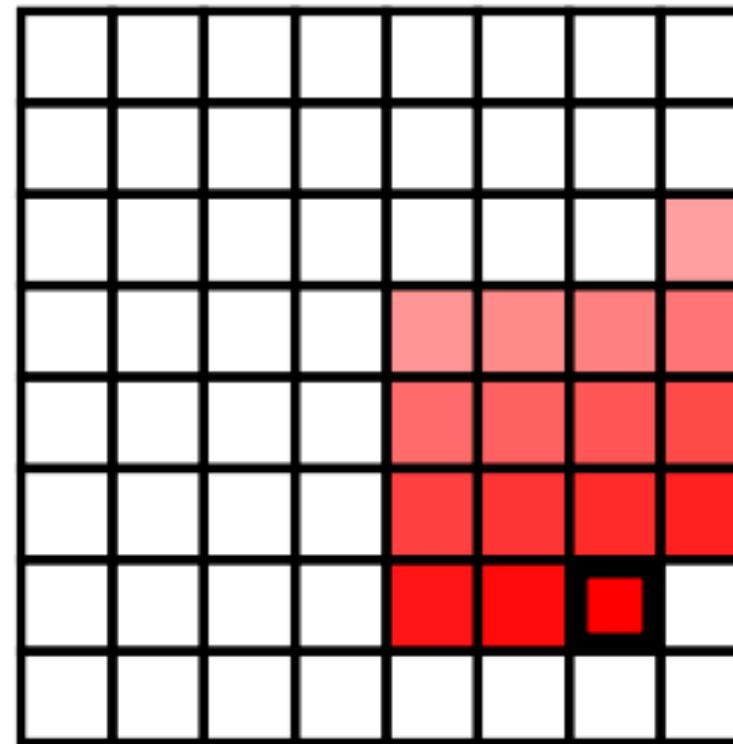


A mem:395 cache hit:295



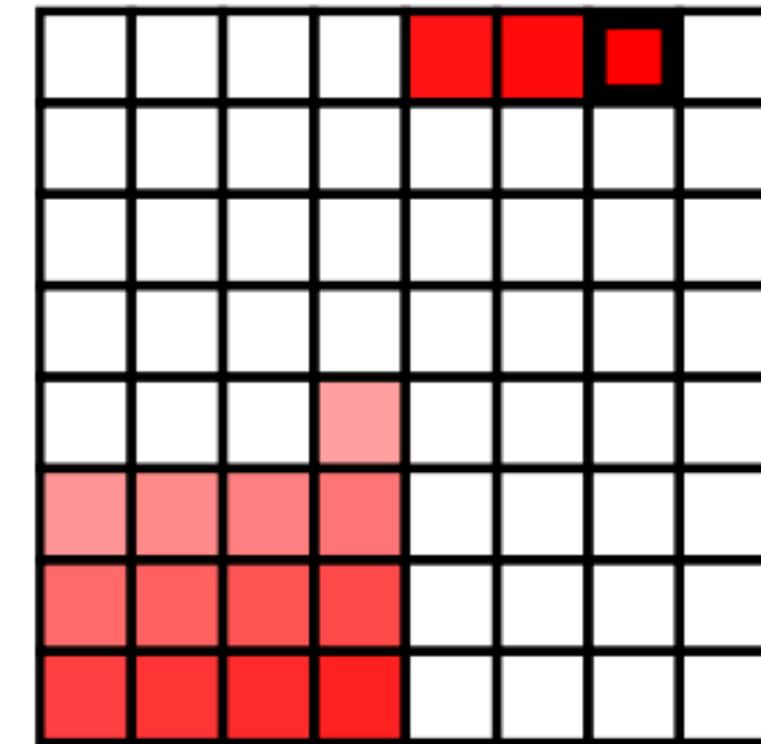
mem:1185

B mem:395 cache hit:336

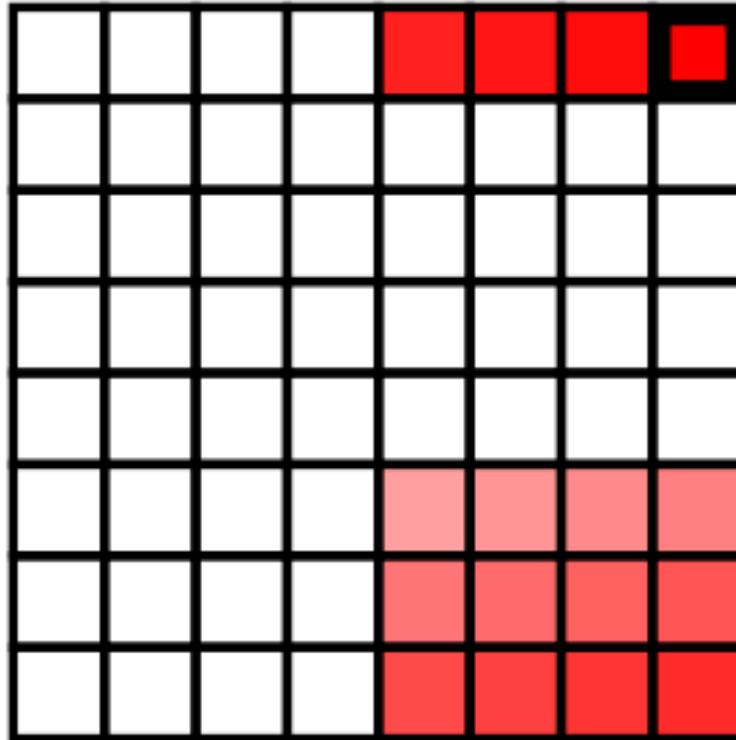


cache hits:927 ≈78%

C mem:395 cache hit:296

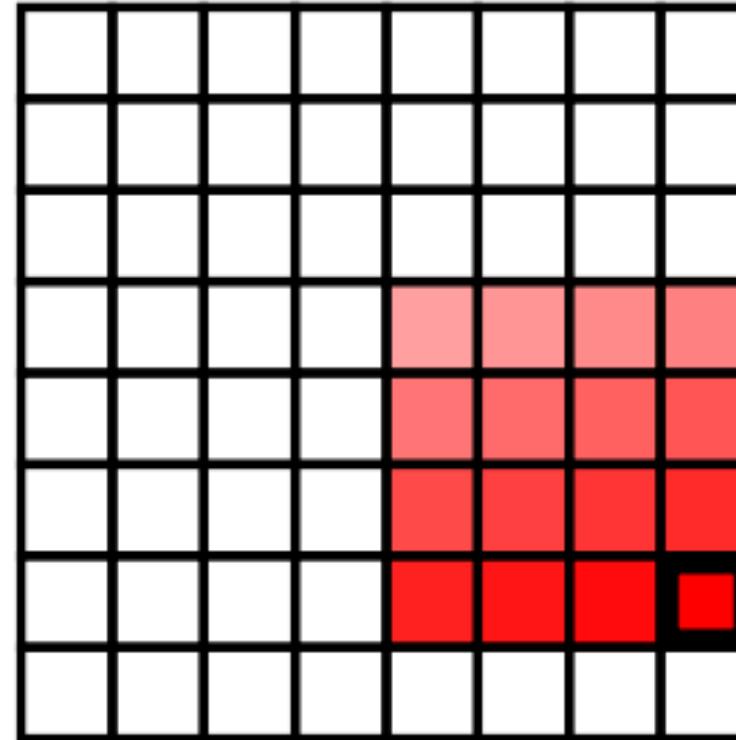


A mem:396 cache hit:296



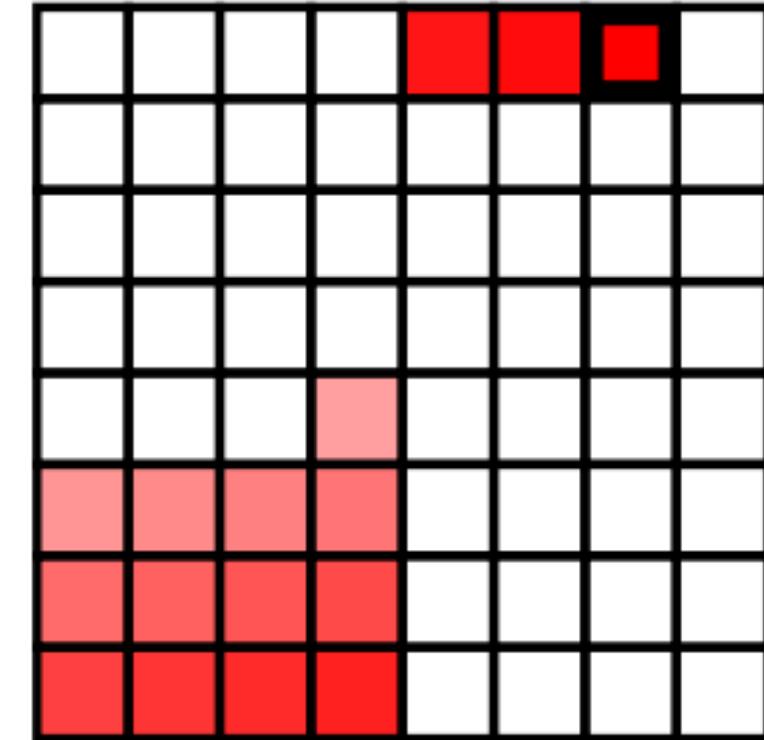
mem:1188

B mem:396 cache hit:336

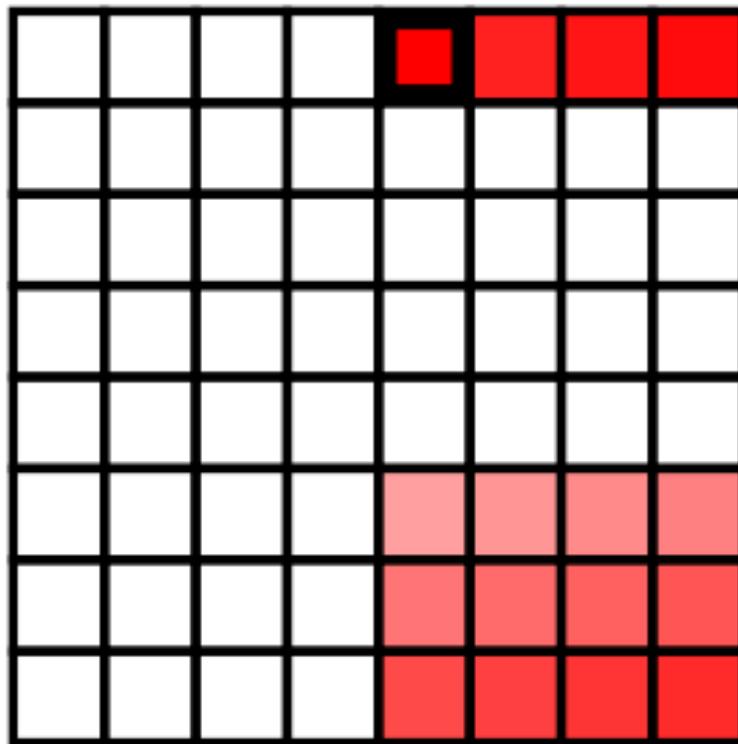


cache hits:929 $\approx 78\%$

C mem:396 cache hit:297

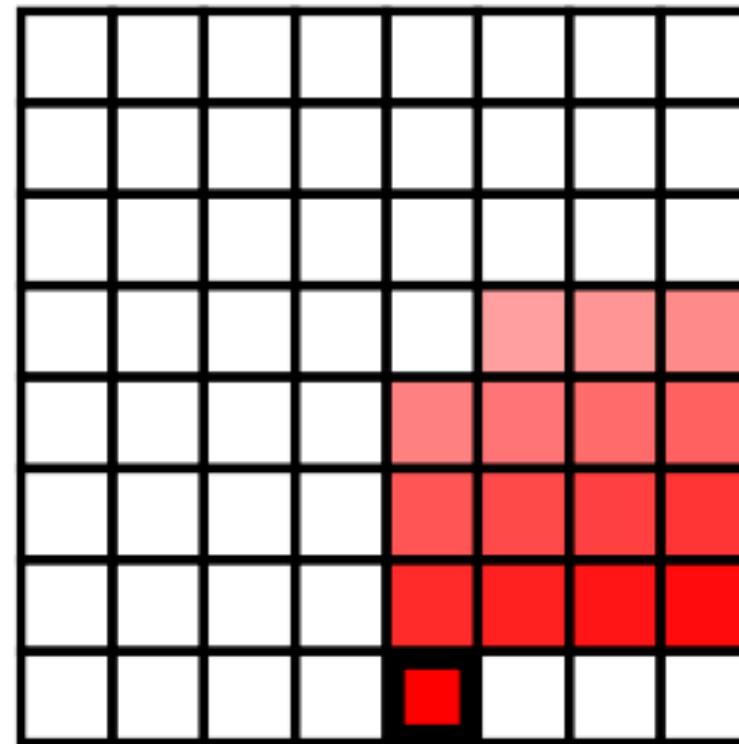


A mem:397 cache hit:297



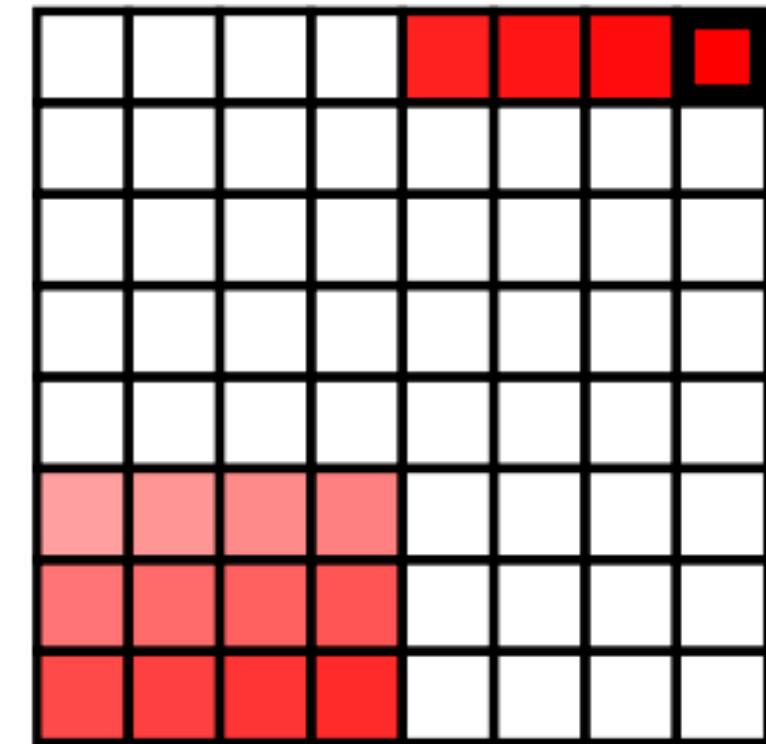
mem:1191

B mem:397 cache hit:336

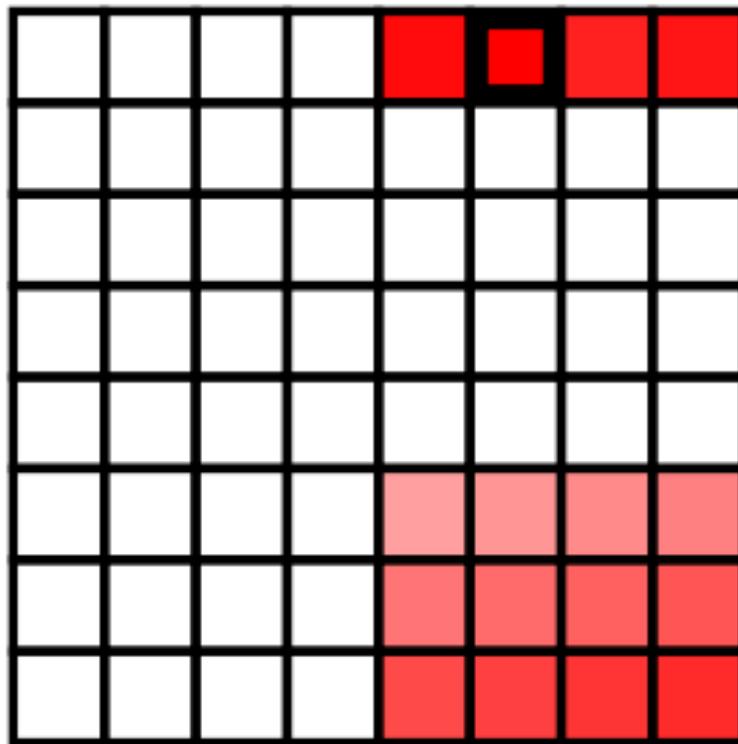


cache hits:930 $\approx 78\%$

C mem:397 cache hit:297

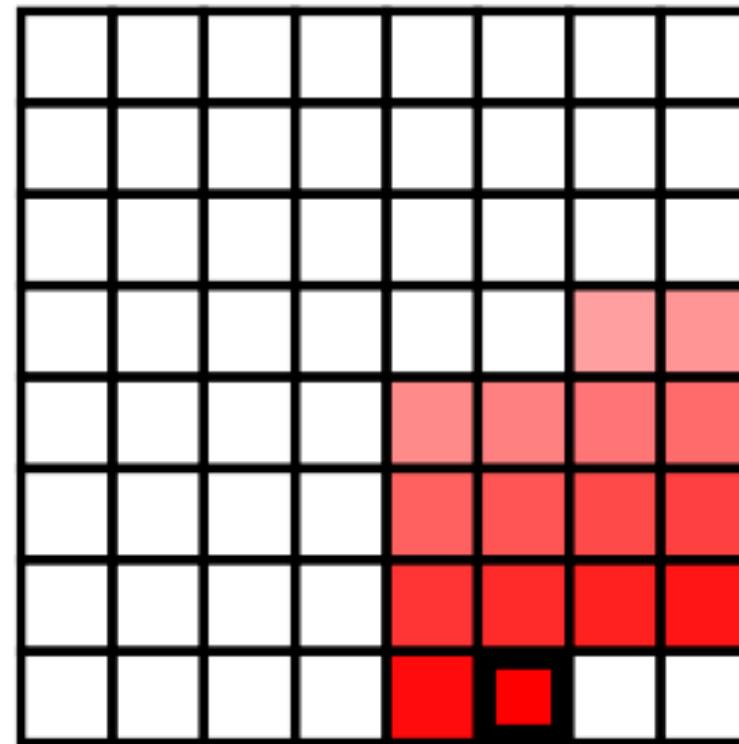


A mem:398 cache hit:298



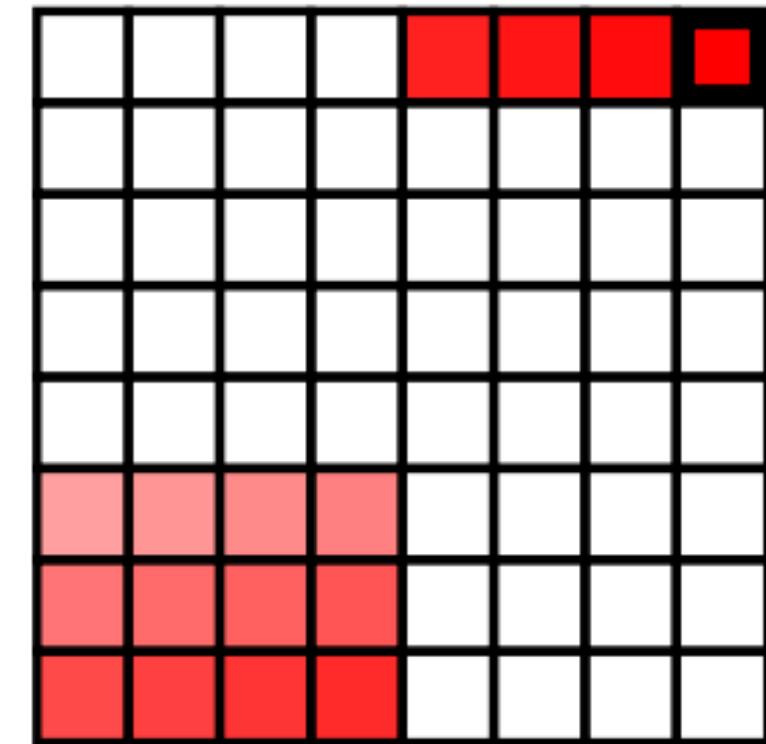
mem:1194

B mem:398 cache hit:336

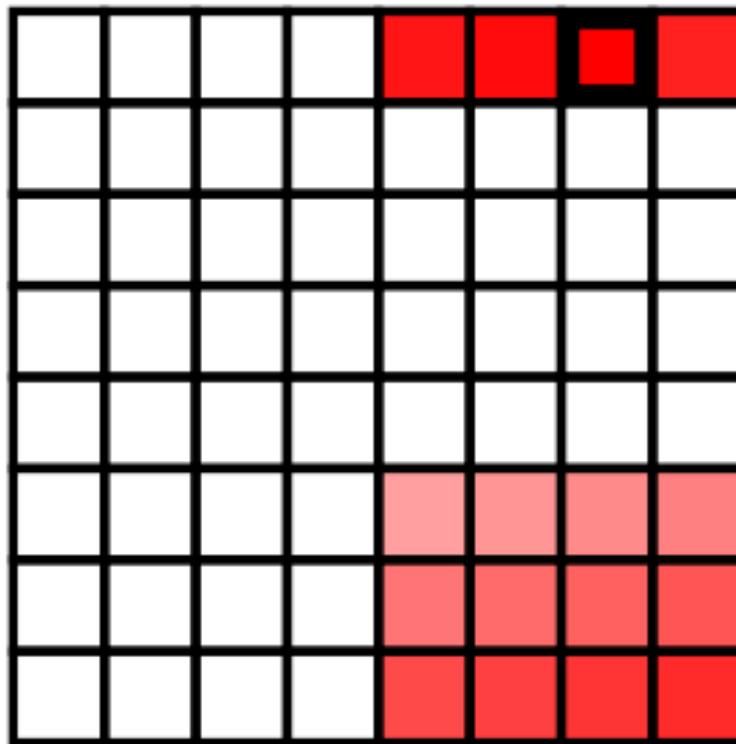


cache hits:932 $\approx 78\%$

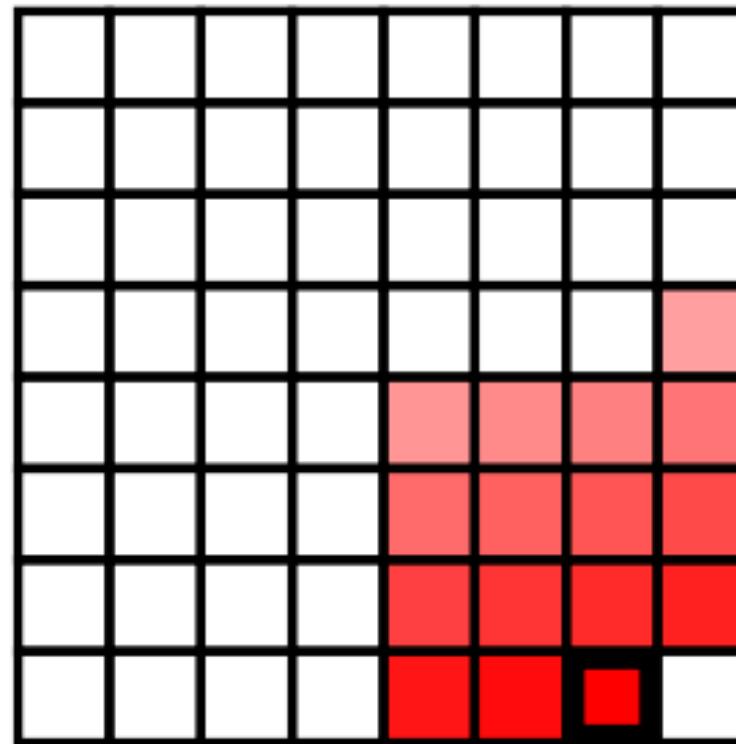
C mem:398 cache hit:298



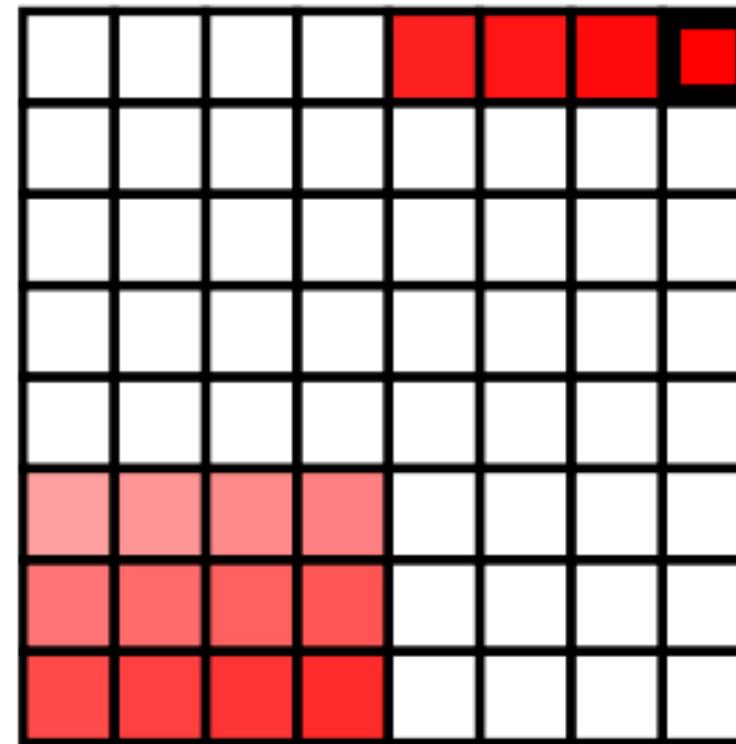
A mem:399 cache hit:299



B mem:399 cache hit:336



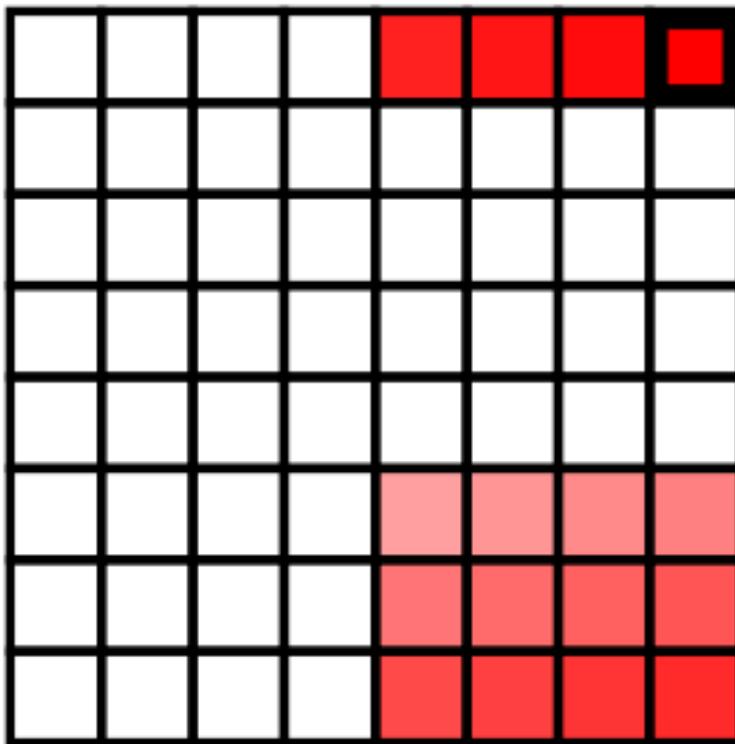
C mem:399 cache hit:299



mem:1197

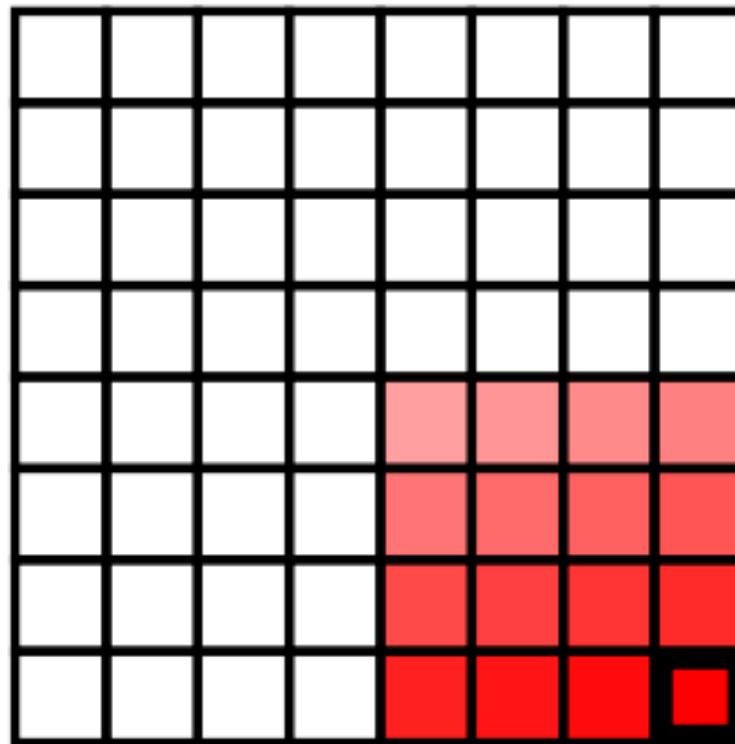
cache hits:934 $\approx 78\%$

A mem:400 cache hit:300



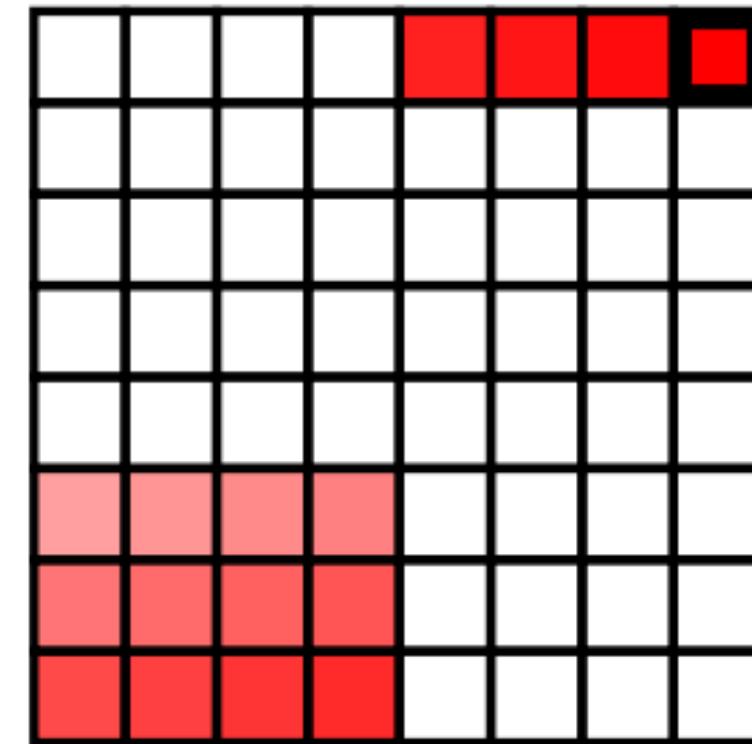
mem:1200

B mem:400 cache hit:336

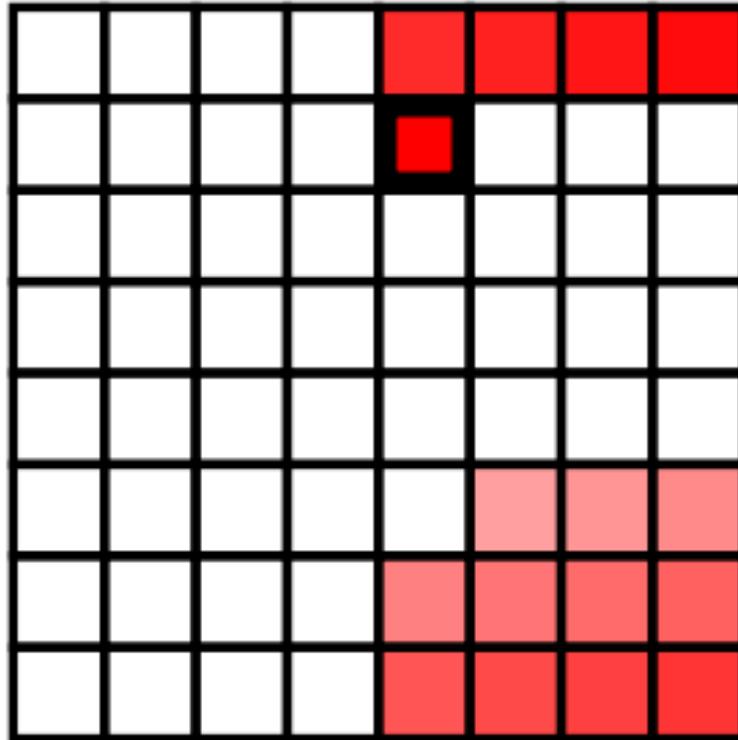


cache hits:936 ≈78%

C mem:400 cache hit:300

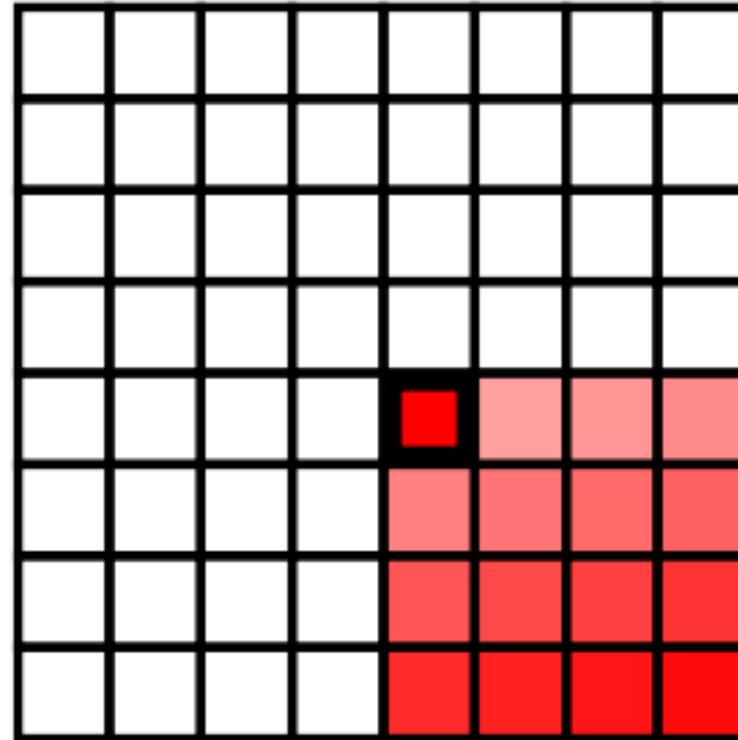


A mem:401 cache hit:300



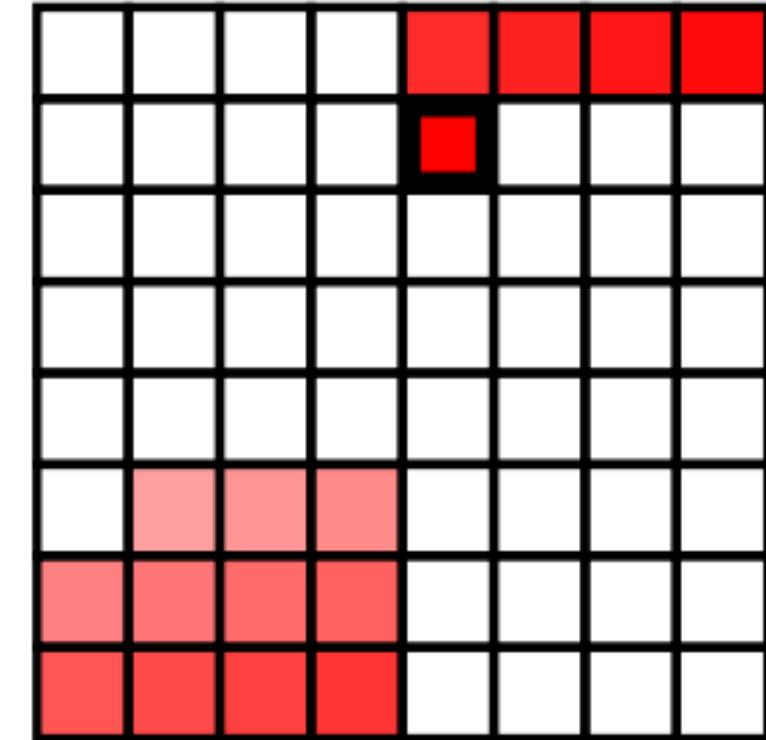
mem:1203

B mem:401 cache hit:337

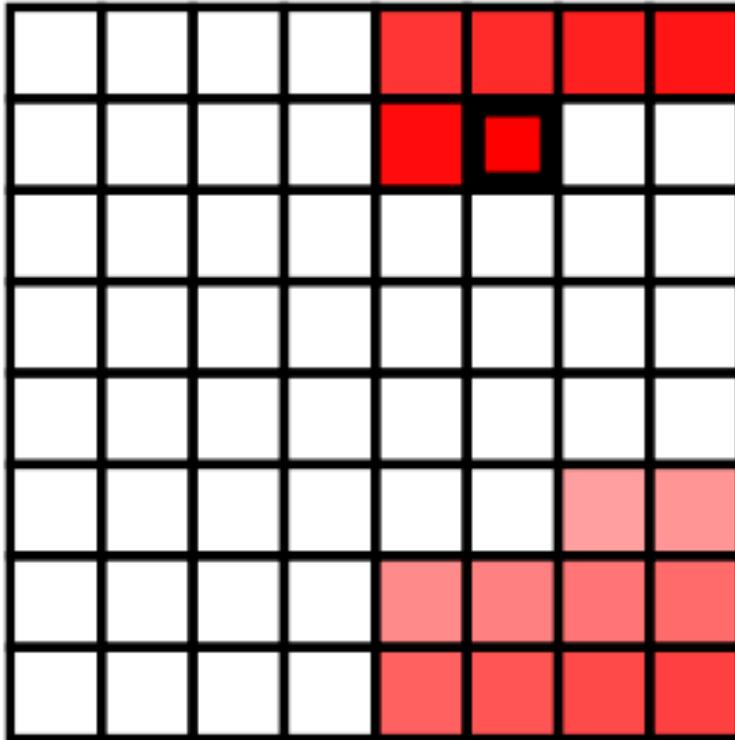


cache hits:937 ≈77%

C mem:401 cache hit:300

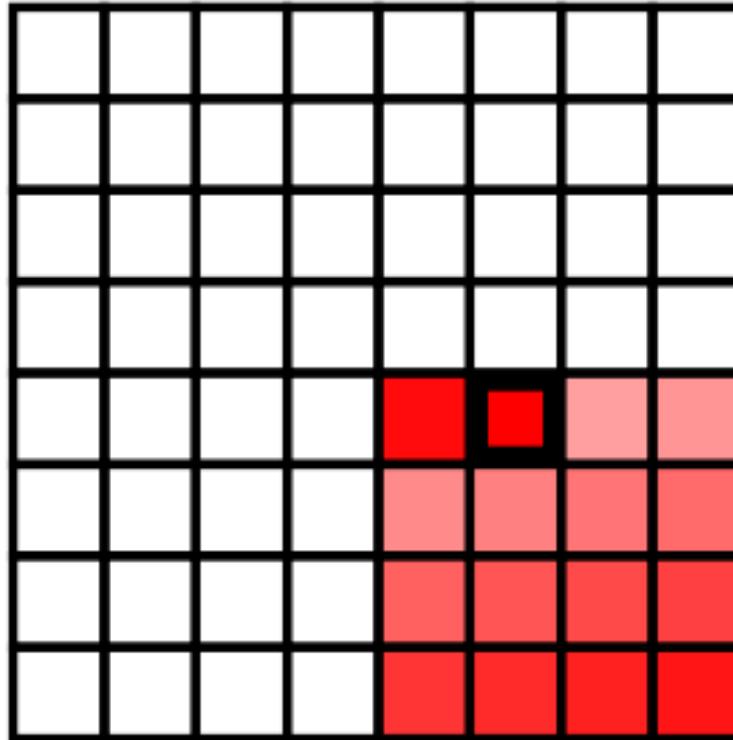


A mem:402 cache hit:300



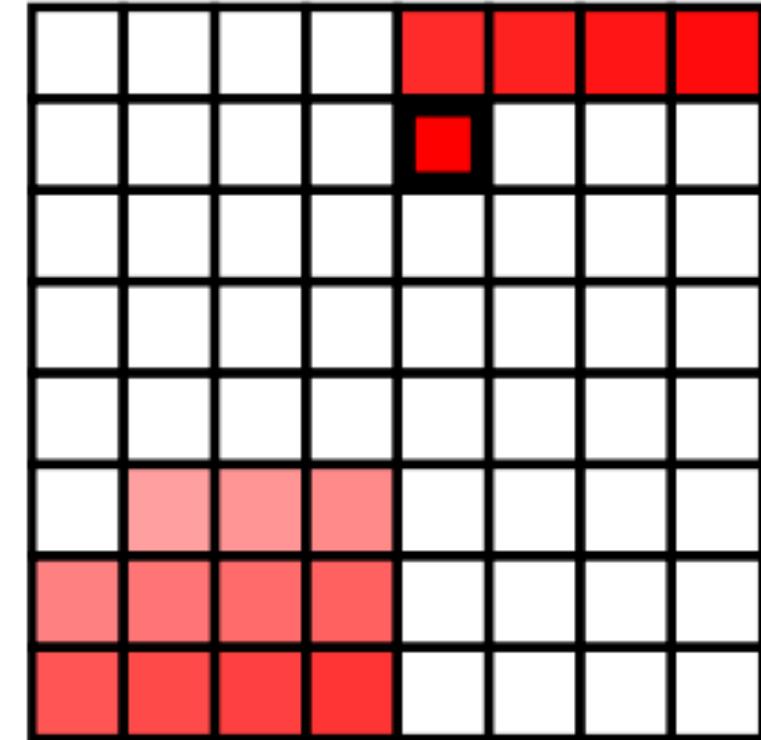
mem:1206

B mem:402 cache hit:338

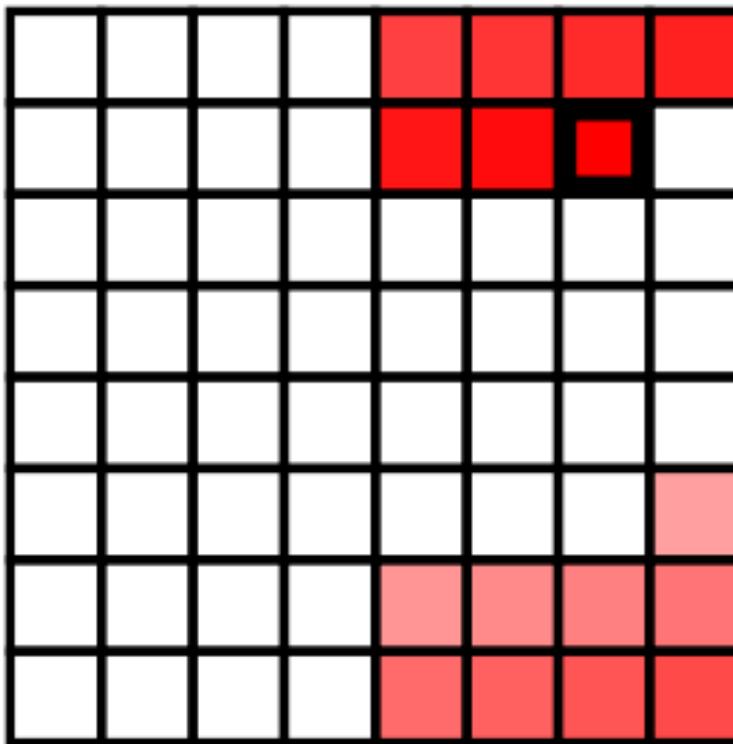


cache hits:939 ≈77%

C mem:402 cache hit:301

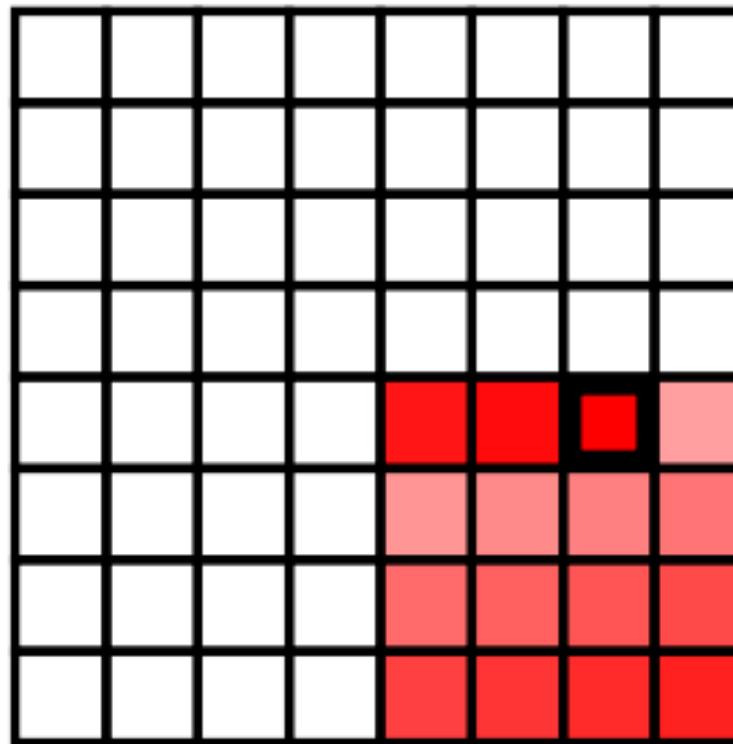


A mem:403 cache hit:300



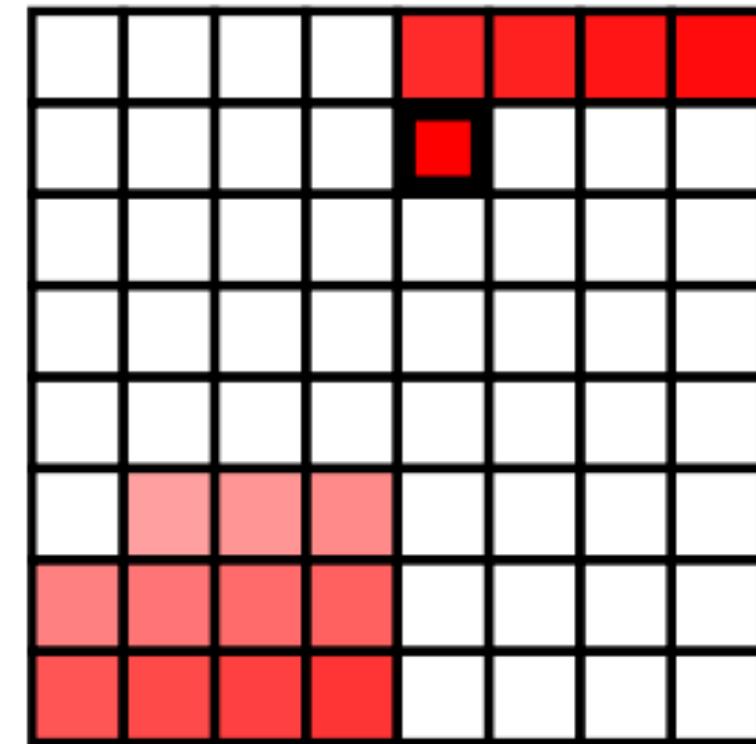
mem:1209

B mem:403 cache hit:339

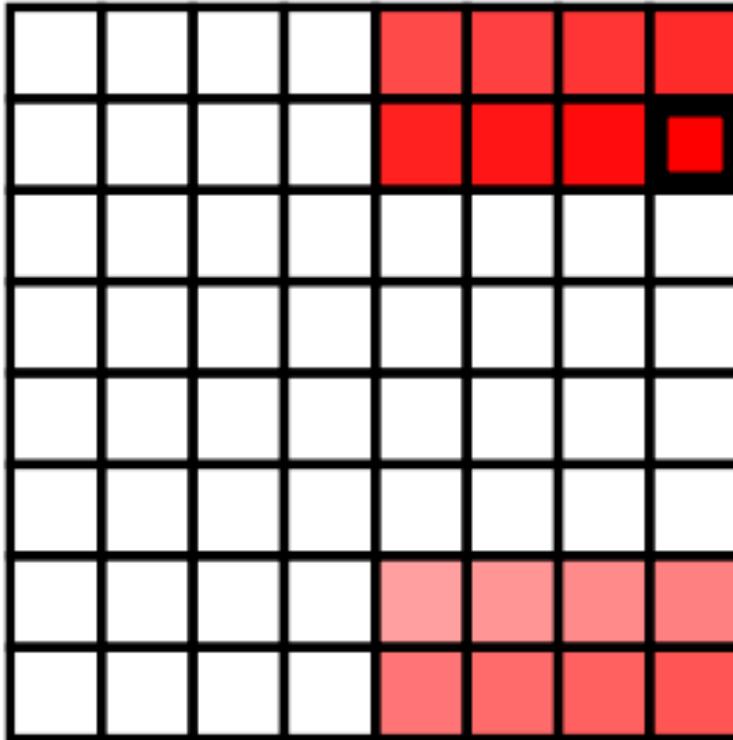


cache hits:941 ≈77%

C mem:403 cache hit:302

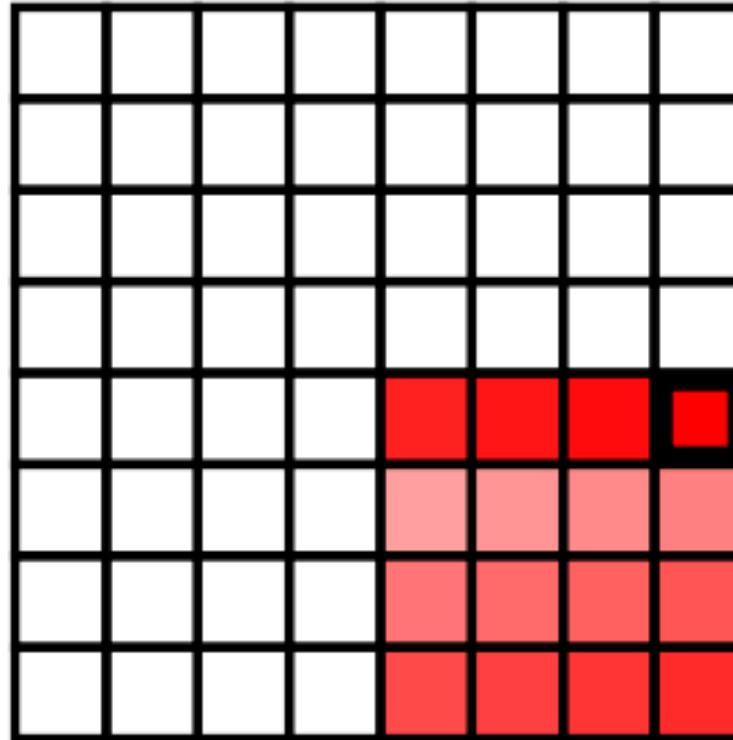


A mem:404 cache hit:300



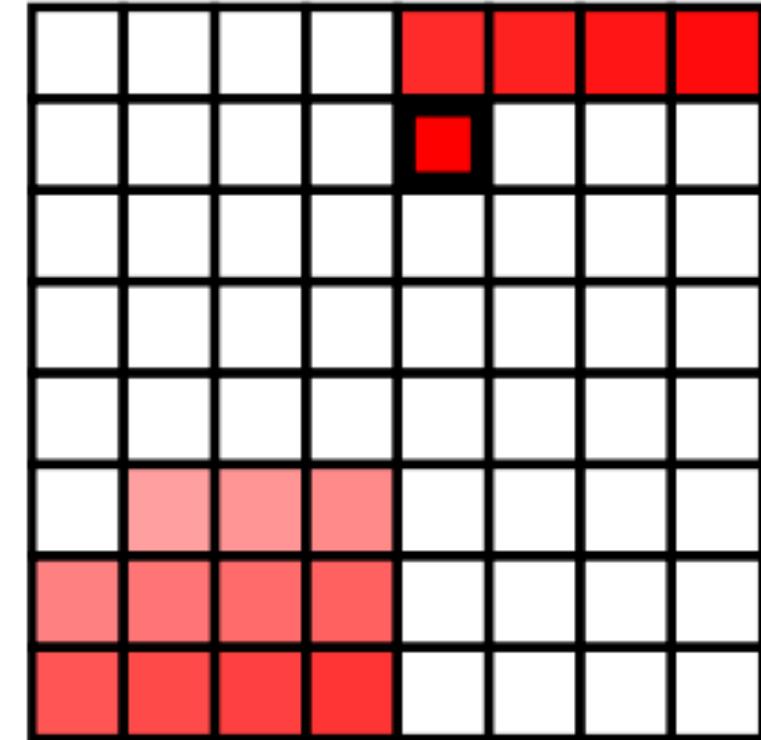
mem:1212

B mem:404 cache hit:340

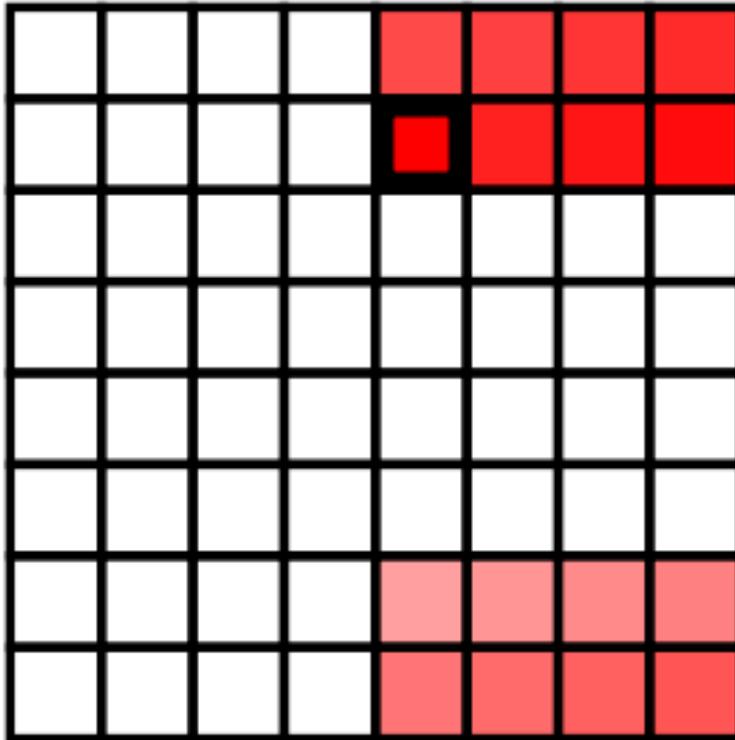


cache hits:943 ≈77%

C mem:404 cache hit:303

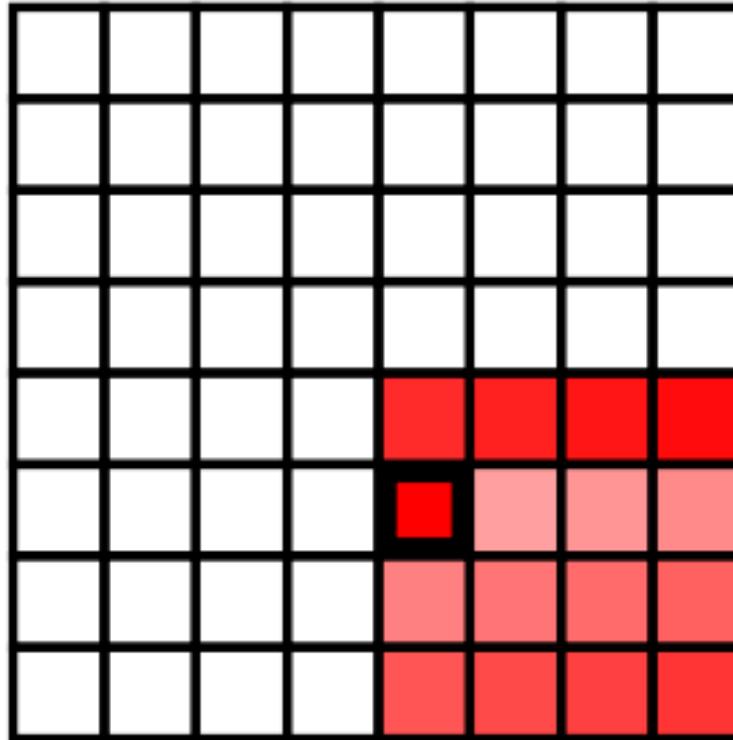


A mem:405 cache hit:301



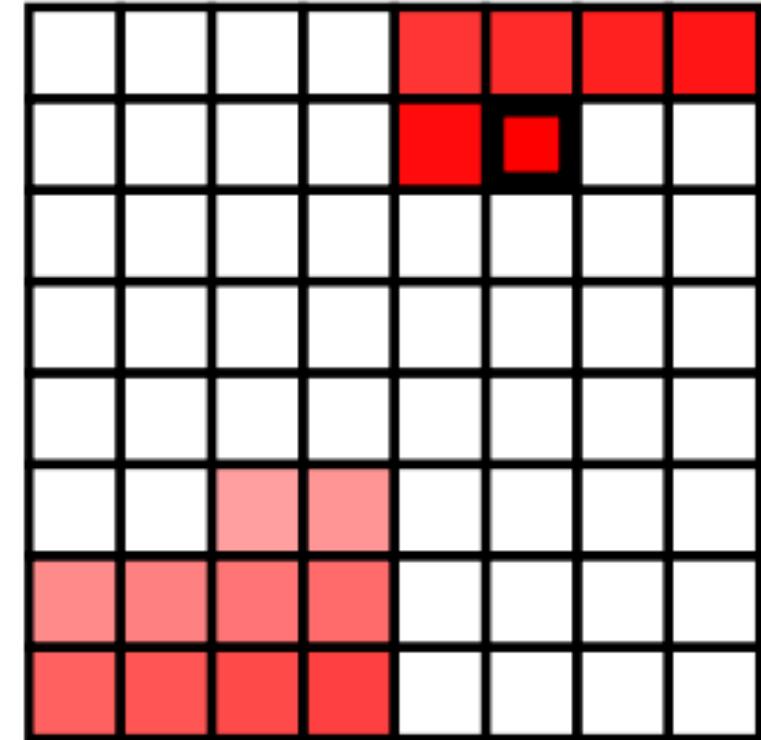
mem:1215

B mem:405 cache hit:341

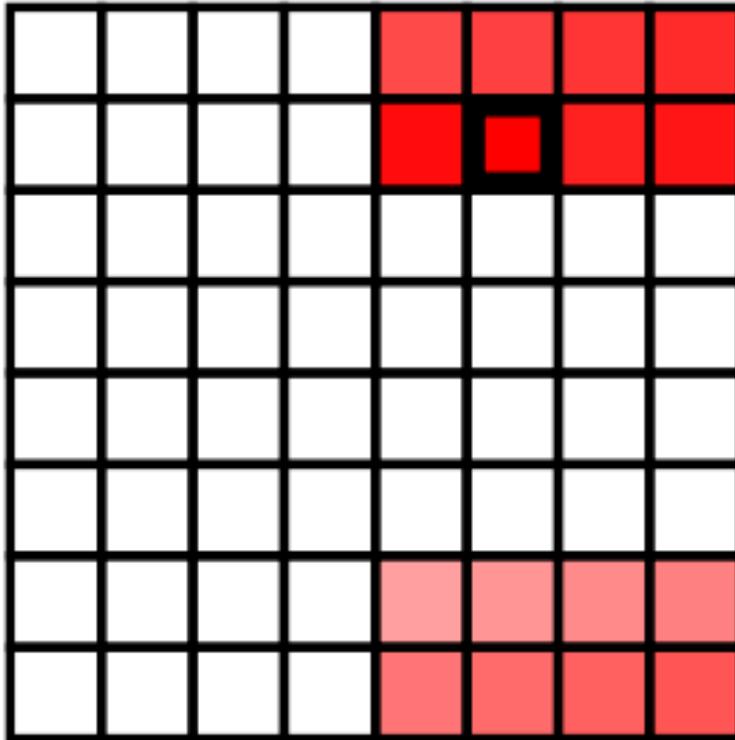


cache hits:945 ≈77%

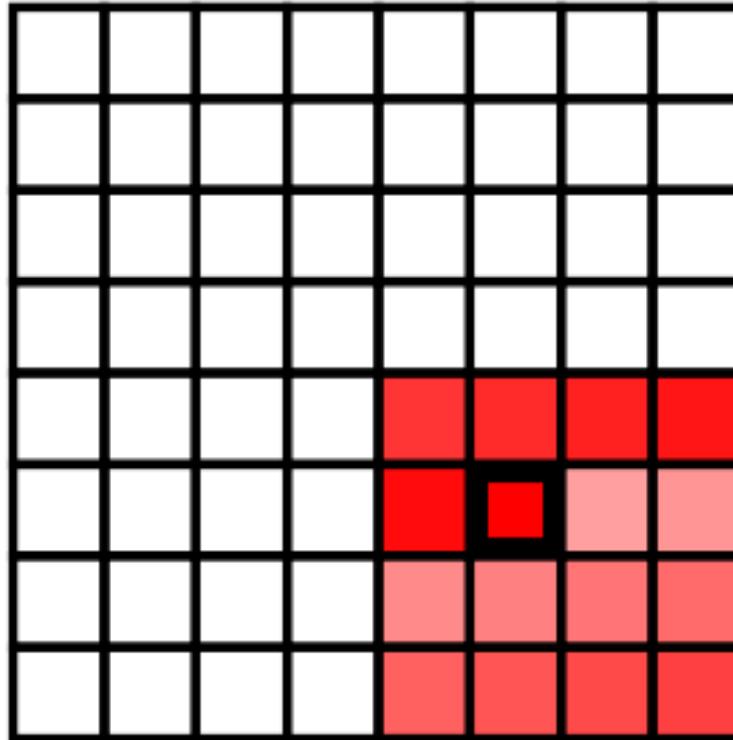
C mem:405 cache hit:303



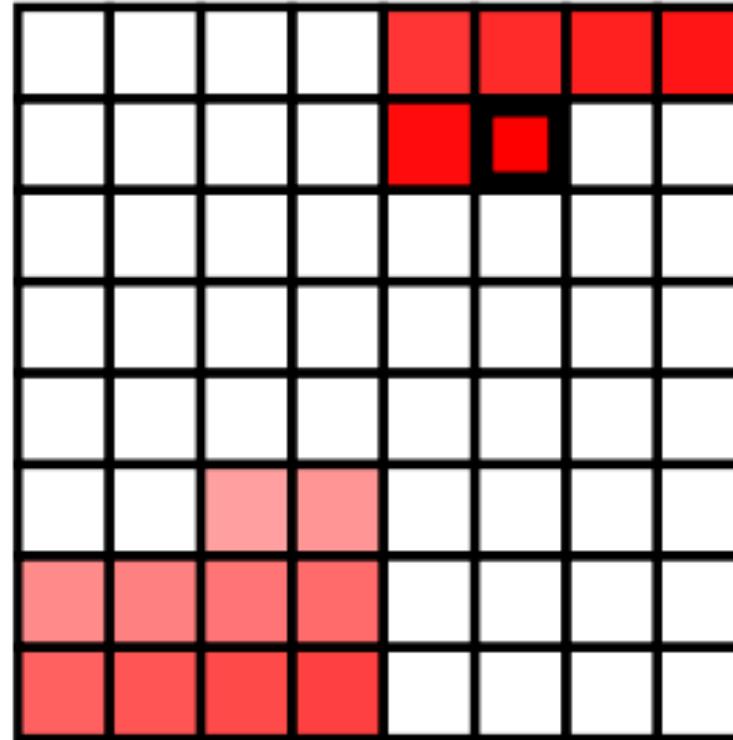
A mem:406 cache hit:302



B mem:406 cache hit:342



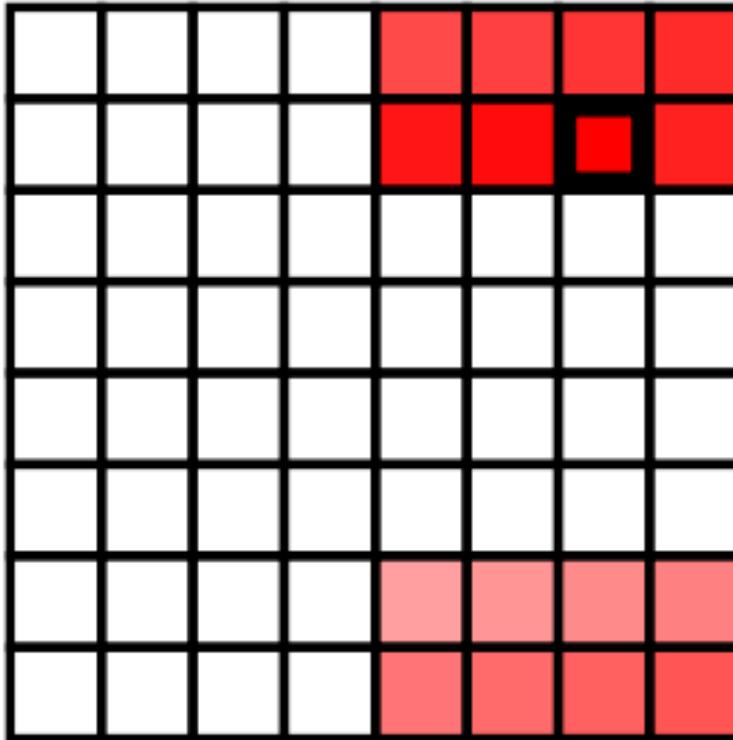
C mem:406 cache hit:304



mem:1218

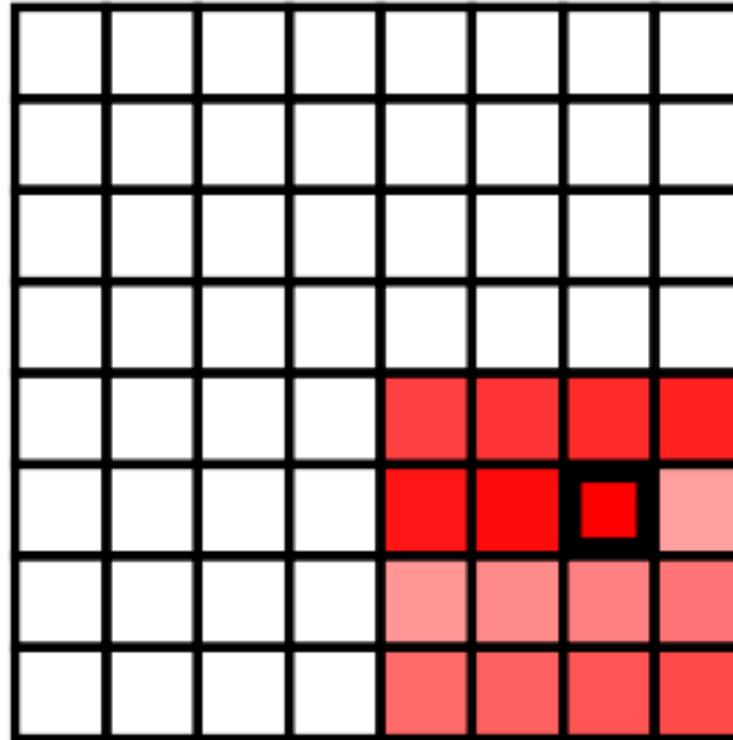
cache hits:948 $\approx 77\%$

A mem:407 cache hit:303



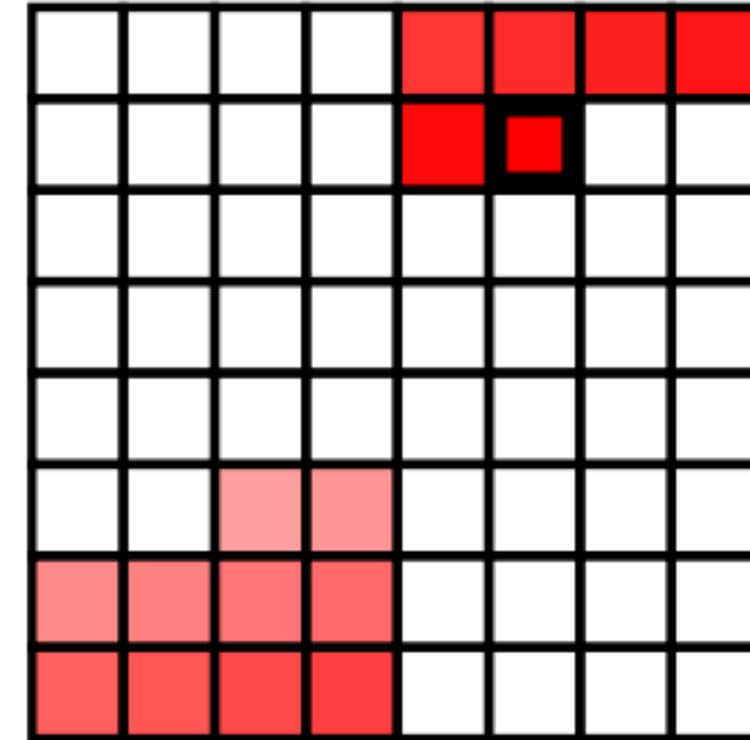
mem:1221

B mem:407 cache hit:343

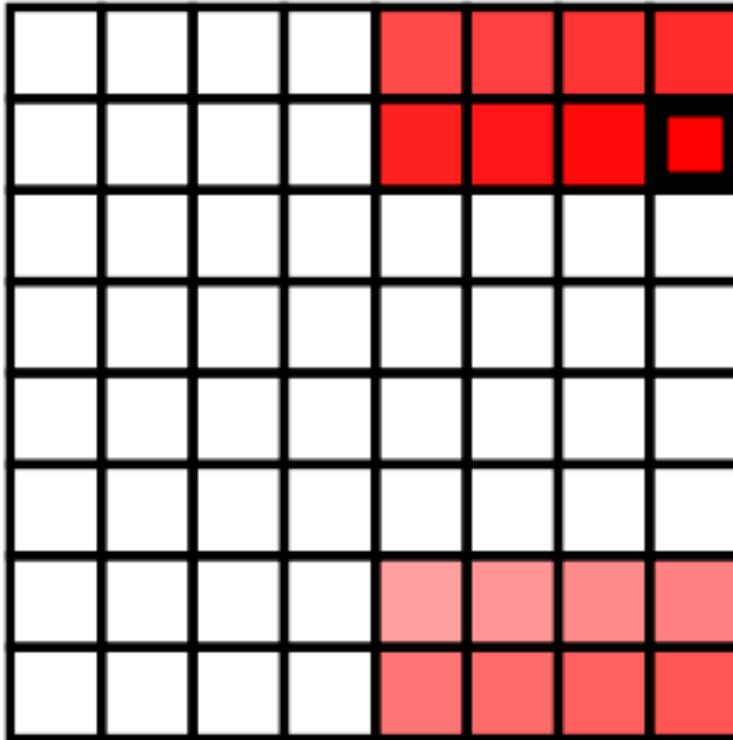


cache hits:951 ≈77%

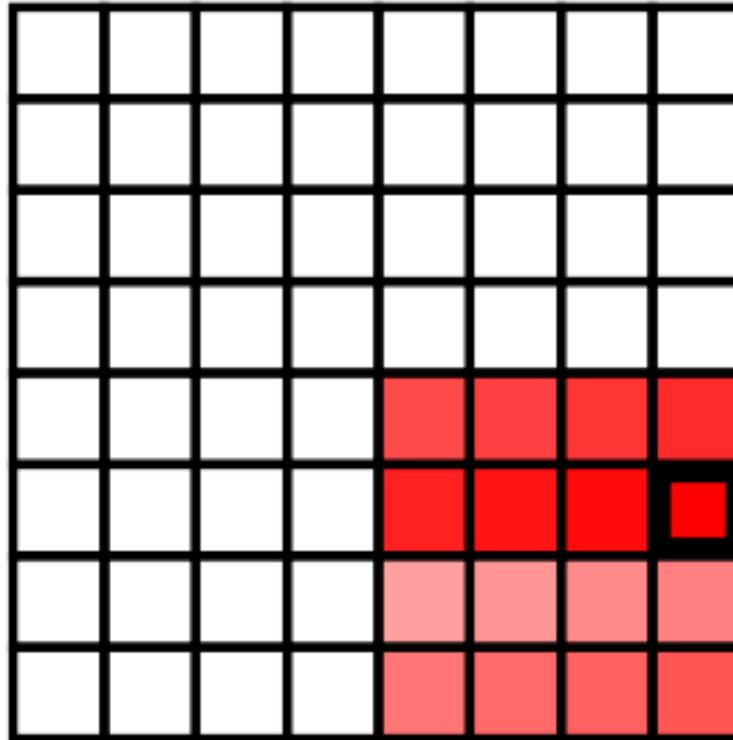
C mem:407 cache hit:305



A mem:408 cache hit:304

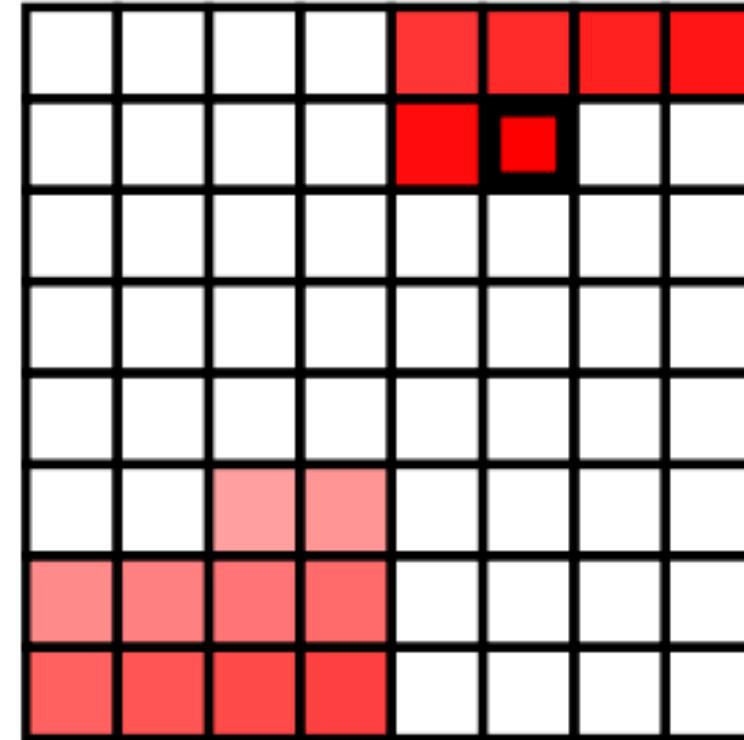


B mem:408 cache hit:344



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C mem:408 cache hit:306

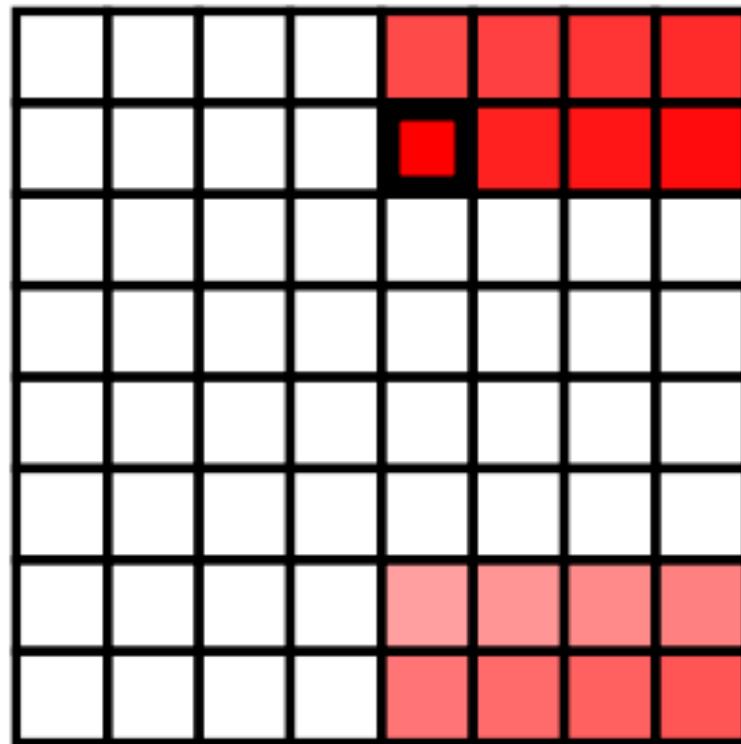


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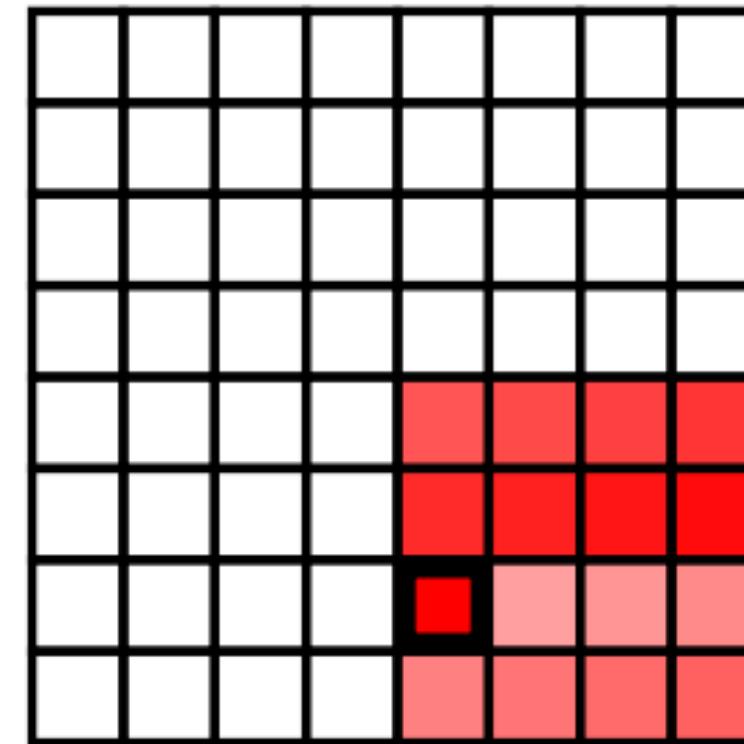
mem:1224

cache hits:954 ≈77%

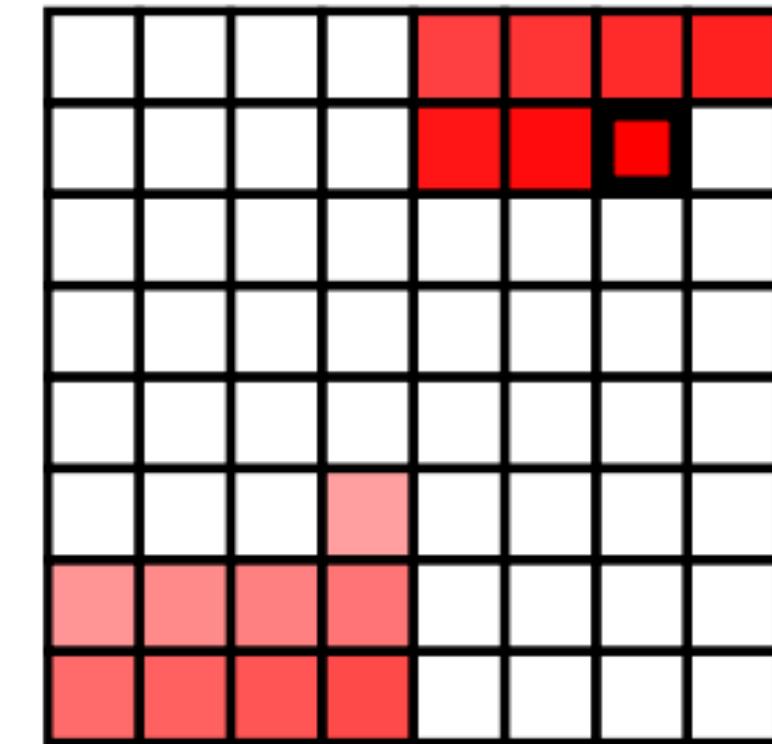
A mem:409 cache hit:305



B mem:409 cache hit:345



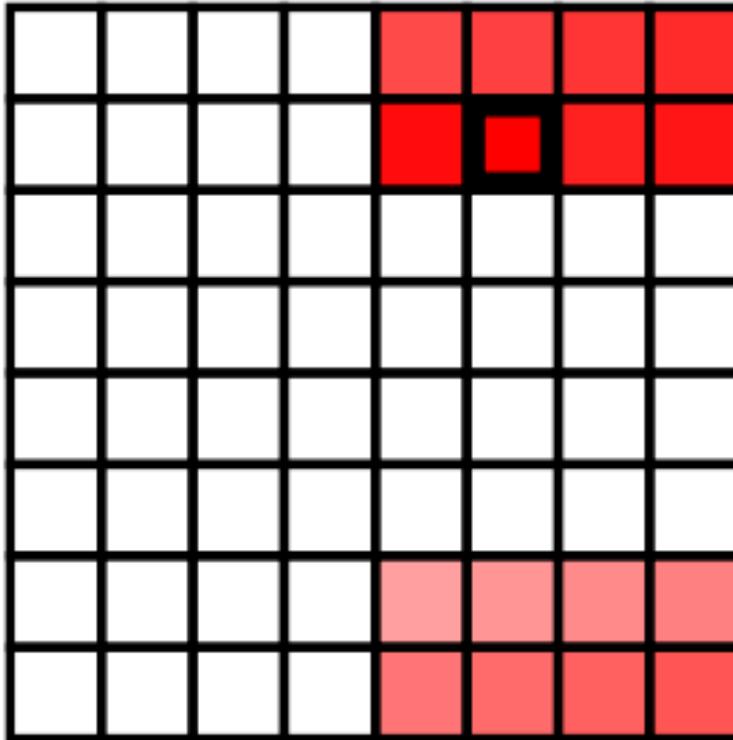
C mem:409 cache hit:306



mem:1227

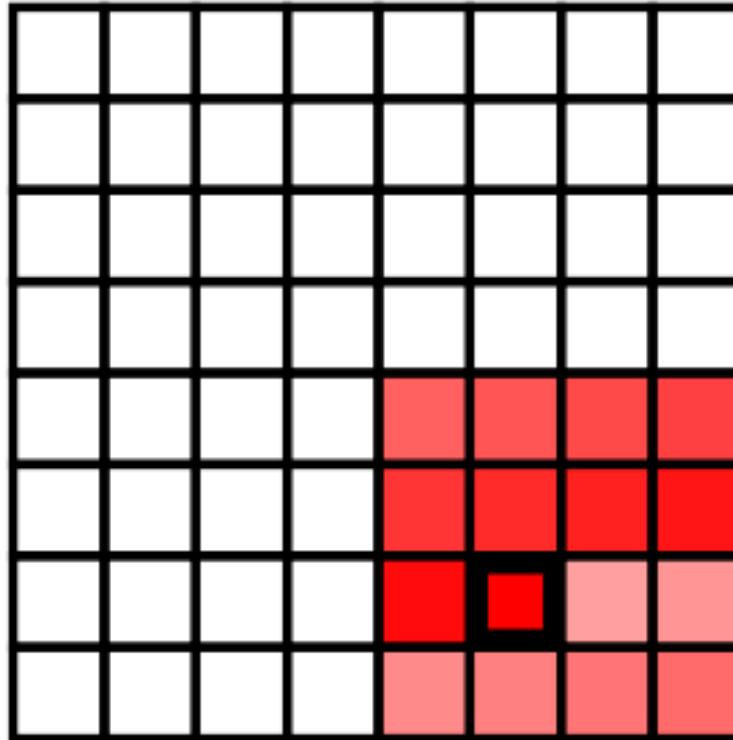
cache hits:956 ≈77%

A mem:410 cache hit:306



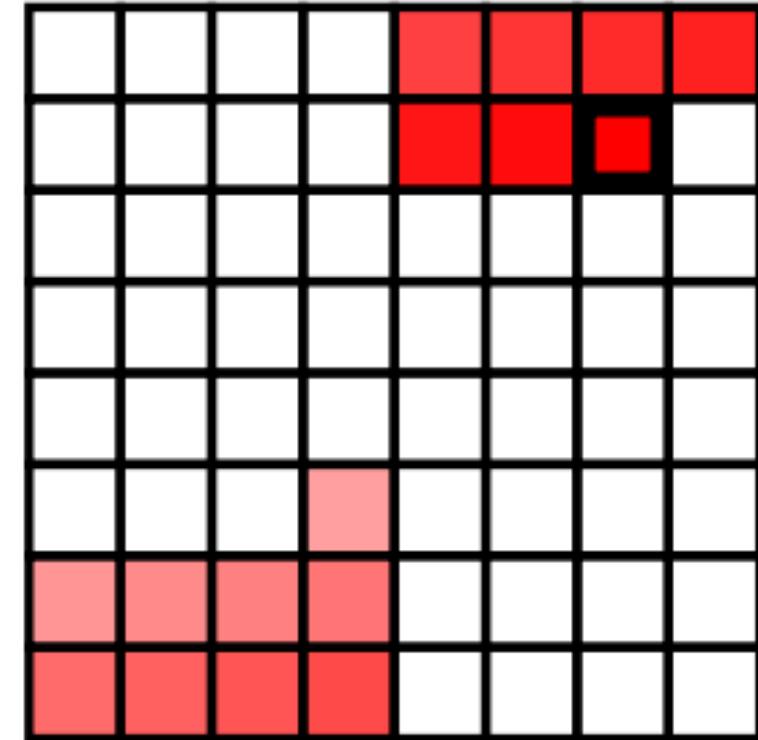
mem:1230

B mem:410 cache hit:346

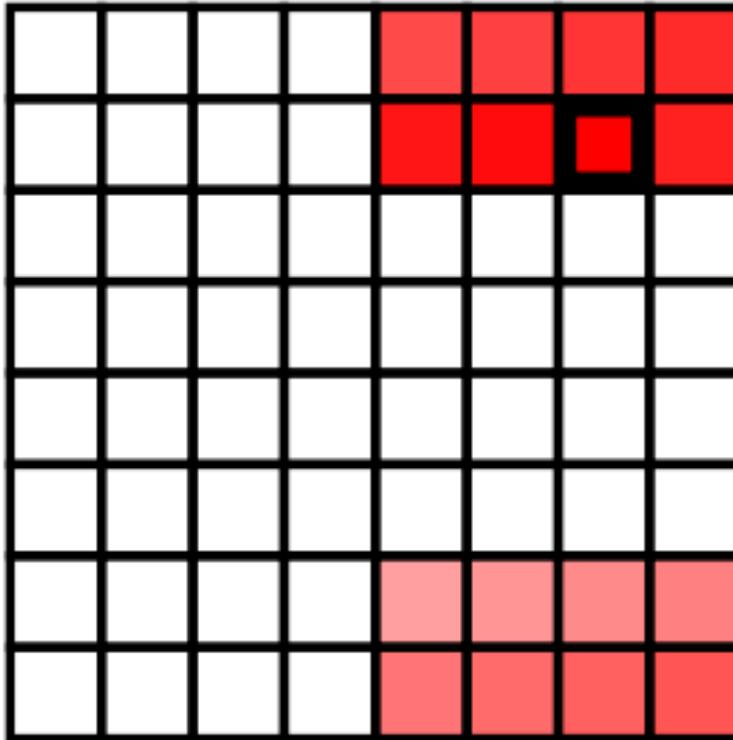


cache hits:959 ≈77%

C mem:410 cache hit:307

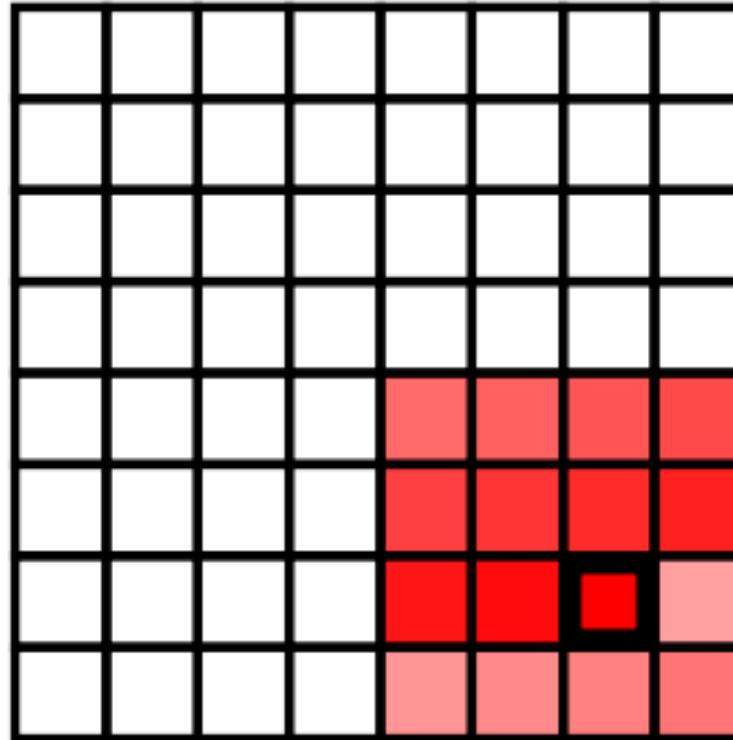


A mem:411 cache hit:307



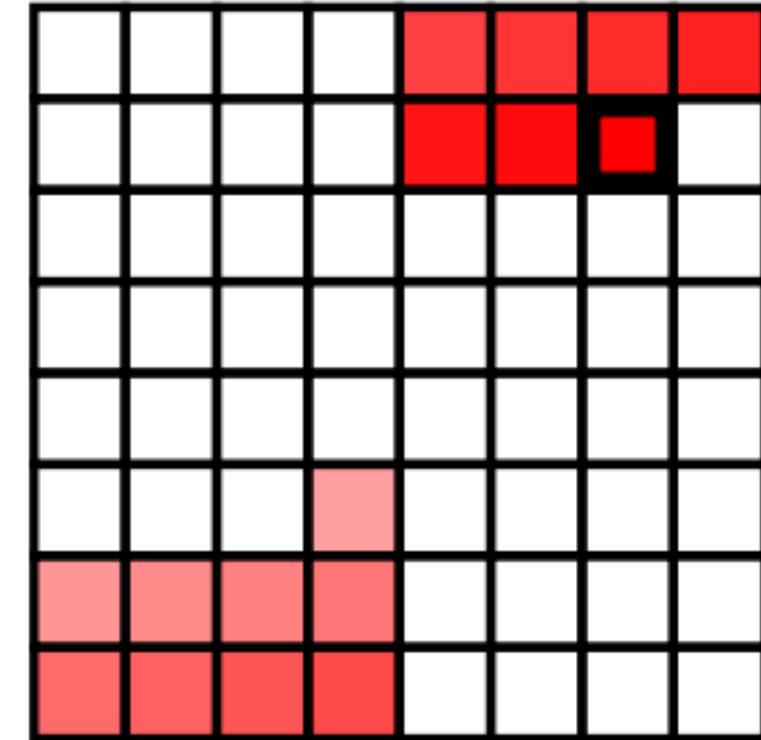
mem:1233

B mem:411 cache hit:347

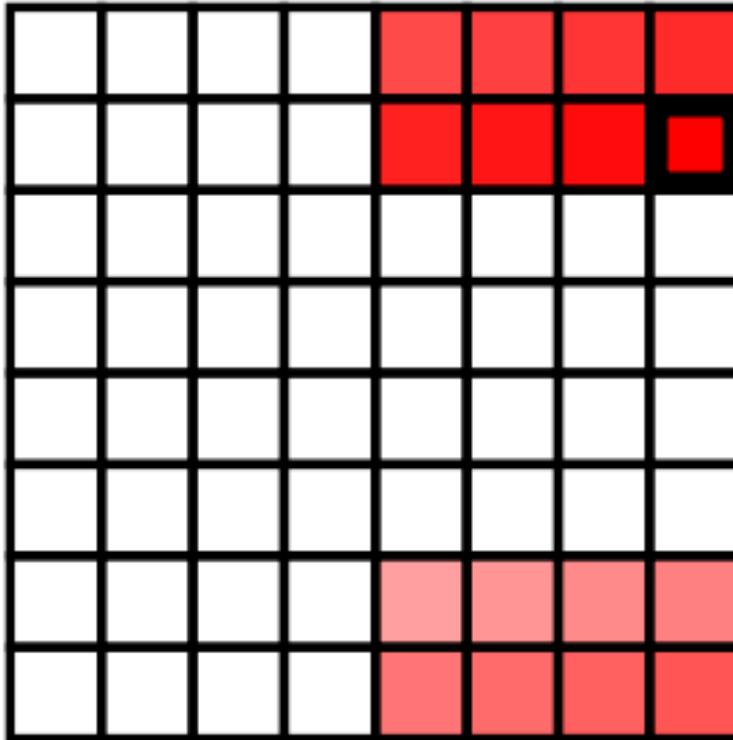


cache hits:962 ≈78%

C mem:411 cache hit:308

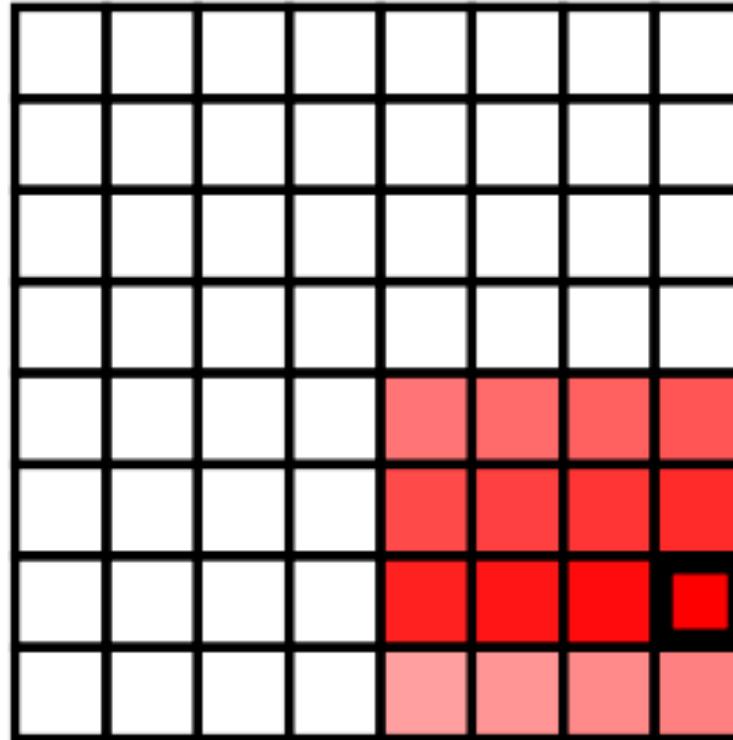


A mem:412 cache hit:308



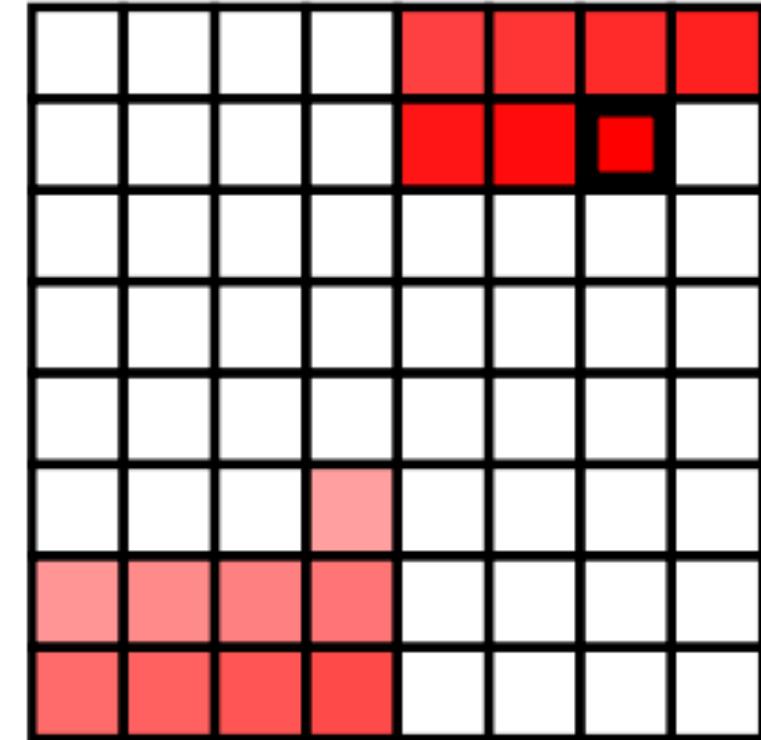
mem:1236

B mem:412 cache hit:348

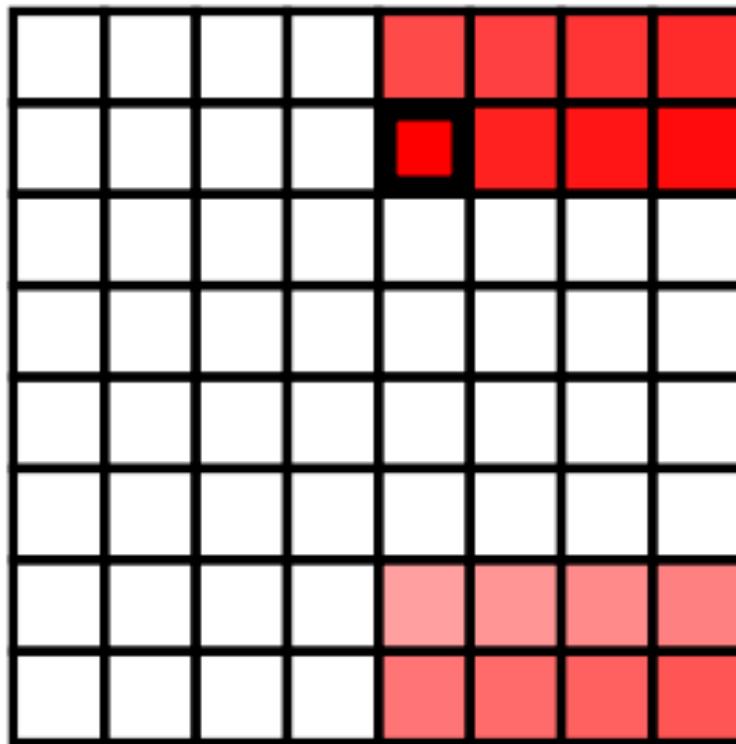


cache hits:965 ≈78%

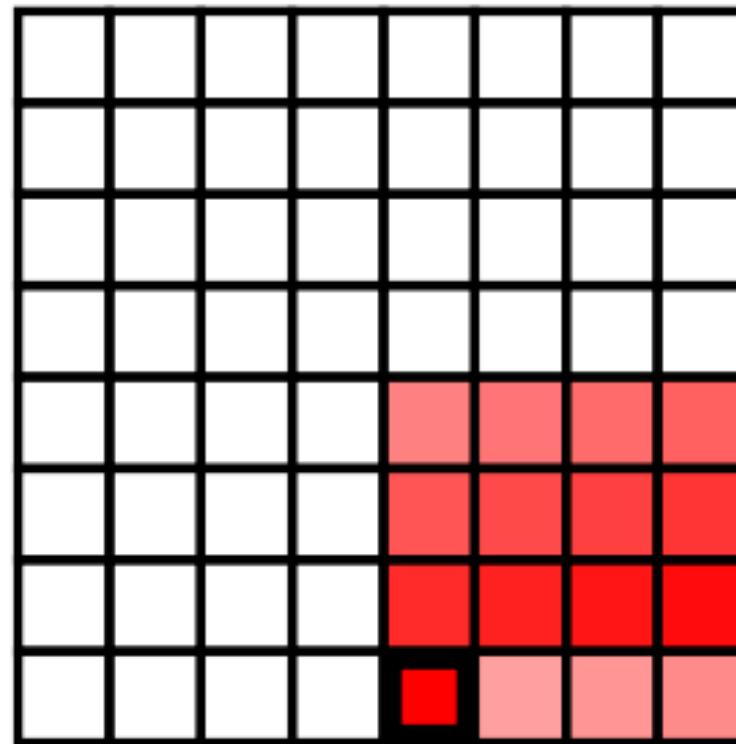
C mem:412 cache hit:309



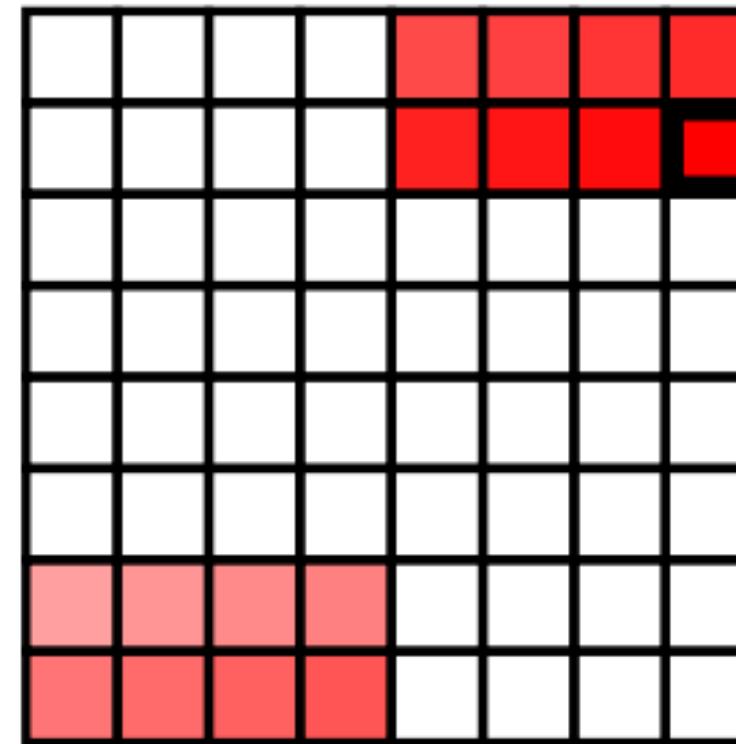
A mem:413 cache hit:309



B mem:413 cache hit:349



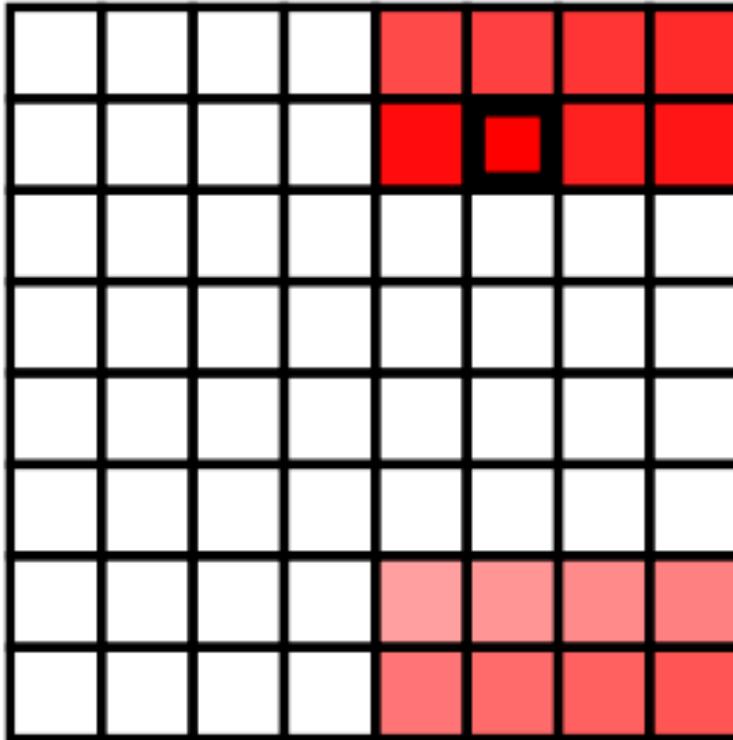
C mem:413 cache hit:309



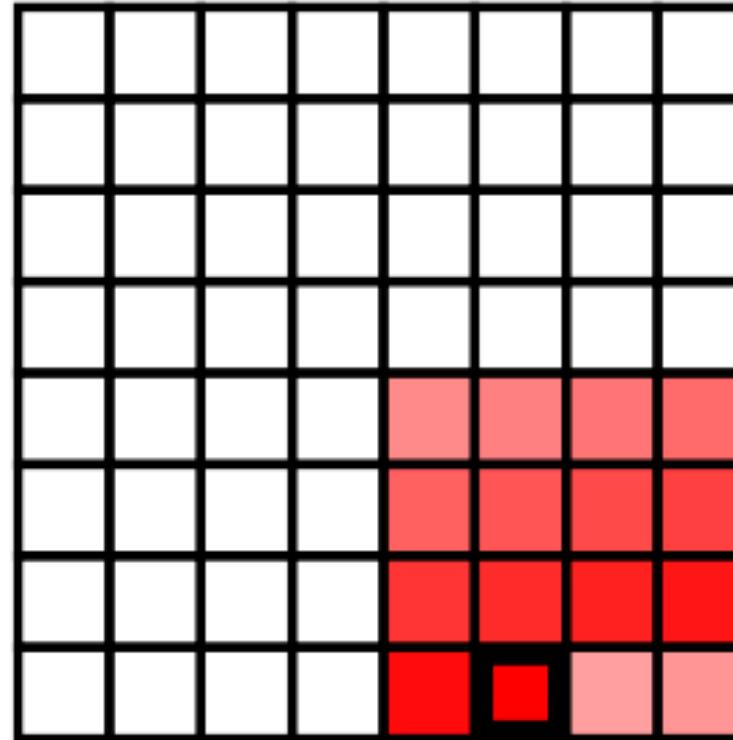
mem:1239

cache hits:967 $\approx 78\%$

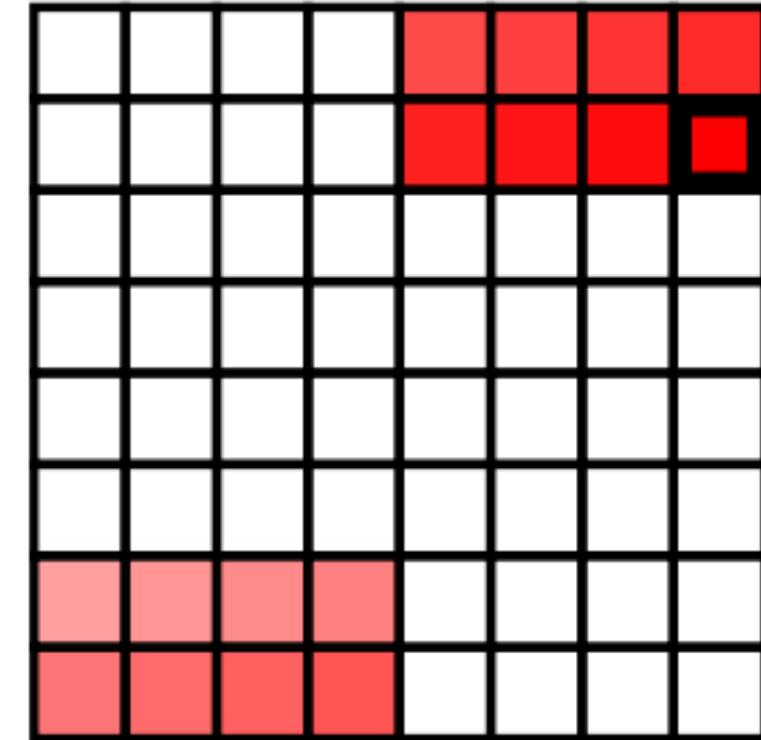
A mem:414 cache hit:310



B mem:414 cache hit:350



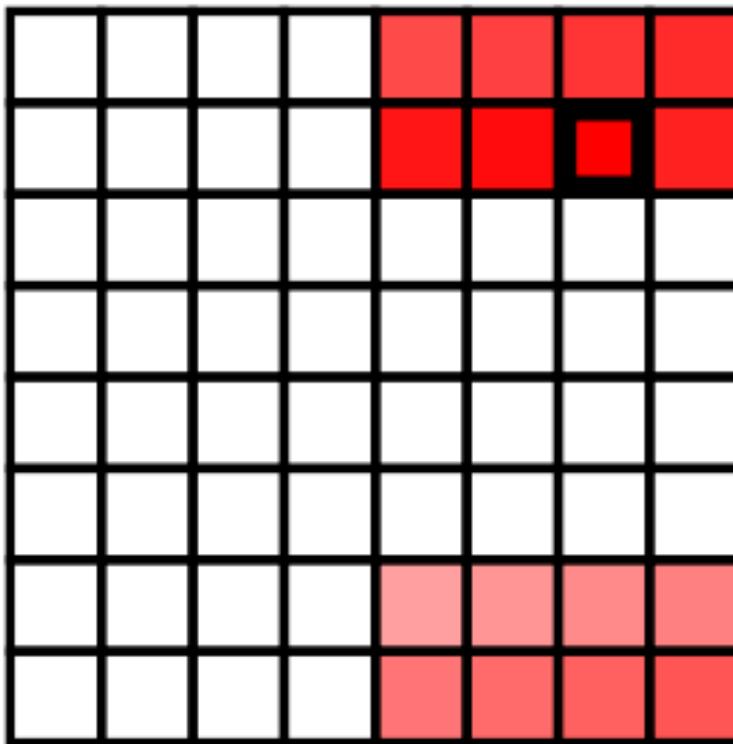
C mem:414 cache hit:310



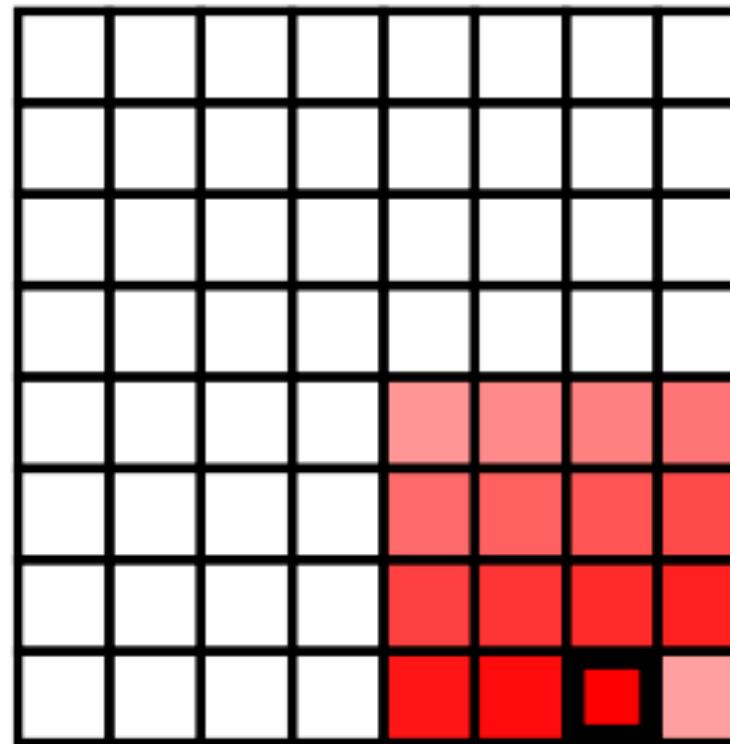
mem:1242

cache hits:970 $\approx 78\%$

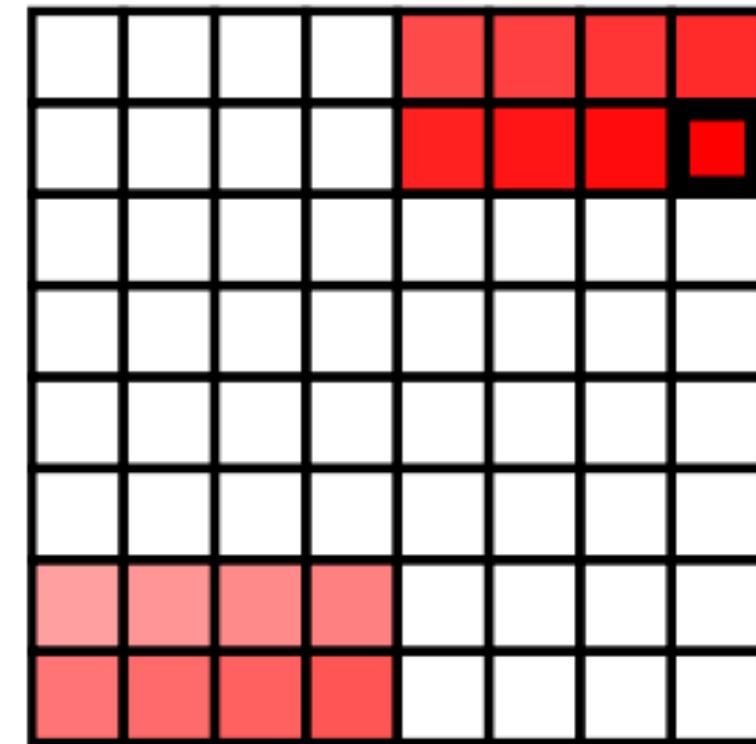
A mem:415 cache hit:311



B mem:415 cache hit:351



C mem:415 cache hit:311



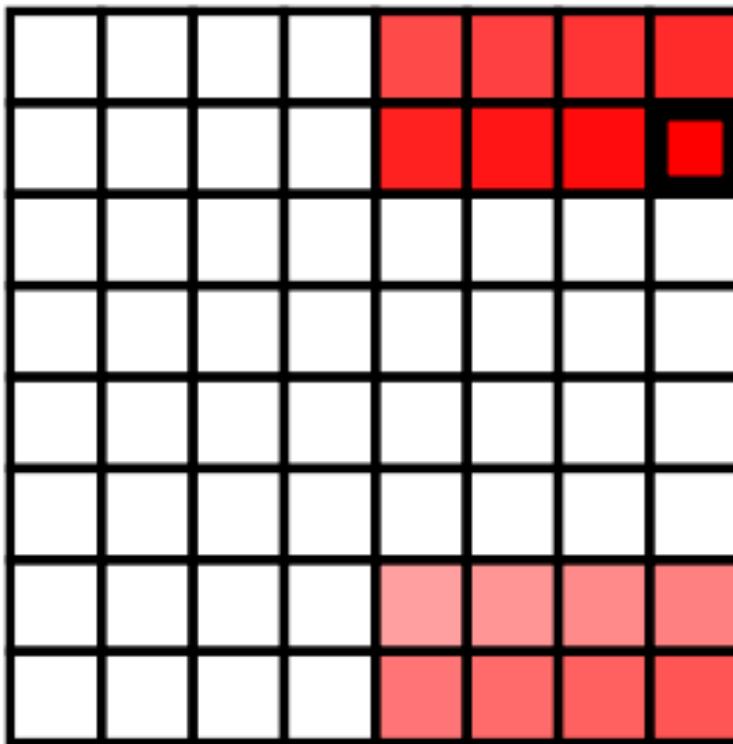
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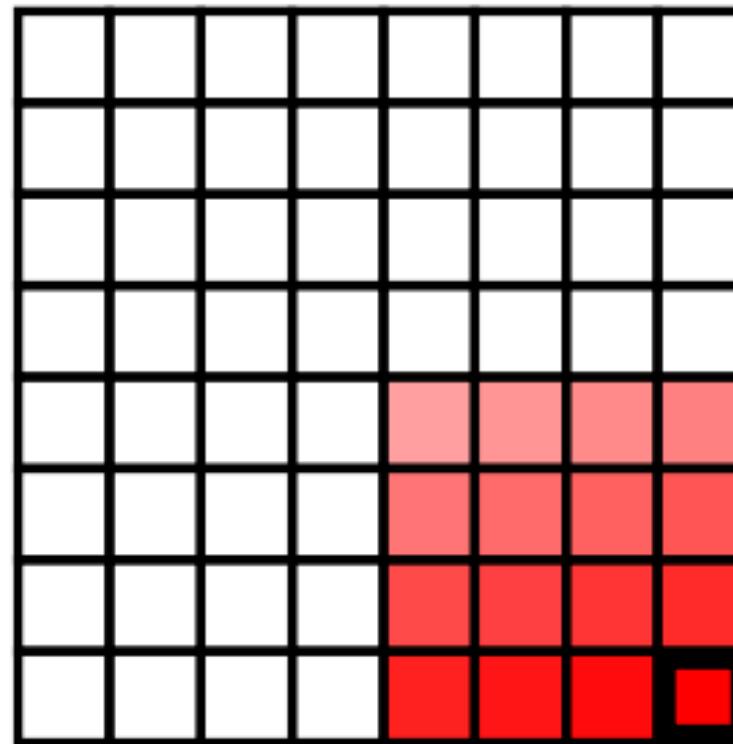
mem:1245

cache hits:973 ≈78%

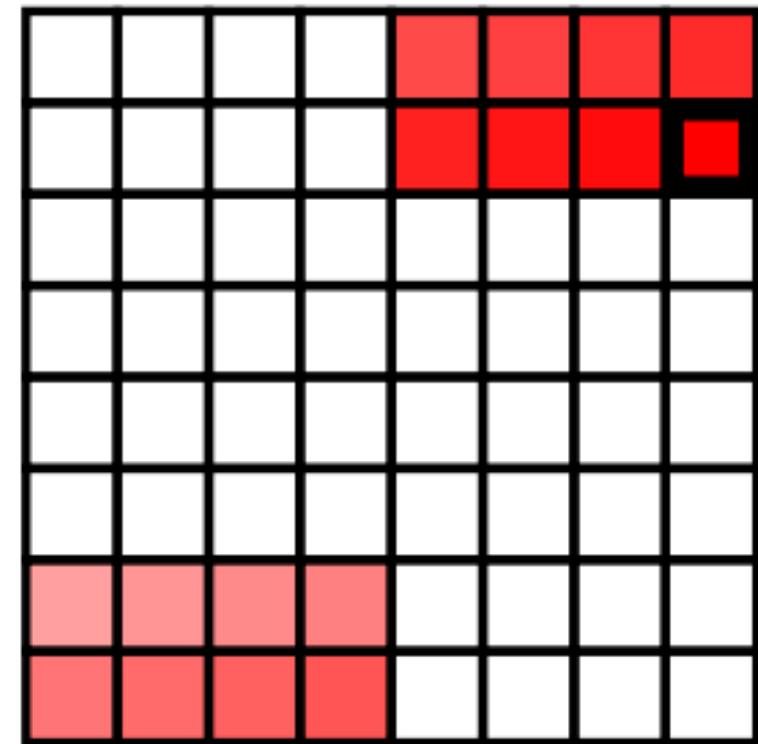
A mem:416 cache hit:312



B mem:416 cache hit:352



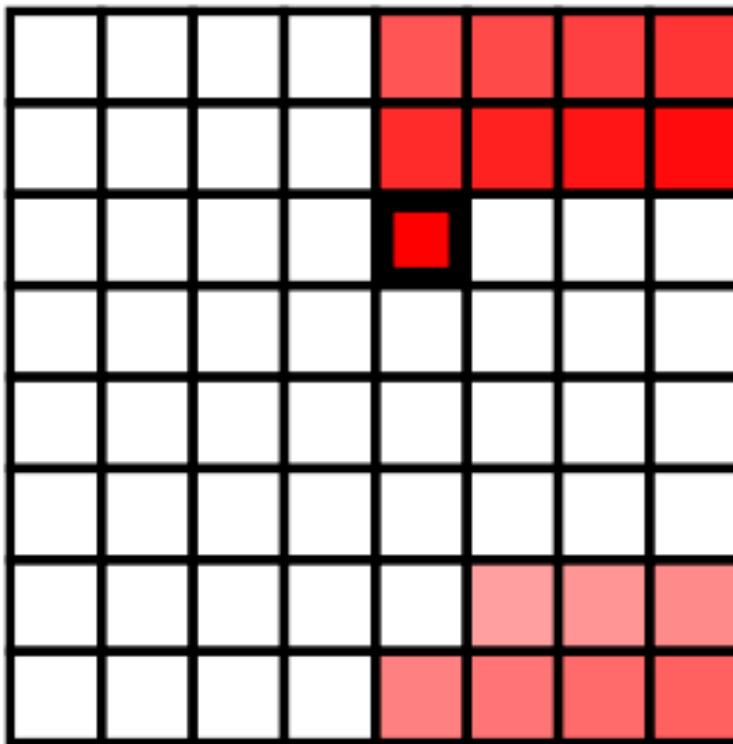
C mem:416 cache hit:312



mem:1248

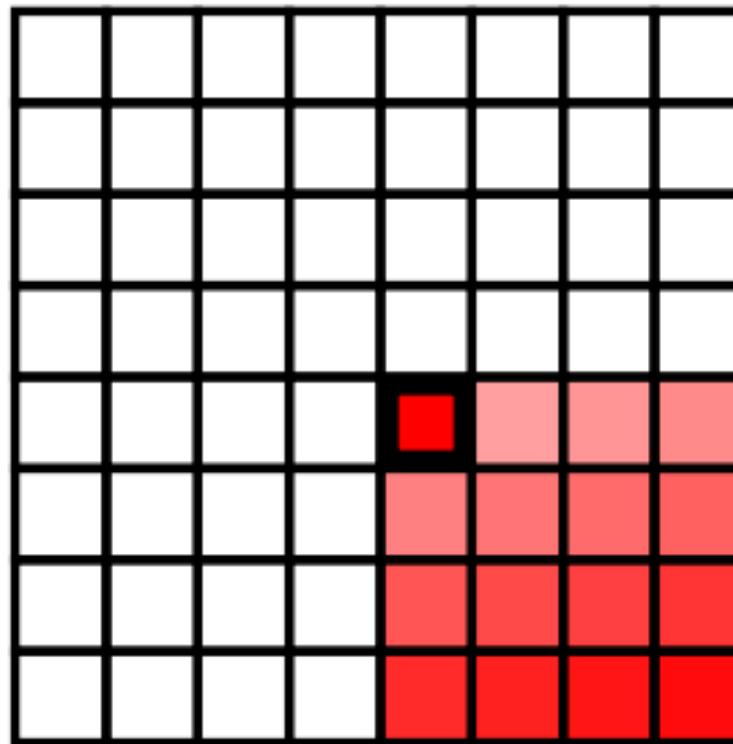
cache hits:976 $\approx 78\%$

A mem:417 cache hit:312



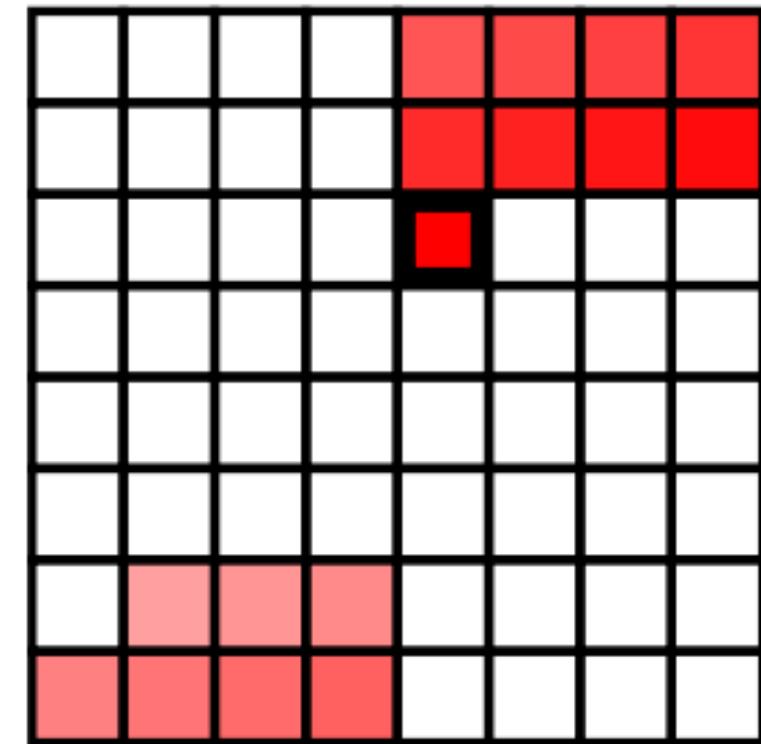
mem:1251

B mem:417 cache hit:353

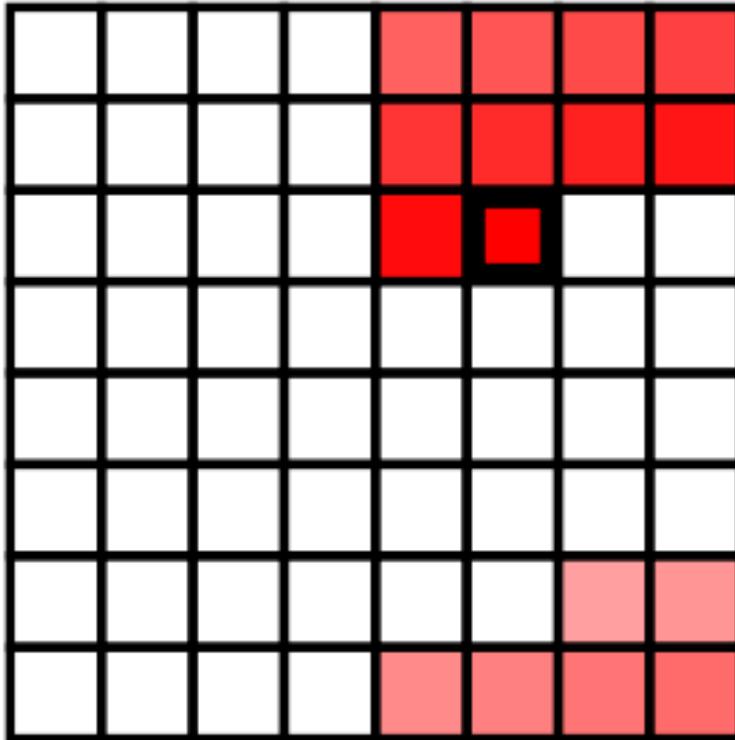


cache hits:977 ≈78%

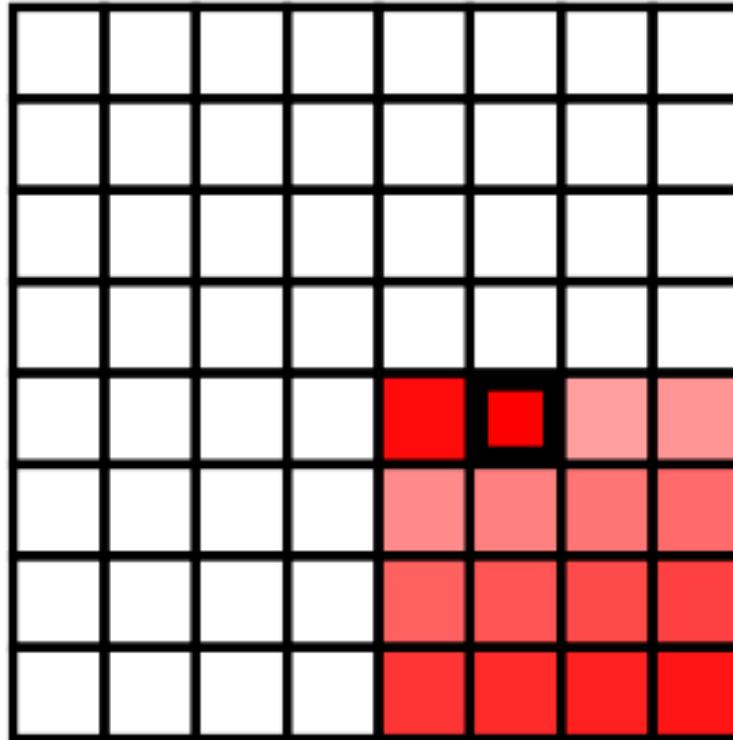
C mem:417 cache hit:312



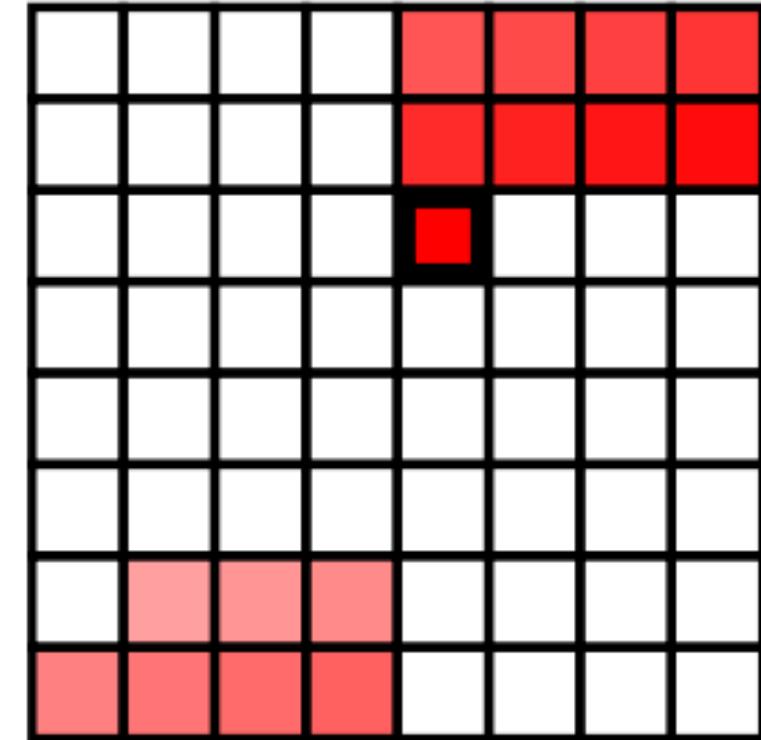
A mem:418 cache hit:312



B mem:418 cache hit:354



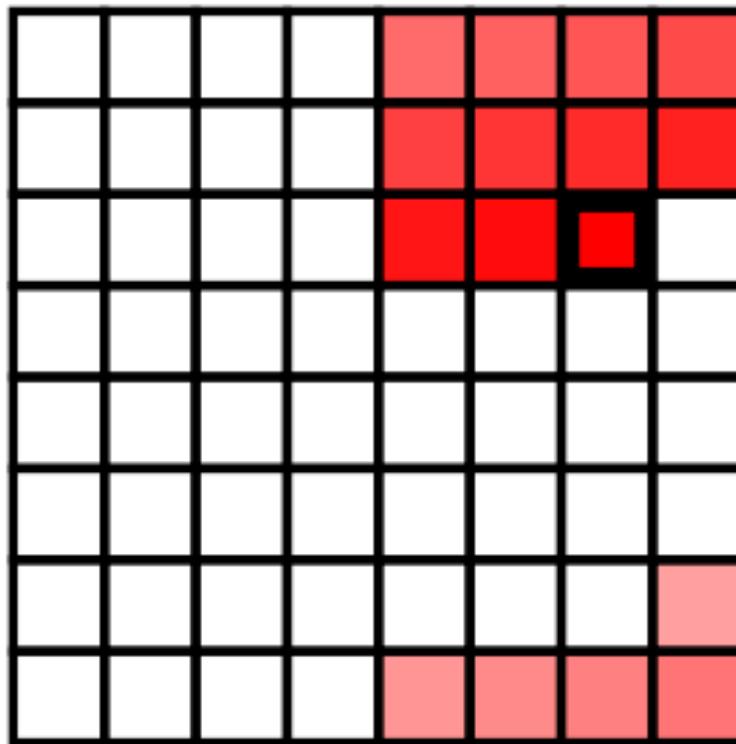
C mem:418 cache hit:313



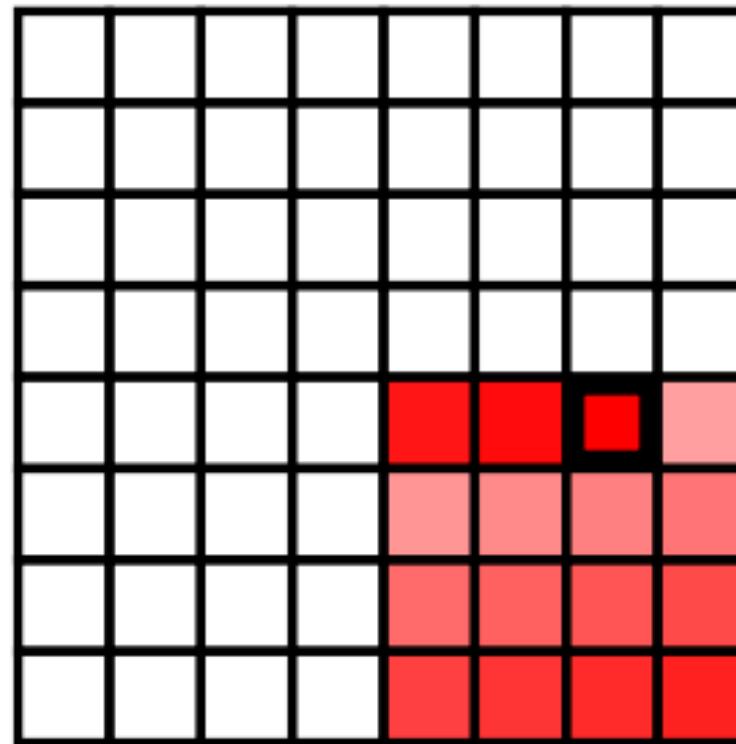
mem:1254

cache hits:979 $\approx 78\%$

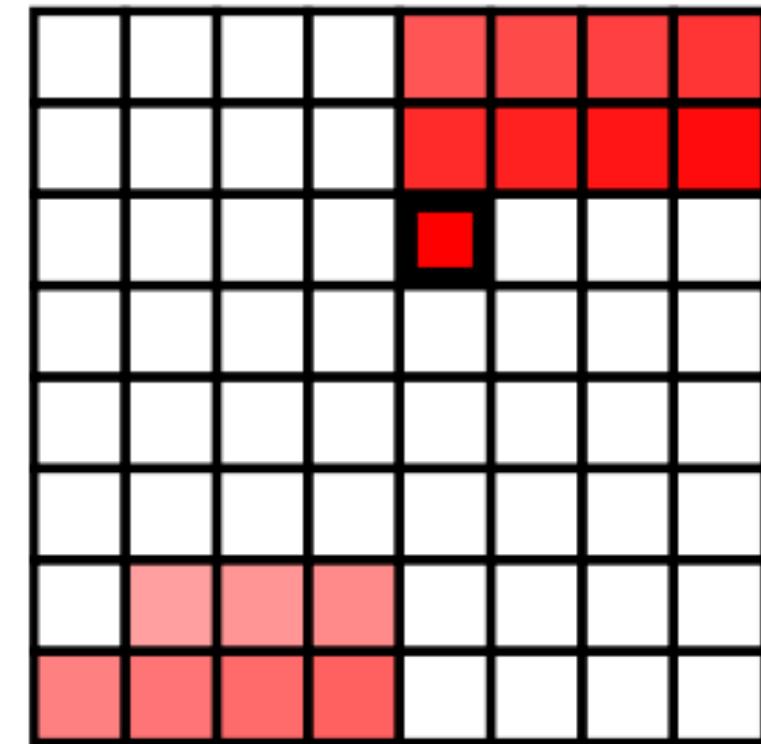
A mem:419 cache hit:312



B mem:419 cache hit:355



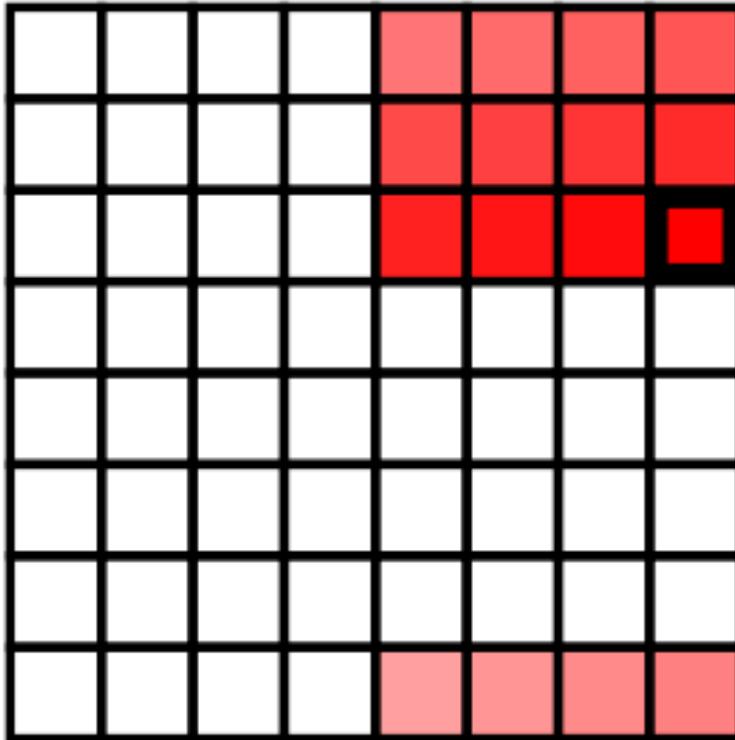
C mem:419 cache hit:314



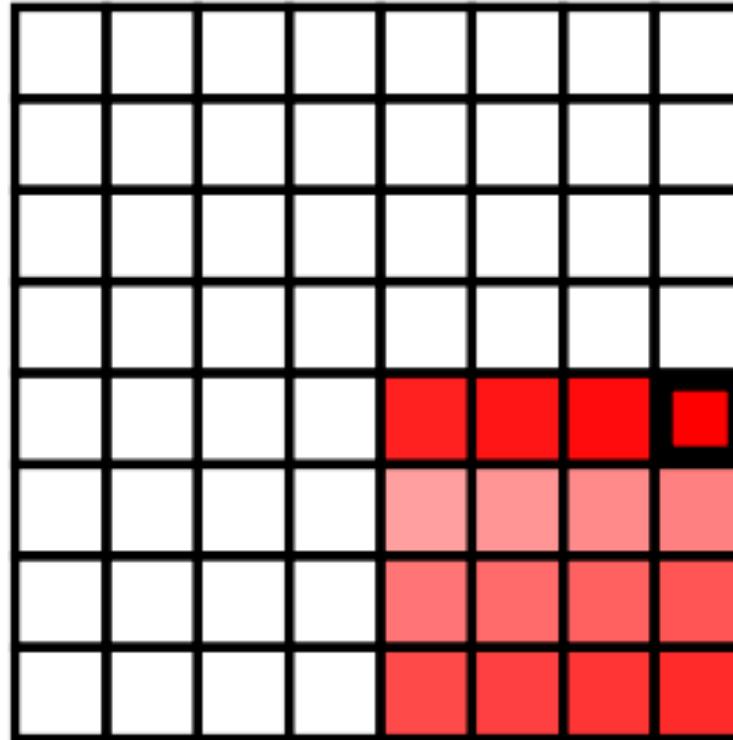
mem:1257

cache hits:981 $\approx 78\%$

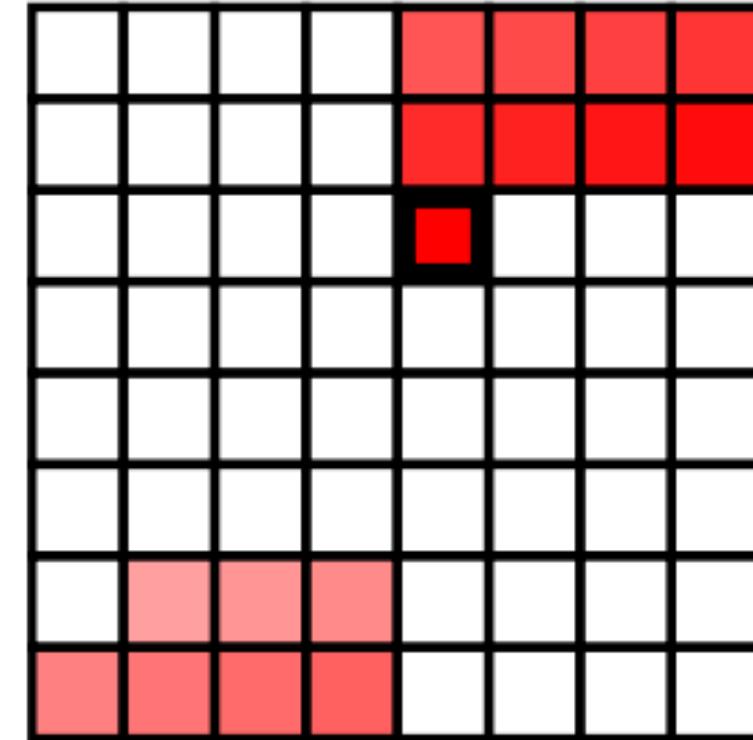
A mem:420 cache hit:312



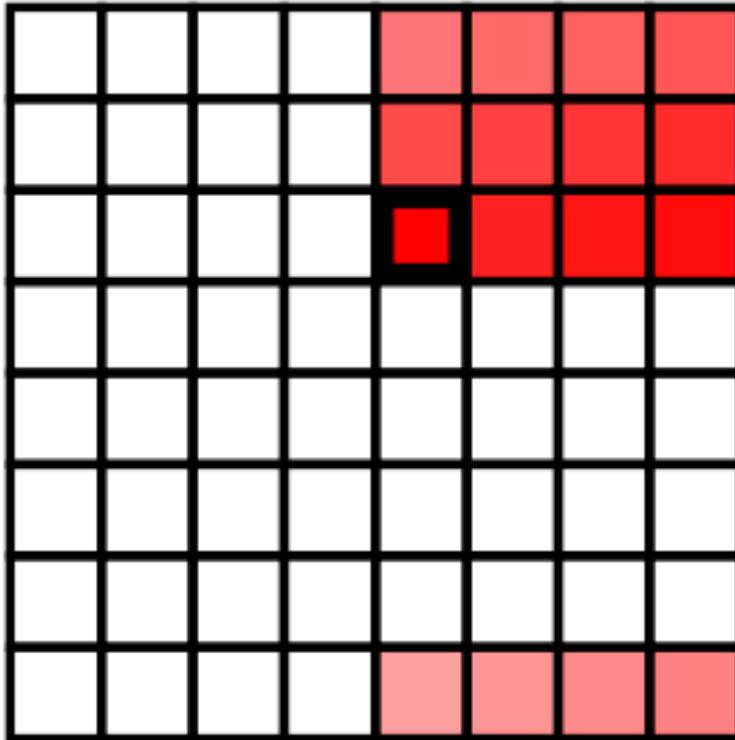
B mem:420 cache hit:356



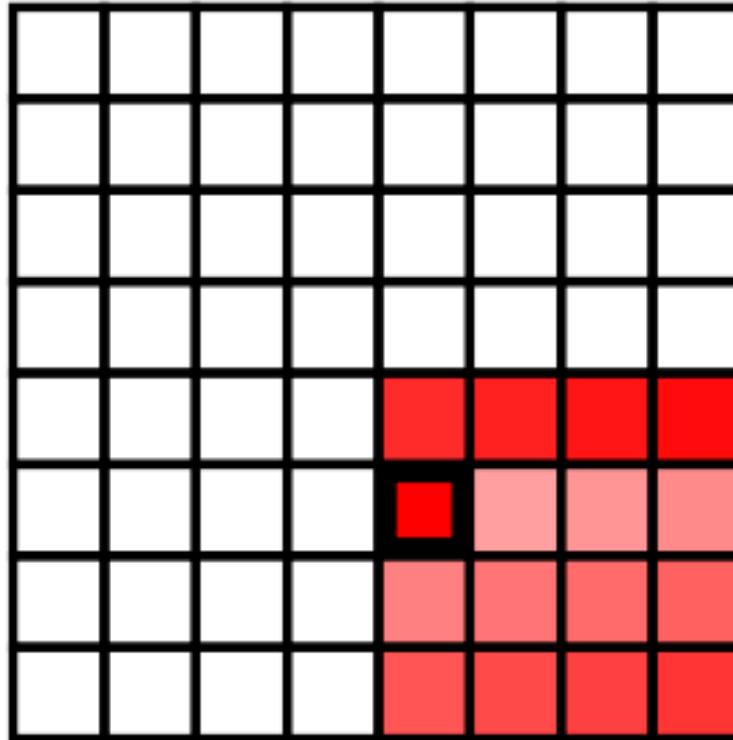
C mem:420 cache hit:315



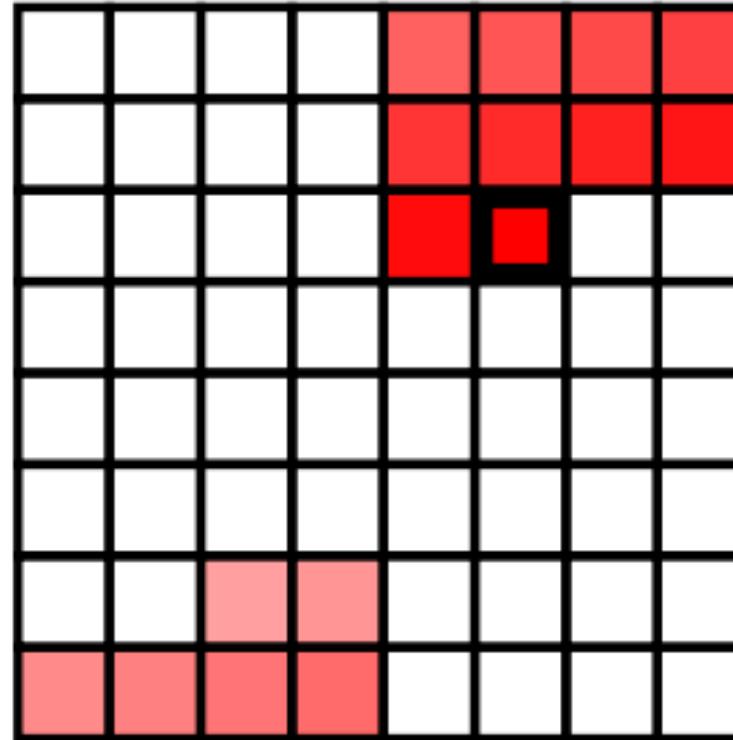
A mem:421 cache hit:313



B mem:421 cache hit:357



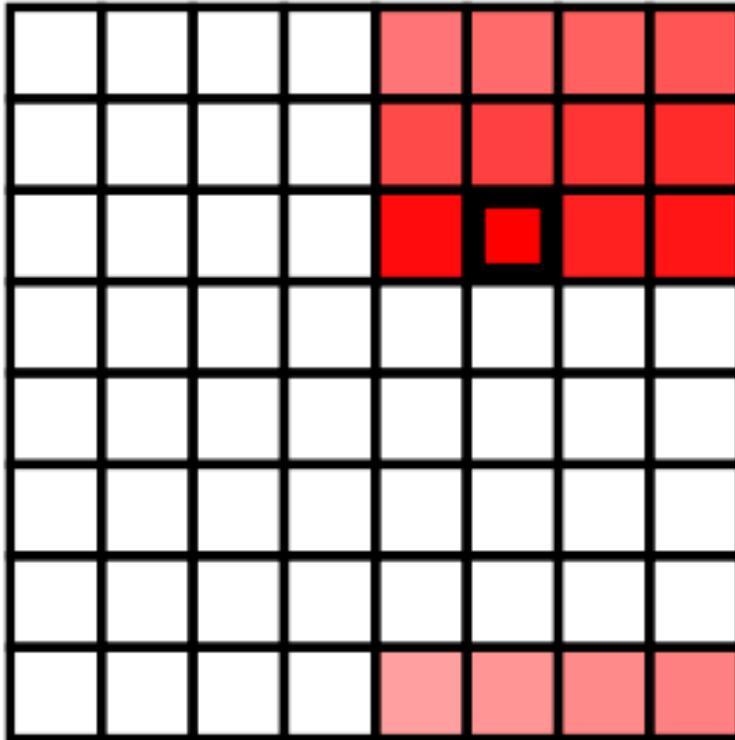
C mem:421 cache hit:315



mem:1263

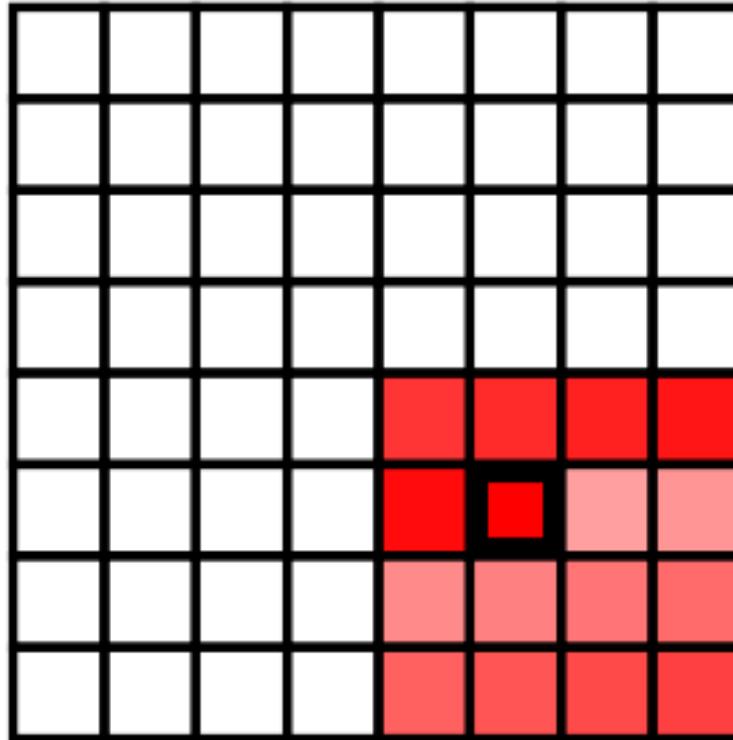
cache hits:985 $\approx 77\%$

A mem:422 cache hit:314



mem:1266

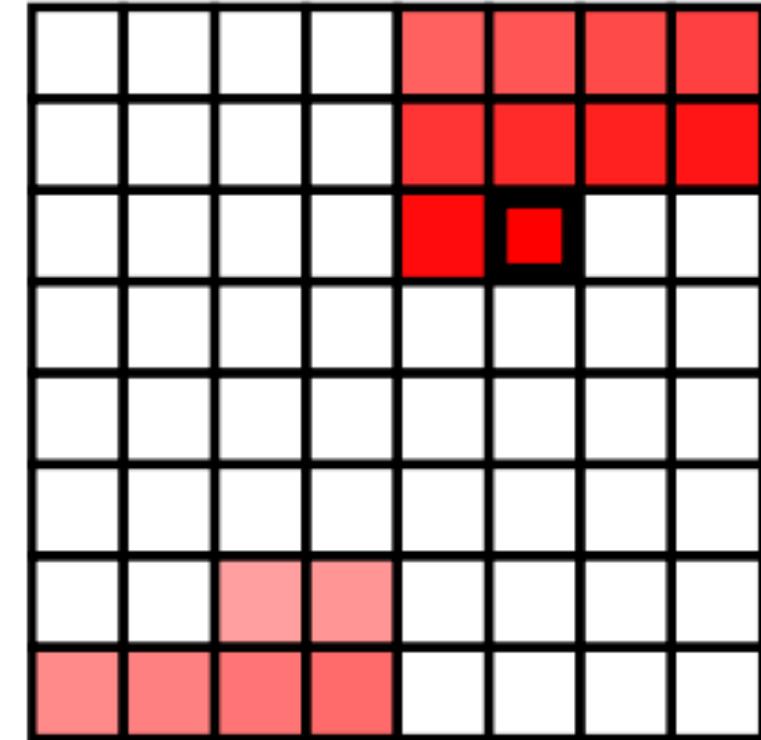
B mem:422 cache hit:358



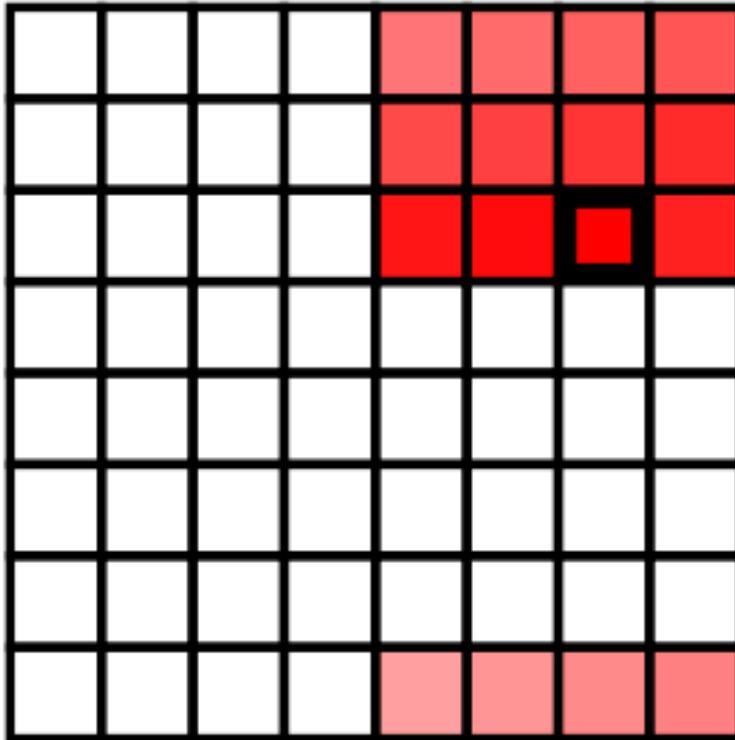
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cache hits:988 ≈78%

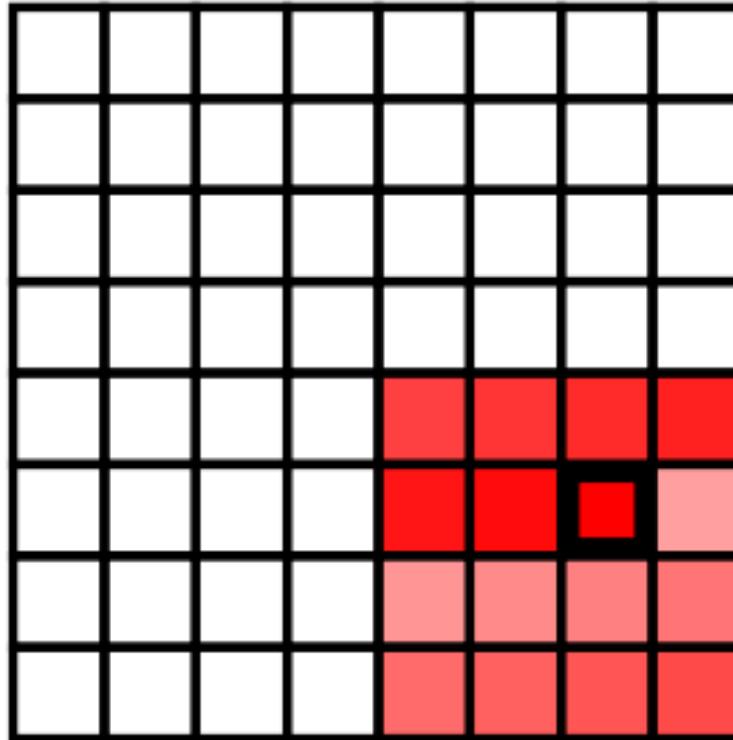
C mem:422 cache hit:316



A mem:423 cache hit:315



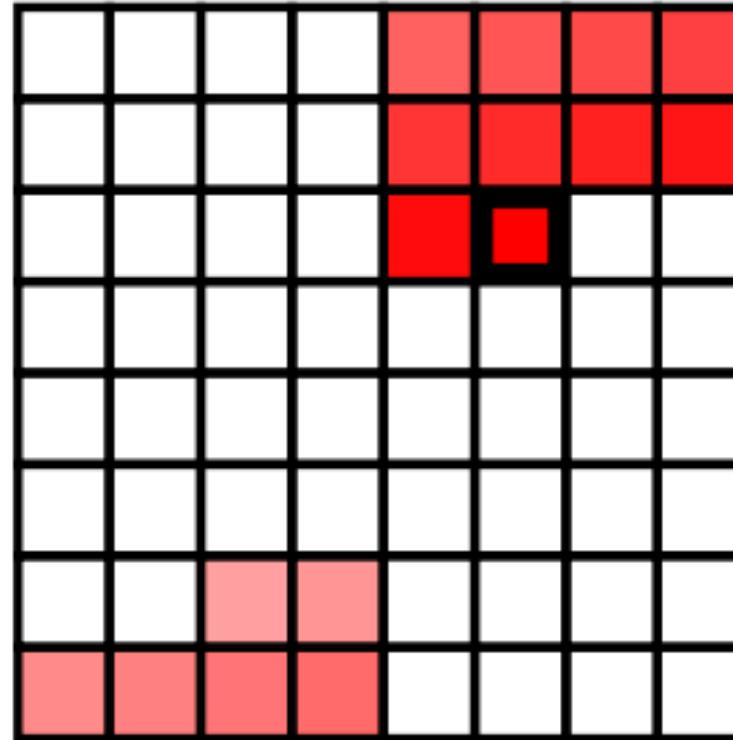
B mem:423 cache hit:359



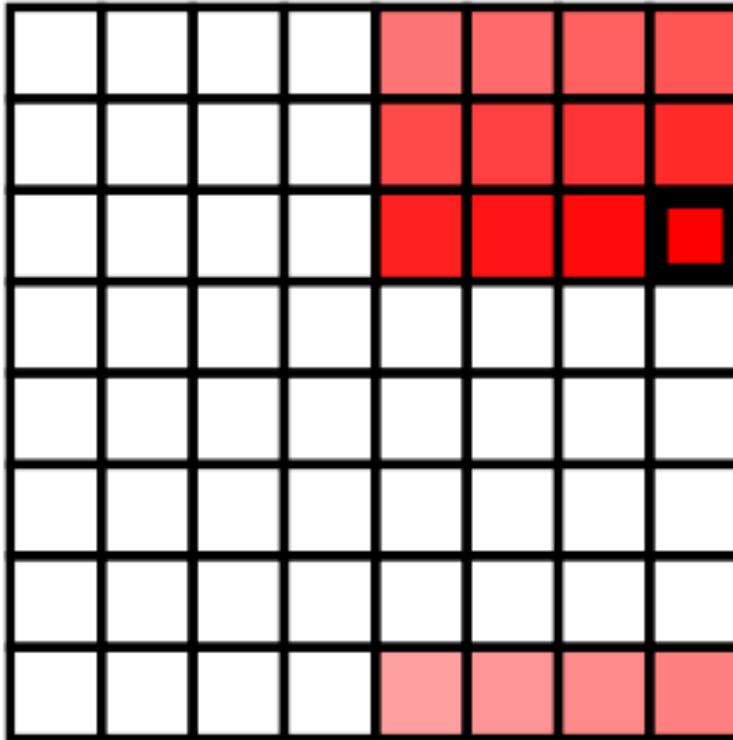
mem:1269

cache hits:991 $\approx 78\%$

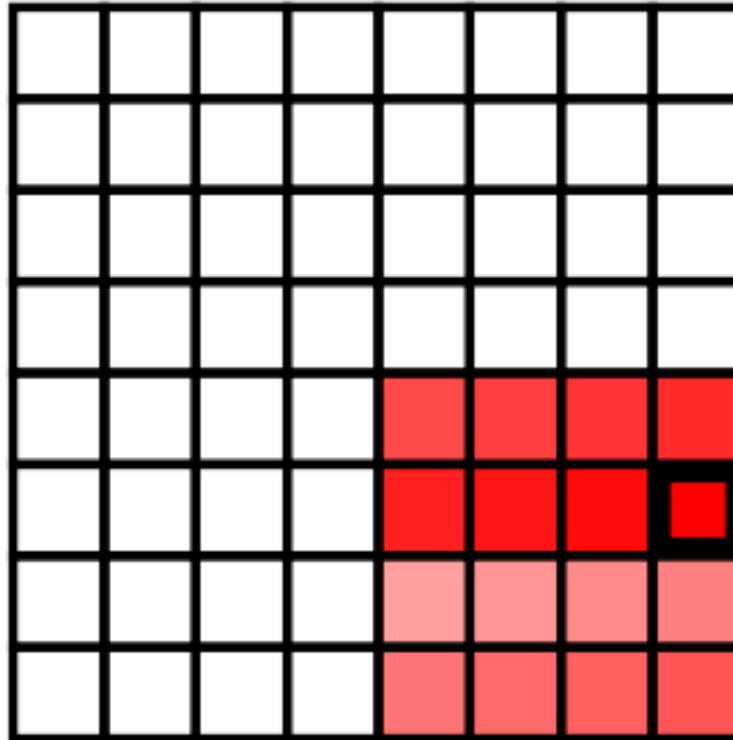
C mem:423 cache hit:317



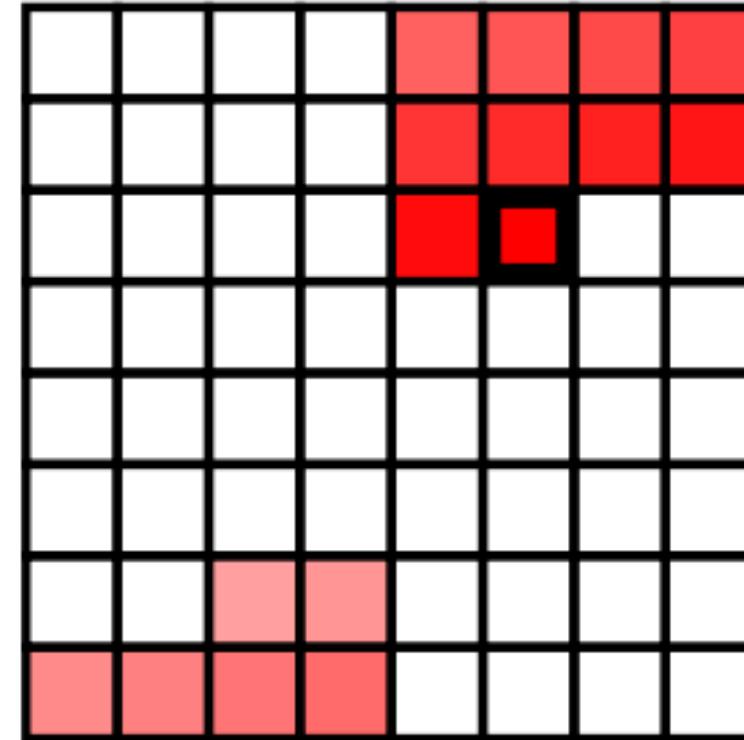
A mem:424 cache hit:316



B mem:424 cache hit:360



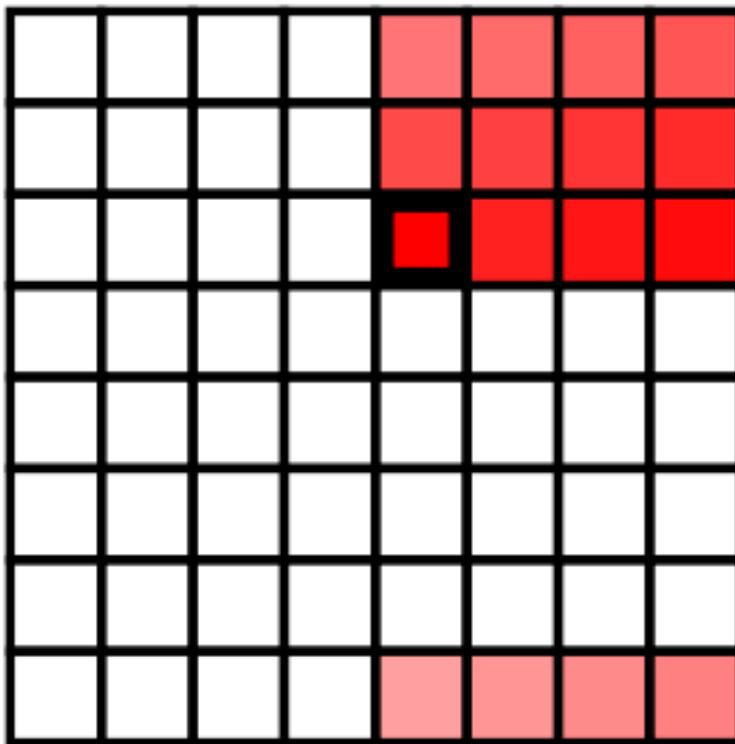
C mem:424 cache hit:318



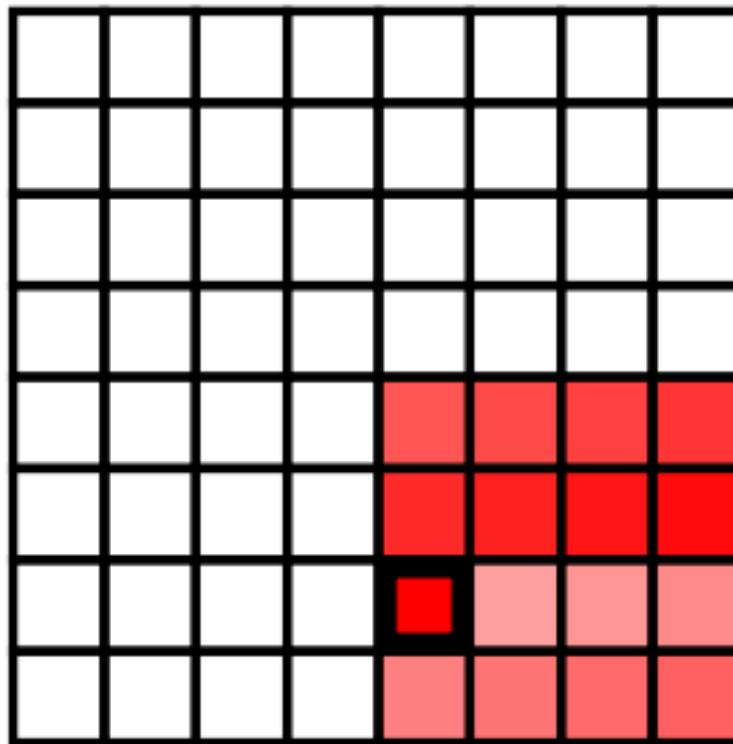
mem:1272

cache hits:994 $\approx 78\%$

A mem:425 cache hit:317

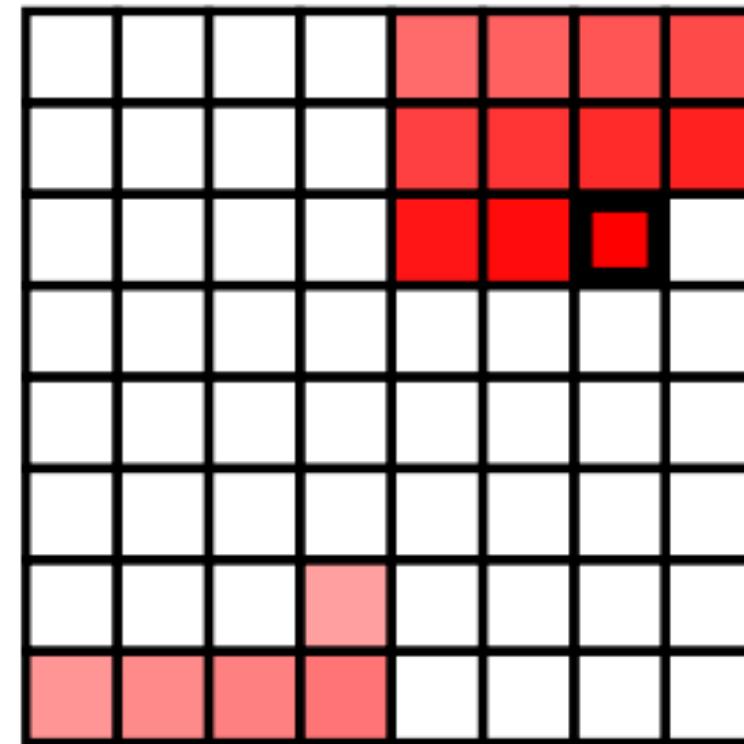


B mem:425 cache hit:361



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C mem:425 cache hit:318

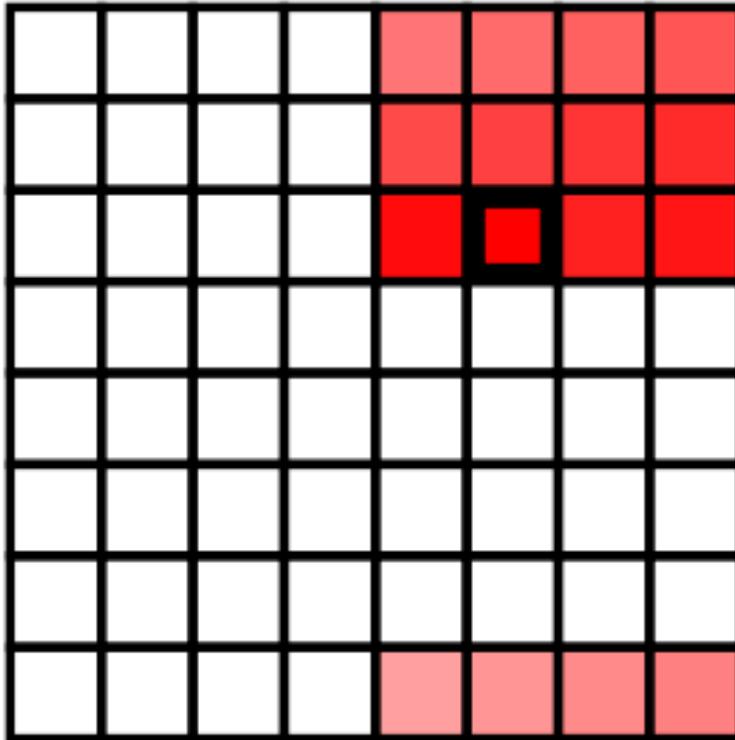


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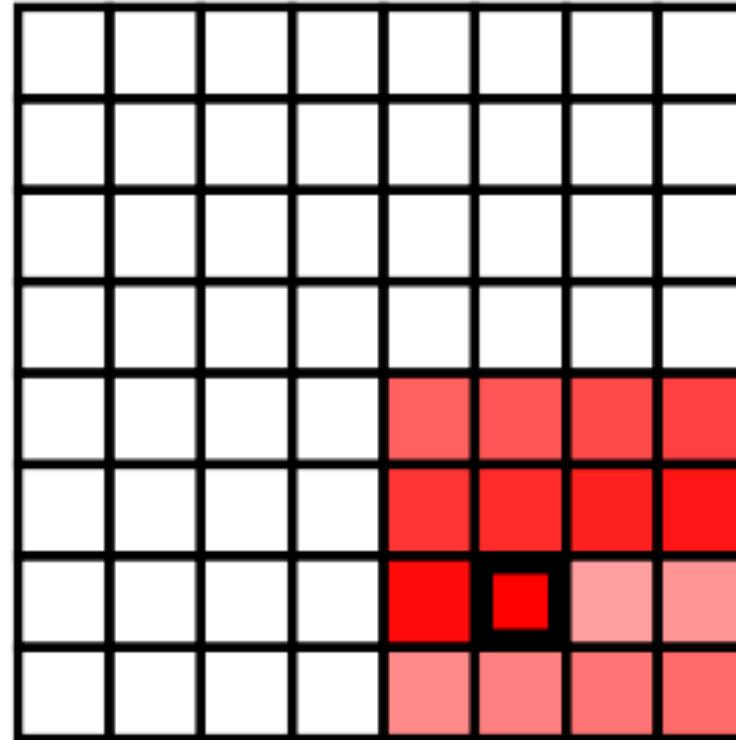
mem:1275

cache hits:996 $\approx 78\%$

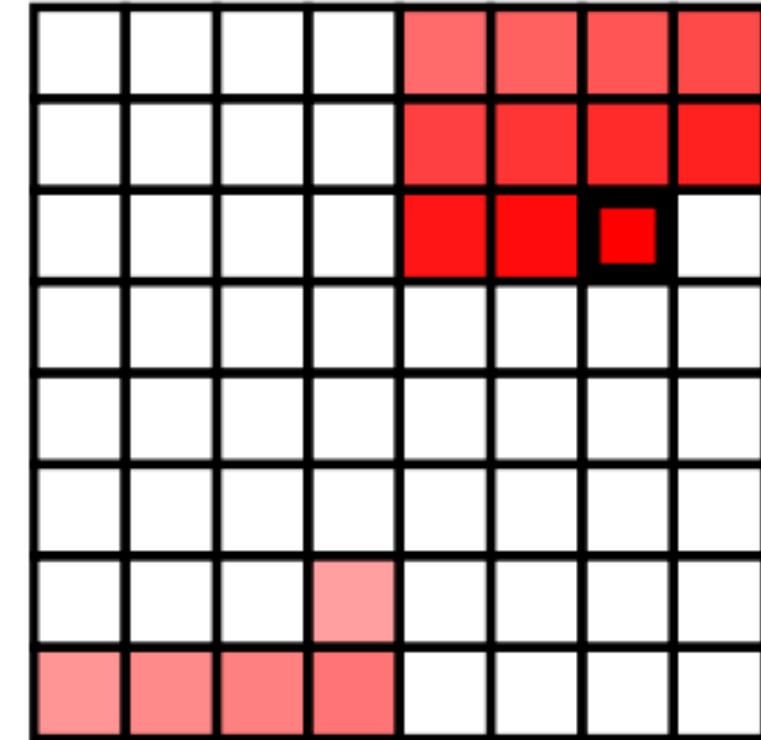
A mem:426 cache hit:318



B mem:426 cache hit:362



C mem:426 cache hit:319



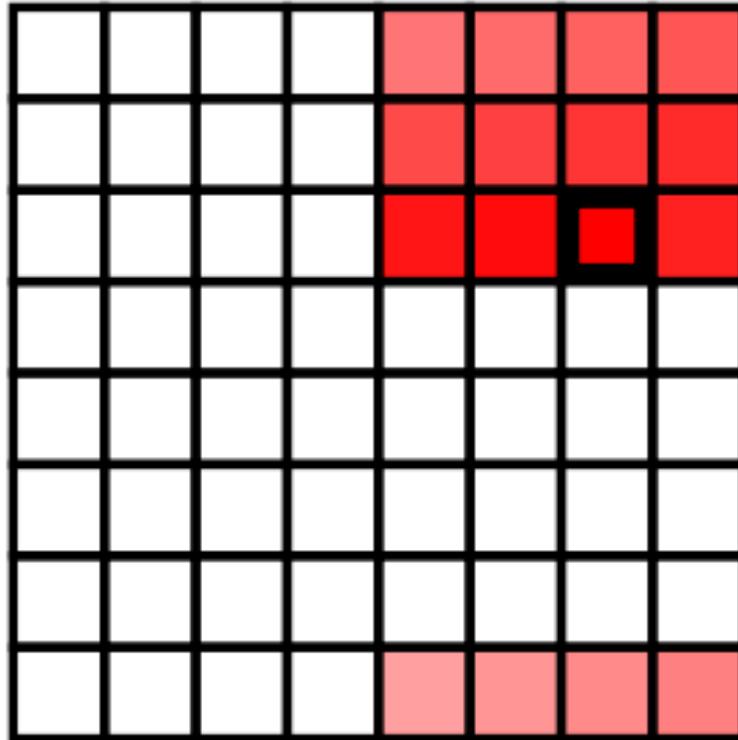
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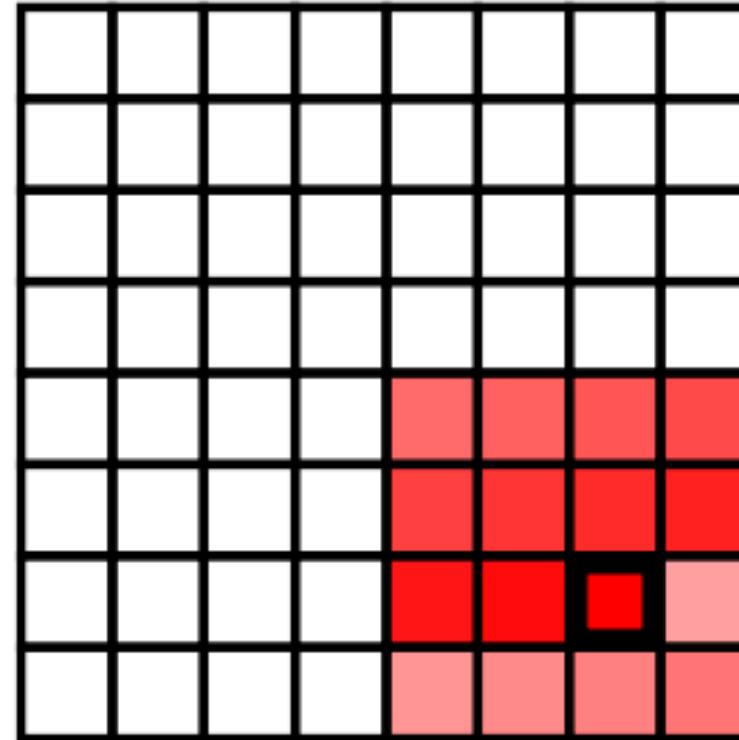
mem:1278

cache hits:999 $\approx 78\%$

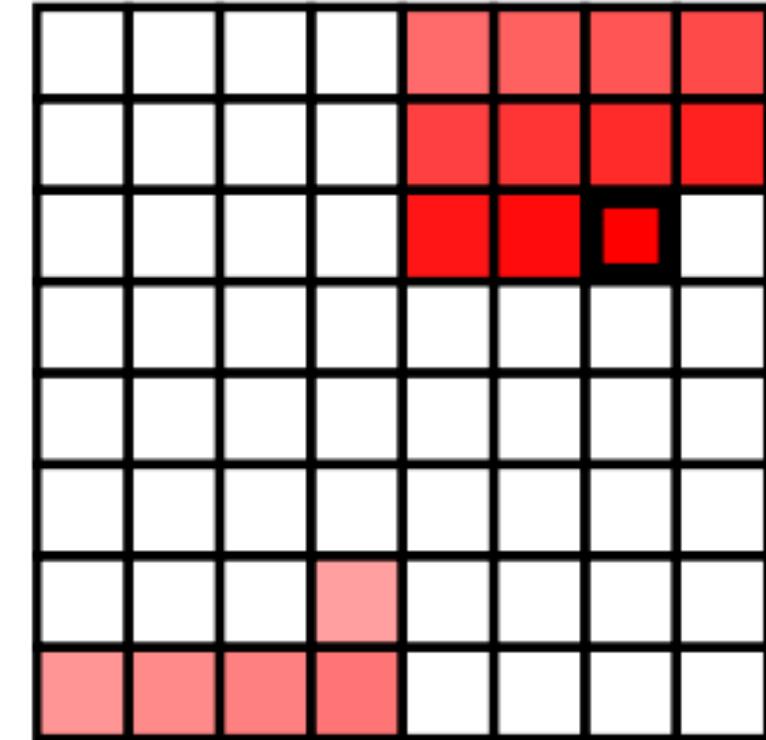
A mem:427 cache hit:319



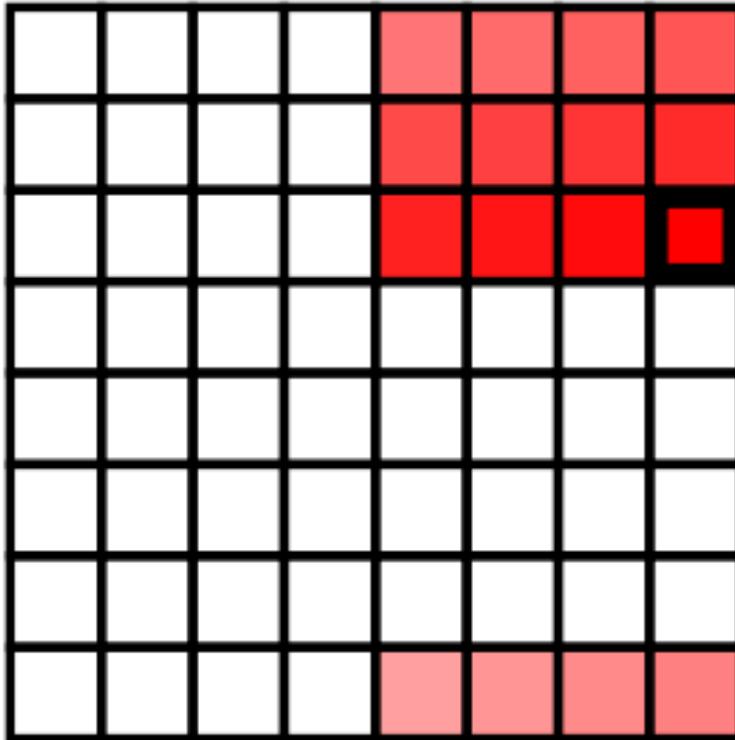
B mem:427 cache hit:363



C mem:427 cache hit:320

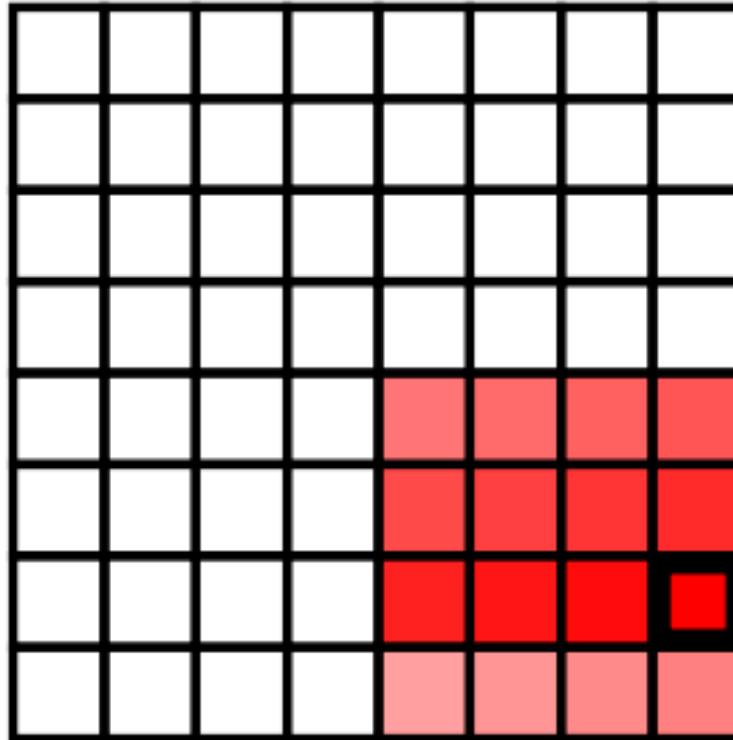


A mem:428 cache hit:320



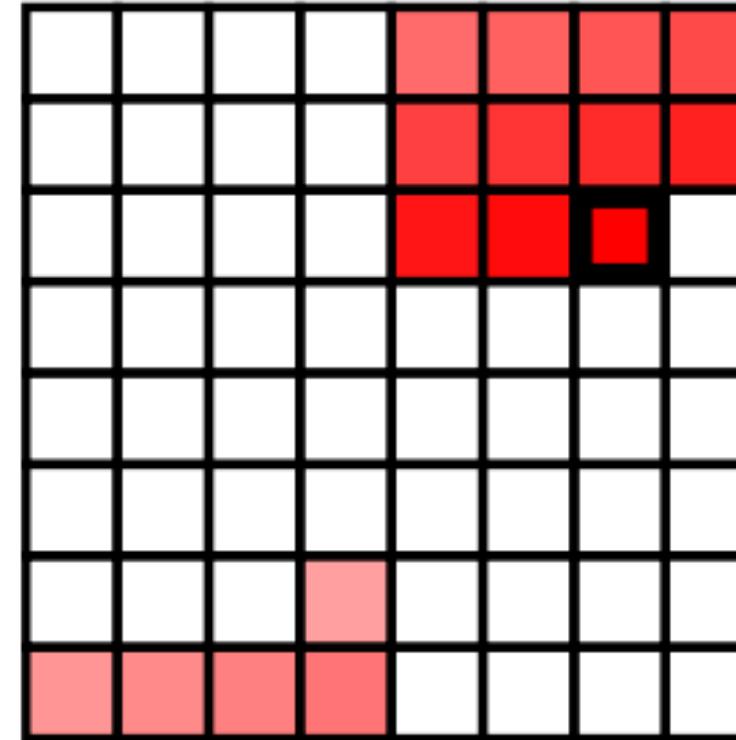
mem:1284

B mem:428 cache hit:364

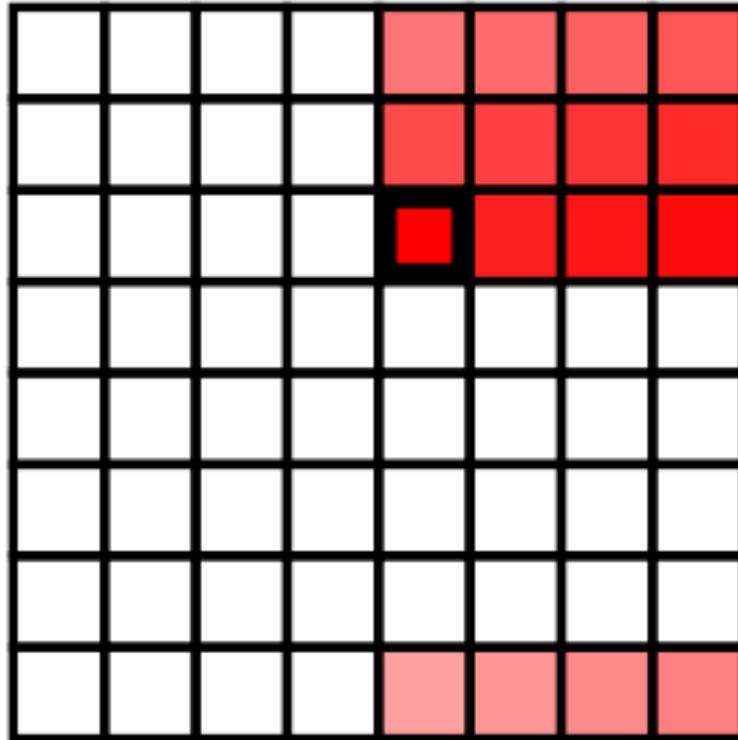


cache hits:1005 \approx 78%

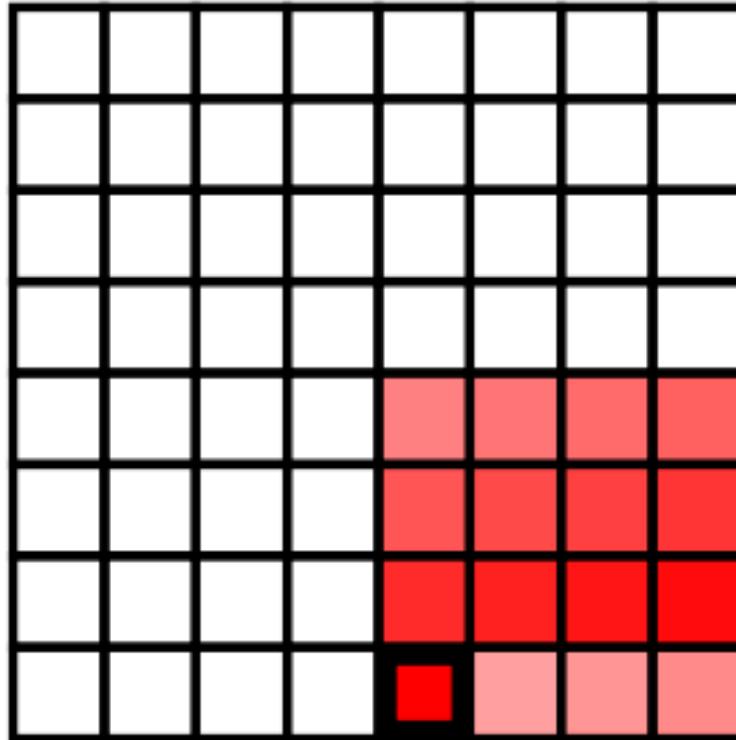
C mem:428 cache hit:321



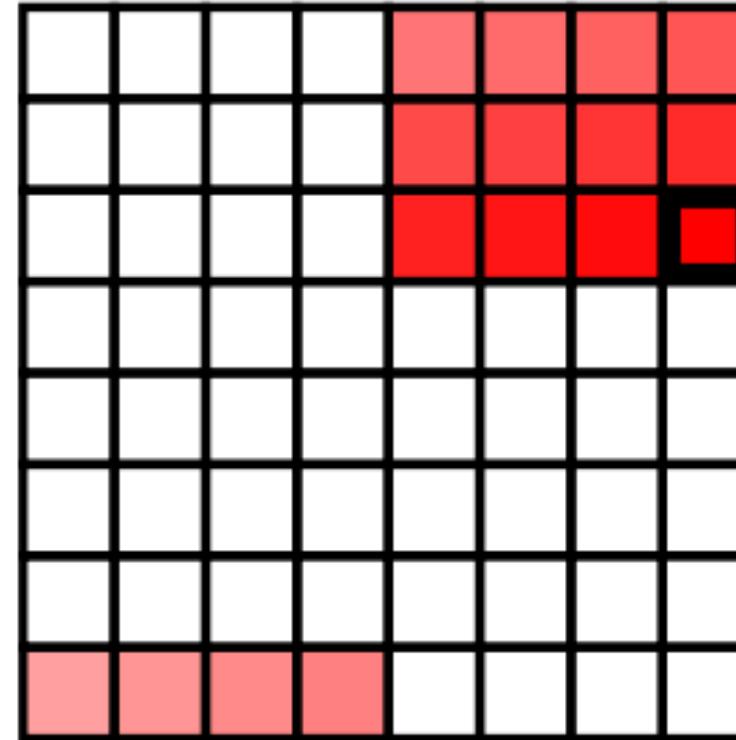
A mem:429 cache hit:321



B mem:429 cache hit:365



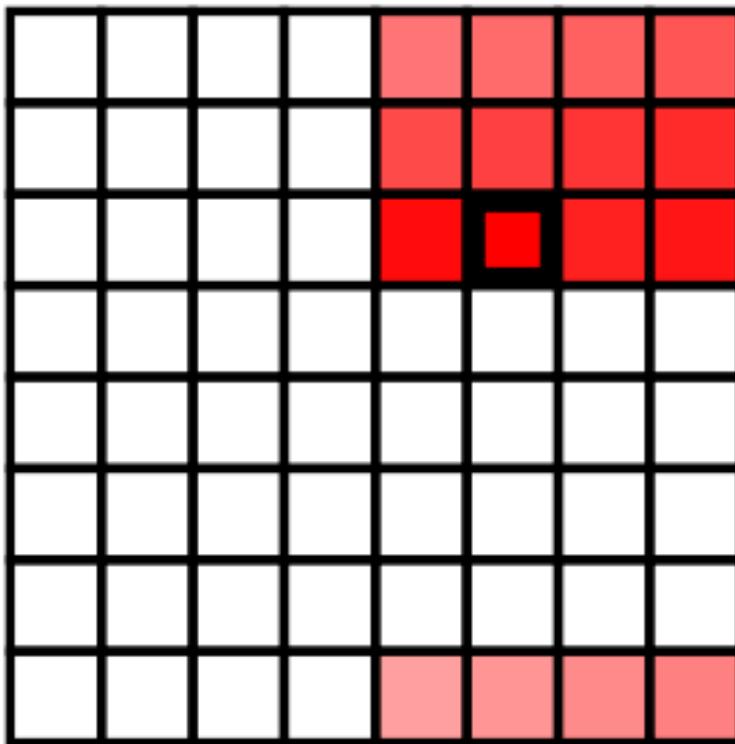
C mem:429 cache hit:321



mem:1287

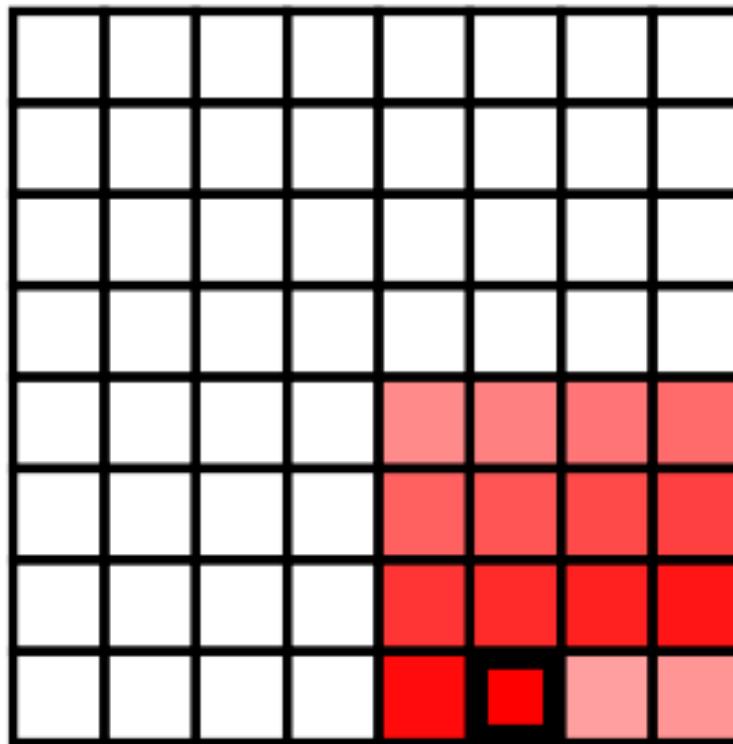
cache hits:1007 \approx 78%

A mem:430 cache hit:322



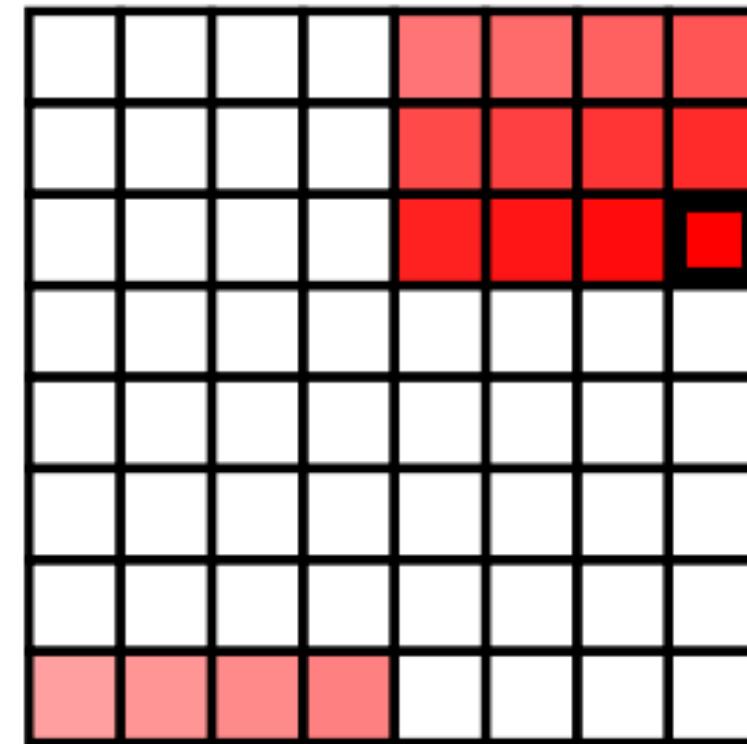
mem:1290

B mem:430 cache hit:366

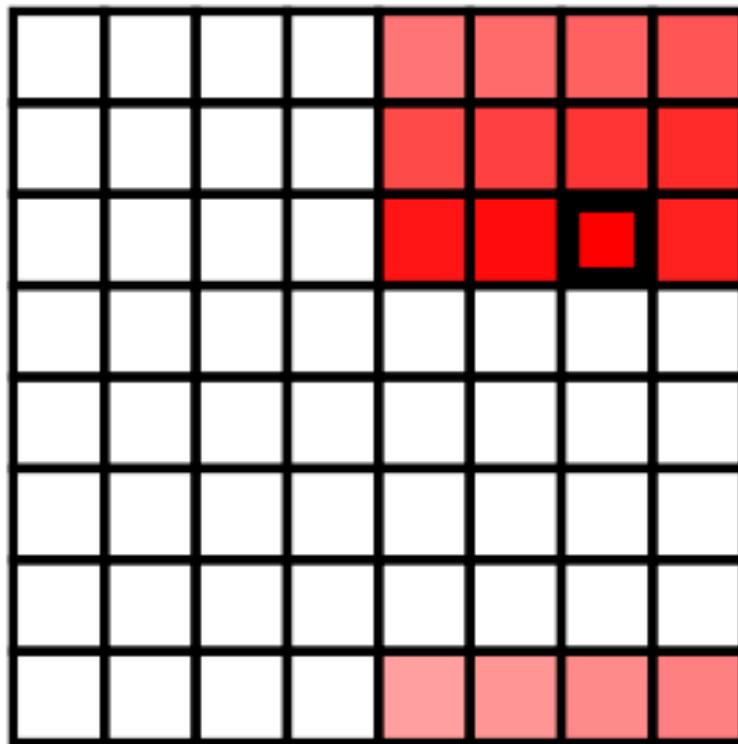


cache hits:1010 \approx 78%

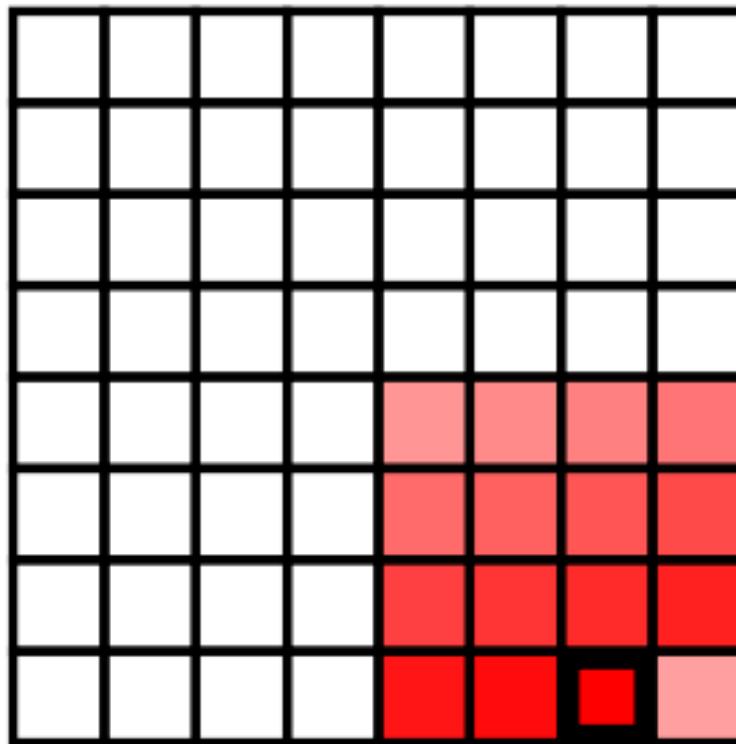
C mem:430 cache hit:322



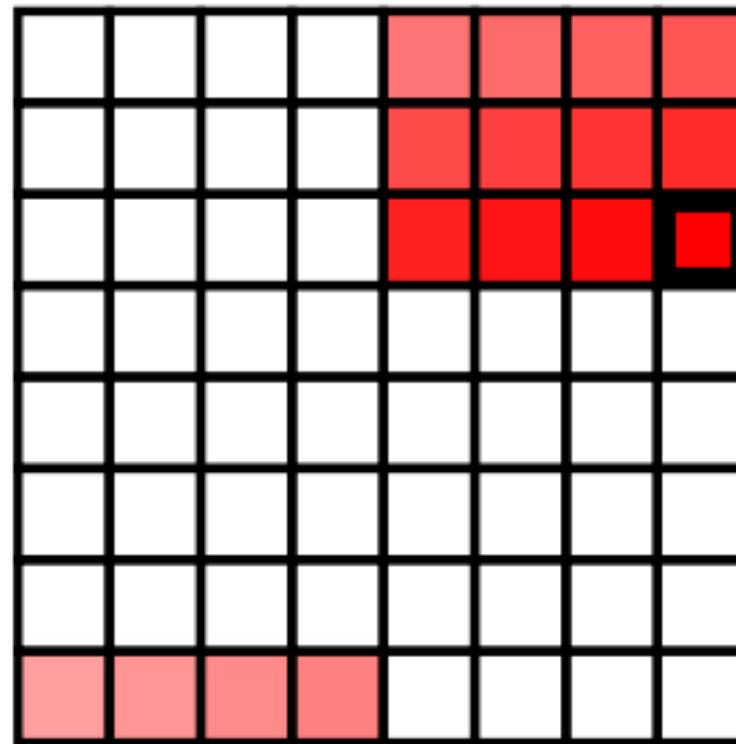
A mem:431 cache hit:323



B mem:431 cache hit:367



C mem:431 cache hit:323



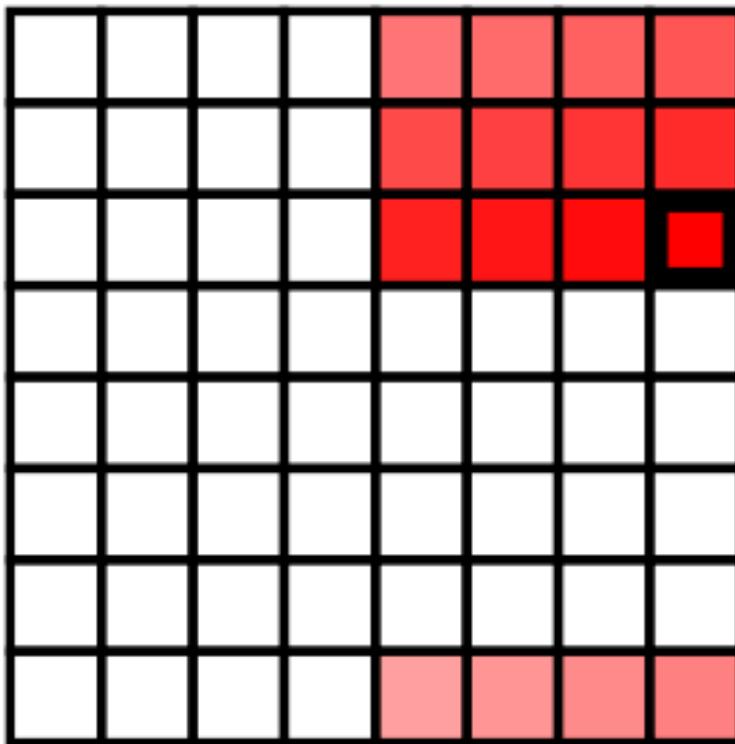
mem:1293

cache hits:1013 \approx 78%

\times

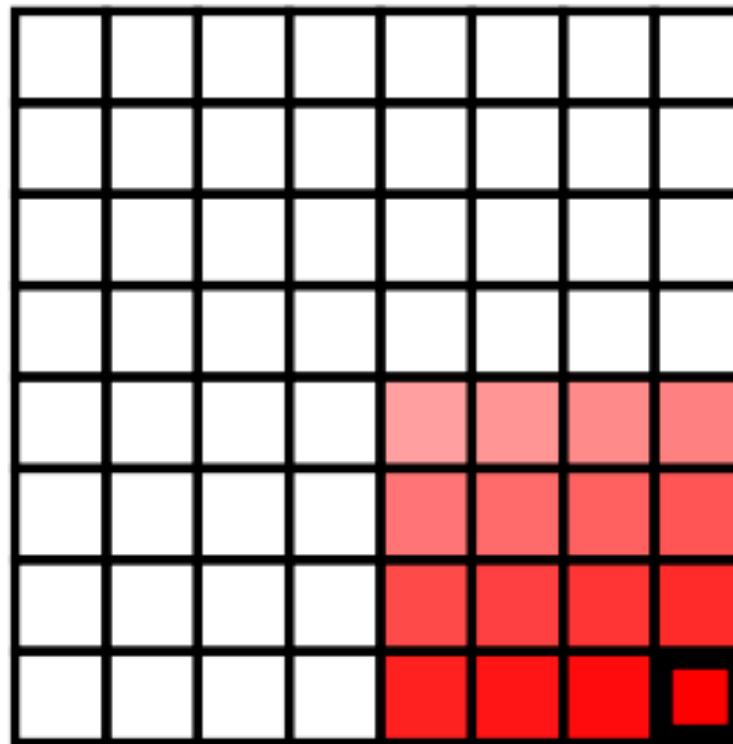
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A mem:432 cache hit:324



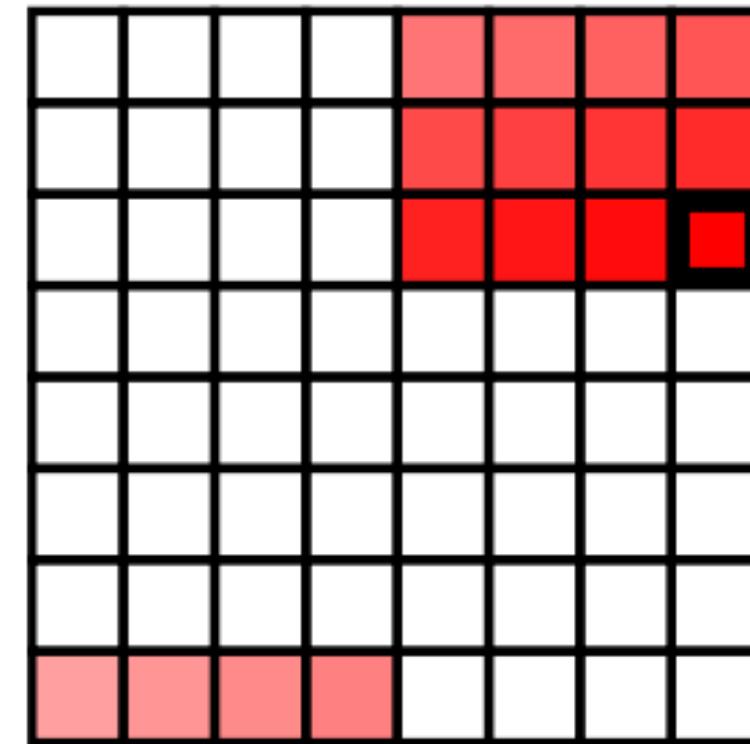
mem:1296

B mem:432 cache hit:368

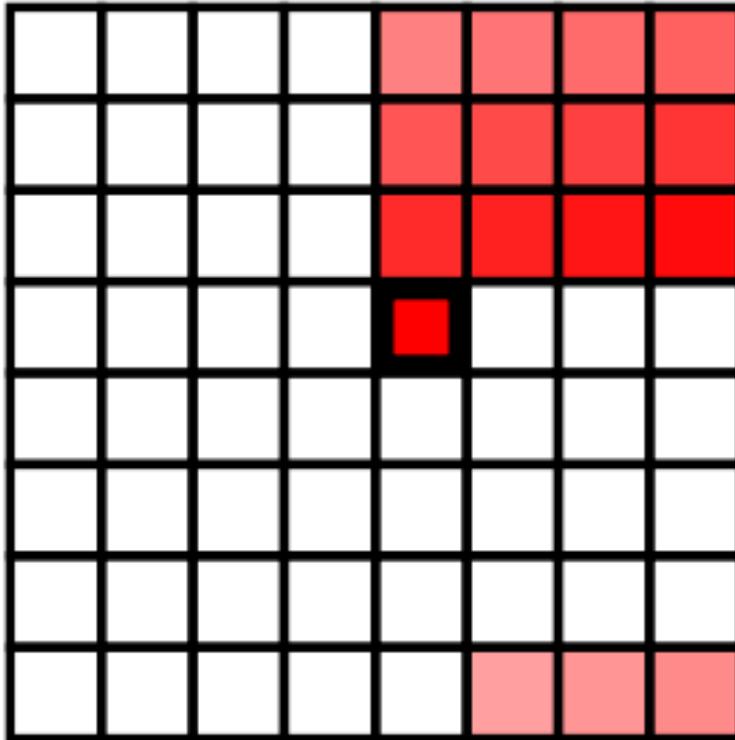


cache hits:1016 \approx 78%

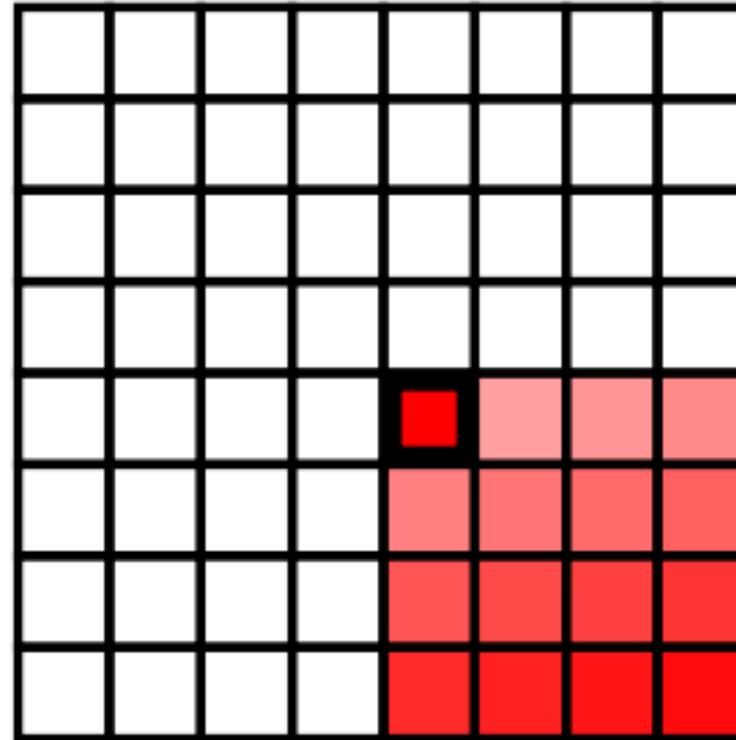
C mem:432 cache hit:324



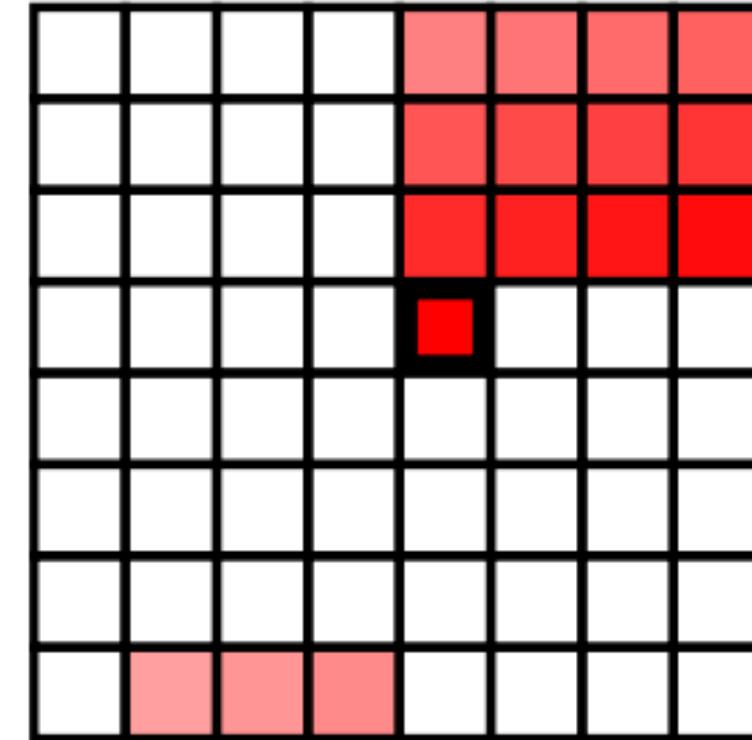
A mem:433 cache hit:324



B mem:433 cache hit:369



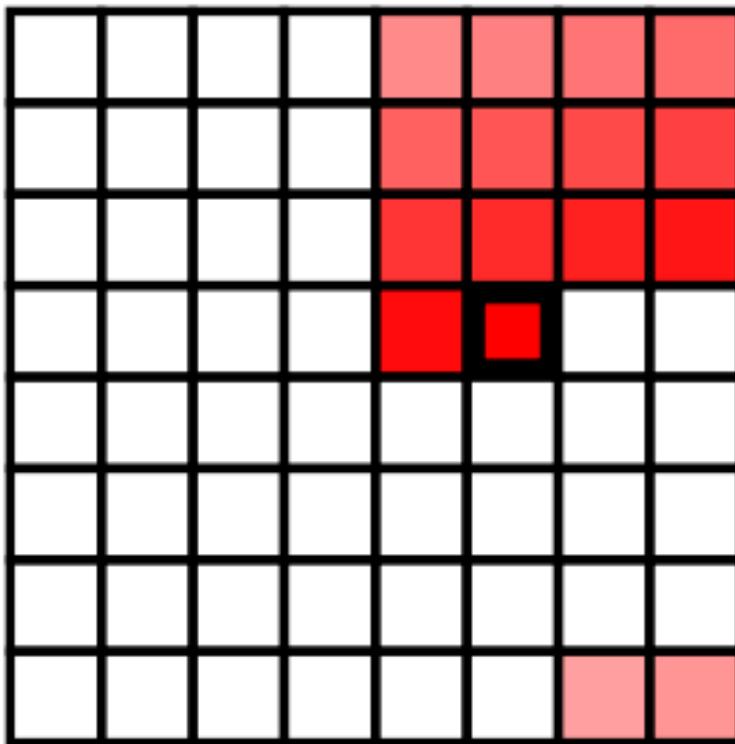
C mem:433 cache hit:324



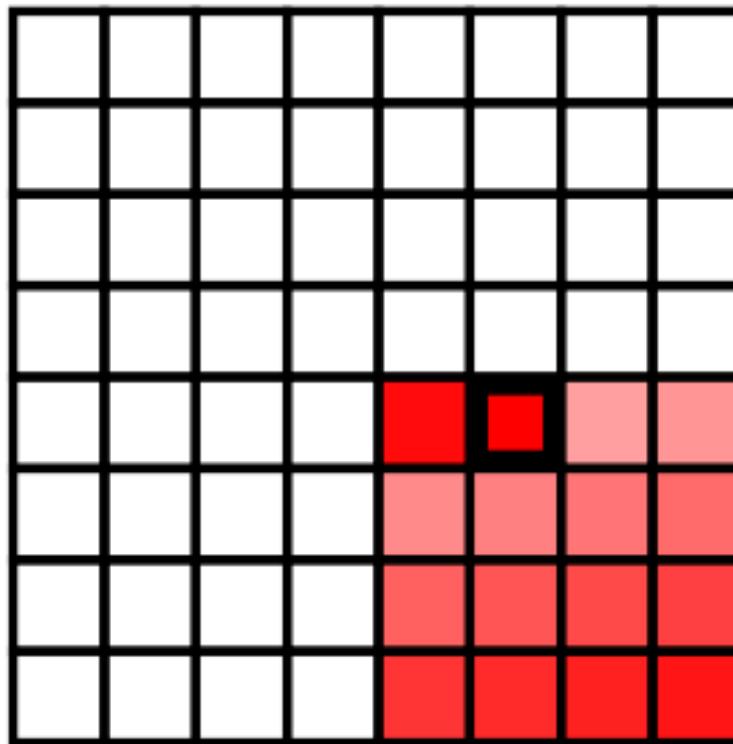
mem:1299

cache hits:1017 \approx 78%

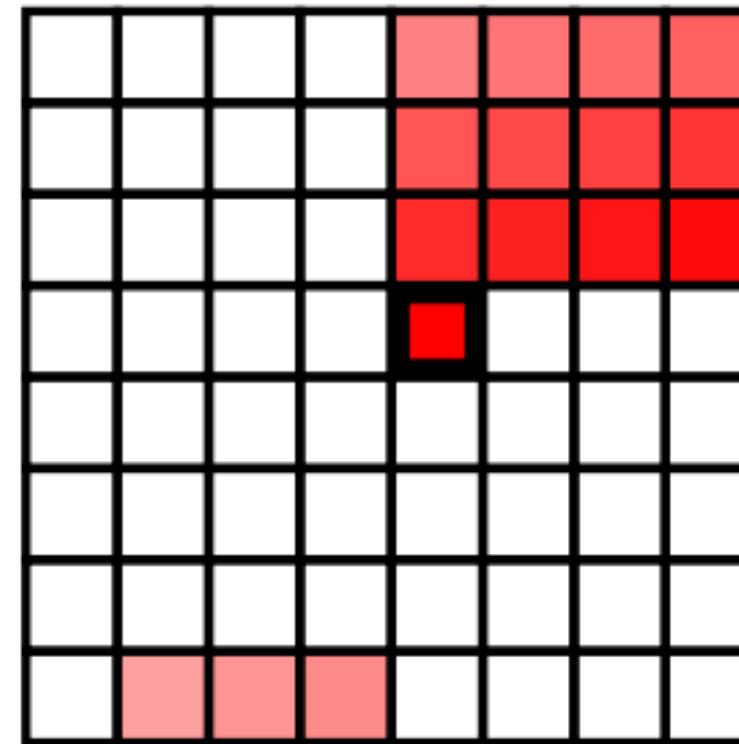
A mem:434 cache hit:324



B mem:434 cache hit:370



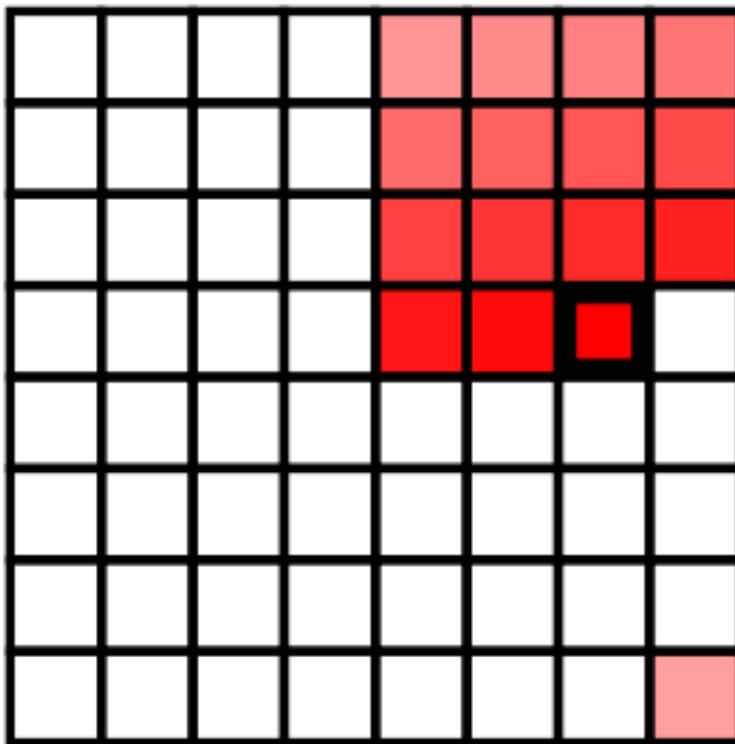
C mem:434 cache hit:325



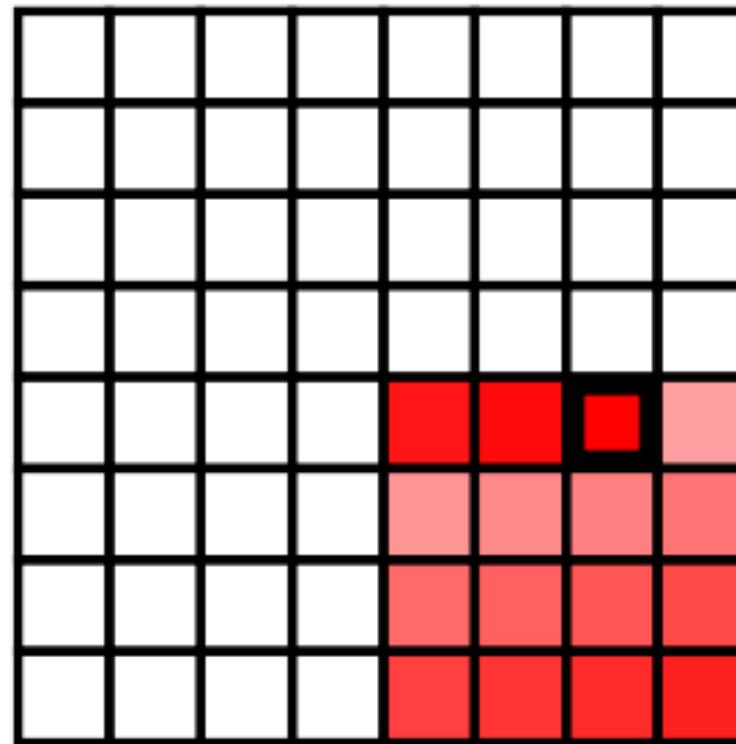
mem:1302

cache hits:1019 \approx 78%

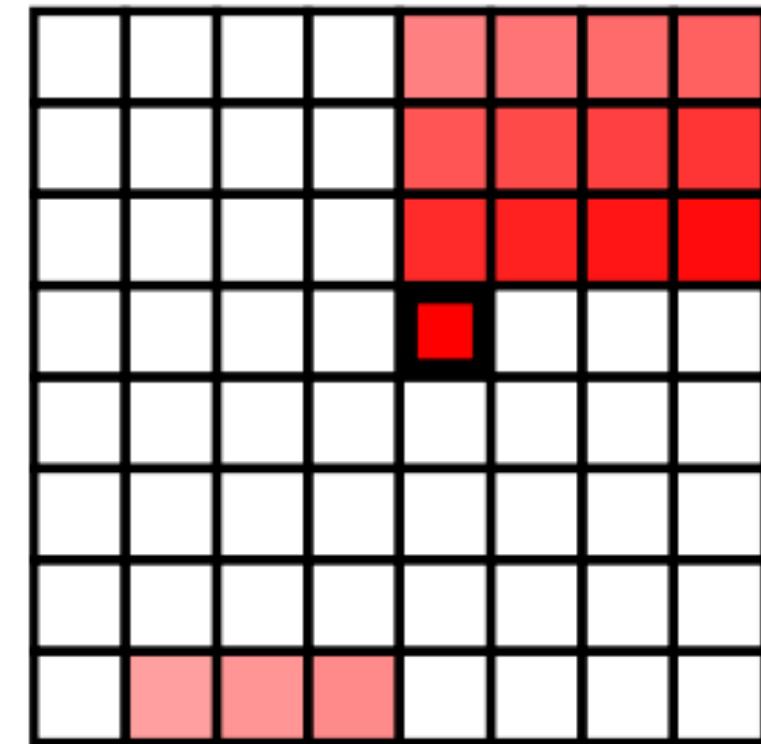
A mem:435 cache hit:324



B mem:435 cache hit:371



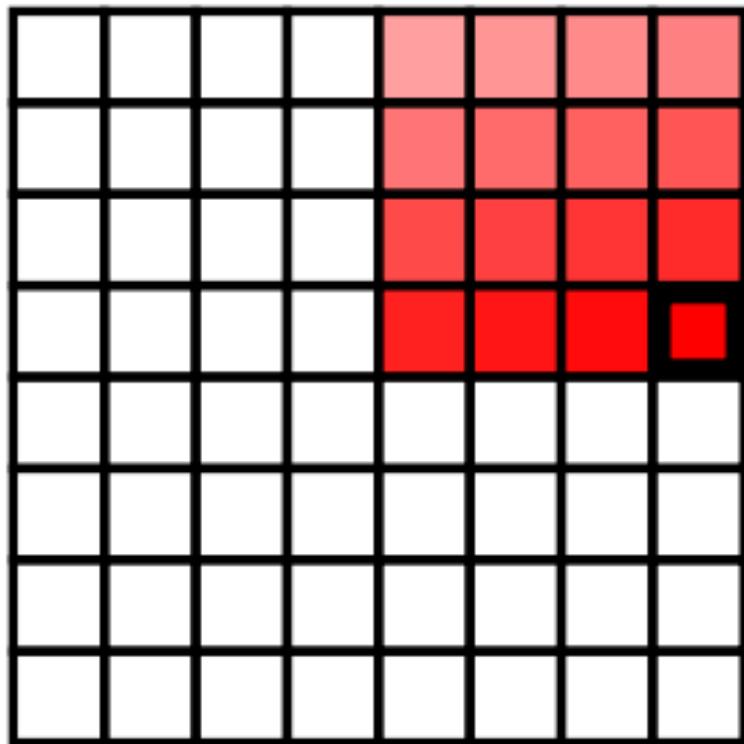
C mem:435 cache hit:326



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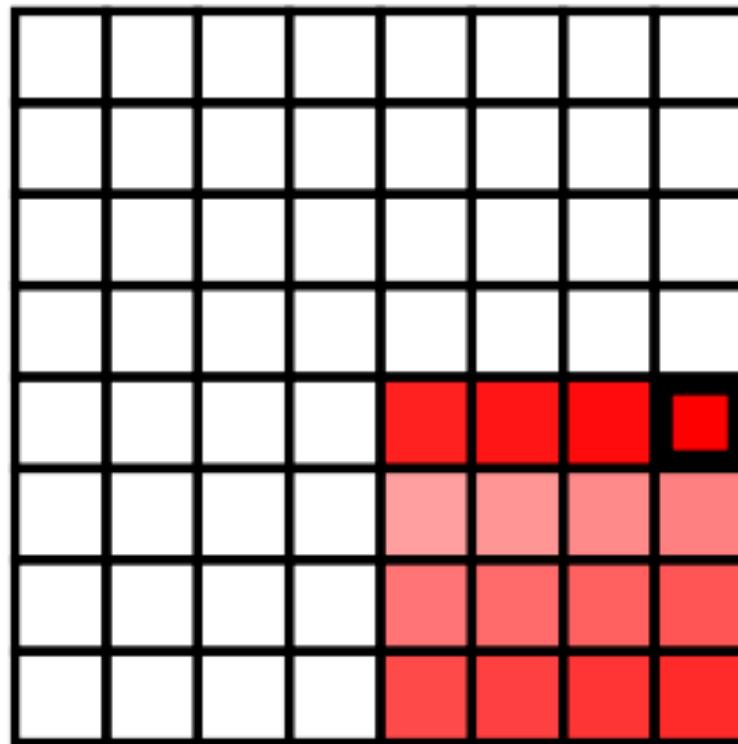
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A mem:436 cache hit:324



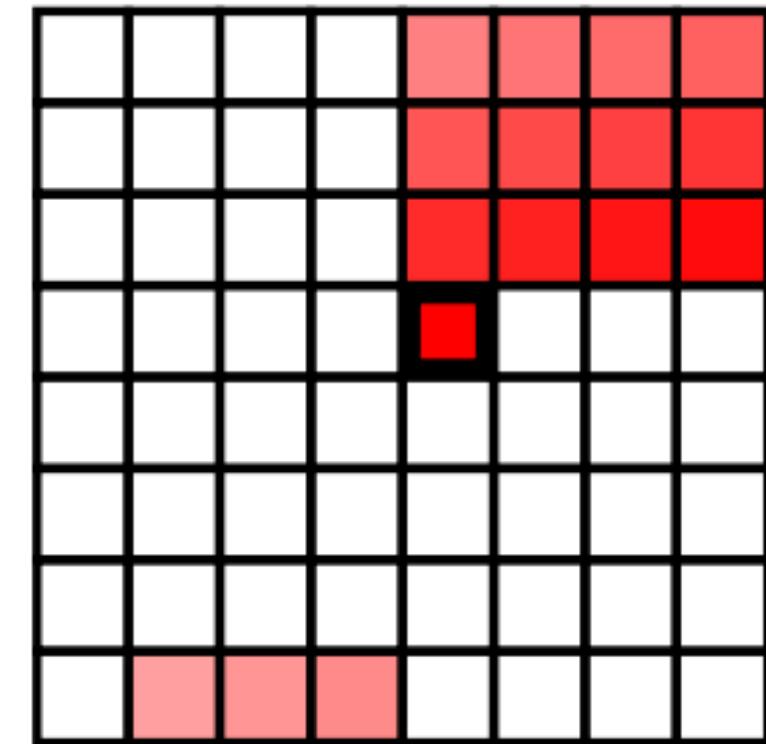
mem:1308

B mem:436 cache hit:372

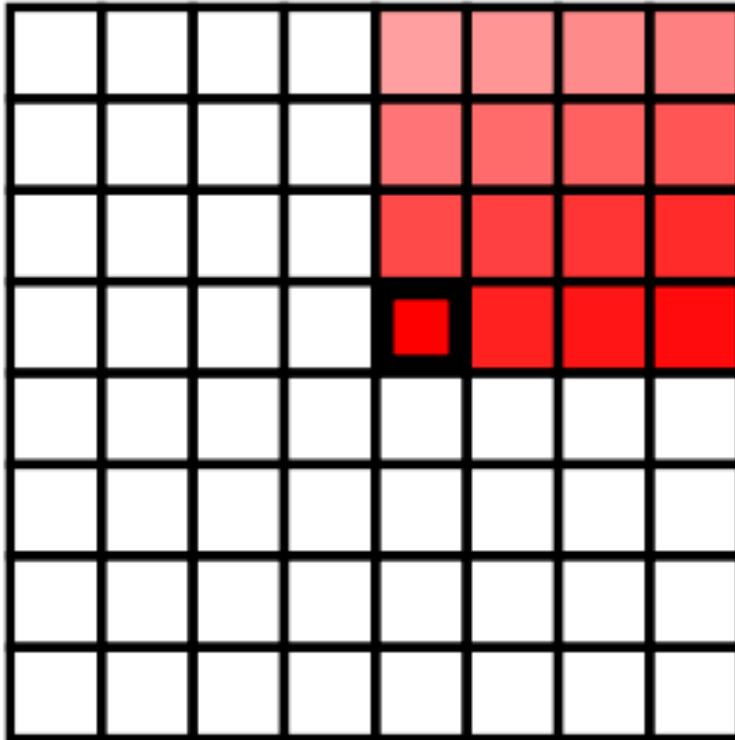


cache hits:1023 \approx 78%

C mem:436 cache hit:327

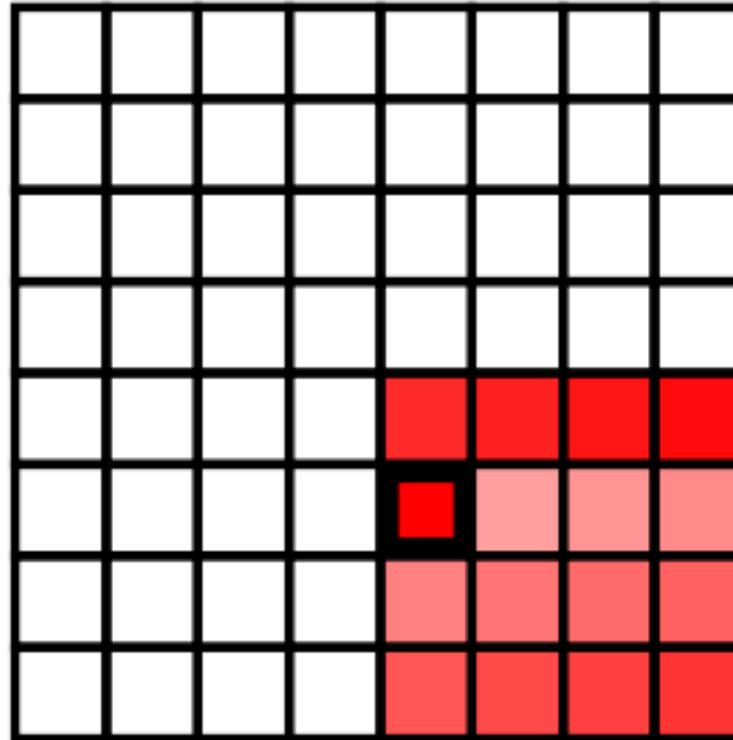


A mem:437 cache hit:325



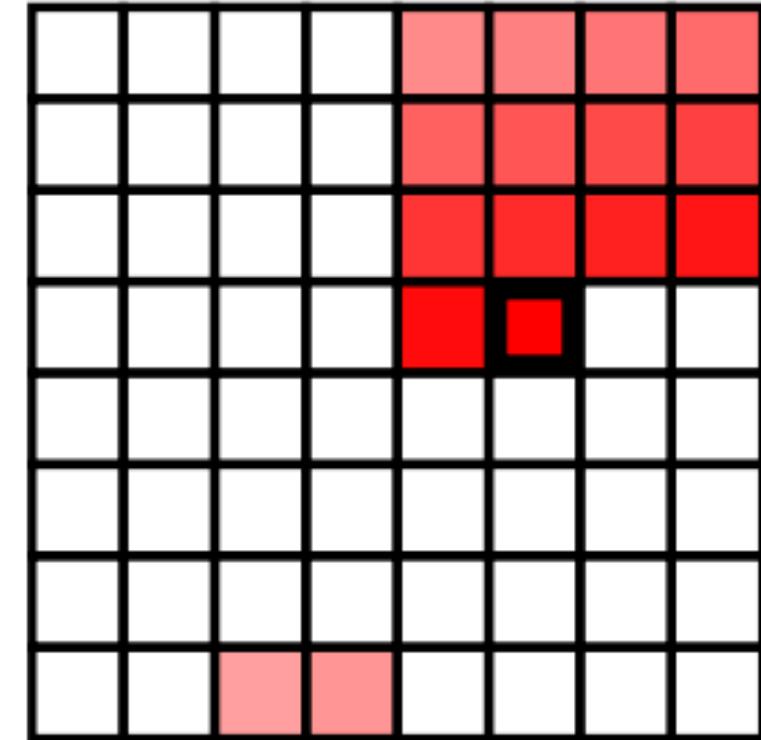
mem:1311

B mem:437 cache hit:373

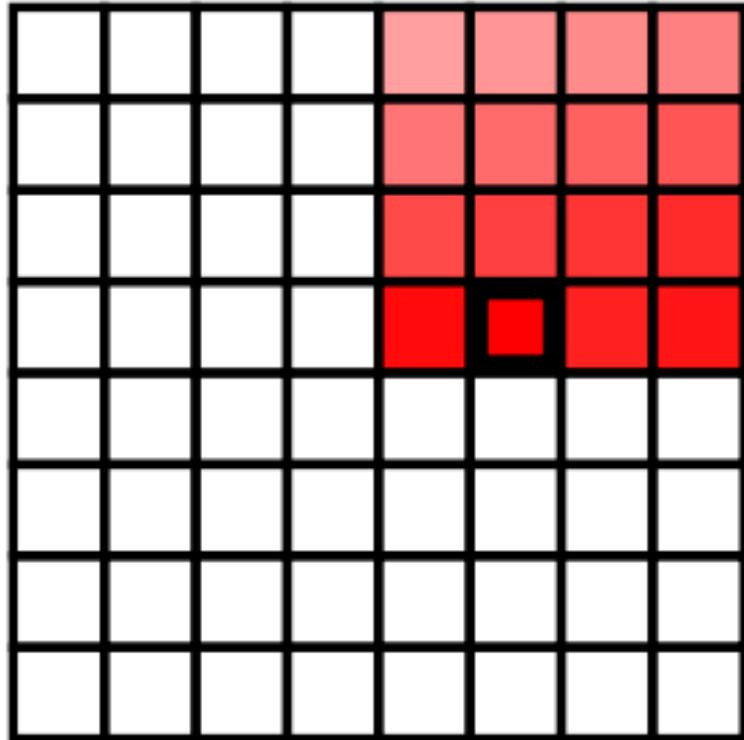


cache hits:1025 \approx 78%

C mem:437 cache hit:327

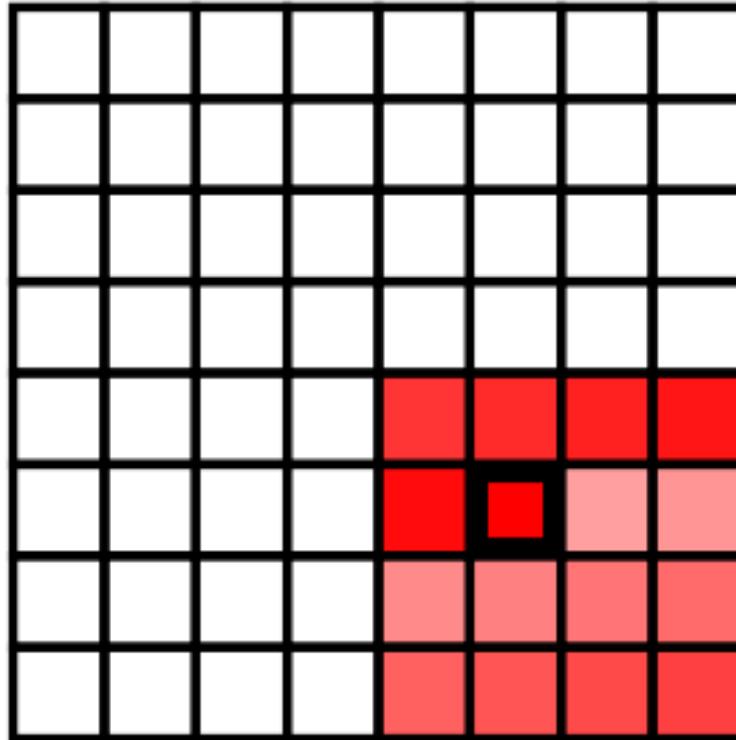


A mem:438 cache hit:326



mem:1314

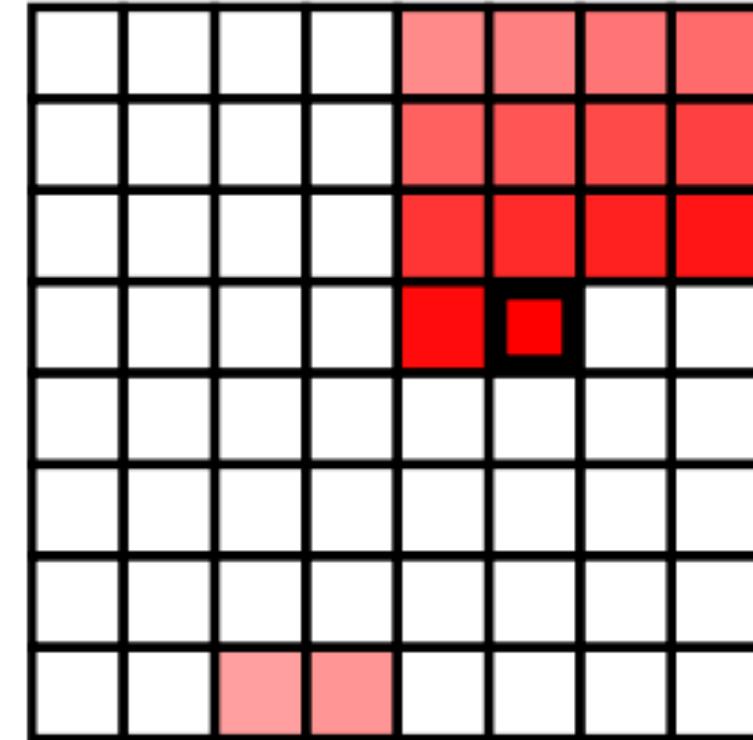
B mem:438 cache hit:374



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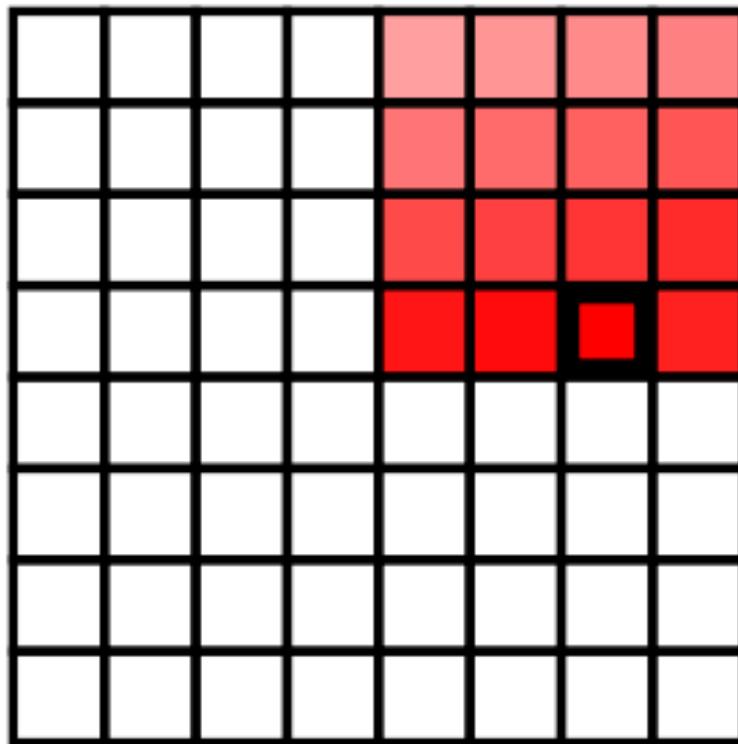
cache hits:1028 \approx 78%

C mem:438 cache hit:328



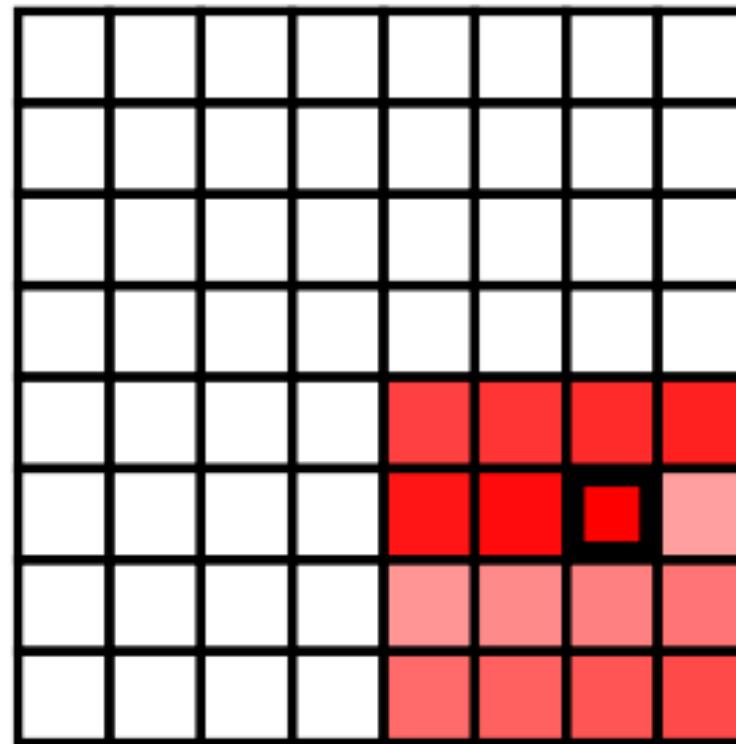
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A mem:439 cache hit:327



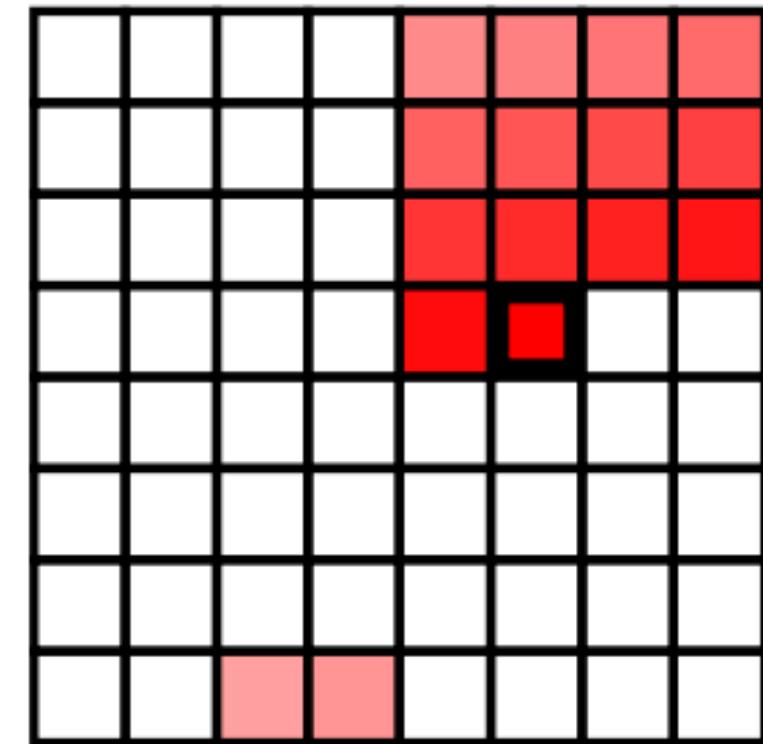
mem:1317

B mem:439 cache hit:375

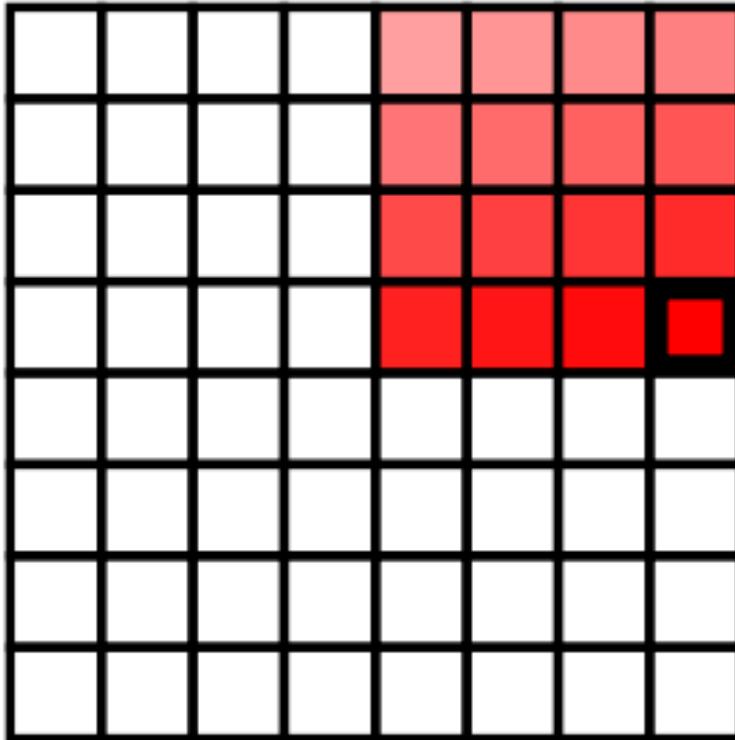


cache hits:1031 \approx 78%

C mem:439 cache hit:329

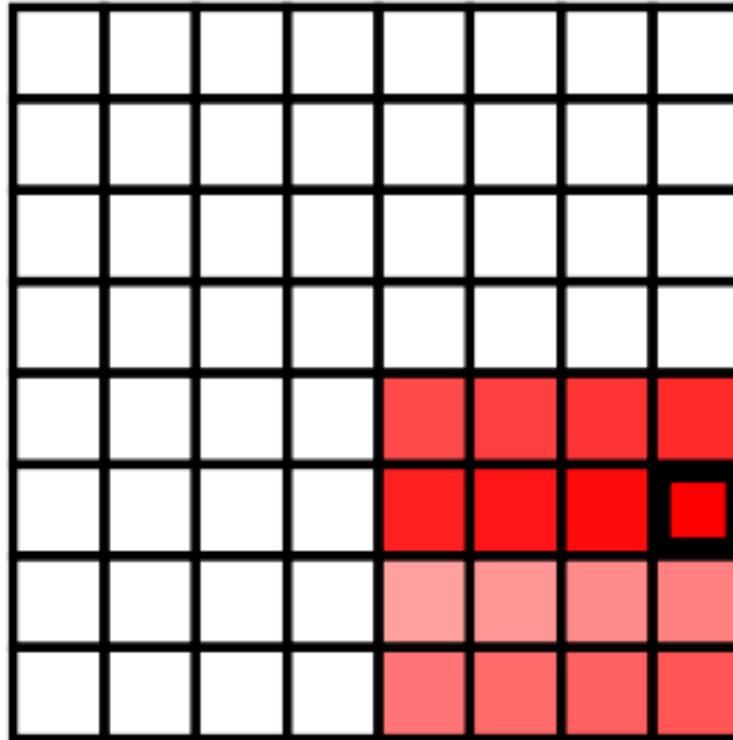


A mem:440 cache hit:328



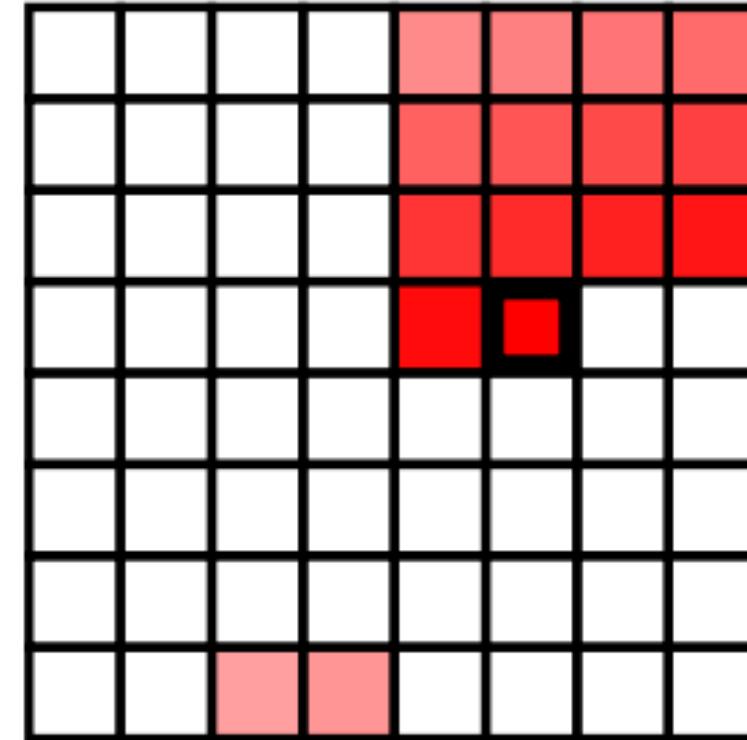
mem:1320

B mem:440 cache hit:376

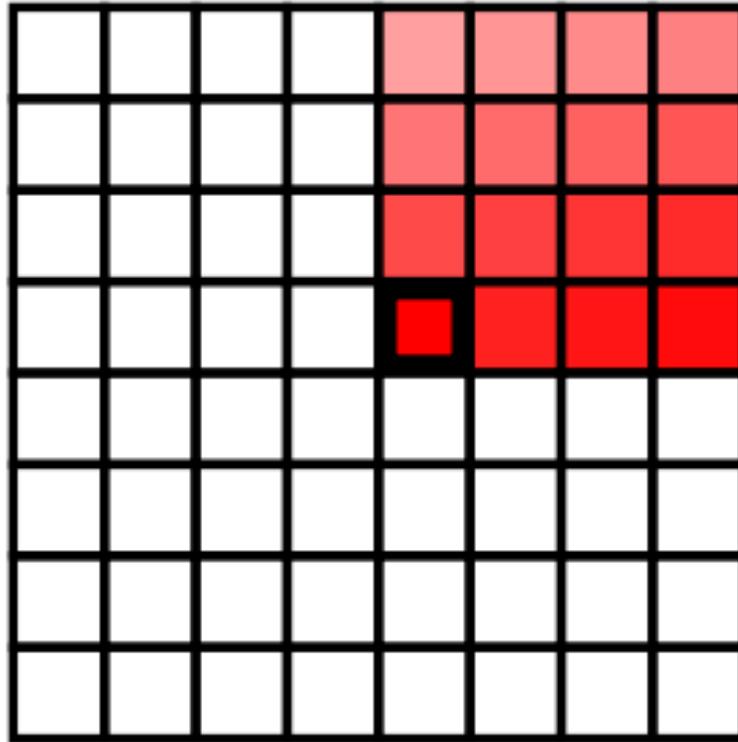


cache hits:1034 \approx 78%

C mem:440 cache hit:330

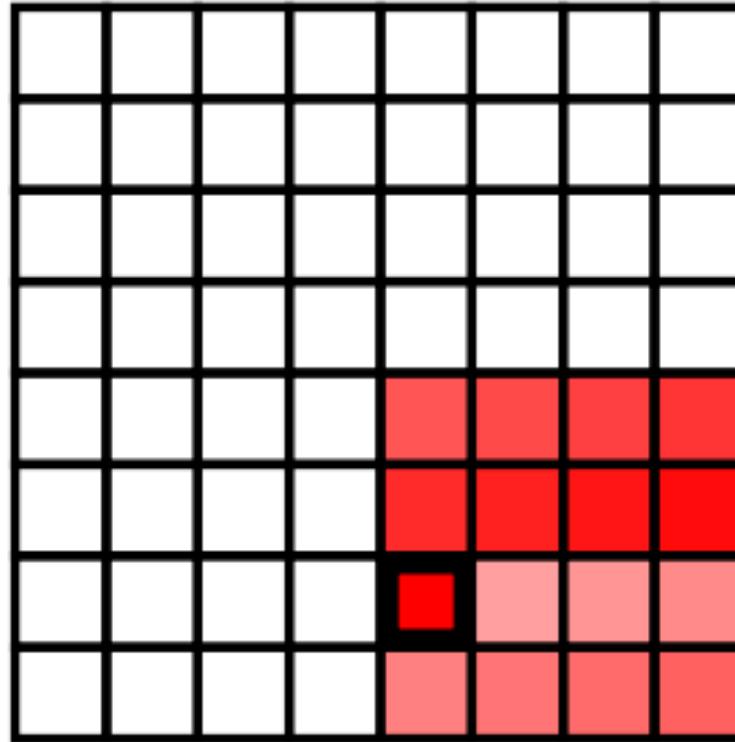


A mem:441 cache hit:329



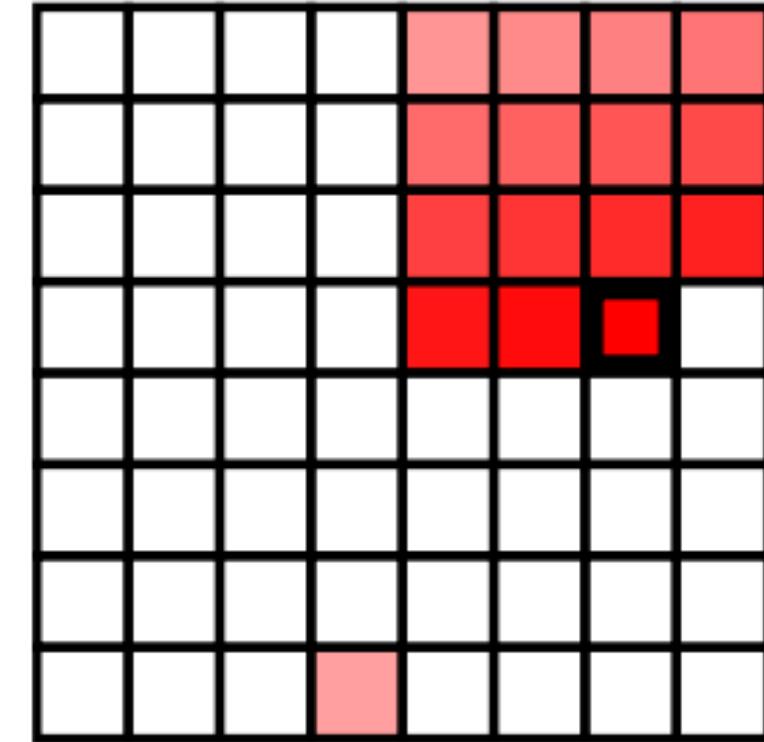
mem:1323

B mem:441 cache hit:377

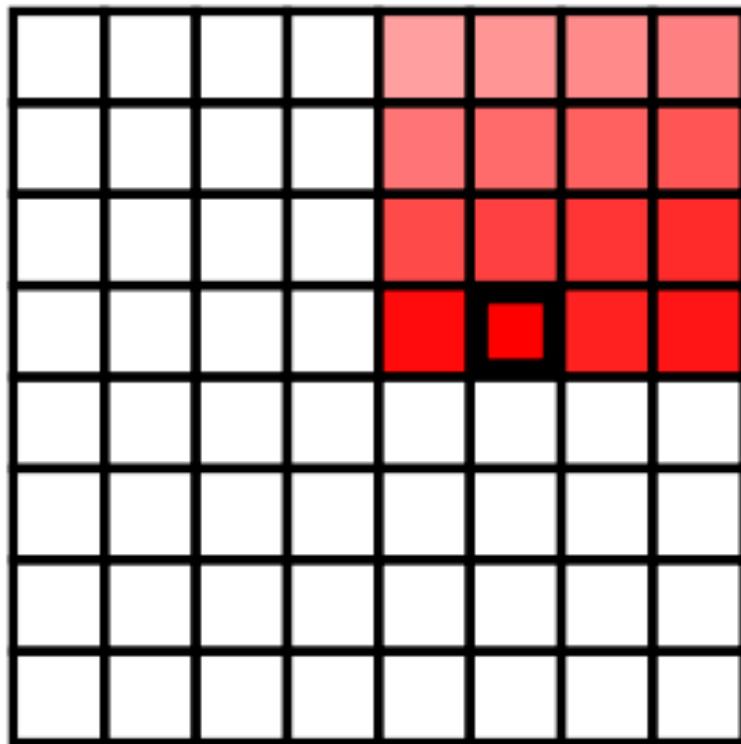


cache hits:1036 \approx 78%

C mem:441 cache hit:330

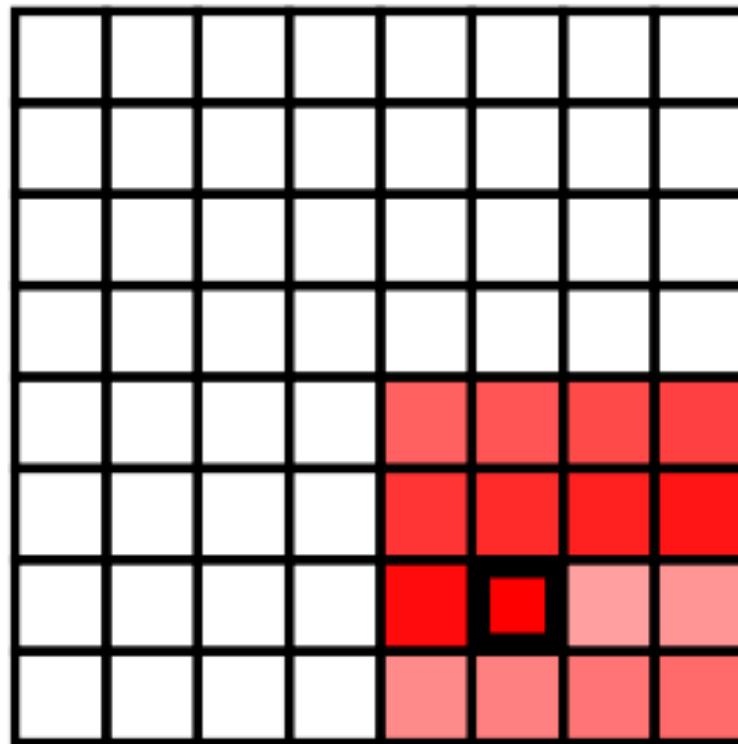


A mem:442 cache hit:330



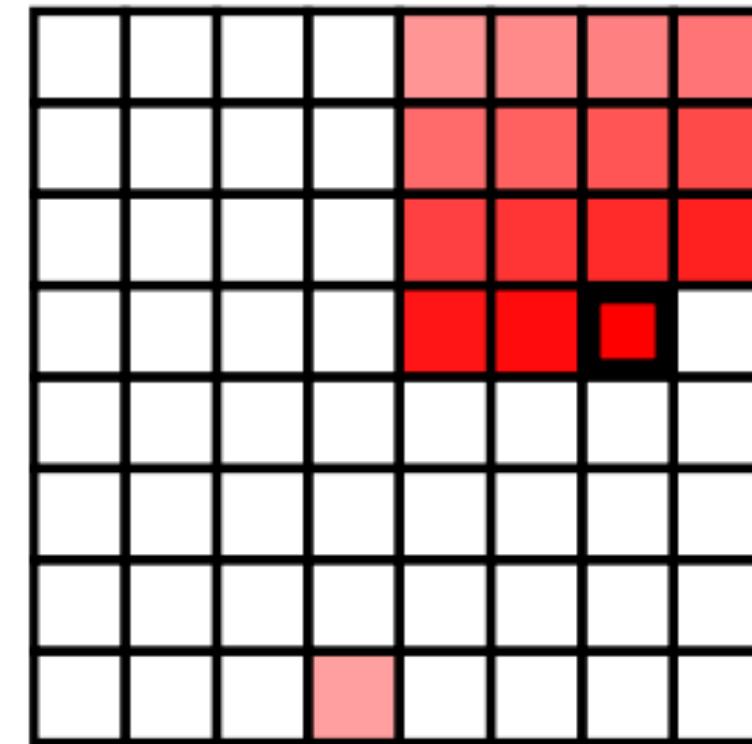
mem:1326

B mem:442 cache hit:378

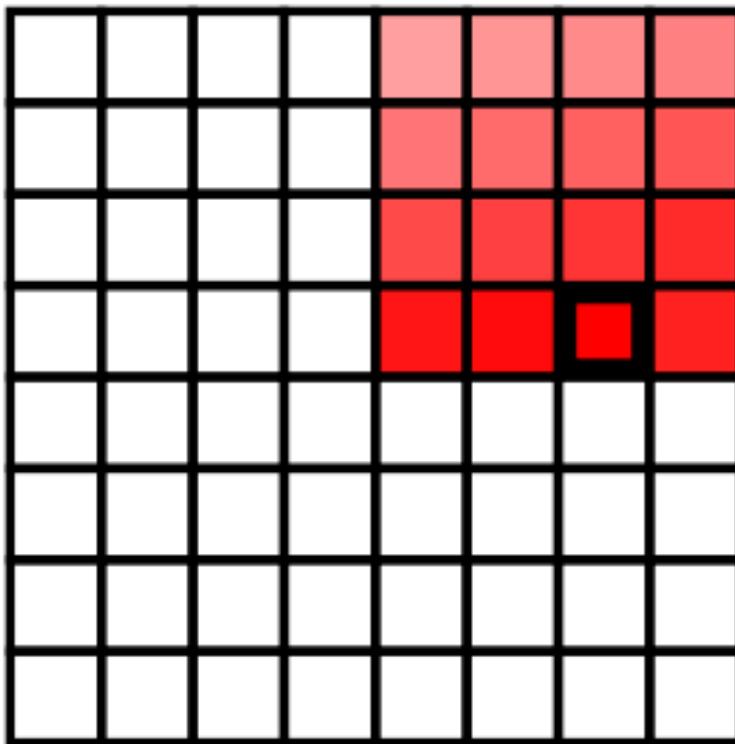


cache hits:1039 \approx 78%

C mem:442 cache hit:331

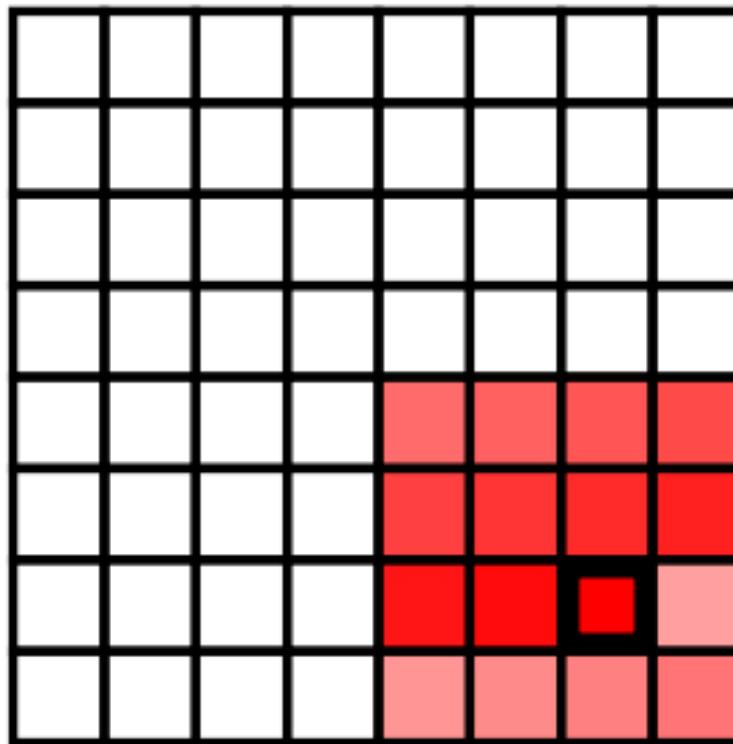


A mem:443 cache hit:331



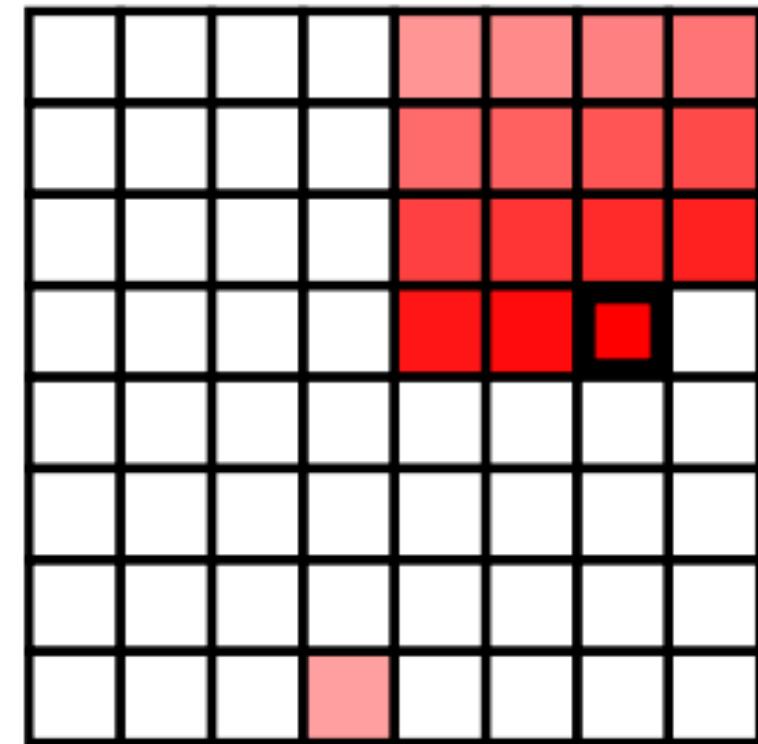
mem:1329

B mem:443 cache hit:379

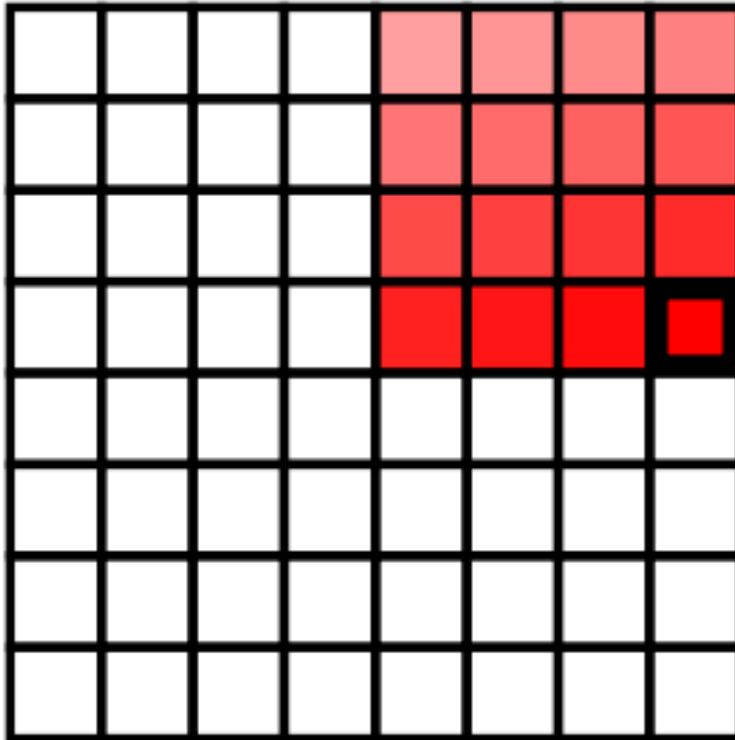


cache hits:1042 \approx 78%

C mem:443 cache hit:332

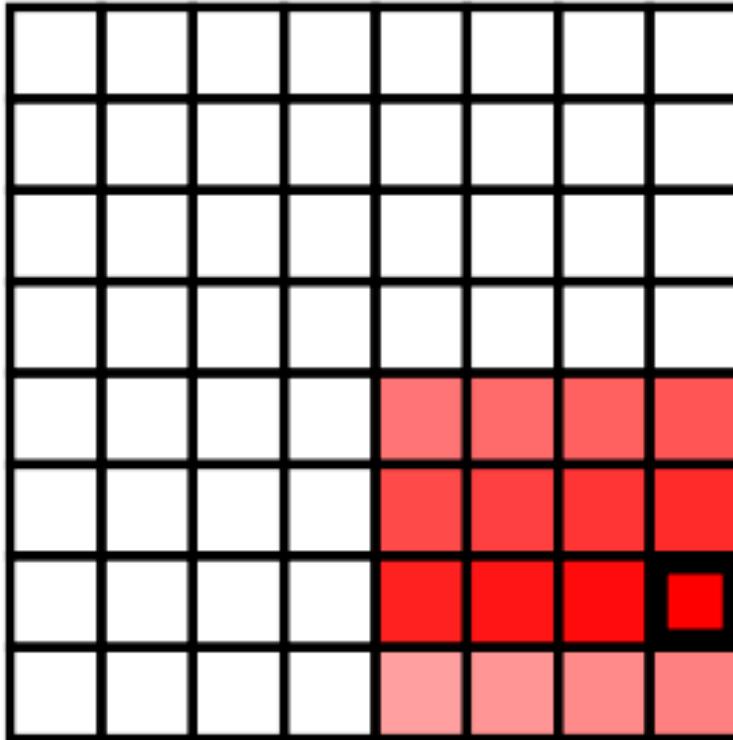


A mem:444 cache hit:332



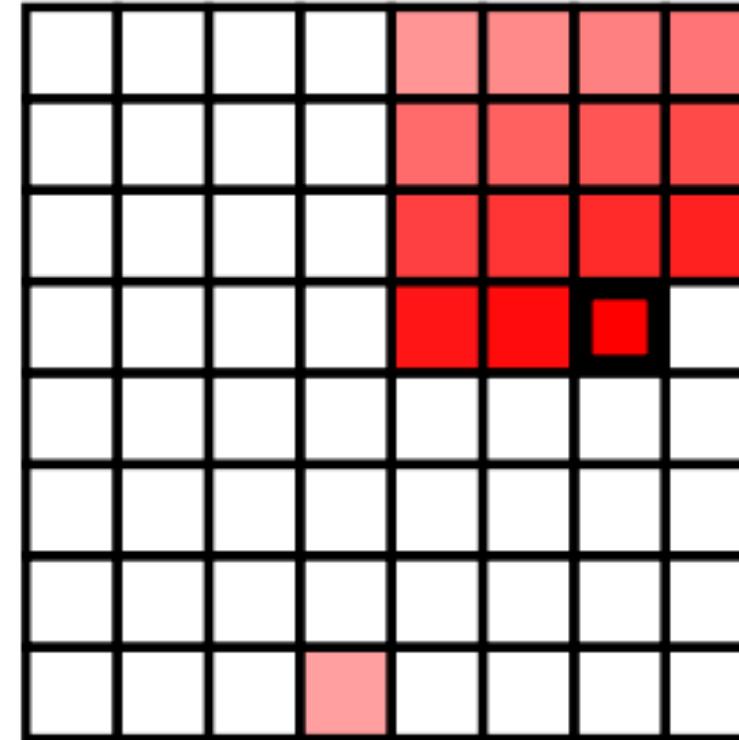
mem:1332

B mem:444 cache hit:380

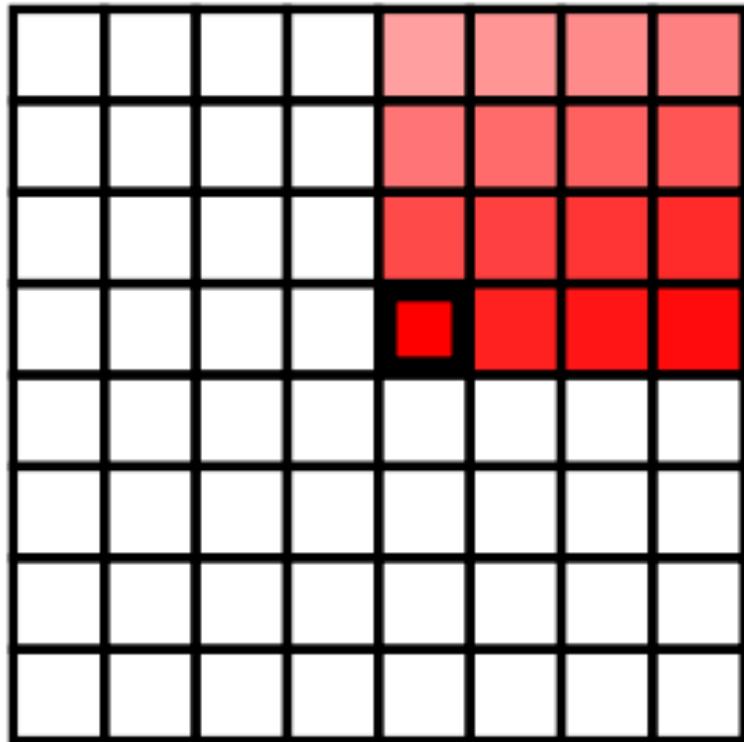


cache hits:1045 \approx 78%

C mem:444 cache hit:333

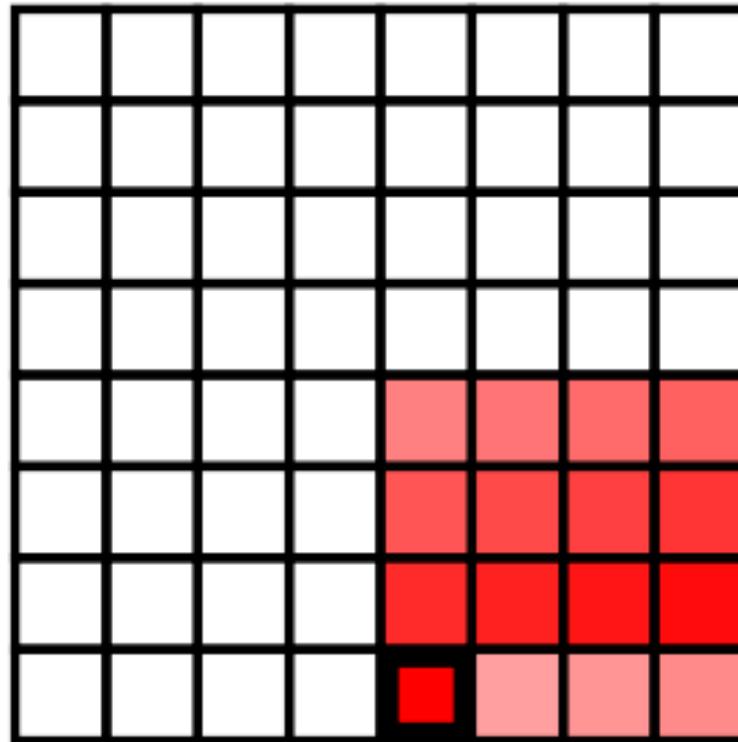


A mem:445 cache hit:333



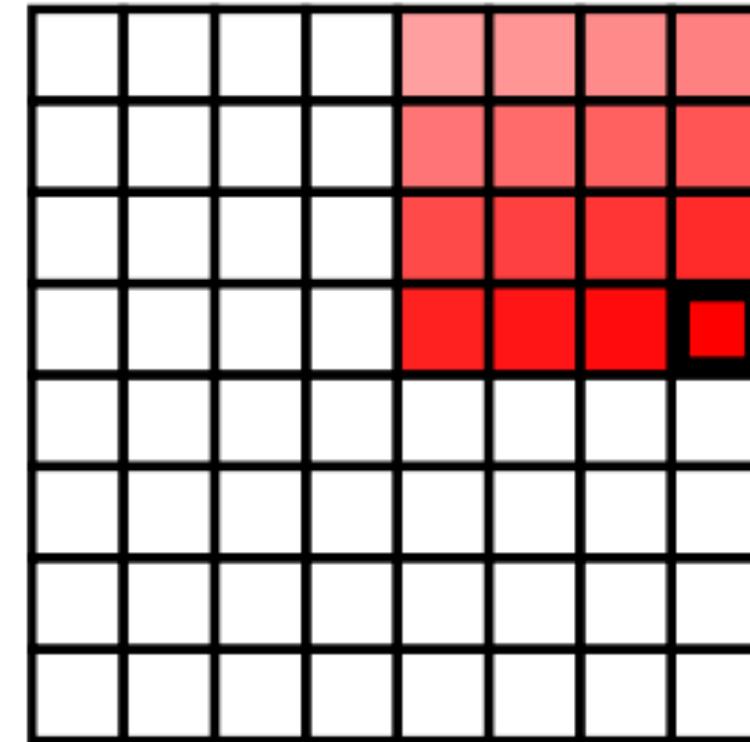
mem:1335

B mem:445 cache hit:381

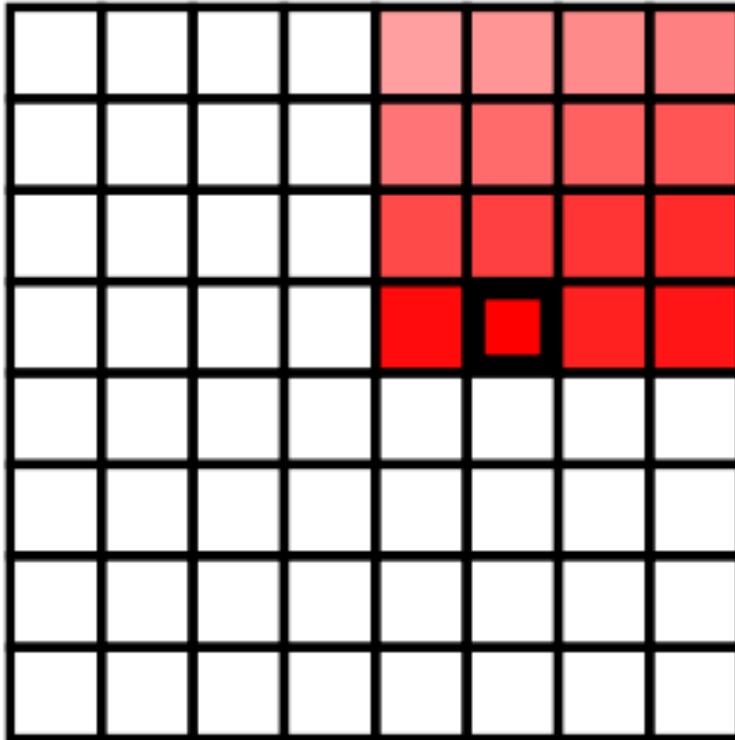


cache hits:1047 \approx 78%

C mem:445 cache hit:333

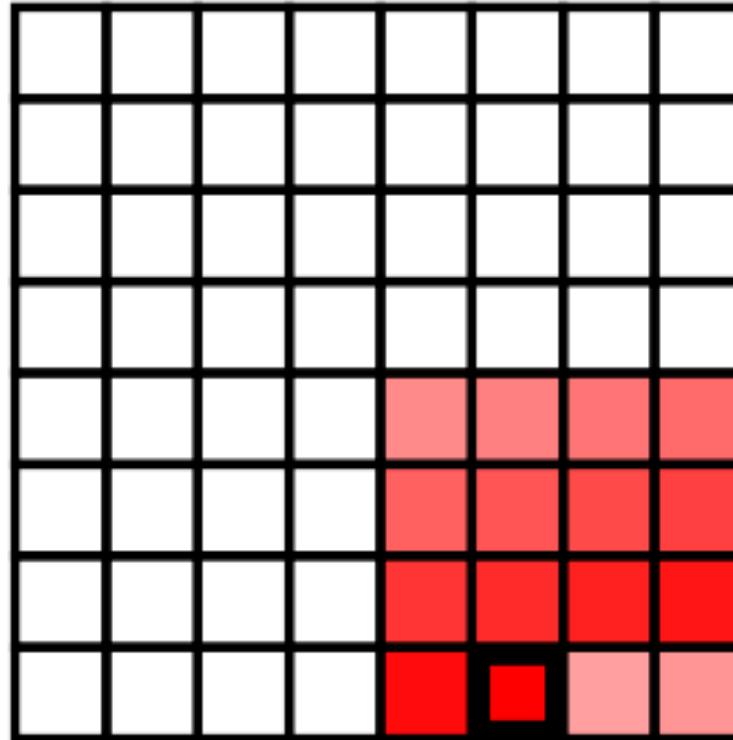


A mem:446 cache hit:334



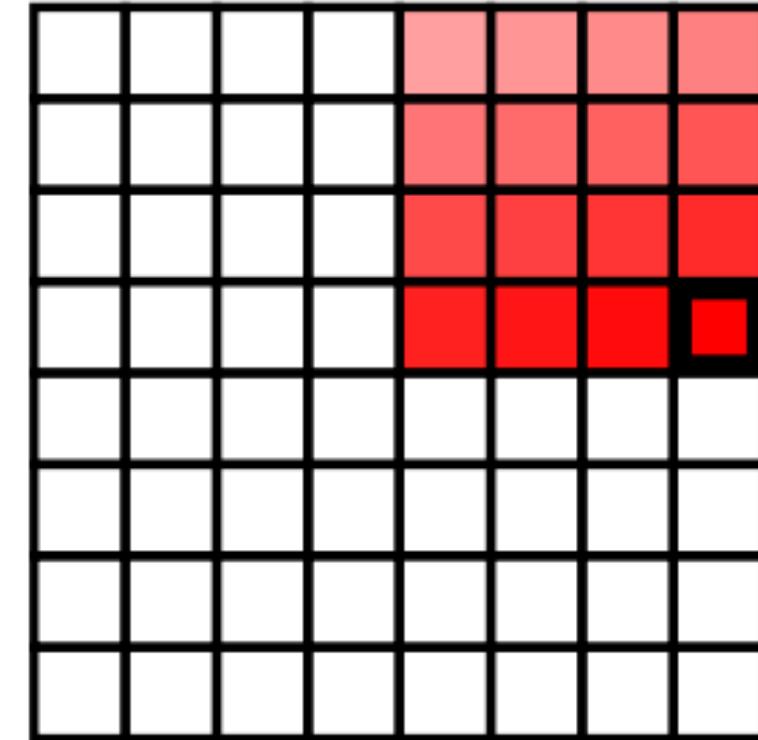
mem:1338

B mem:446 cache hit:382

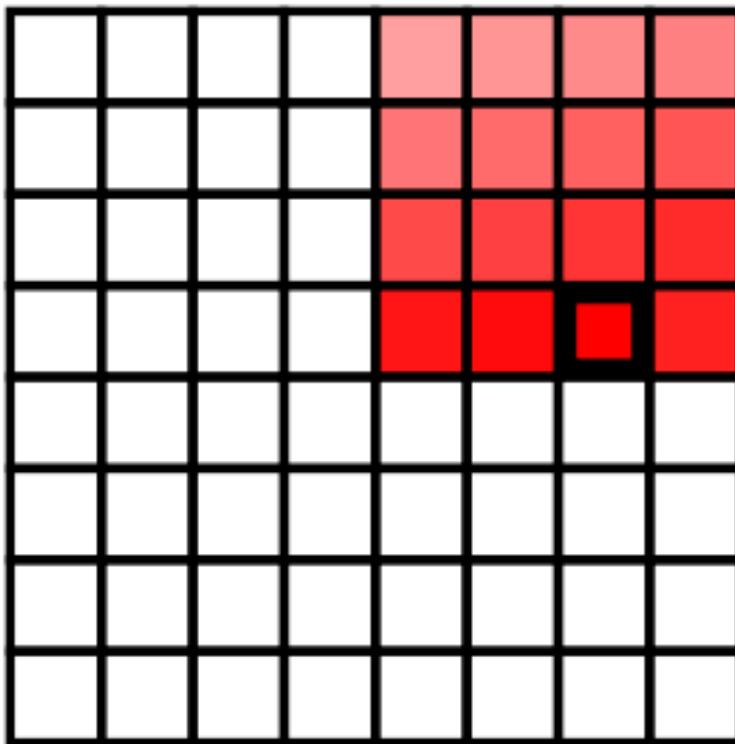


cache hits:1050 \approx 78%

C mem:446 cache hit:334

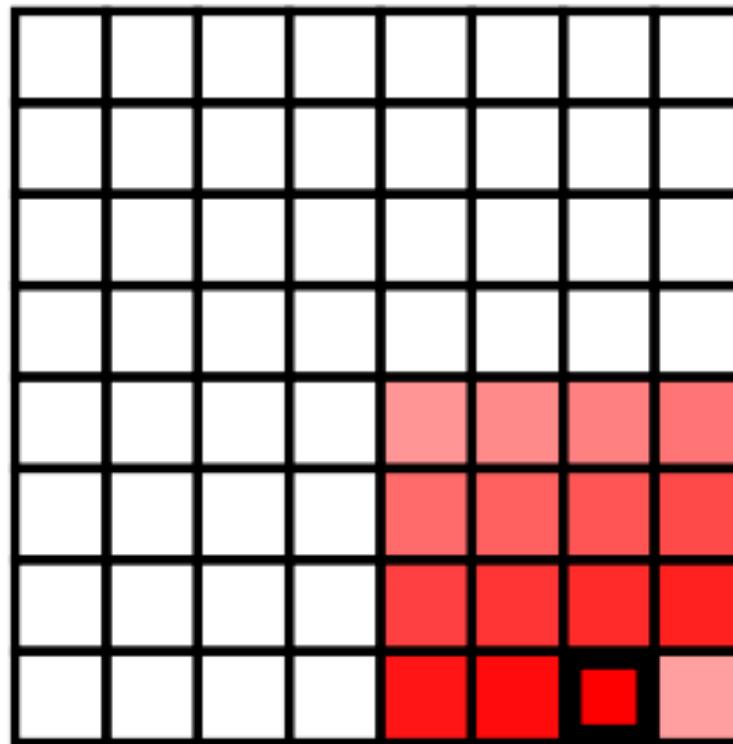


A mem:447 cache hit:335



mem:1341

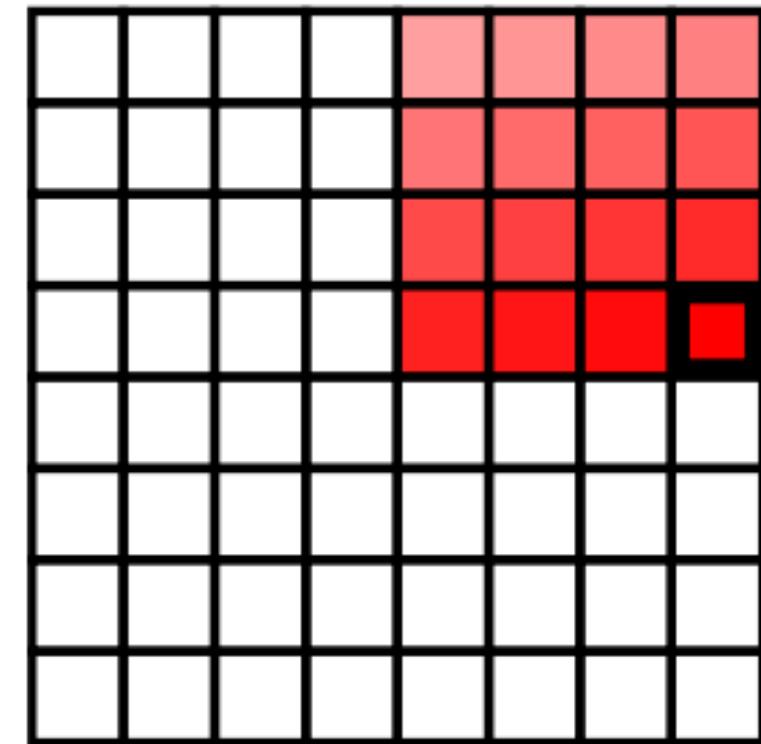
B mem:447 cache hit:383



cache hits:1053 \approx 78%

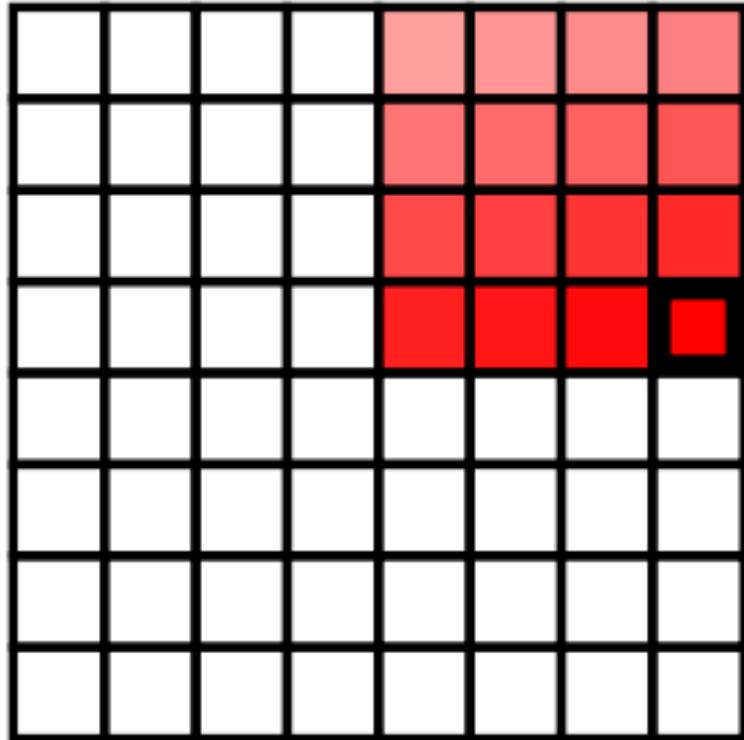
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C mem:447 cache hit:335



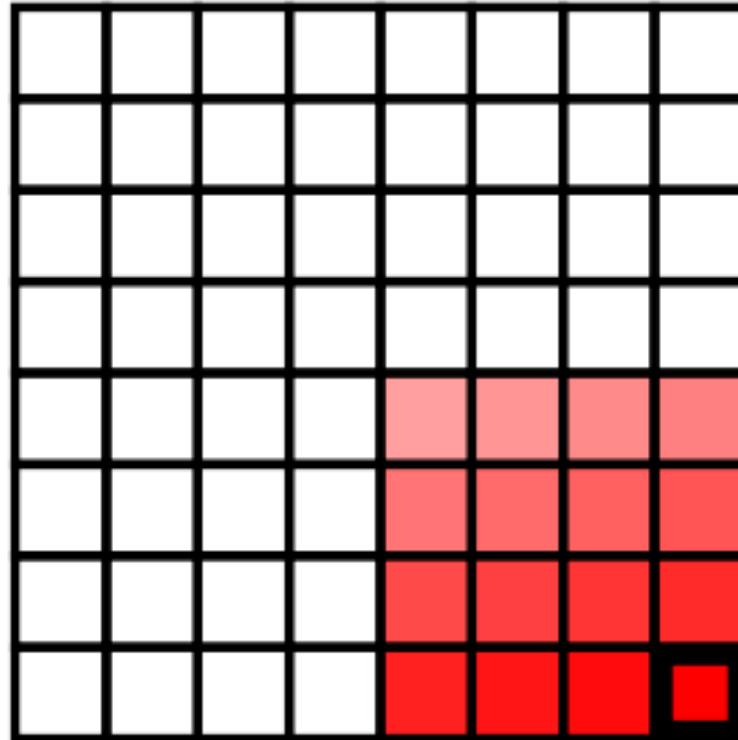
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A mem:448 cache hit:336



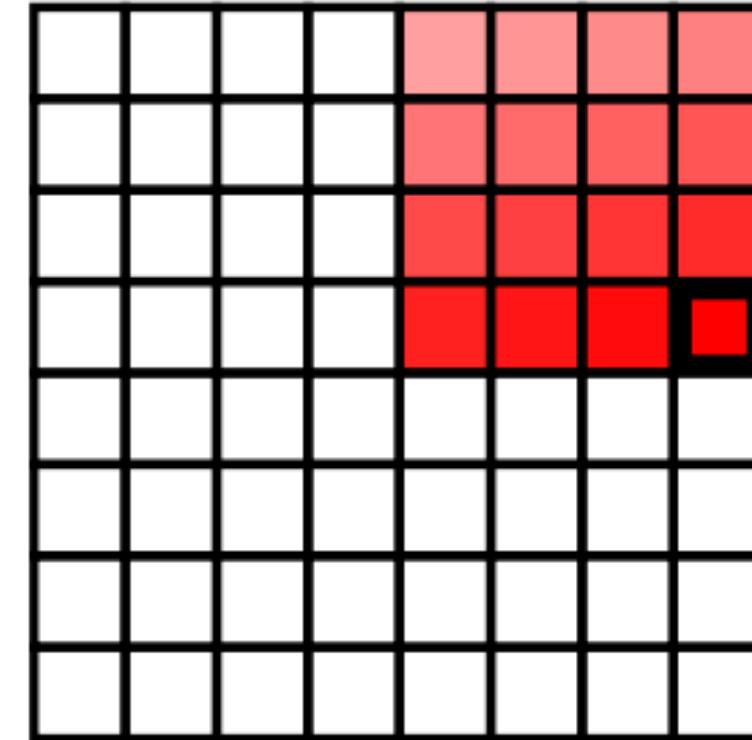
mem:1344

B mem:448 cache hit:384

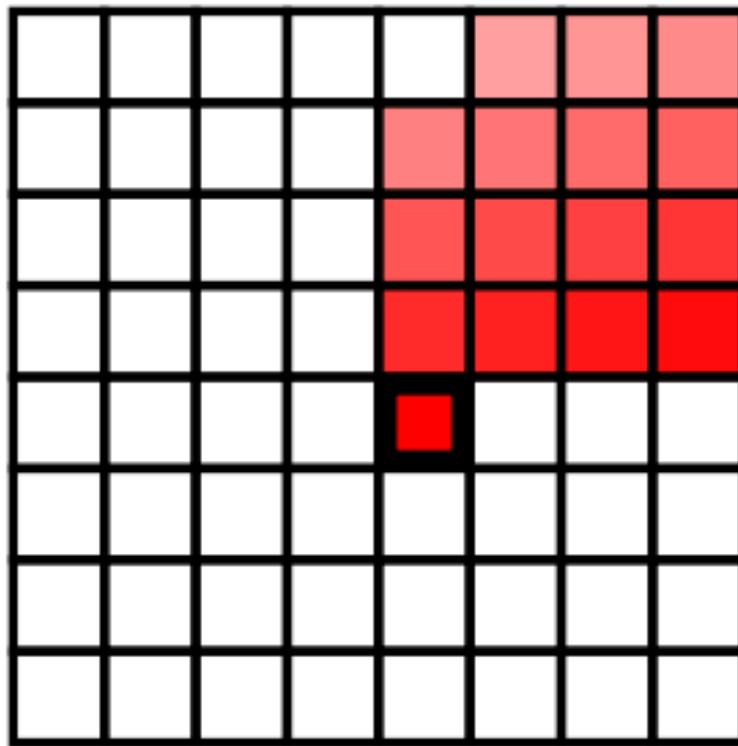


cache hits:1056 \approx 78%

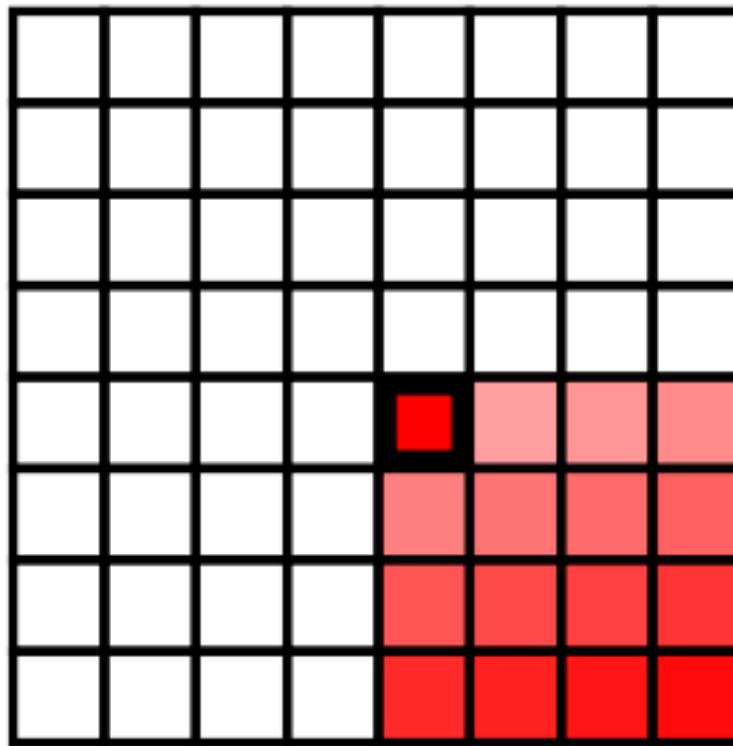
C mem:448 cache hit:336



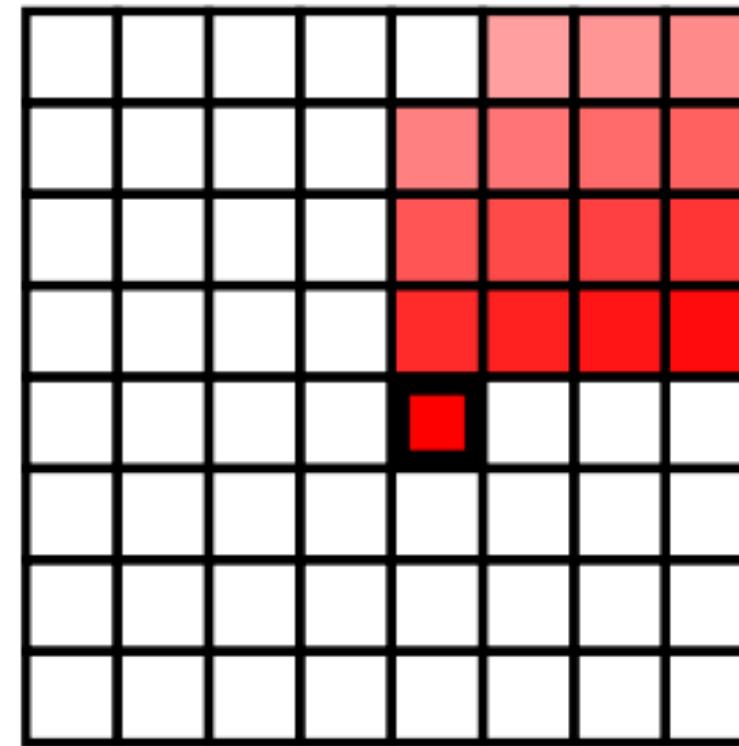
A mem:449 cache hit:336



B mem:449 cache hit:385



C mem:449 cache hit:336



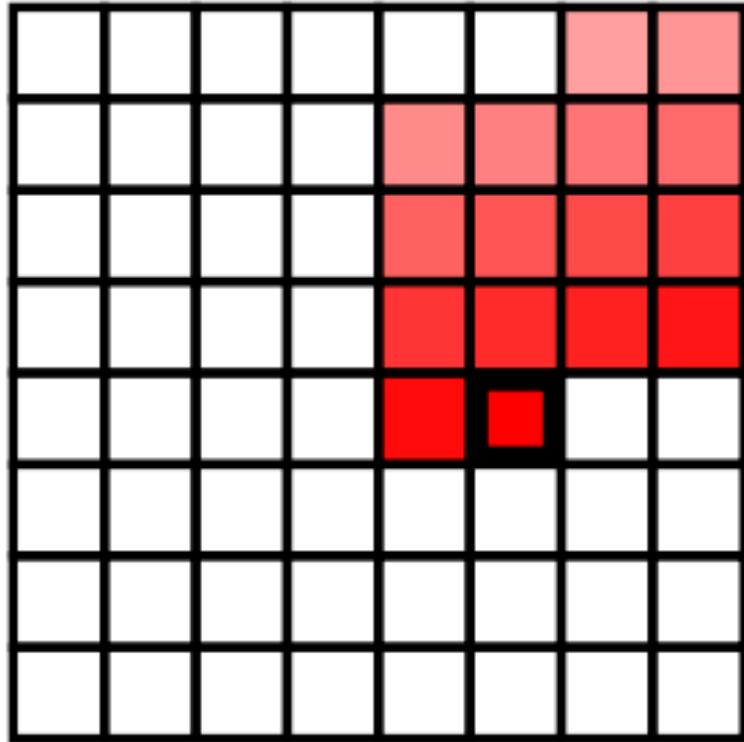
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mem:1347

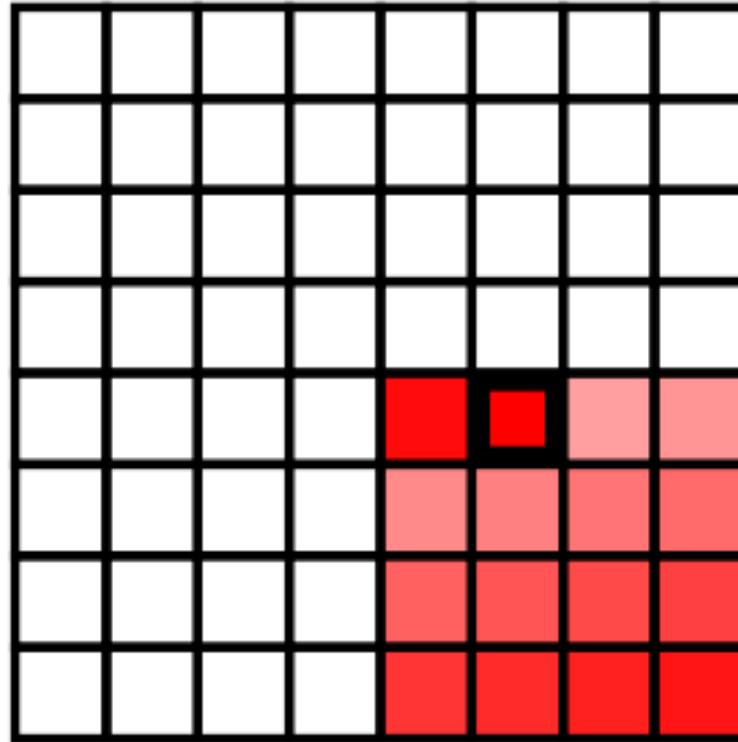
cache hits:1057 \approx 78%

A mem:450 cache hit:336



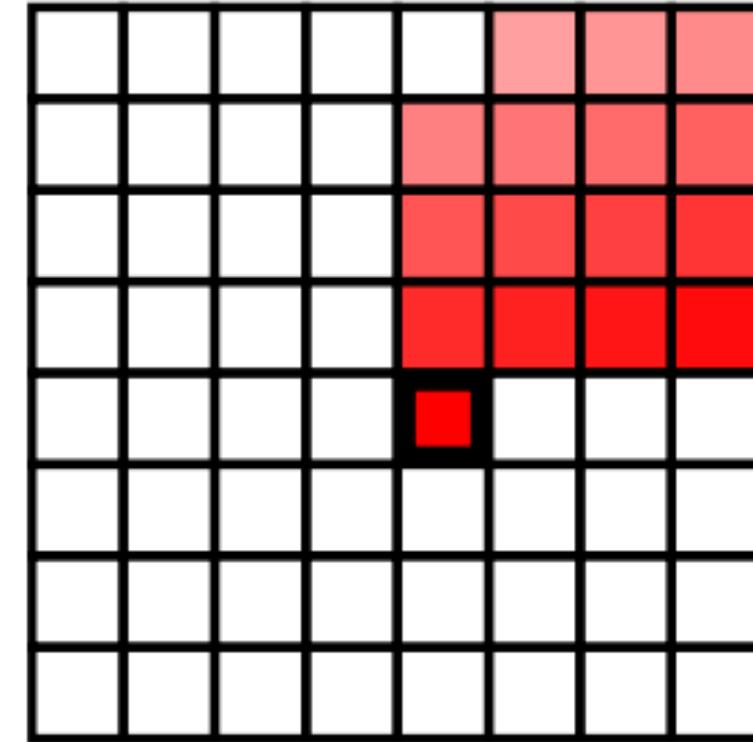
mem:1350

B mem:450 cache hit:386

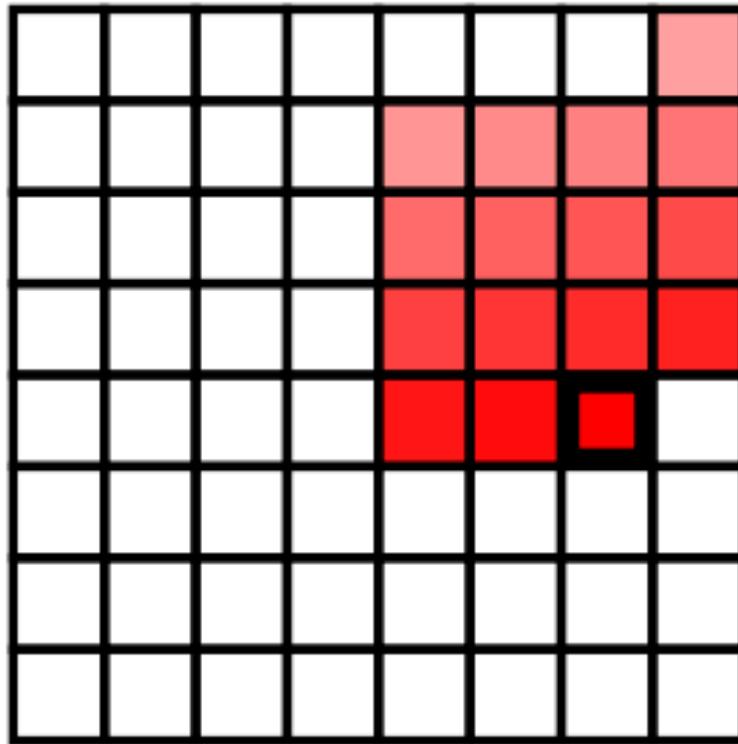


cache hits:1059 \approx 78%

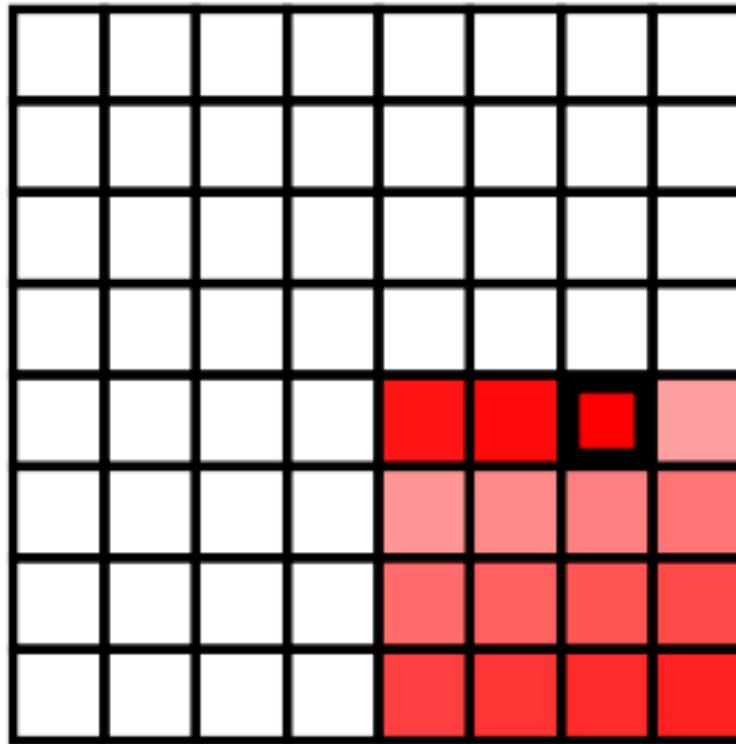
C mem:450 cache hit:337



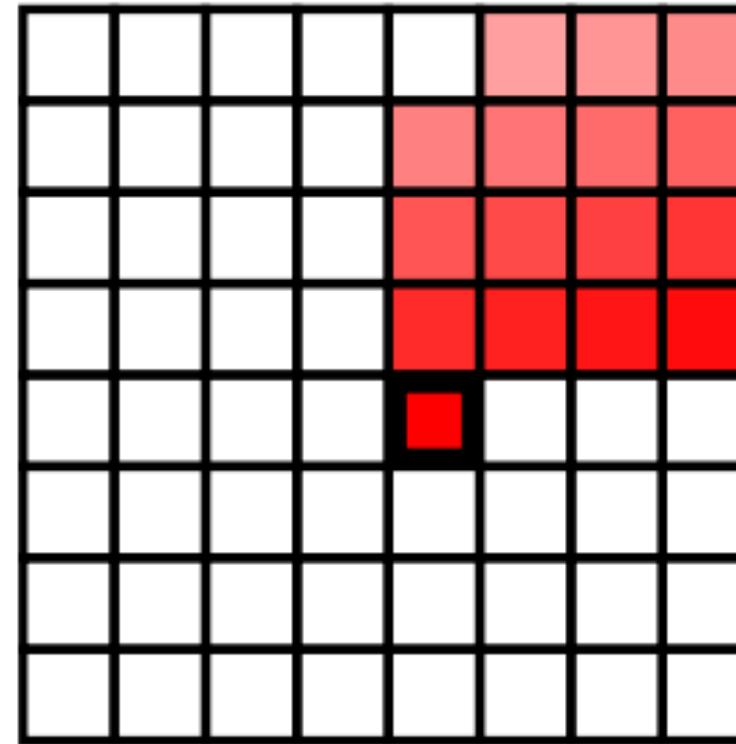
A mem:451 cache hit:336



B mem:451 cache hit:387



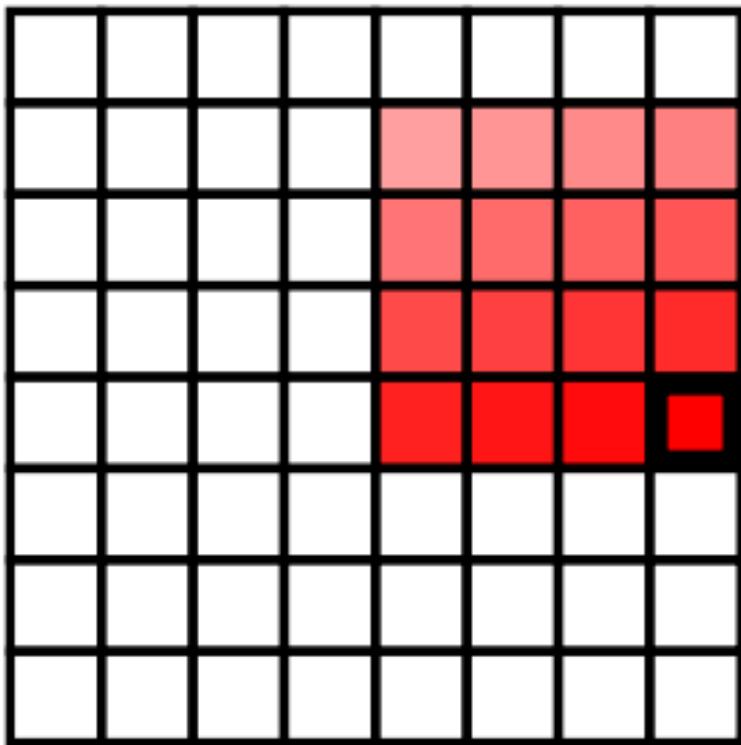
C mem:451 cache hit:338



mem:1353

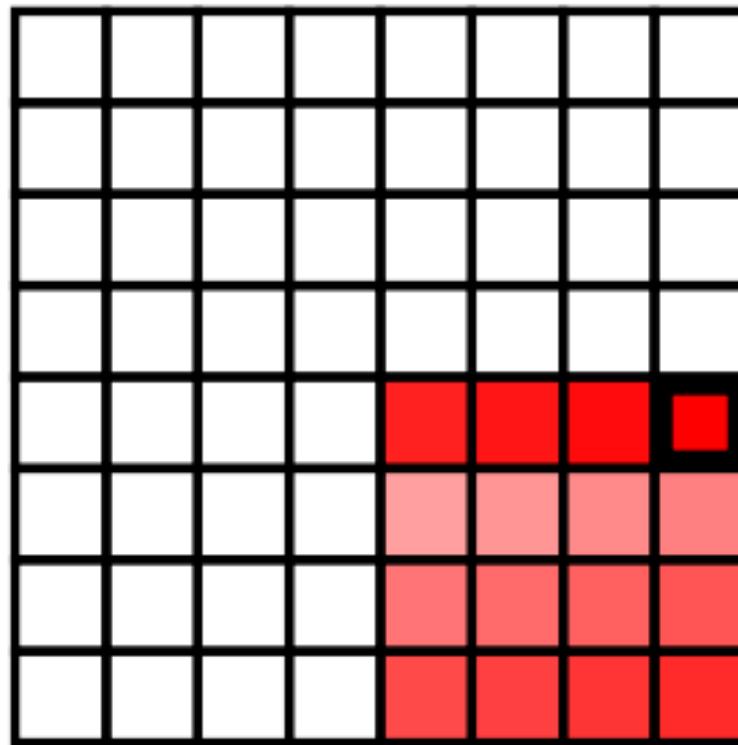
cache hits:1061 \approx 78%

A mem:452 cache hit:336



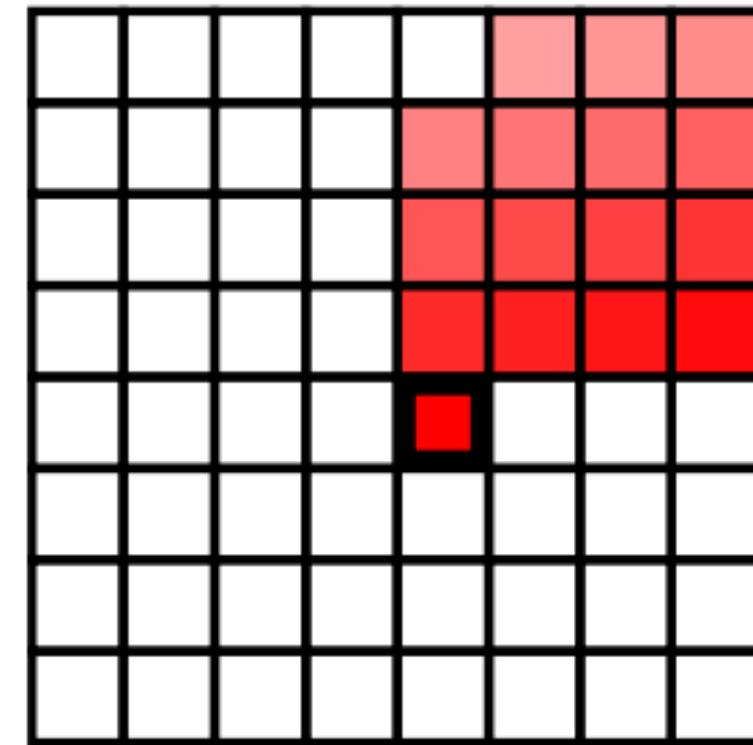
mem:1356

B mem:452 cache hit:388

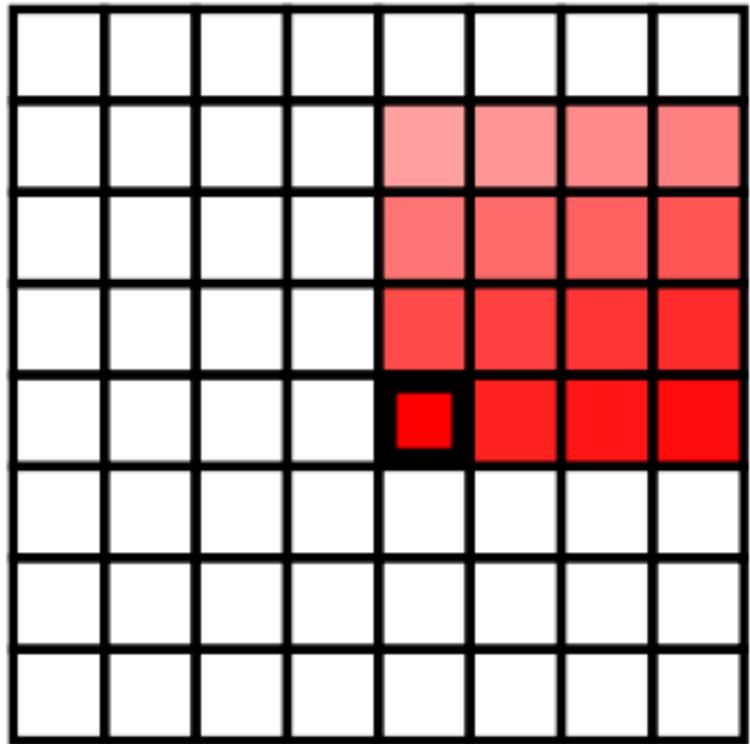


cache hits:1063 \approx 78%

C mem:452 cache hit:339

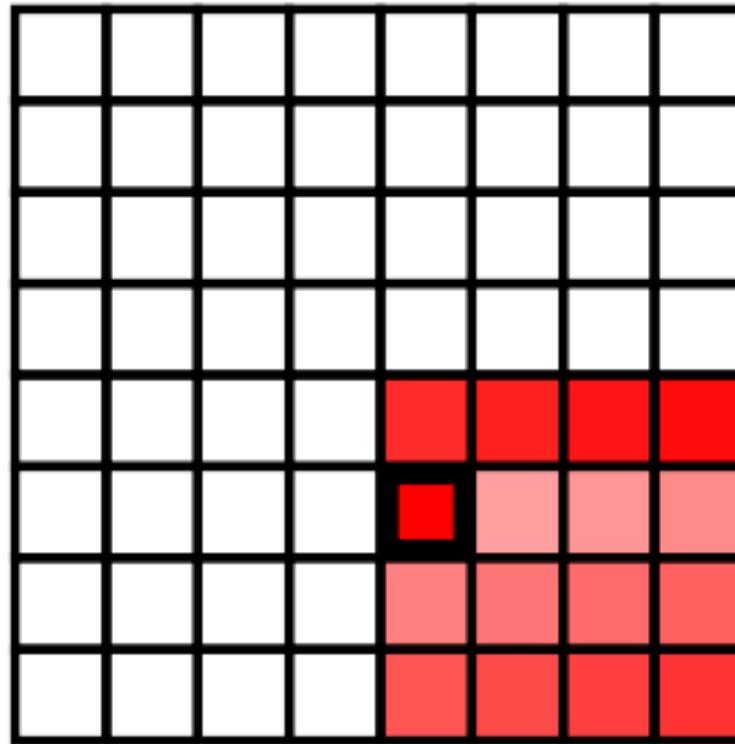


A mem:453 cache hit:337



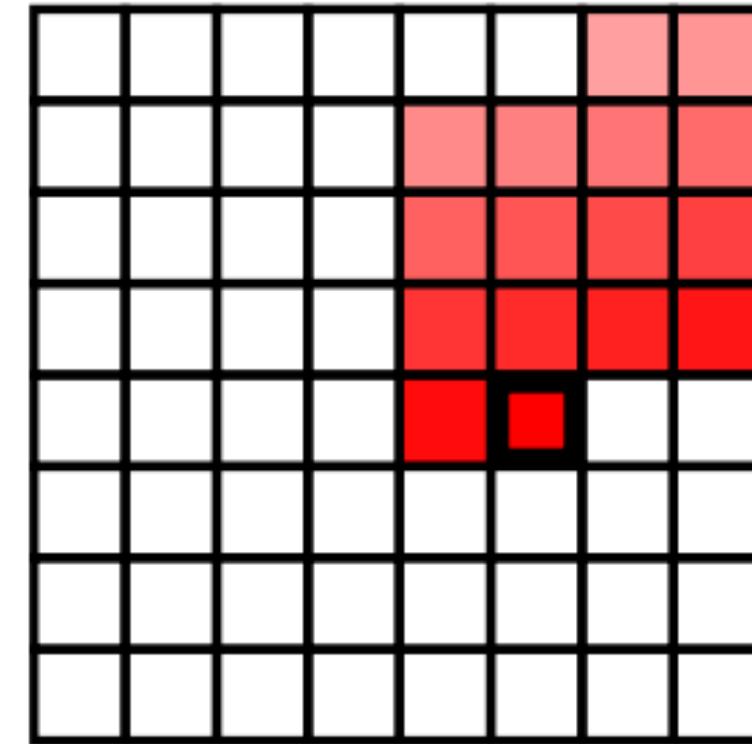
mem:1359

B mem:453 cache hit:389

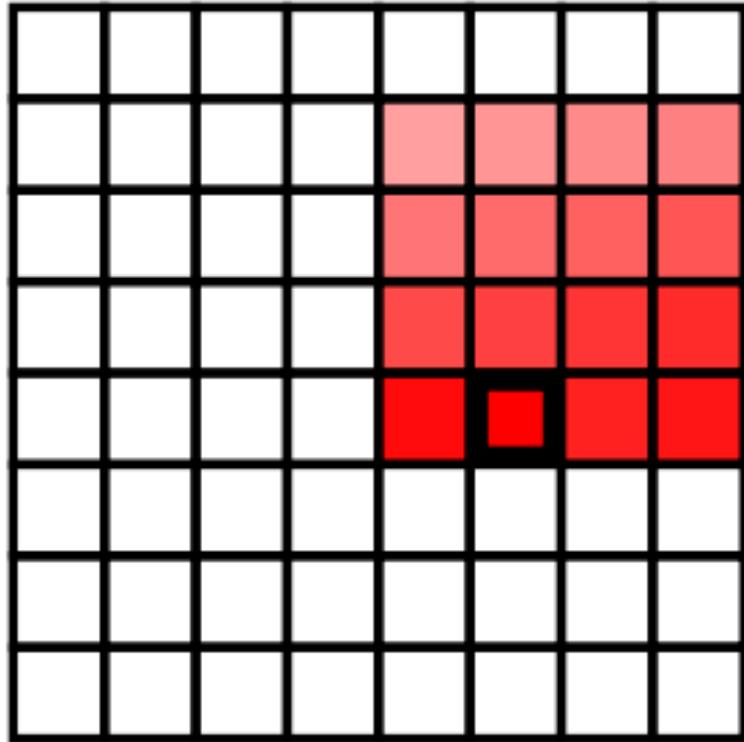


cache hits:1065 \approx 78%

C mem:453 cache hit:339

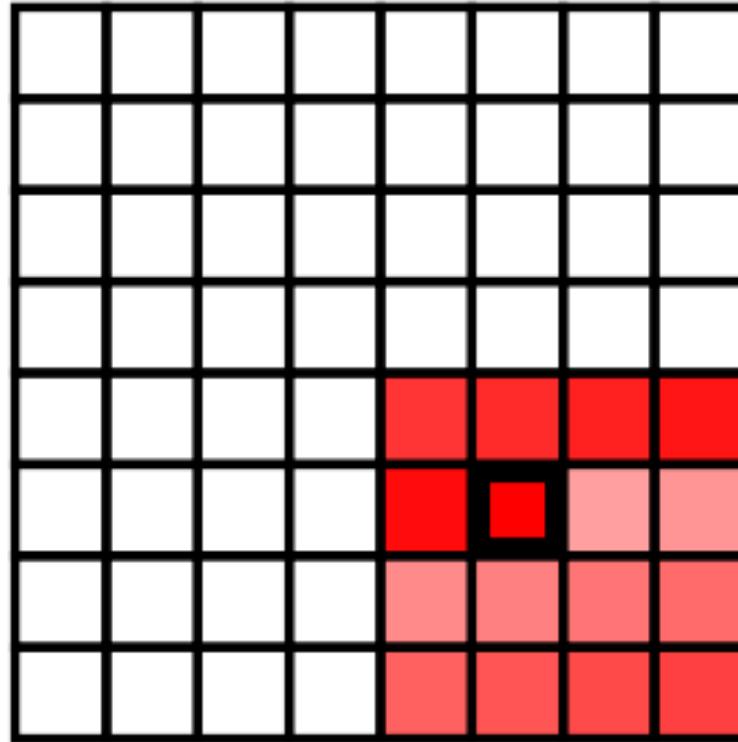


A mem:454 cache hit:338



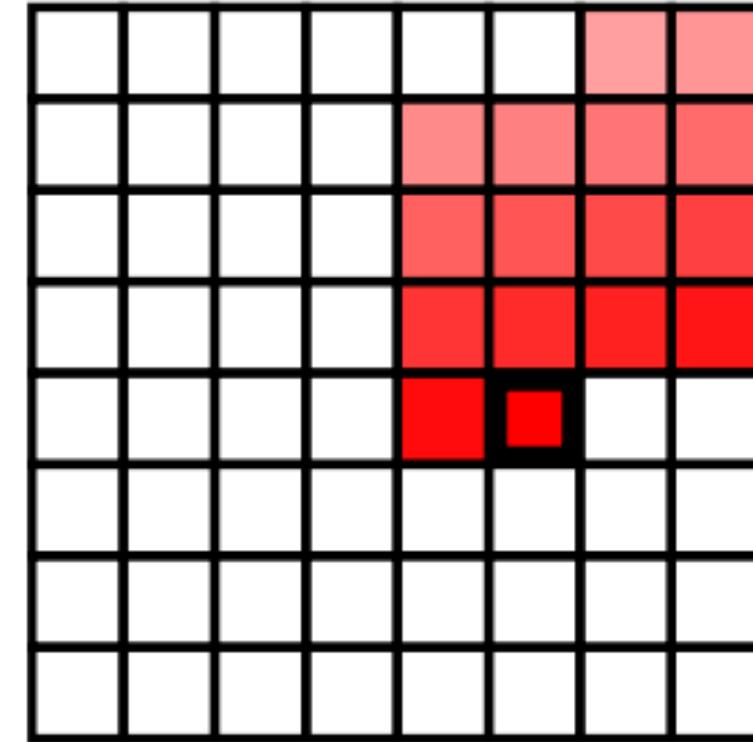
mem:1362

B mem:454 cache hit:390

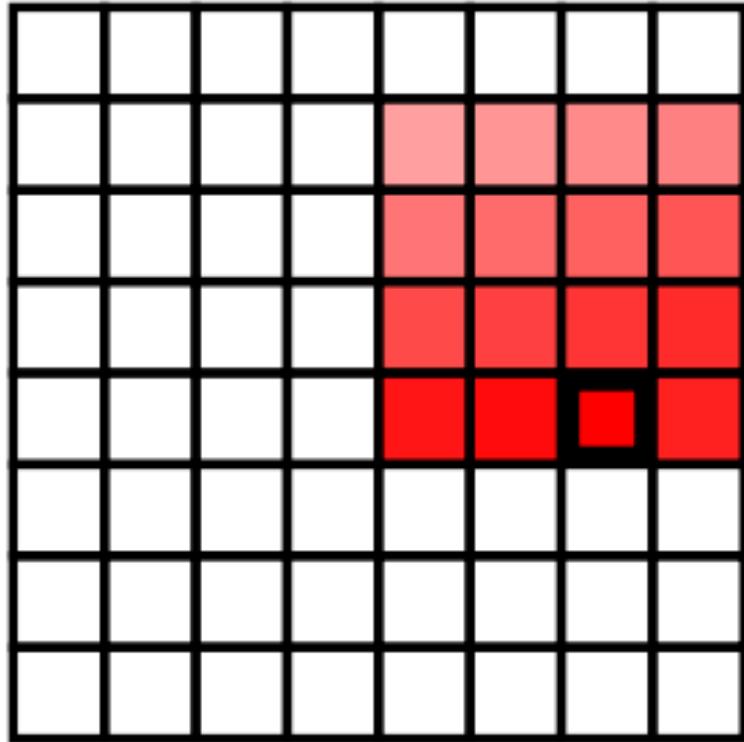


cache hits:1068 \approx 78%

C mem:454 cache hit:340

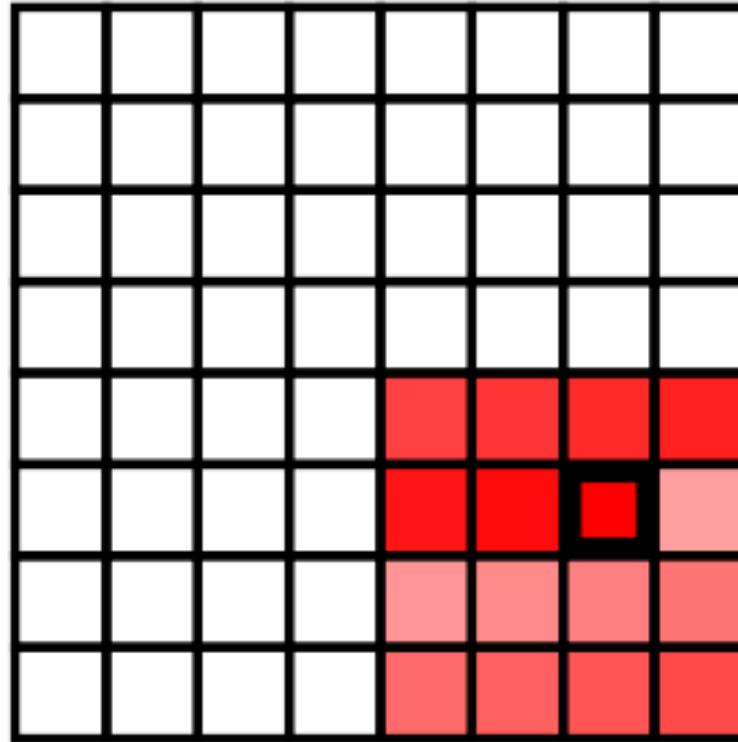


A mem:455 cache hit:339



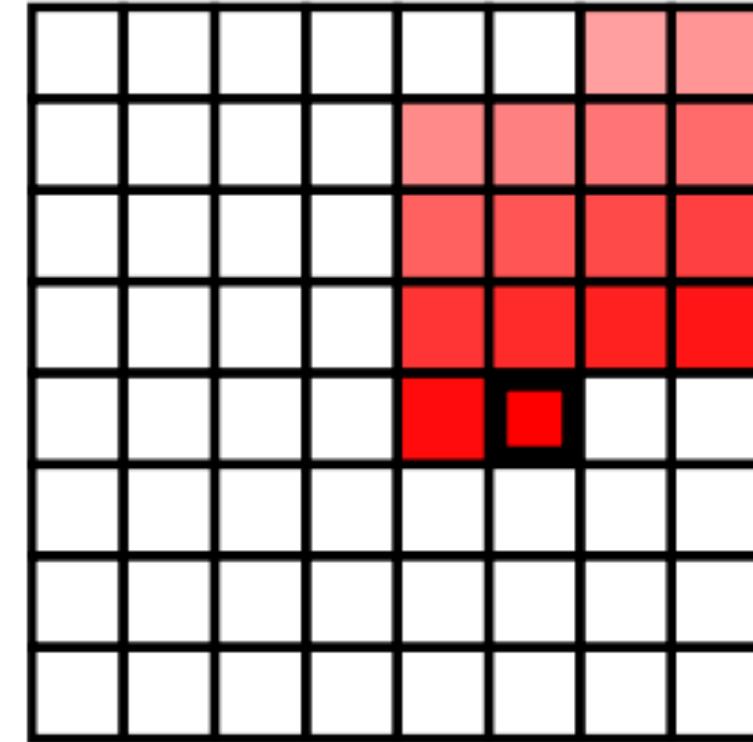
mem:1365

B mem:455 cache hit:391

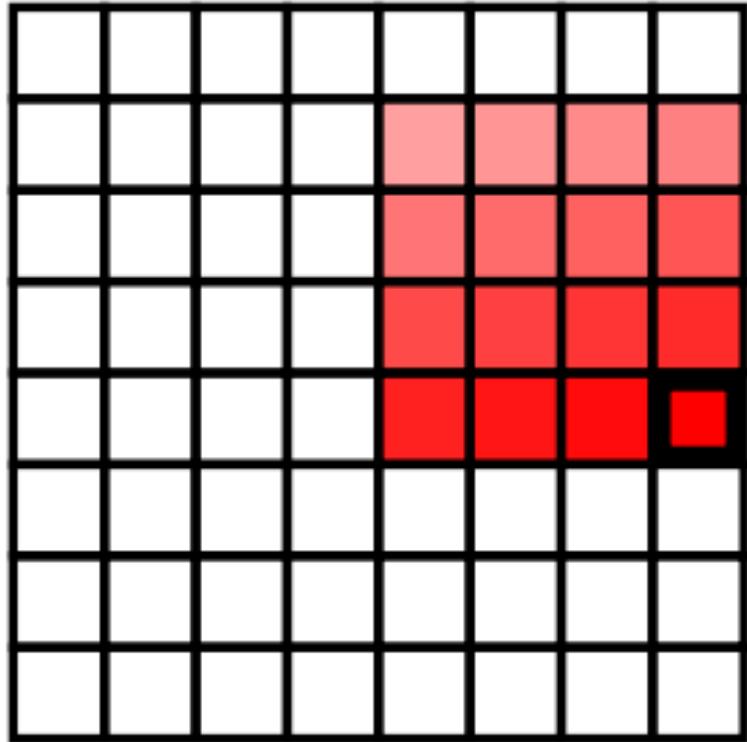


cache hits:1071 \approx 78%

C mem:455 cache hit:341

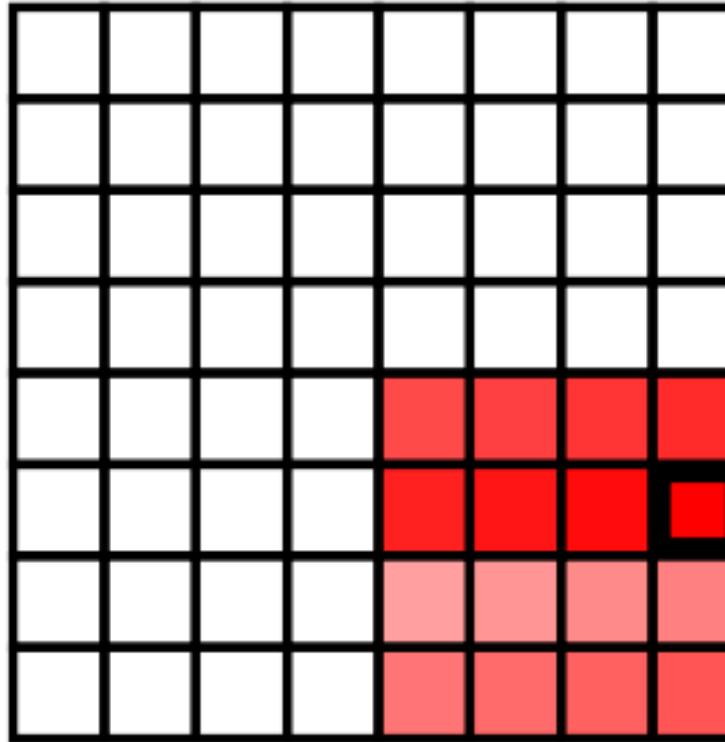


A mem:456 cache hit:340



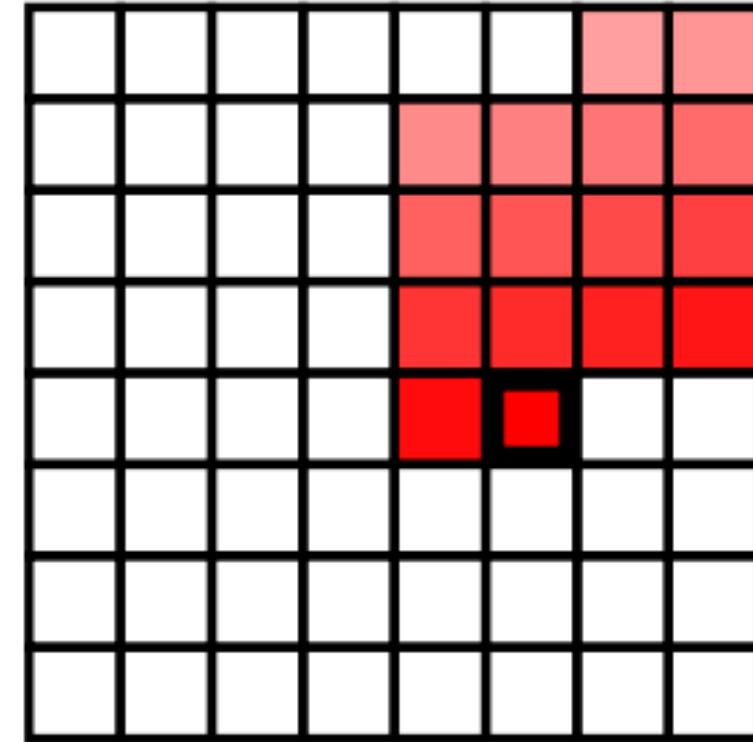
mem:1368

B mem:456 cache hit:392

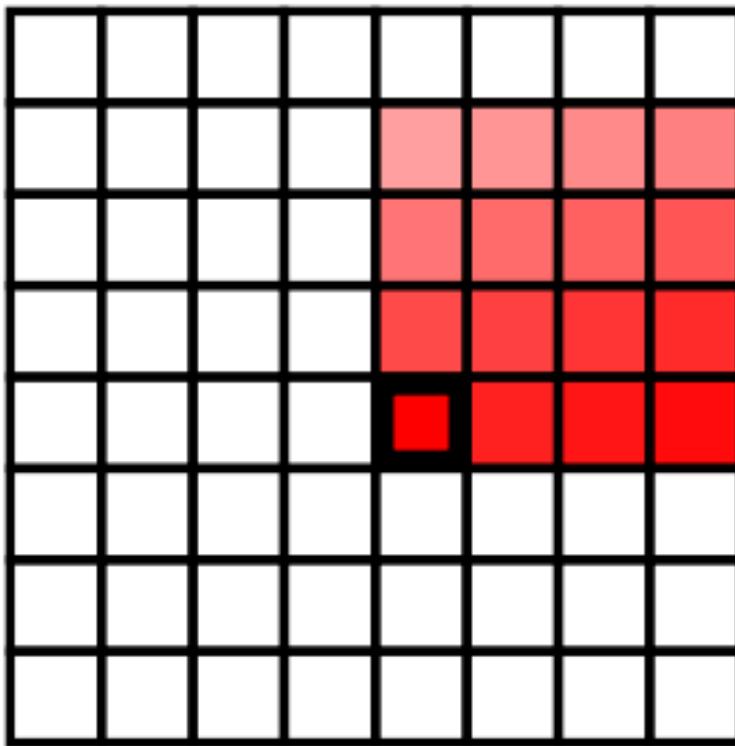


cache hits:1074 \approx 78%

C mem:456 cache hit:342

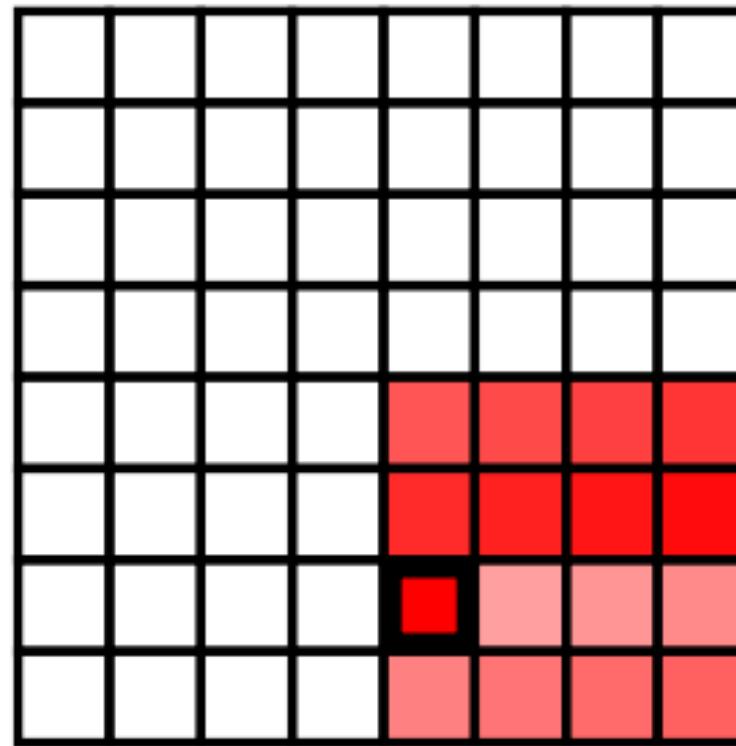


A mem:457 cache hit:341



mem:1371

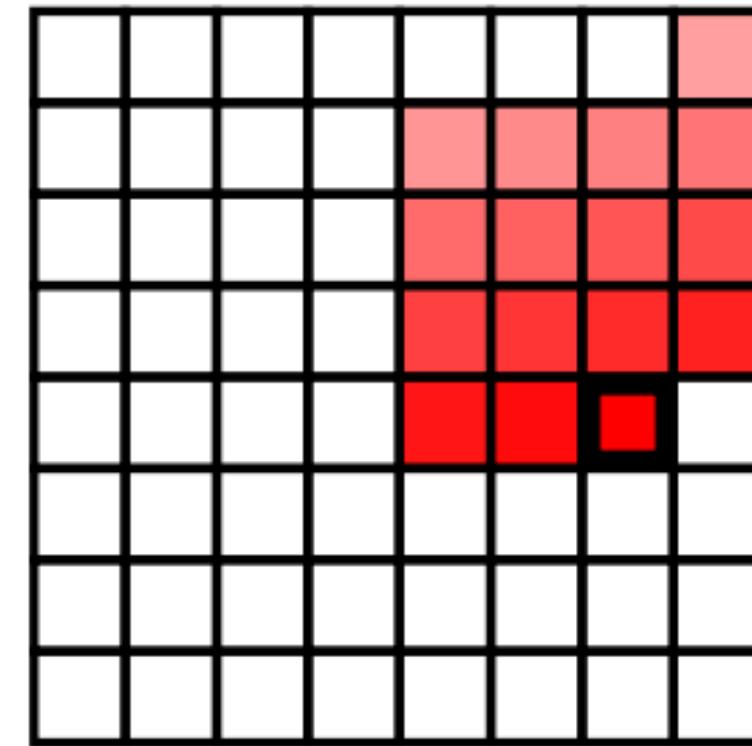
B mem:457 cache hit:393



cache hits:1076 \approx 78%

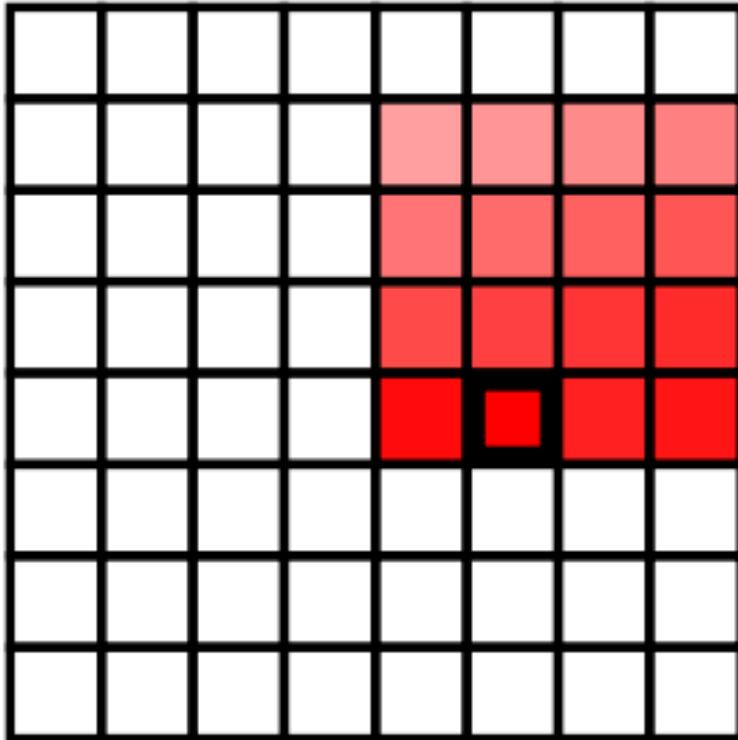
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C mem:457 cache hit:342

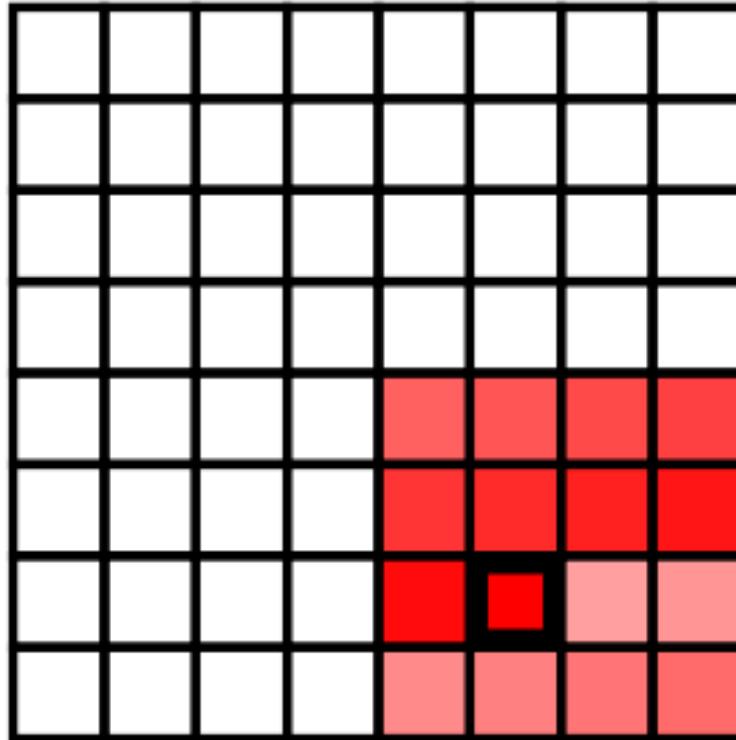


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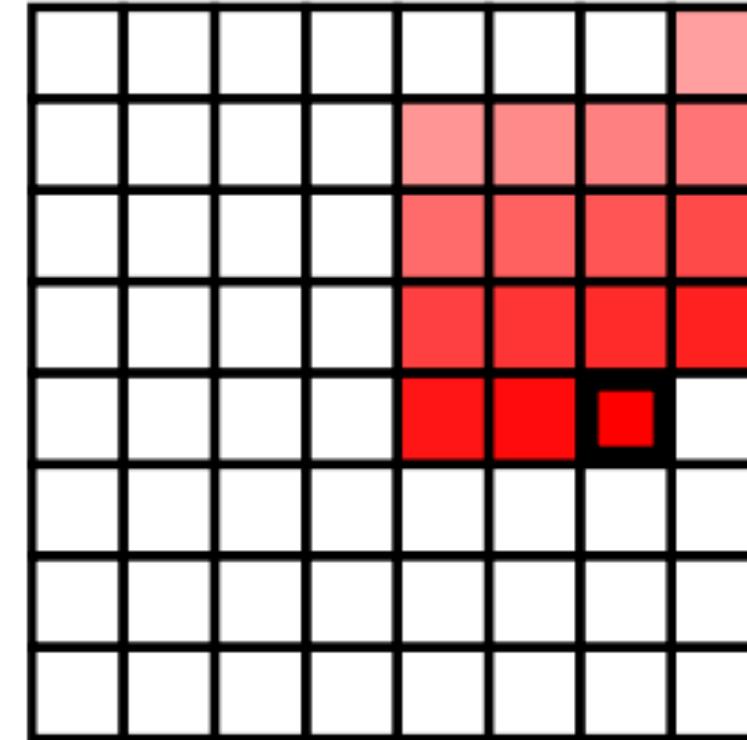
A mem:458 cache hit:342



B mem:458 cache hit:394



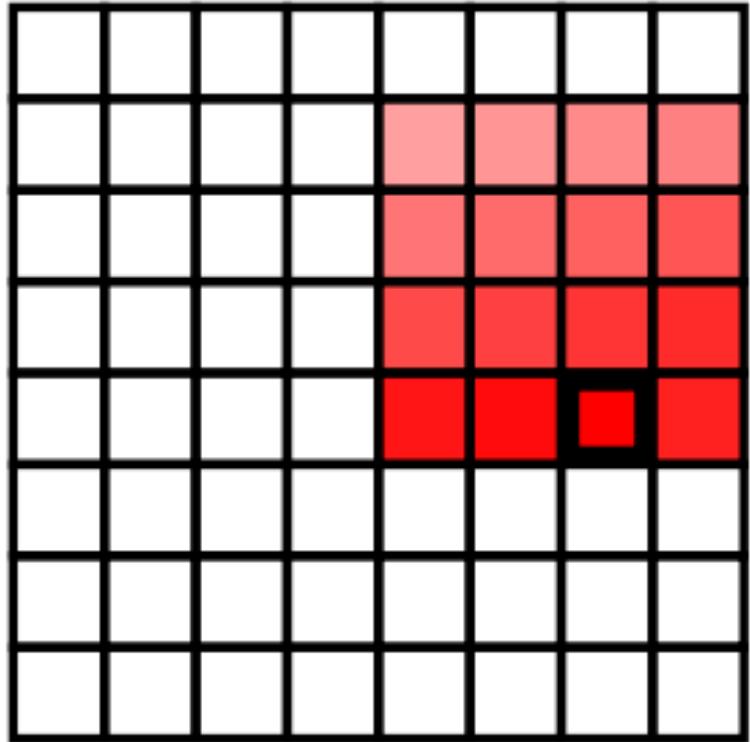
C mem:458 cache hit:343



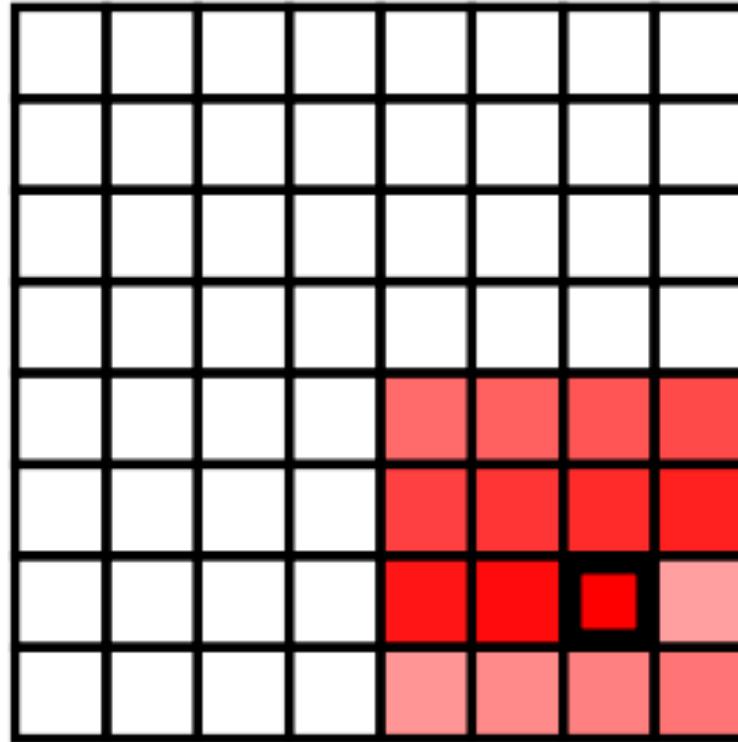
mem:1374

cache hits:1079 \approx 78%

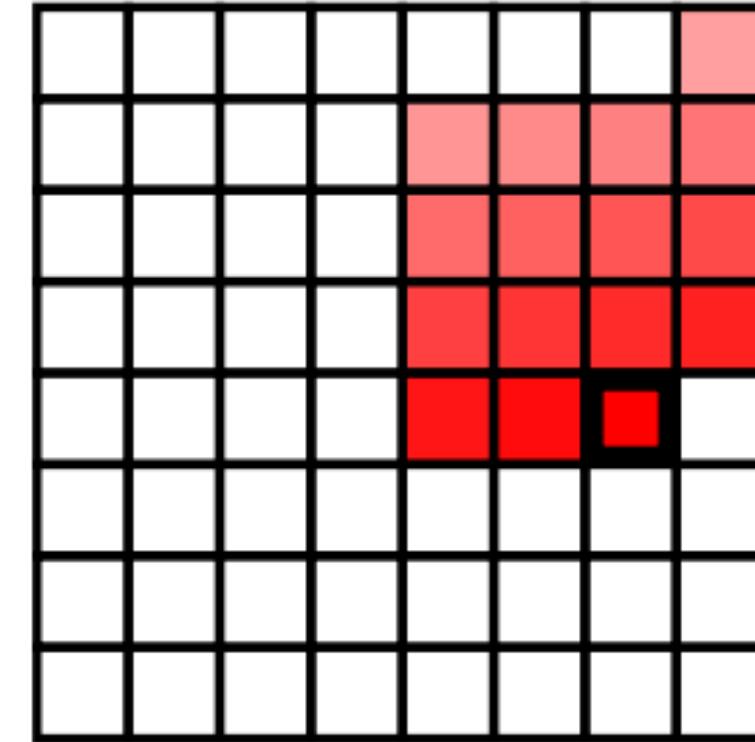
A mem:459 cache hit:343



B mem:459 cache hit:395



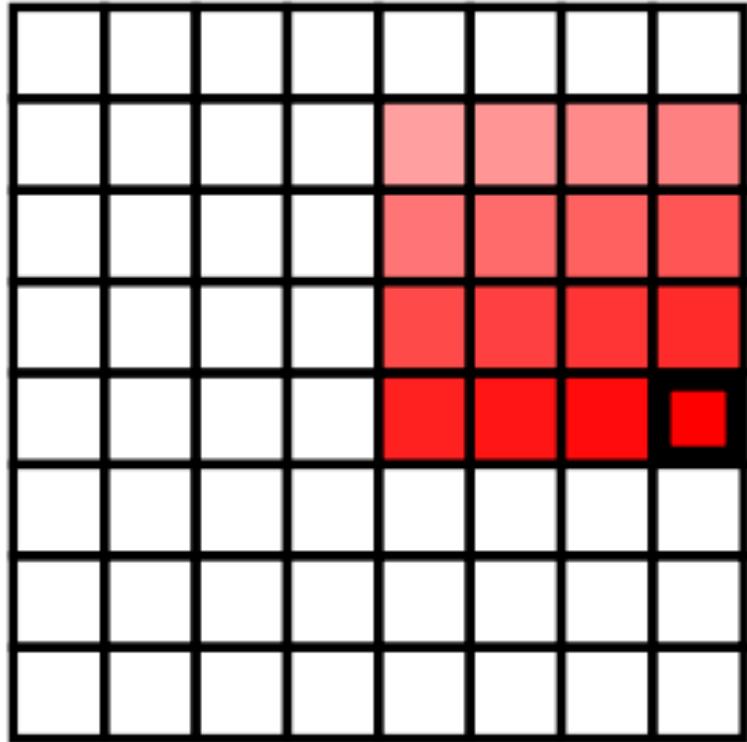
C mem:459 cache hit:344



mem:1377

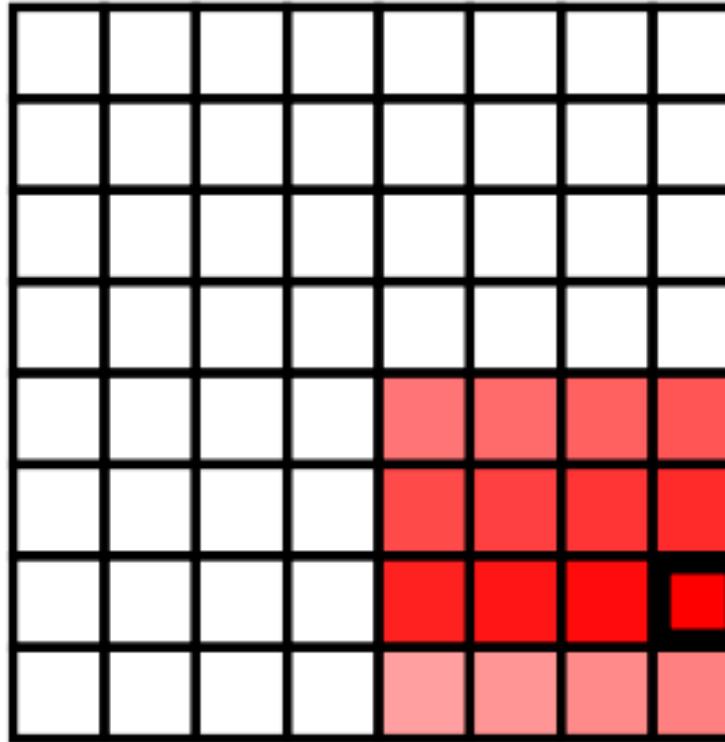
cache hits:1082 \approx 78%

A mem:460 cache hit:344



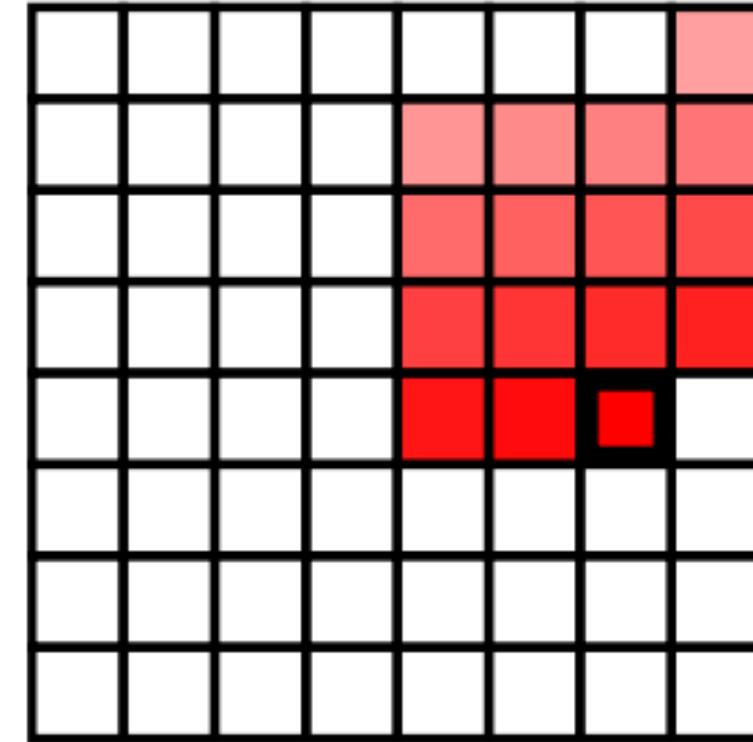
mem:1380

B mem:460 cache hit:396

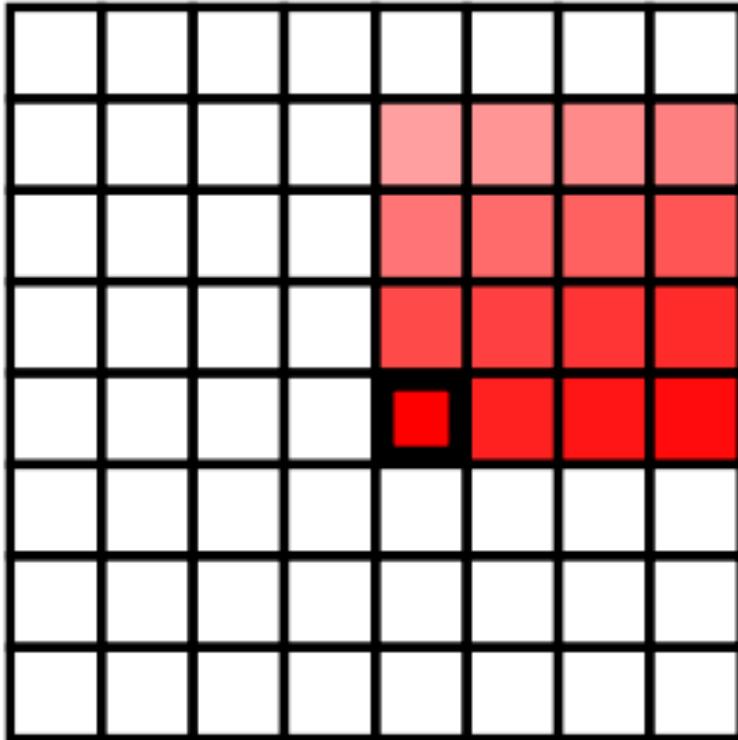


cache hits:1085 \approx 78%

C mem:460 cache hit:345

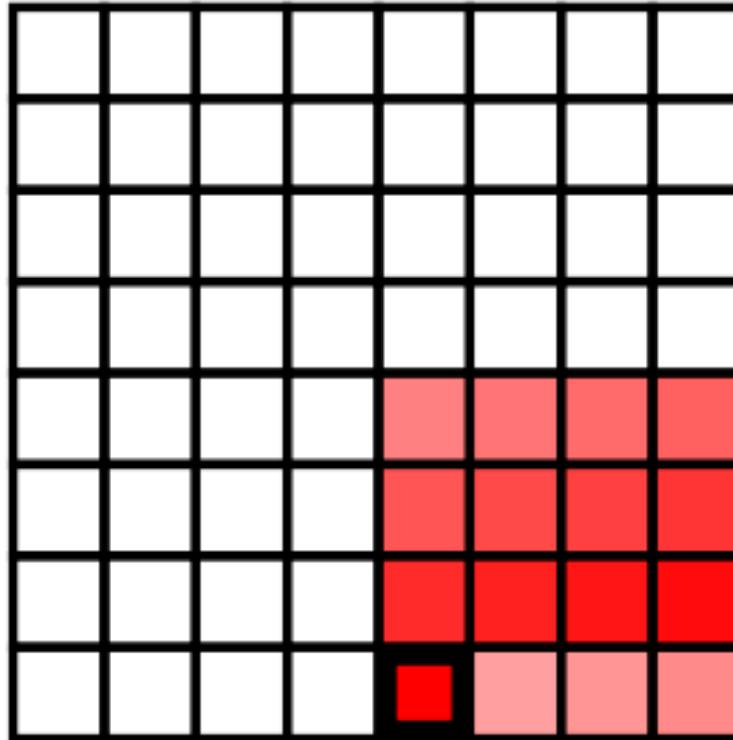


A mem:461 cache hit:345



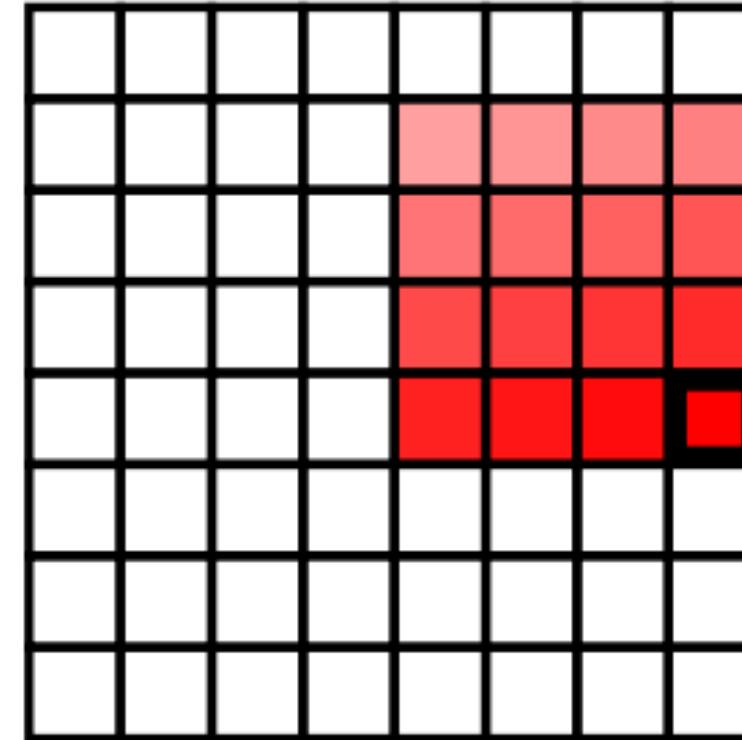
mem:1383

B mem:461 cache hit:397

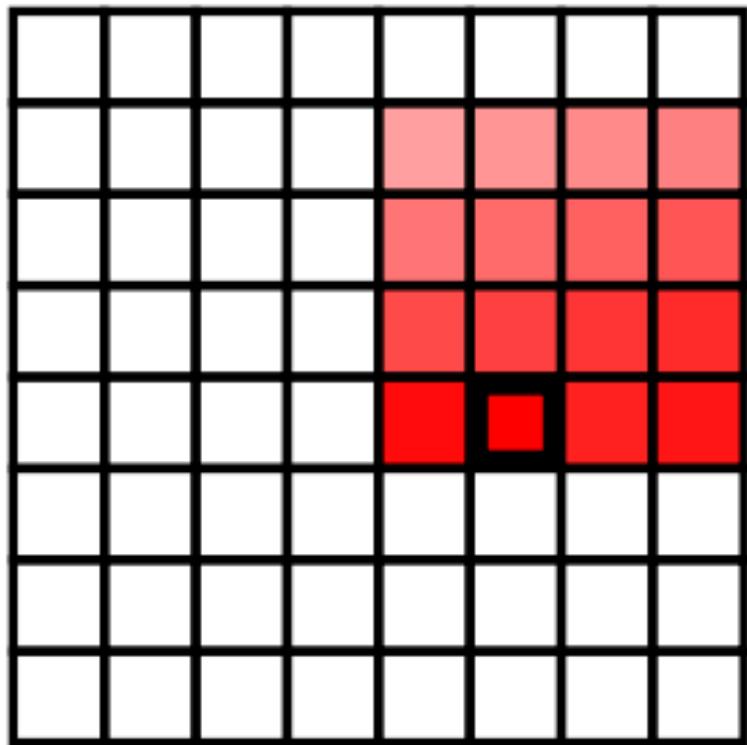


cache hits:1087 \approx 78%

C mem:461 cache hit:345

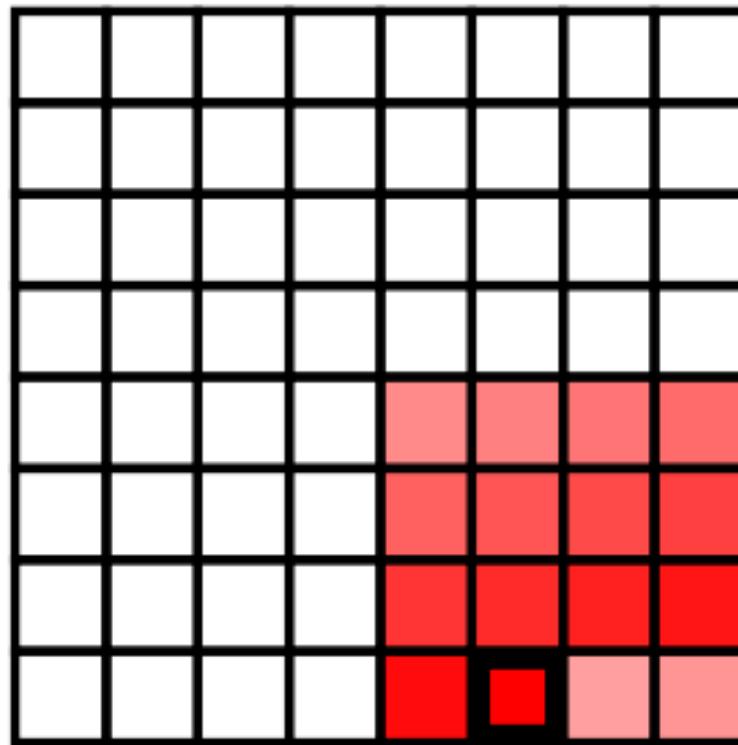


A mem:462 cache hit:346



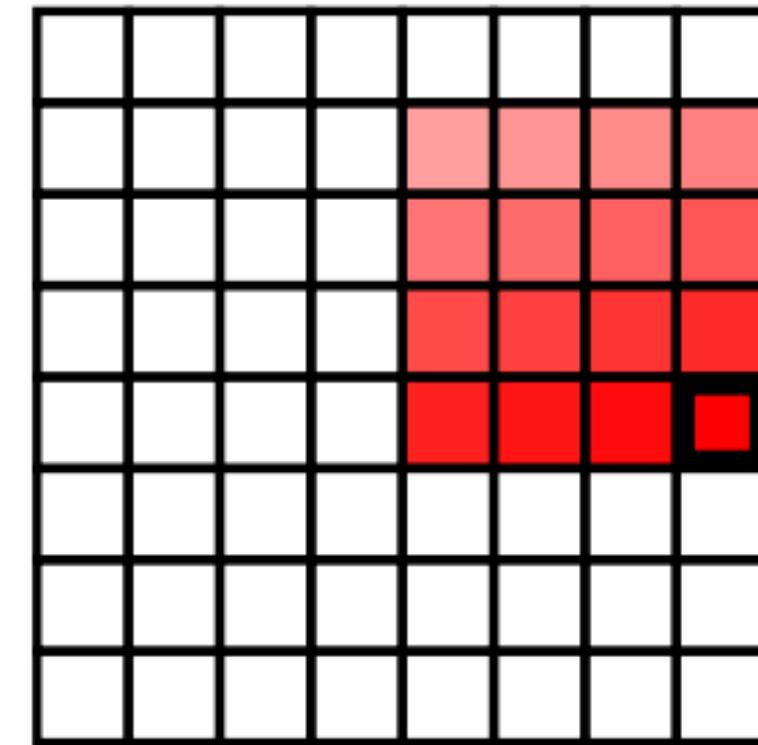
mem:1386

B mem:462 cache hit:398

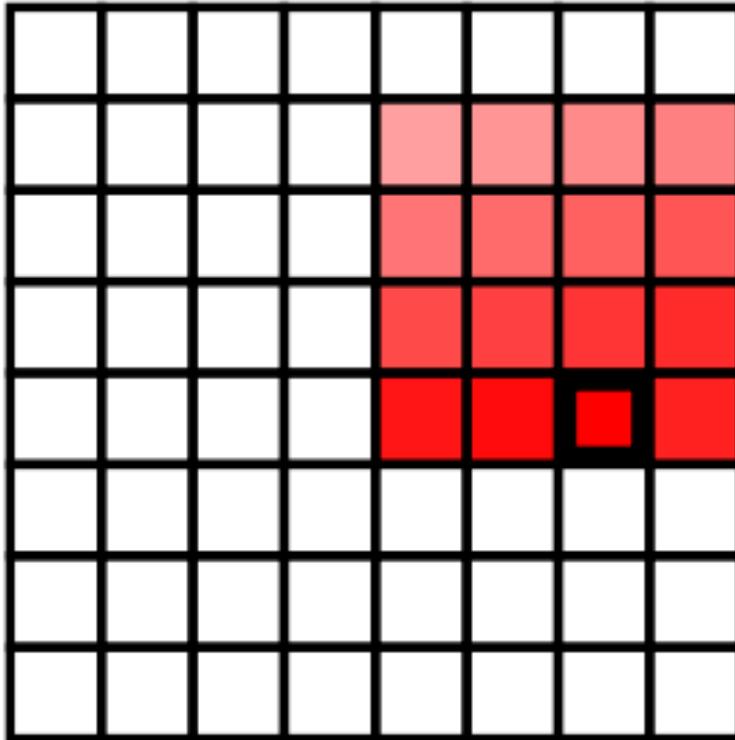


cache hits:1090 \approx 78%

C mem:462 cache hit:346

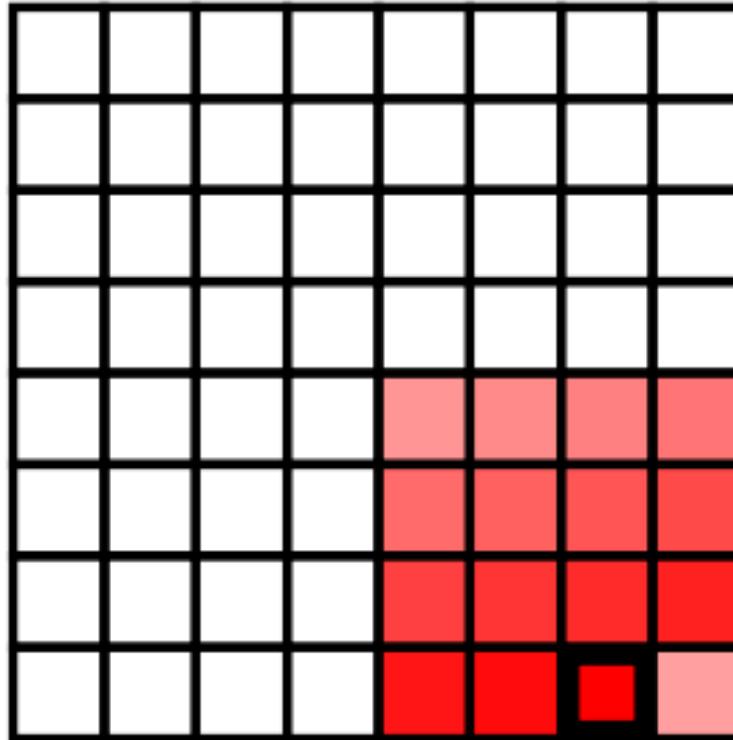


A mem:463 cache hit:347



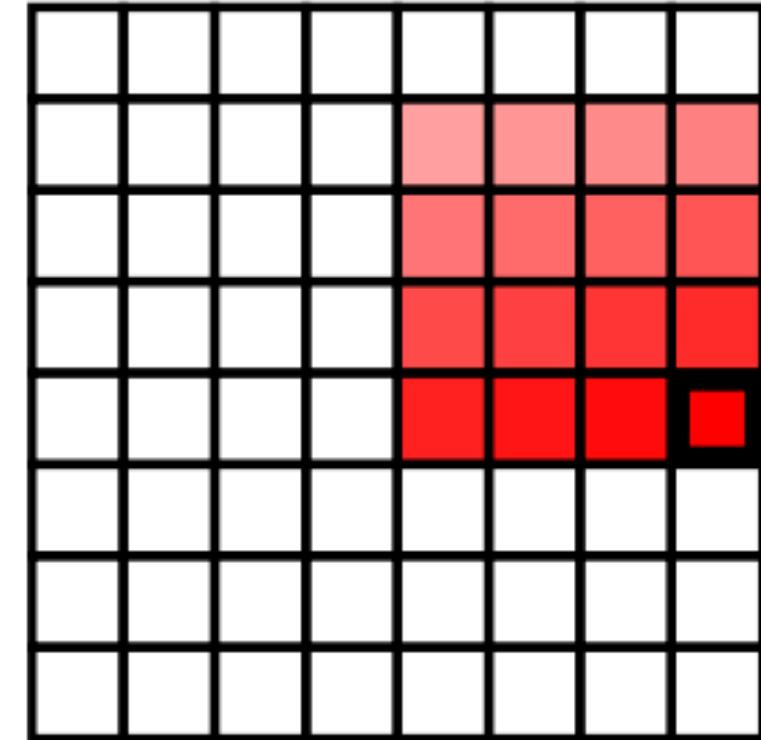
mem:1389

B mem:463 cache hit:399

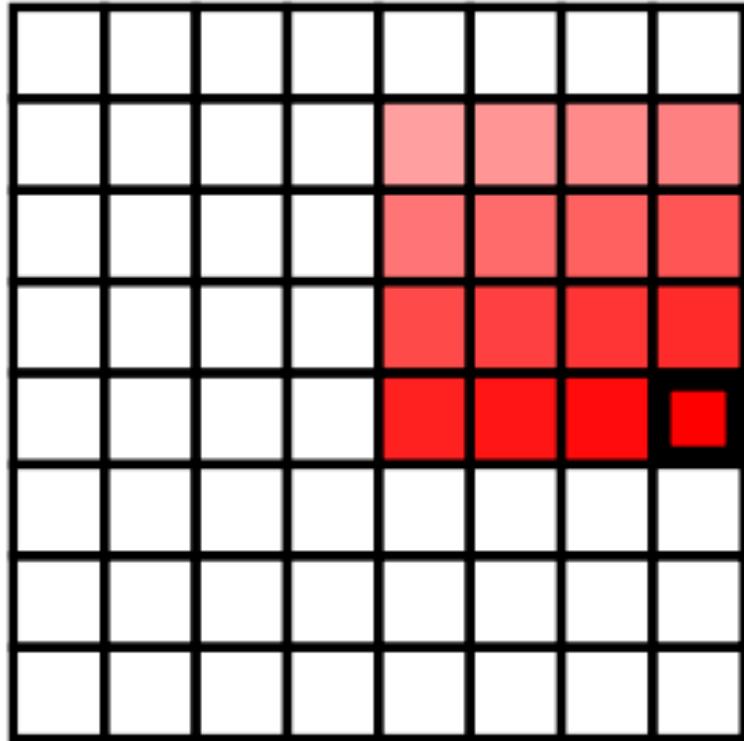


cache hits:1093 \approx 78%

C mem:463 cache hit:347

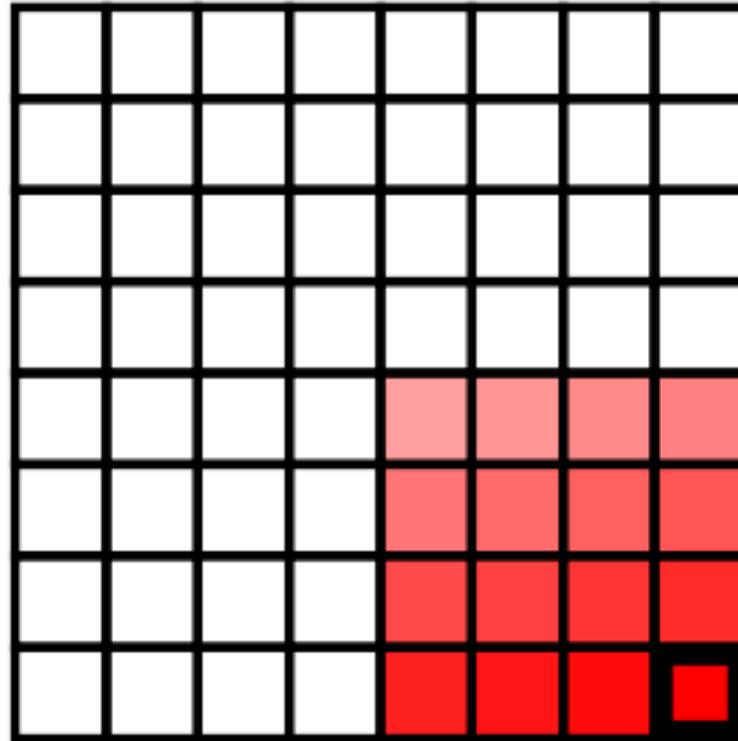


A mem:464 cache hit:348



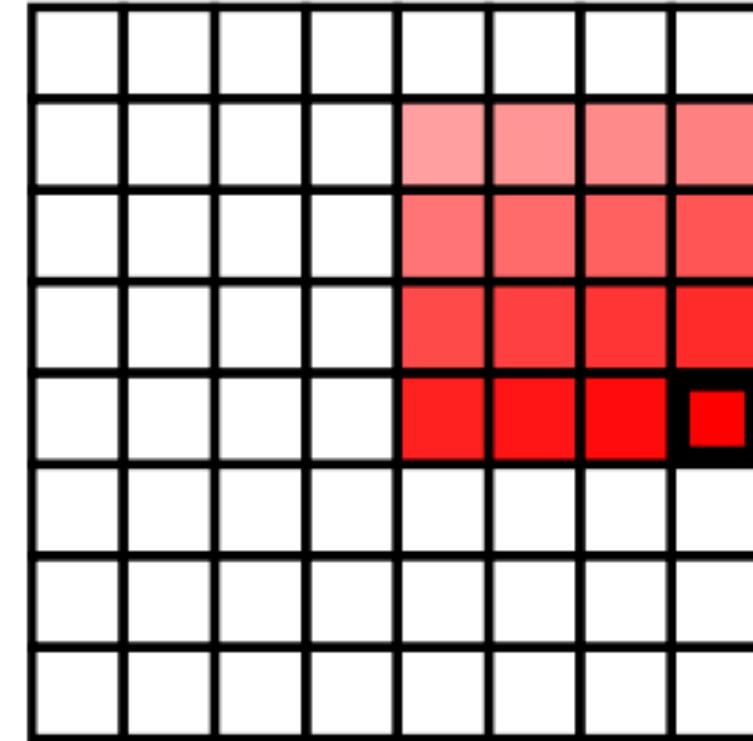
mem:1392

B mem:464 cache hit:400

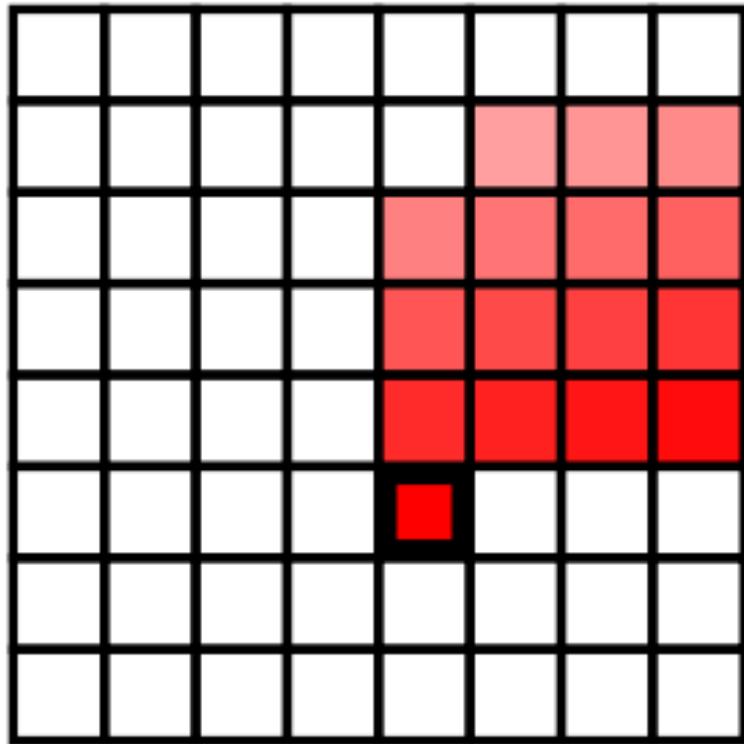


cache hits:1096 \approx 78%

C mem:464 cache hit:348

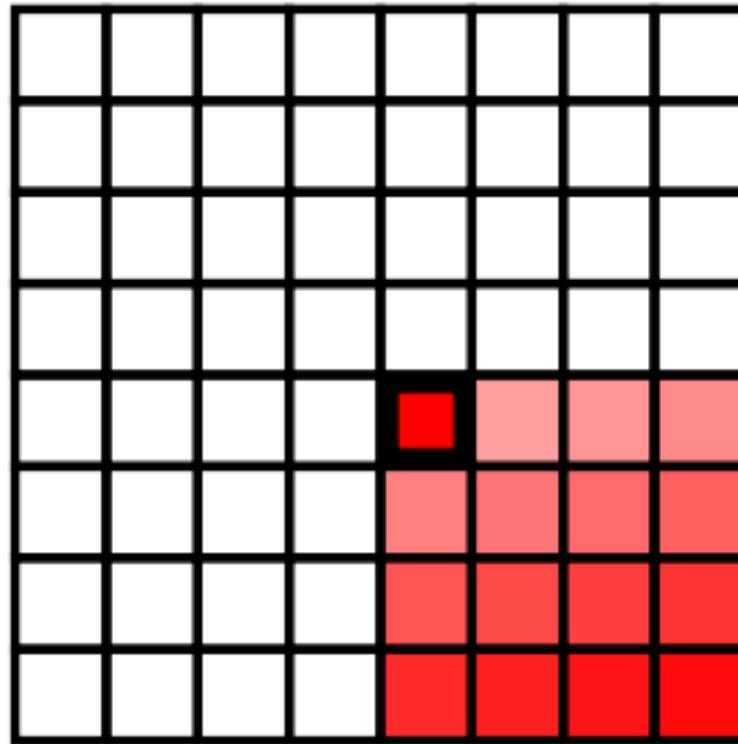


A mem:465 cache hit:348



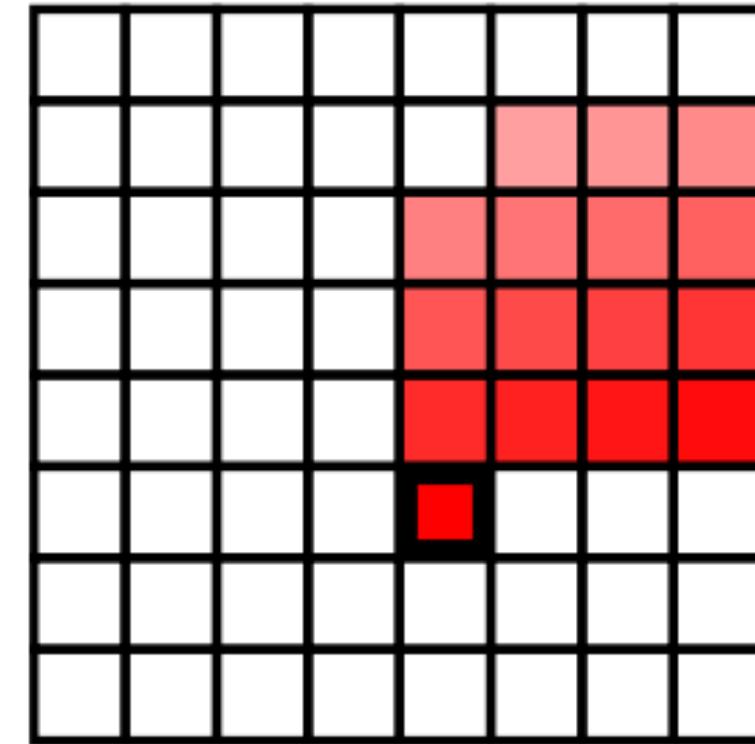
mem:1395

B mem:465 cache hit:401

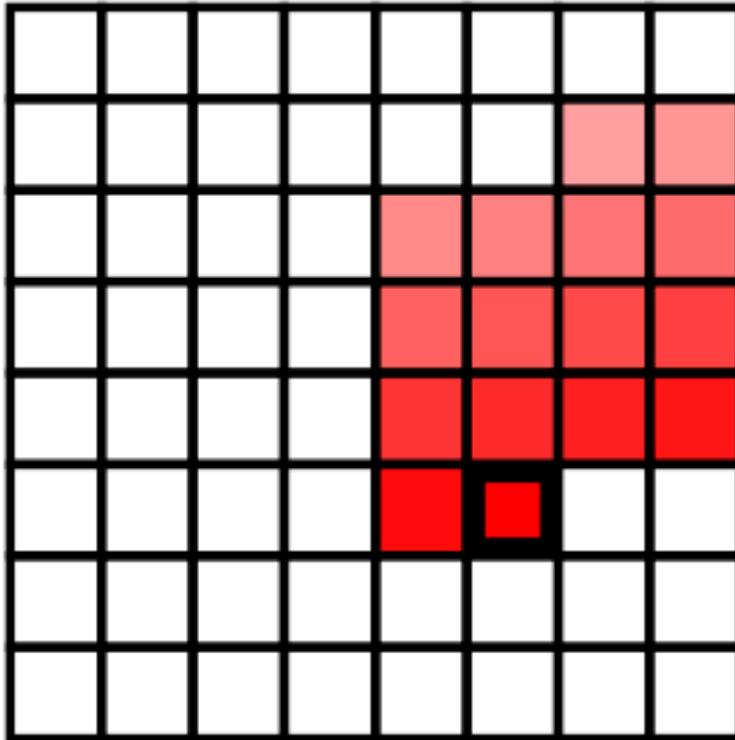


cache hits:1097 \approx 78%

C mem:465 cache hit:348

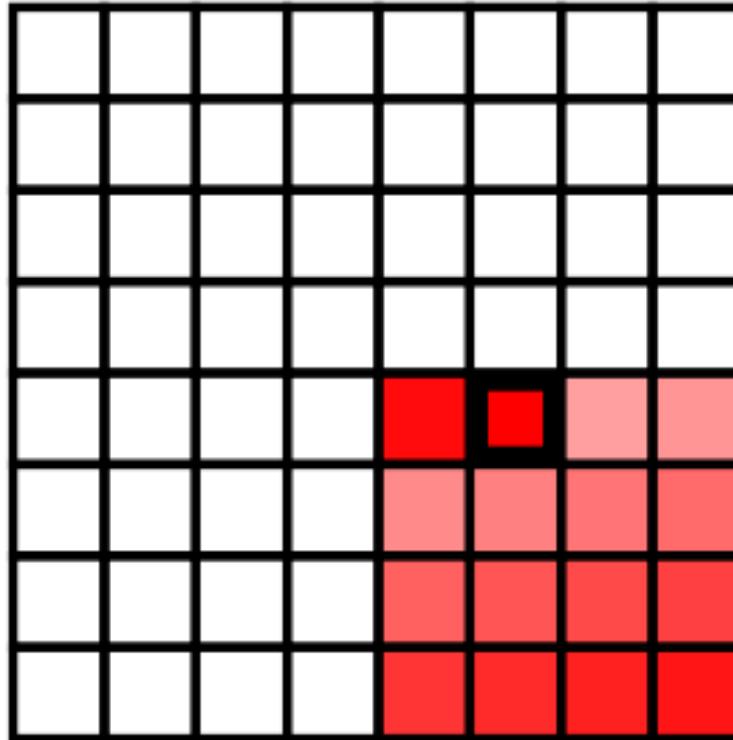


A mem:466 cache hit:348



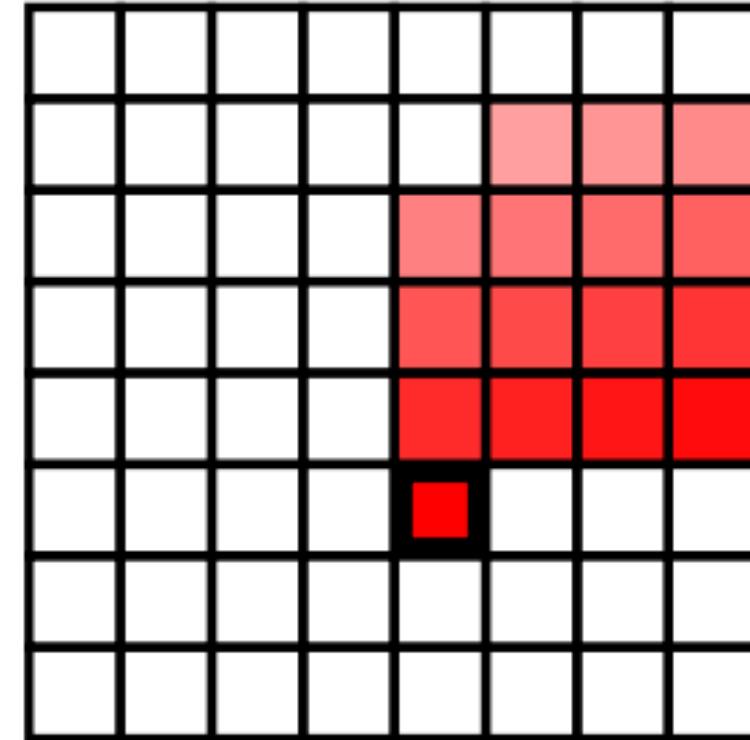
mem:1398

B mem:466 cache hit:402

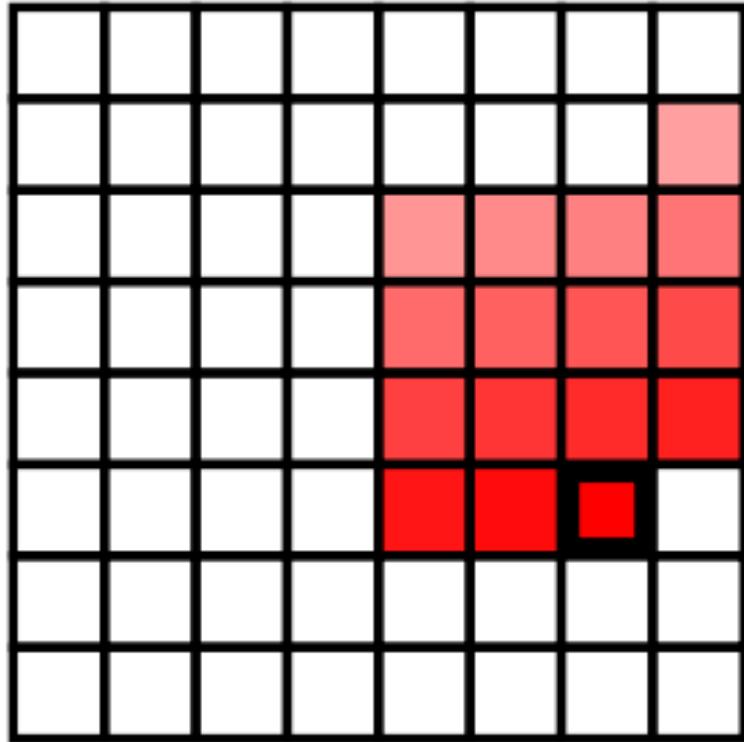


cache hits:1099 \approx 78%

C mem:466 cache hit:349

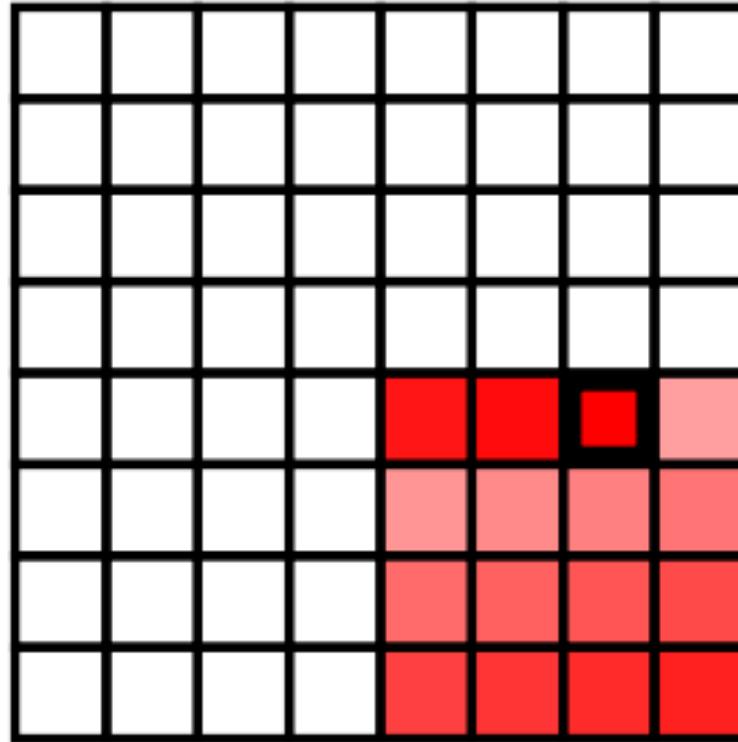


A mem:467 cache hit:348



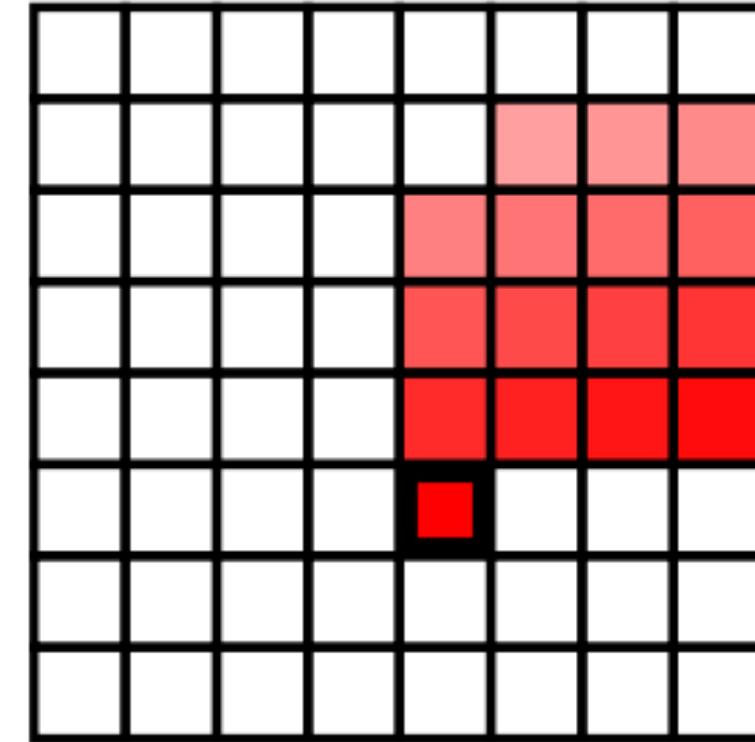
mem:1401

B mem:467 cache hit:403

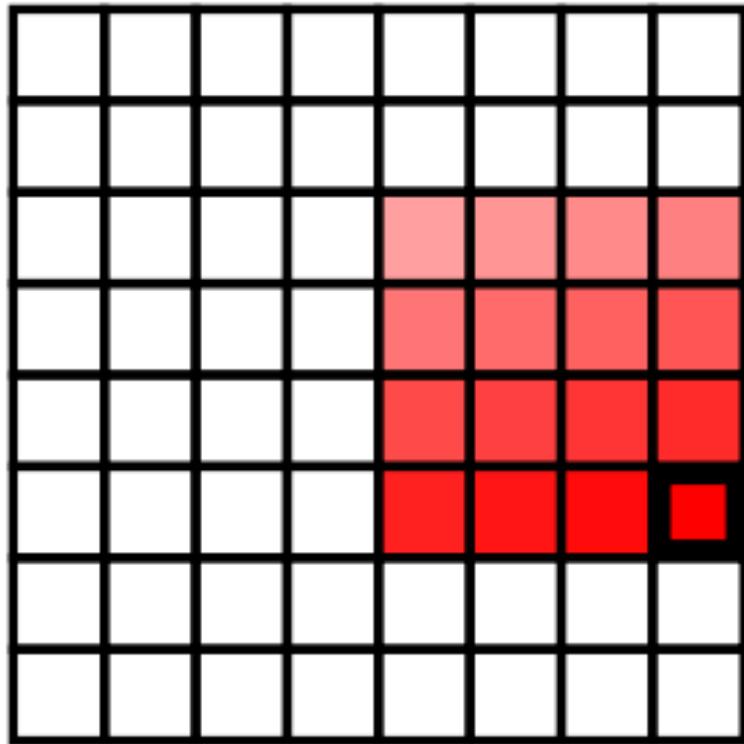


cache hits:1101 \approx 78%

C mem:467 cache hit:350

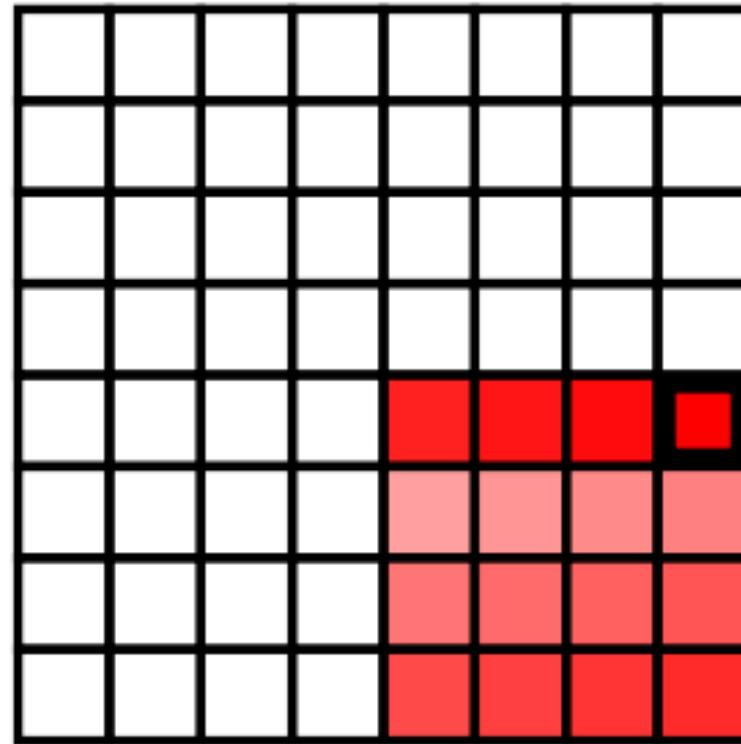


A mem:468 cache hit:348



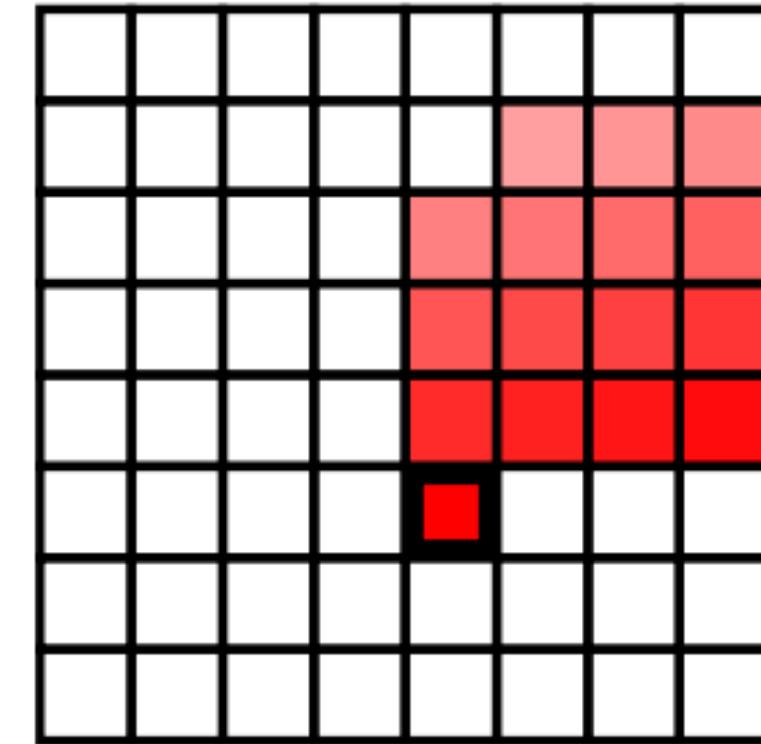
mem:1404

B mem:468 cache hit:404

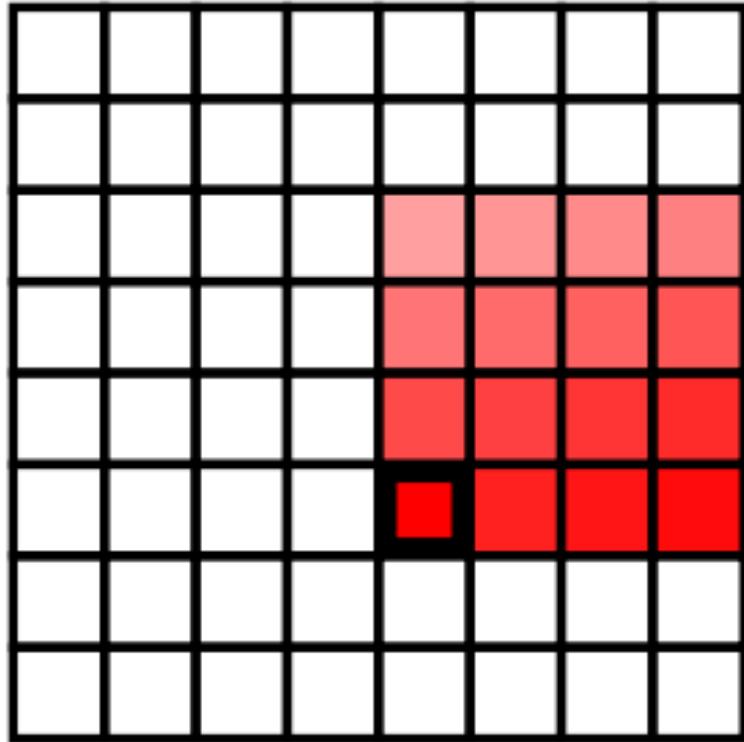


cache hits:1103 \approx 78%

C mem:468 cache hit:351

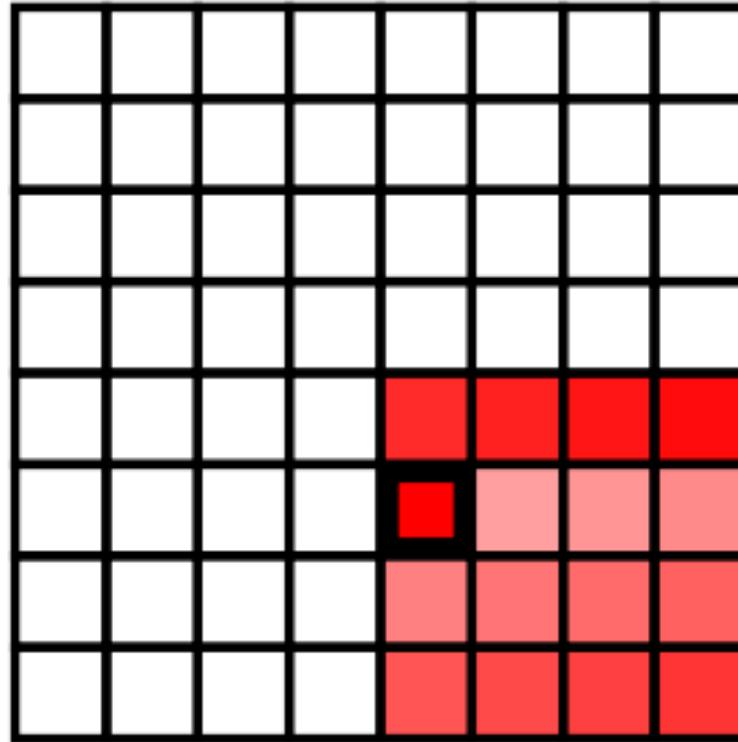


A mem:469 cache hit:349



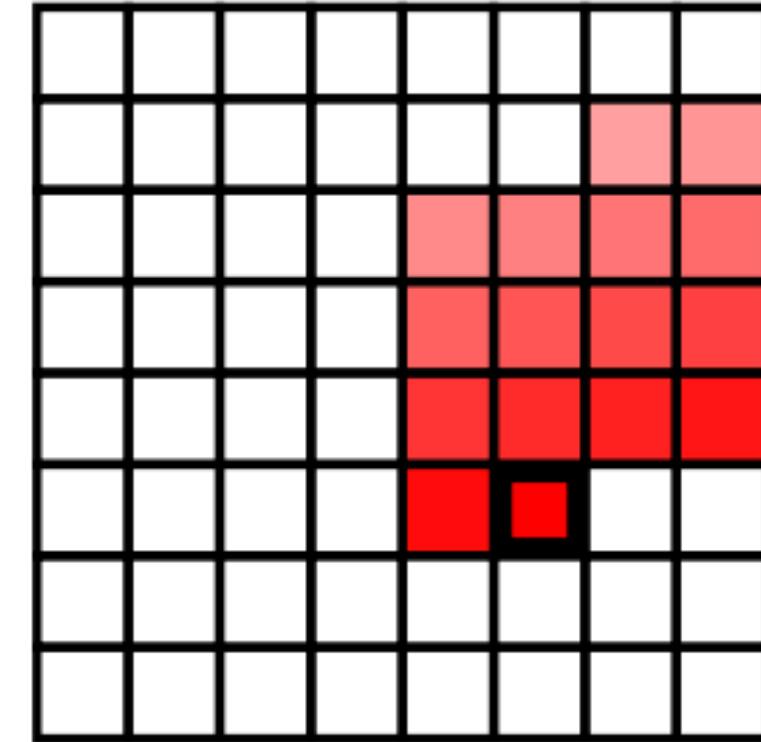
mem:1407

B mem:469 cache hit:405

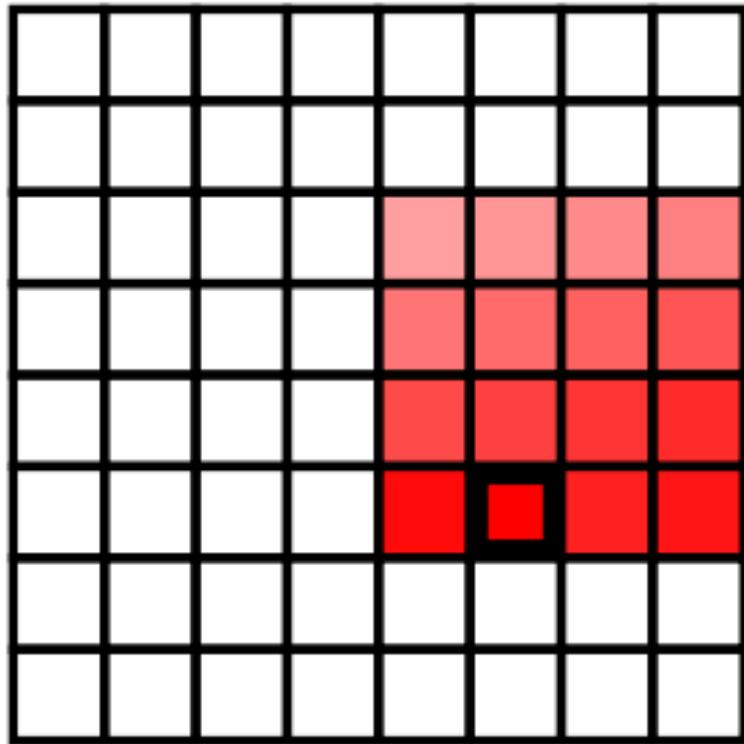


cache hits:1105 \approx 78%

C mem:469 cache hit:351

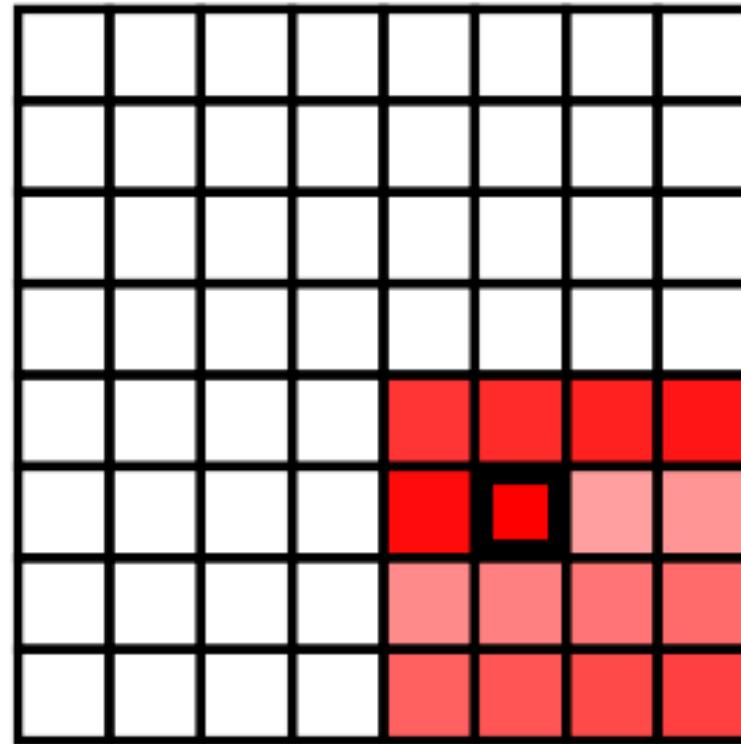


A mem:470 cache hit:350



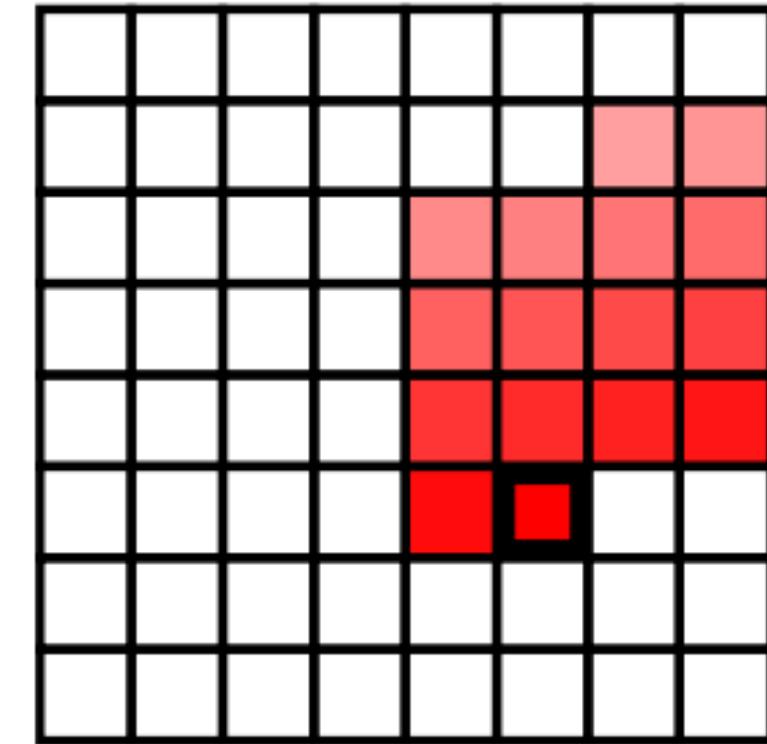
mem:1410

B mem:470 cache hit:406

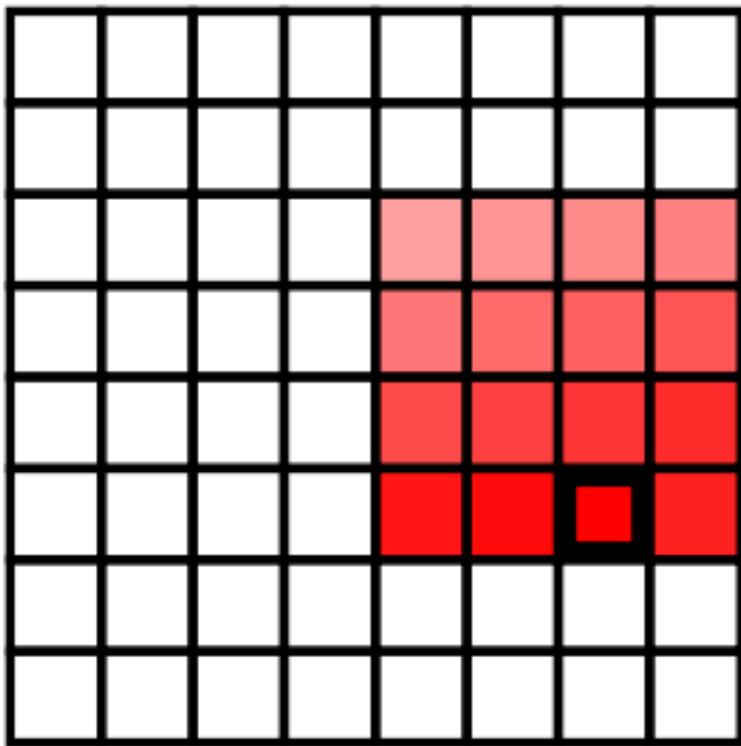


cache hits:1108 \approx 78%

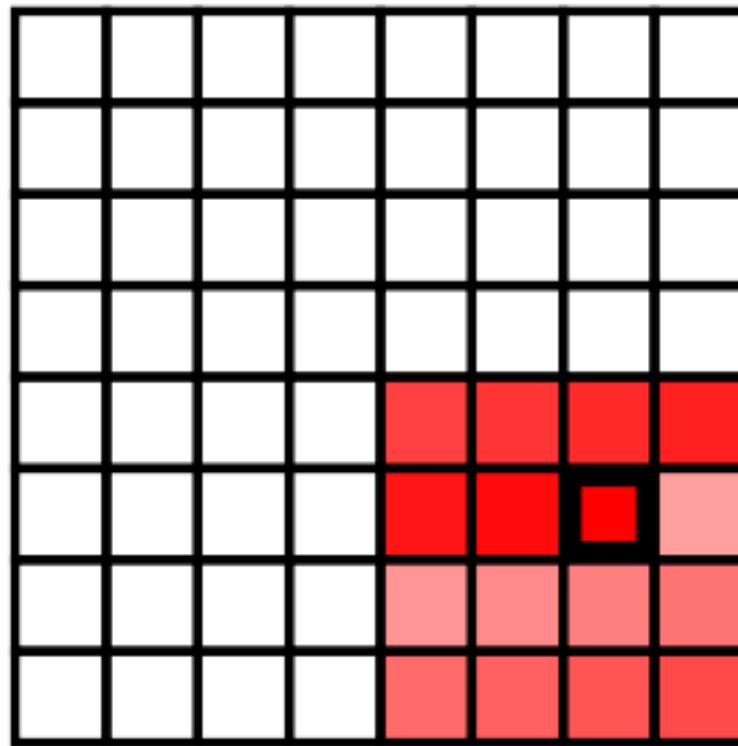
C mem:470 cache hit:352



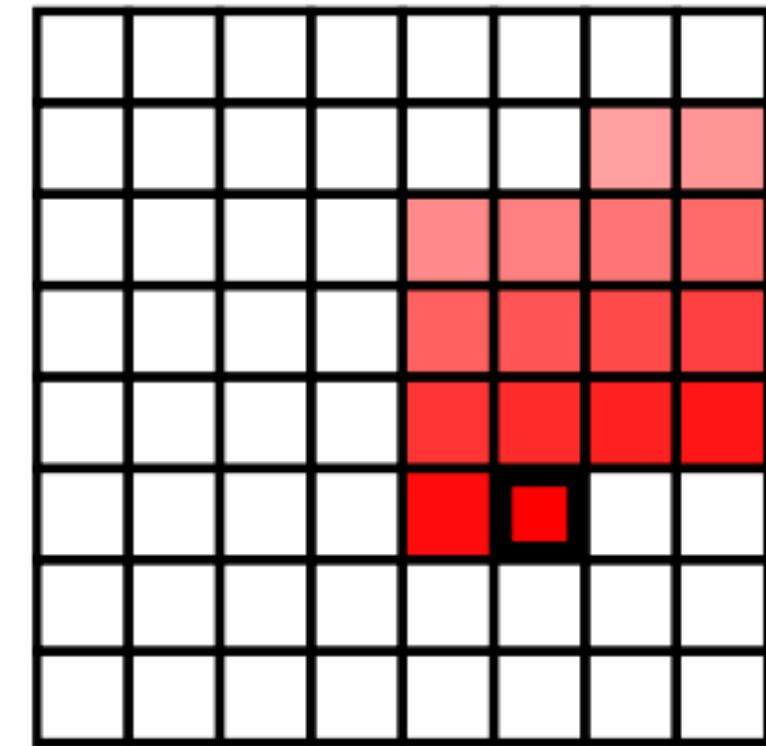
A mem:471 cache hit:351



B mem:471 cache hit:407



C mem:471 cache hit:353



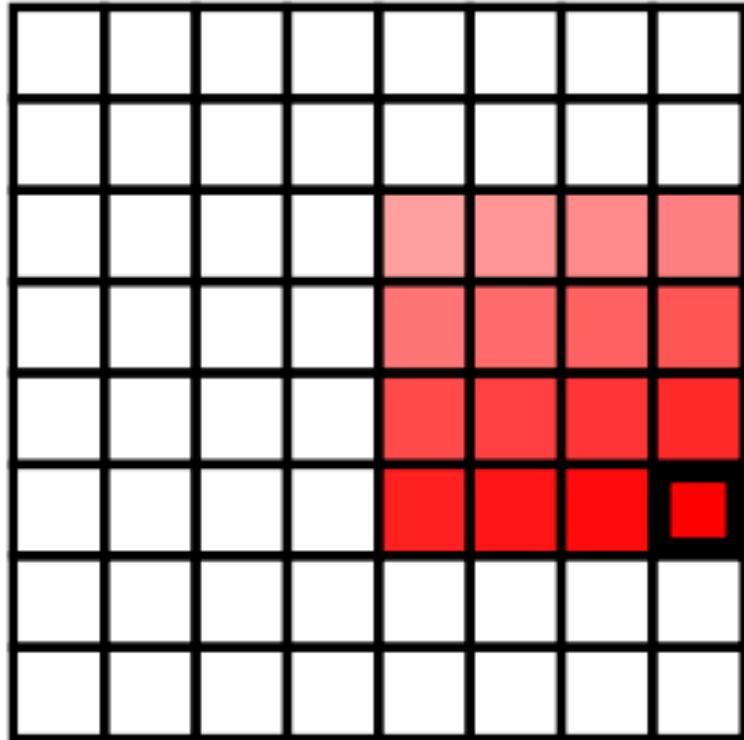
mem:1413

cache hits:1111 \approx 78%

\times

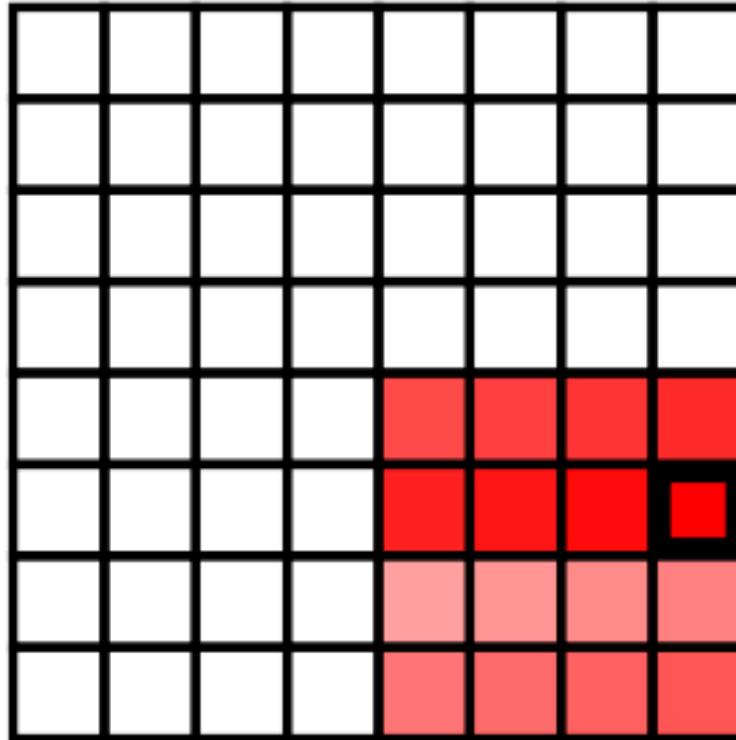
=

A mem:472 cache hit:352



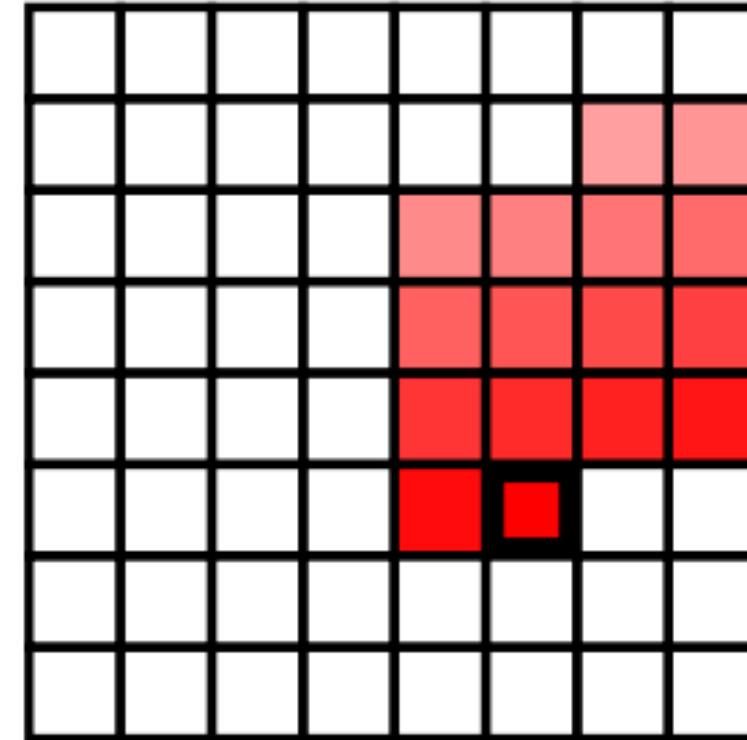
mem:1416

B mem:472 cache hit:408

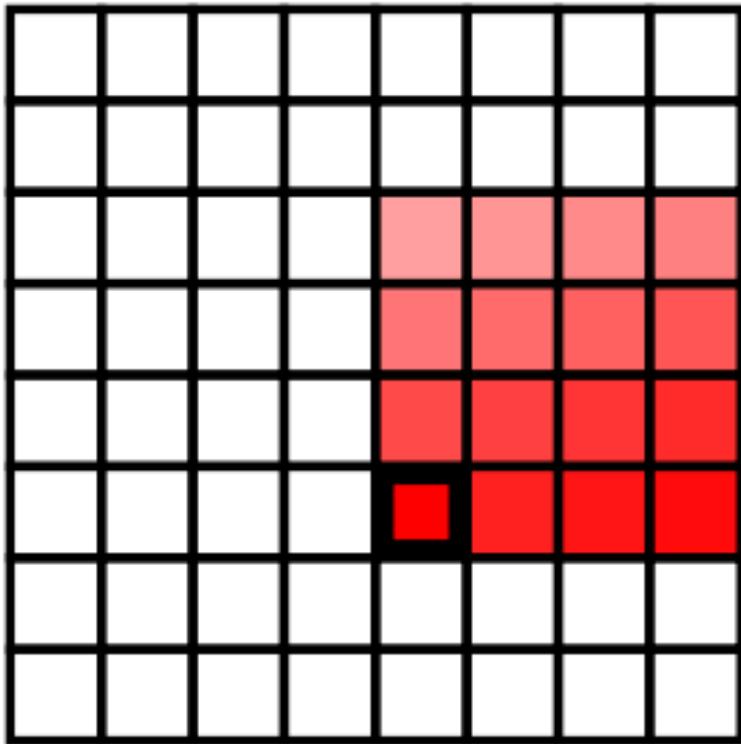


cache hits:1114 \approx 78%

C mem:472 cache hit:354

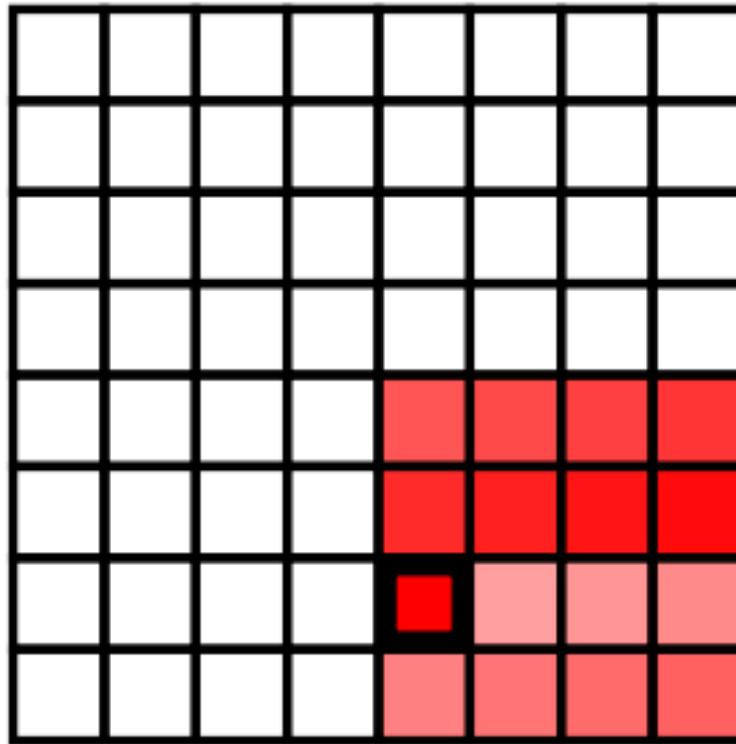


A mem:473 cache hit:353



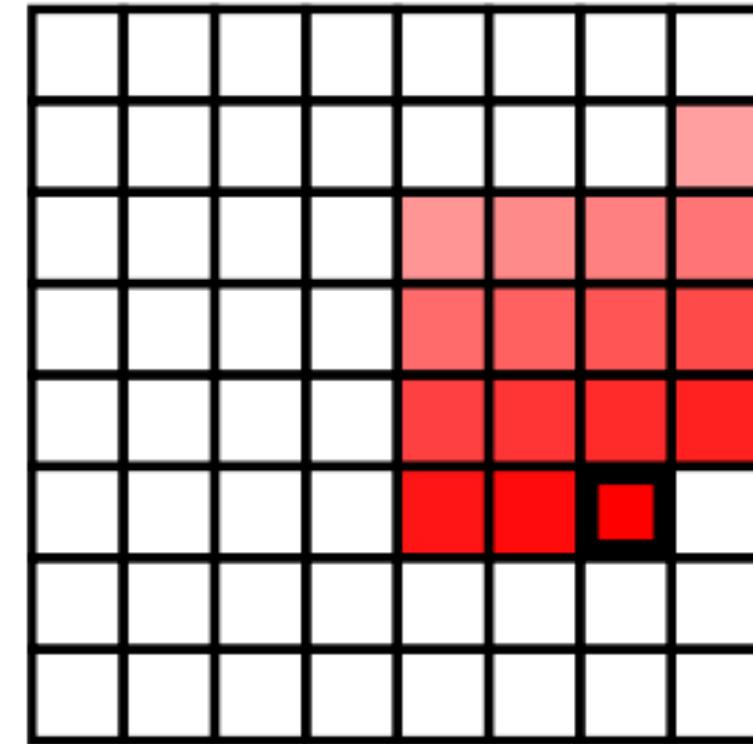
mem:1419

B mem:473 cache hit:409

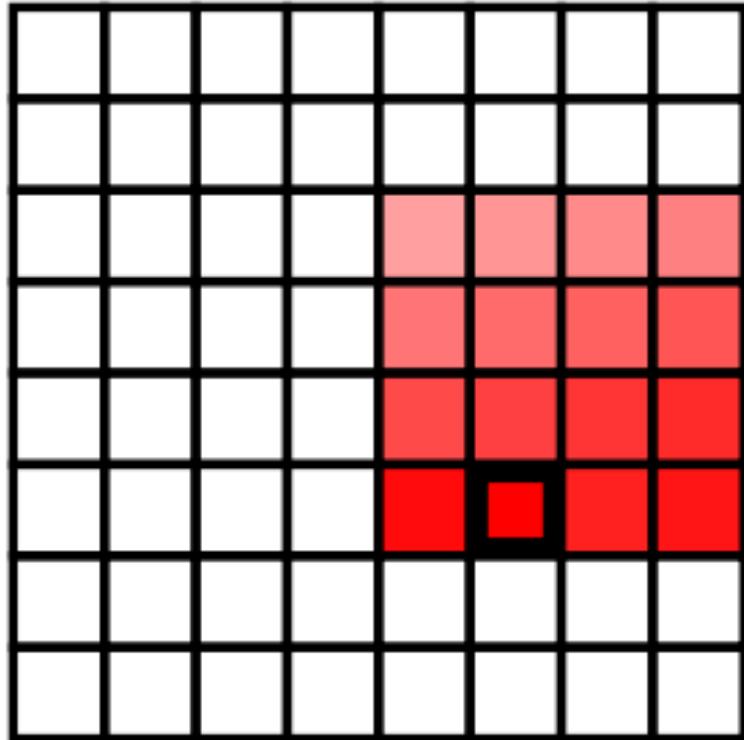


cache hits:1116 \approx 78%

C mem:473 cache hit:354

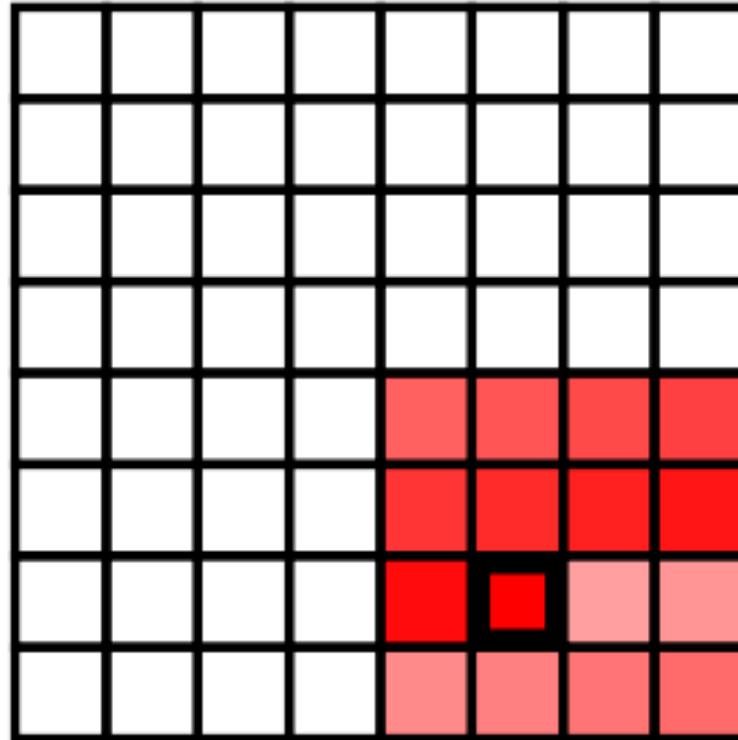


A mem:474 cache hit:354



mem:1422

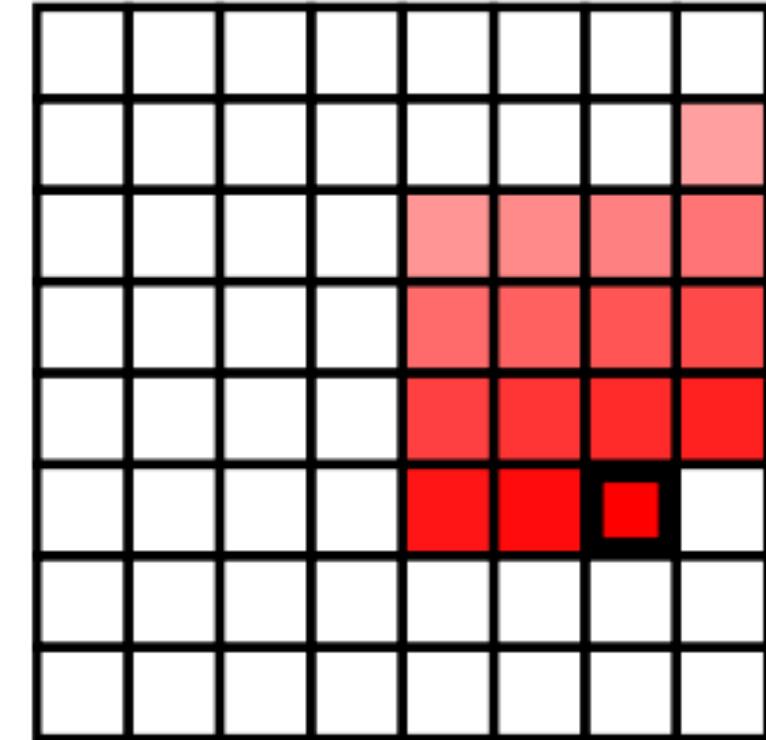
B mem:474 cache hit:410



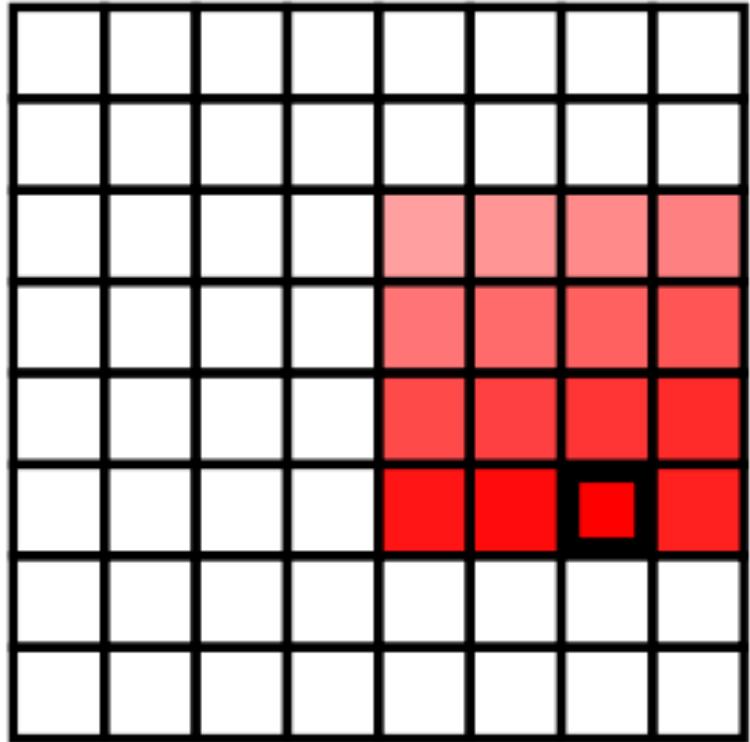
×

cache hits:1119 \approx 78%

C mem:474 cache hit:355

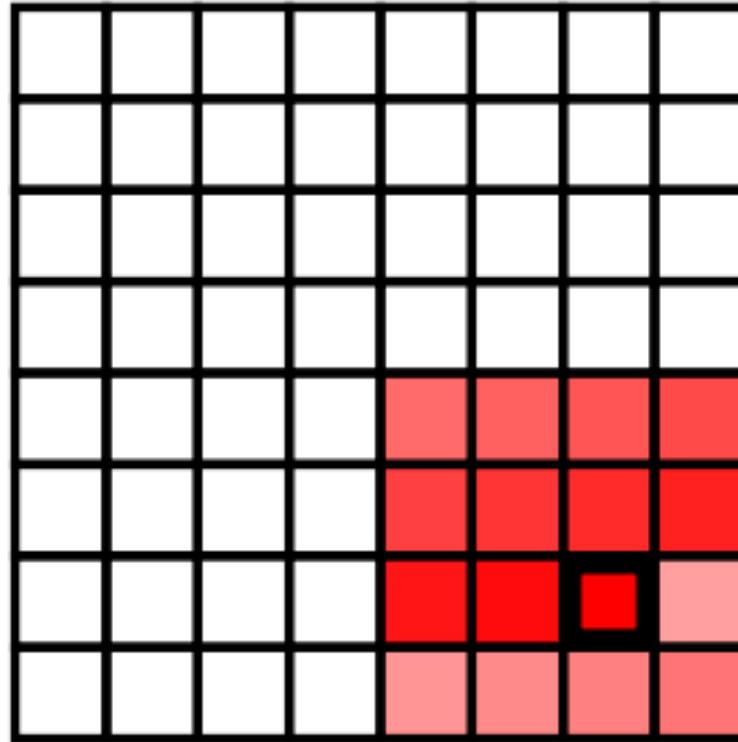


A mem:475 cache hit:355



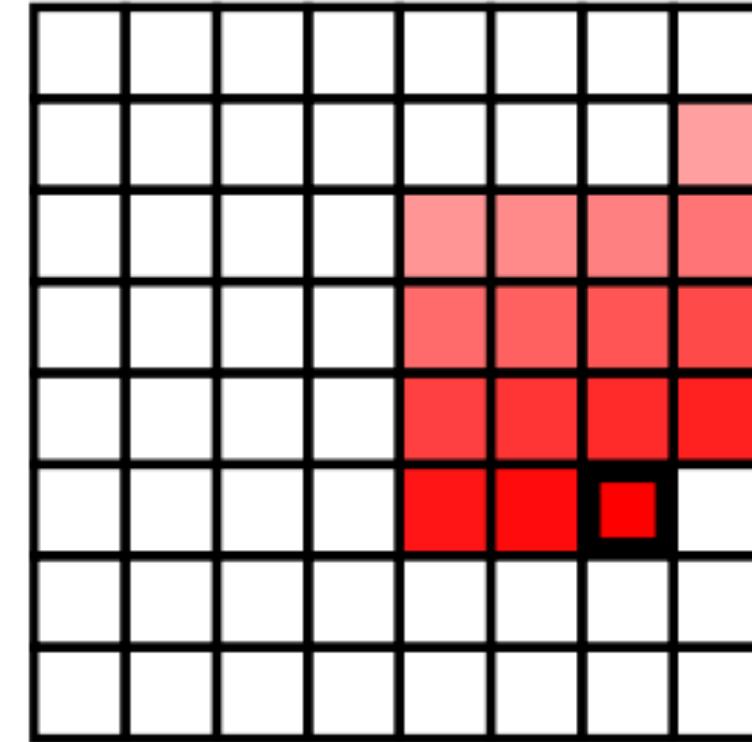
mem:1425

B mem:475 cache hit:411

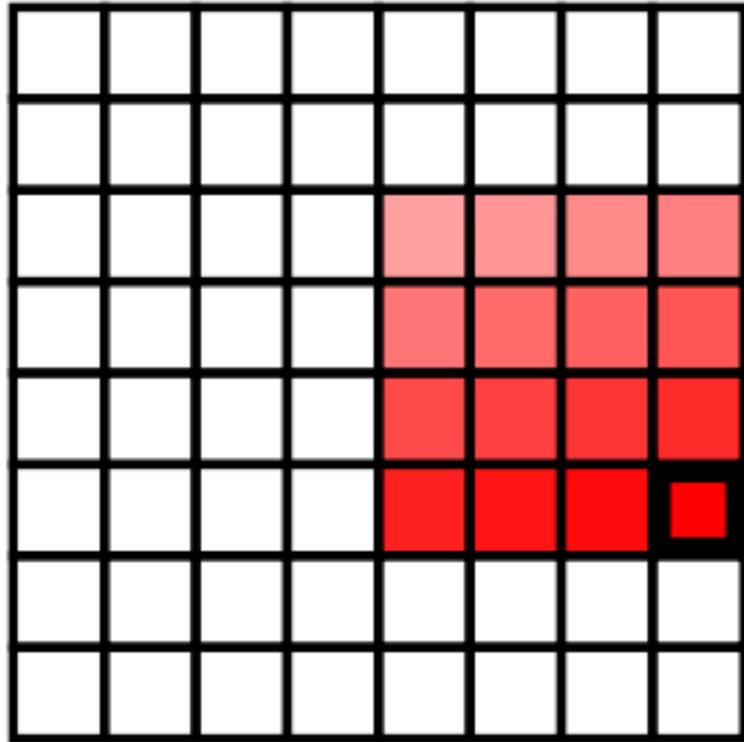


cache hits:1122 \approx 78%

C mem:475 cache hit:356

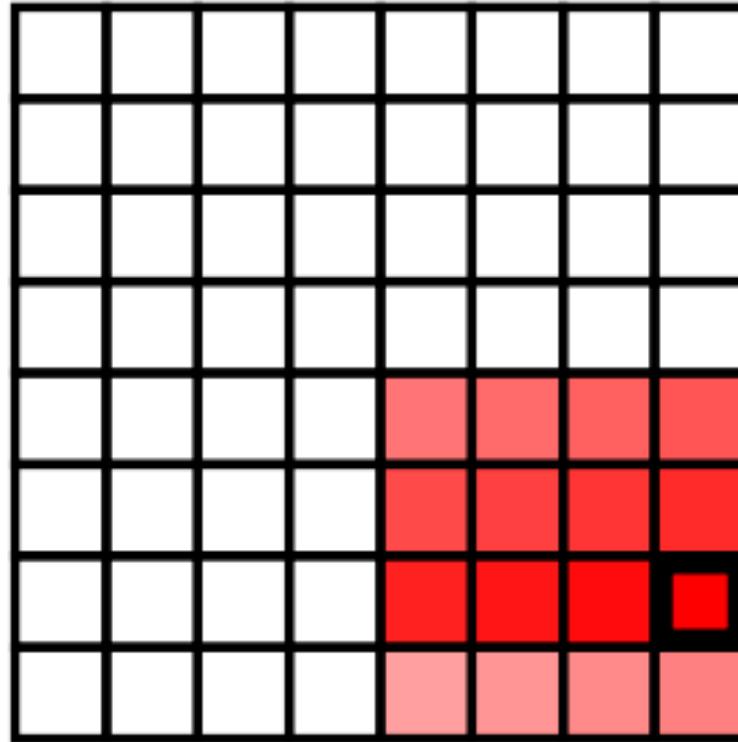


A mem:476 cache hit:356



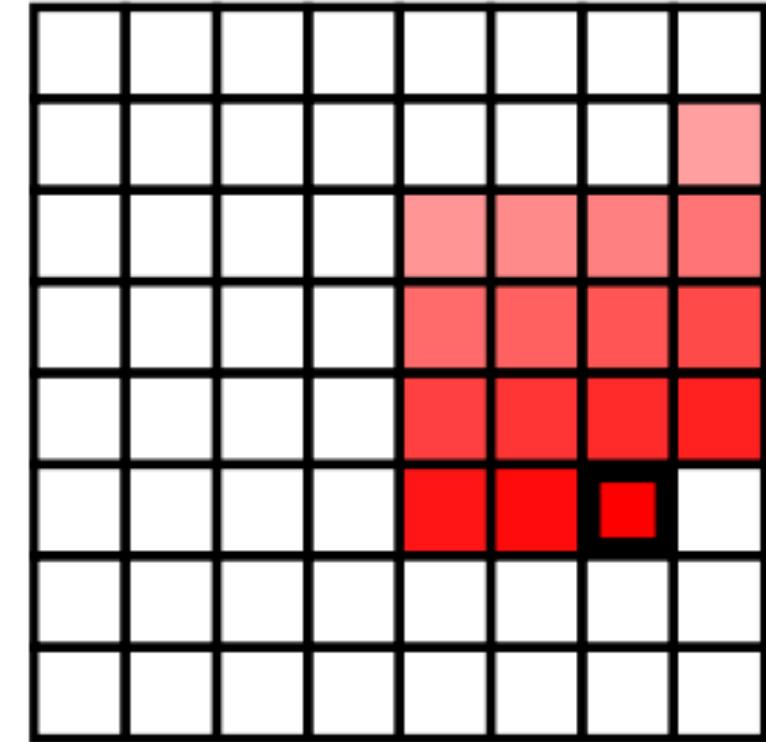
mem:1428

B mem:476 cache hit:412

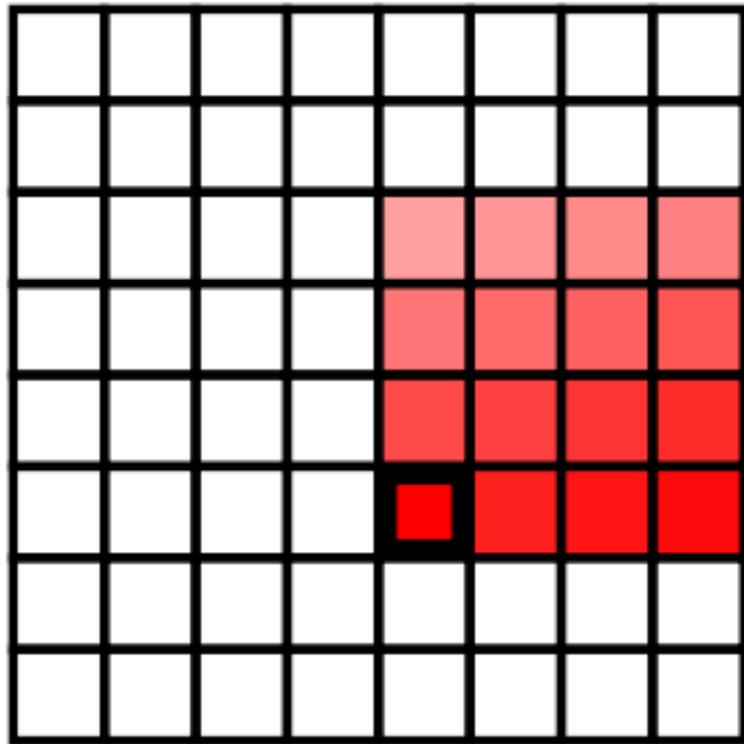


cache hits:1125 \approx 78%

C mem:476 cache hit:357

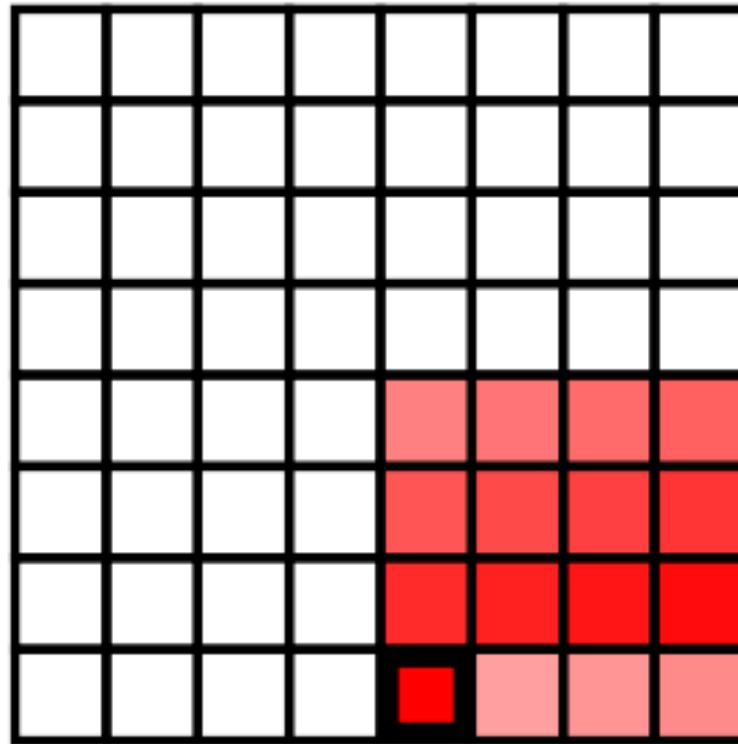


A mem:477 cache hit:357



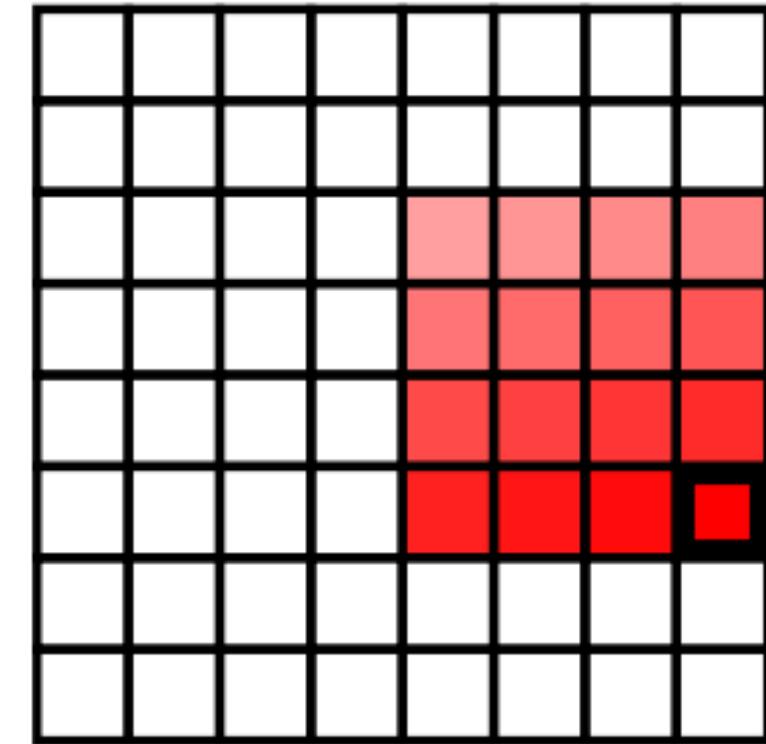
mem:1431

B mem:477 cache hit:413

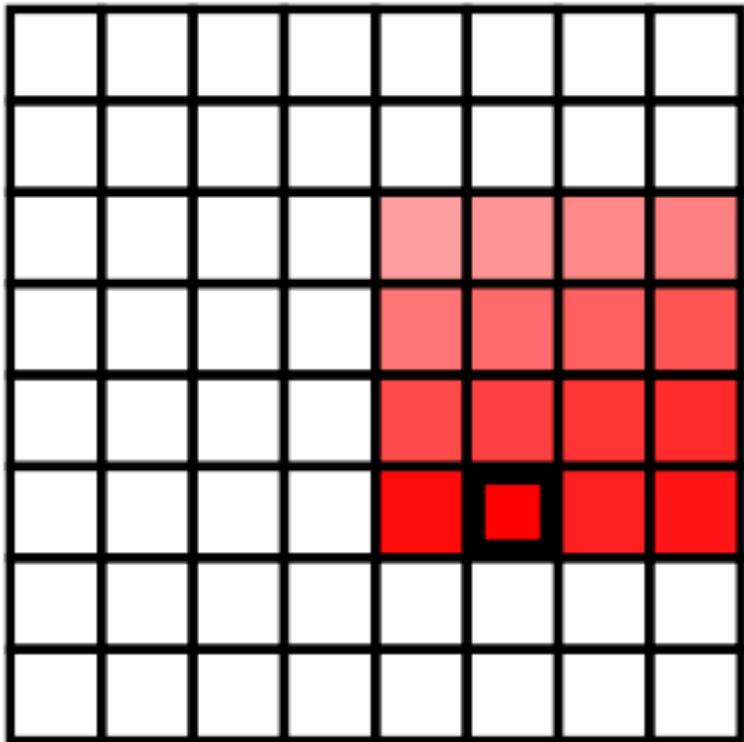


cache hits:1127 \approx 78%

C mem:477 cache hit:357

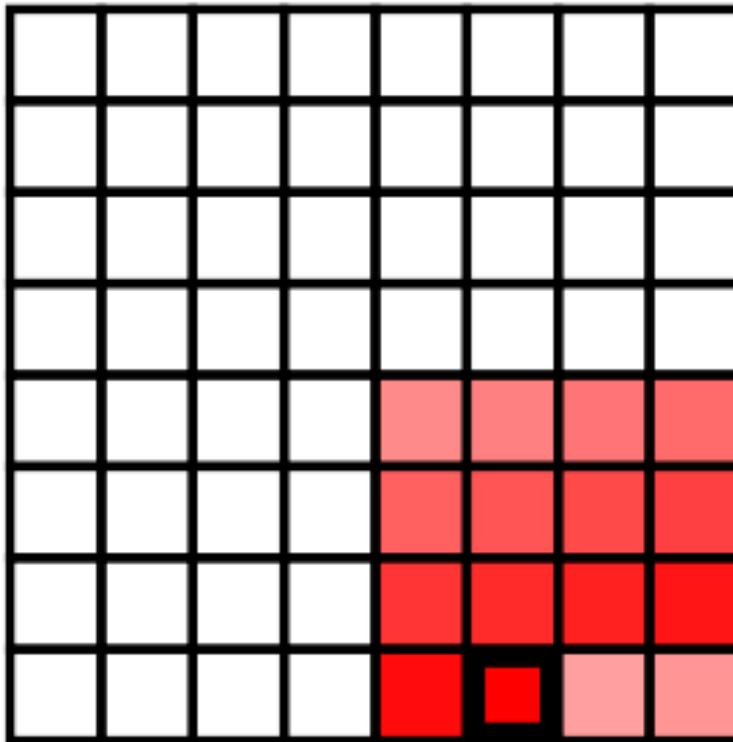


A mem:478 cache hit:358



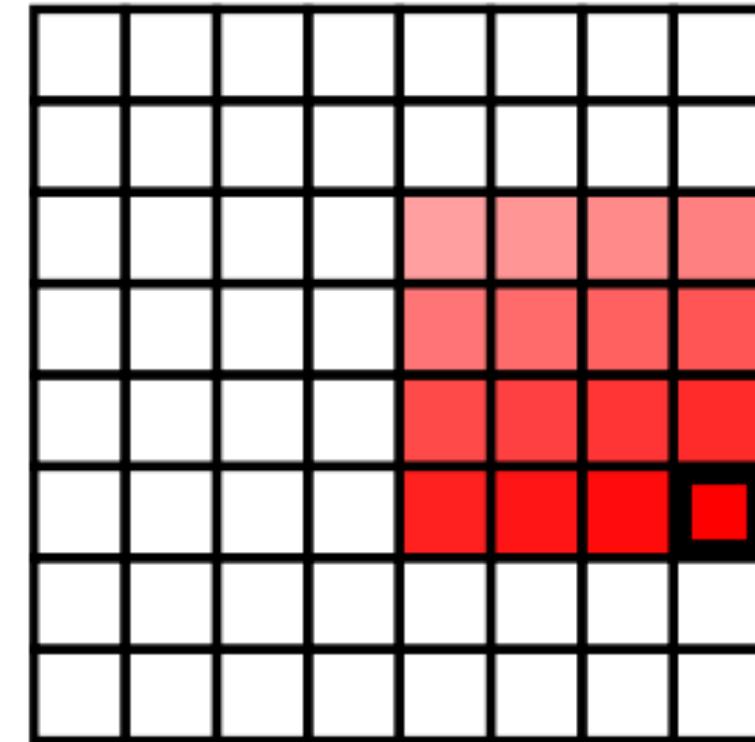
mem:1434

B mem:478 cache hit:414

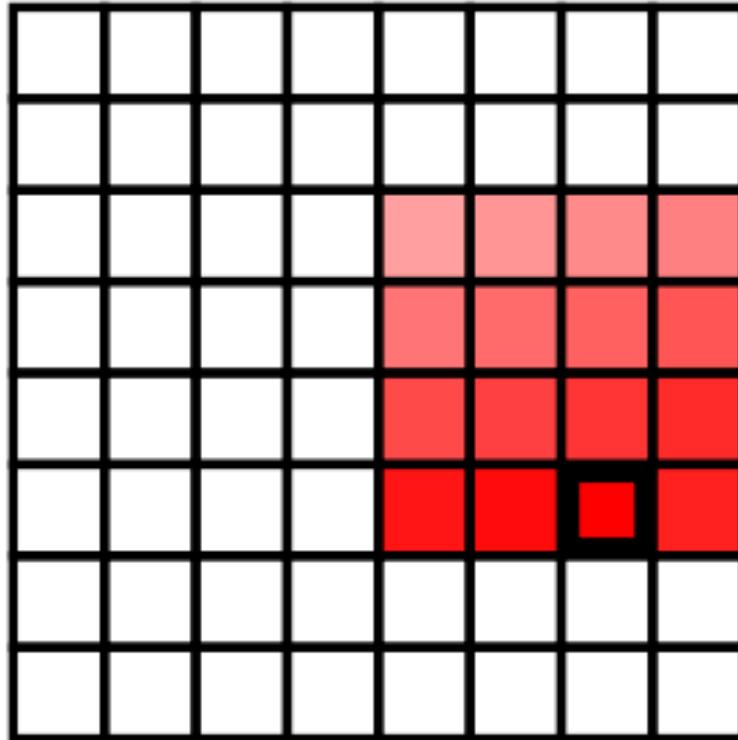


cache hits:1130 \approx 78%

C mem:478 cache hit:358

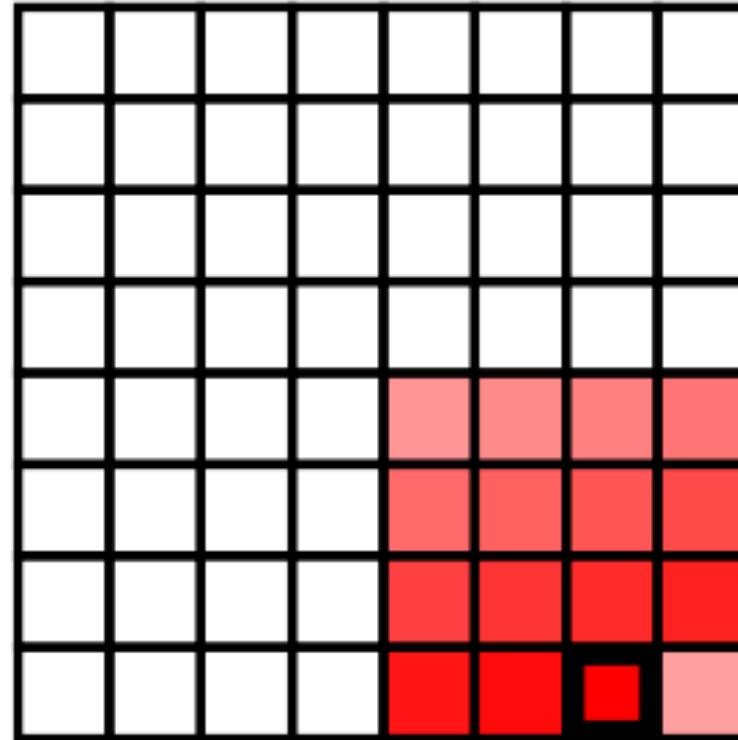


A mem:479 cache hit:359



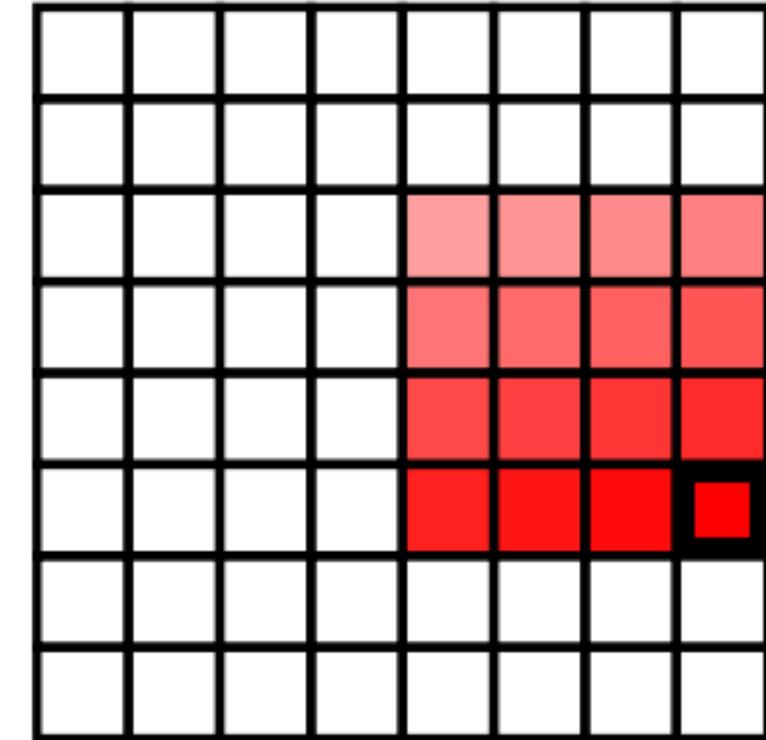
mem:1437

B mem:479 cache hit:415

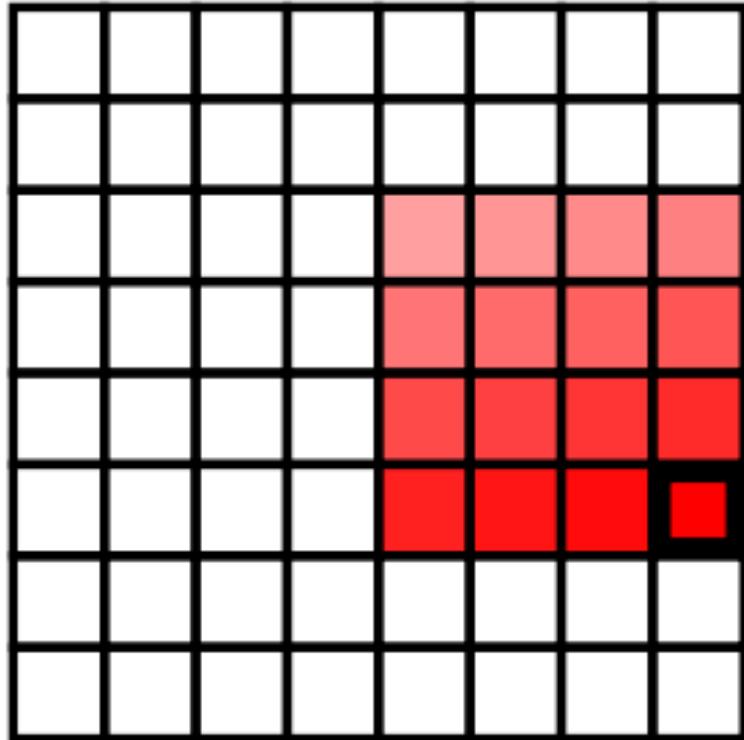


cache hits:1133 \approx 78%

C mem:479 cache hit:359

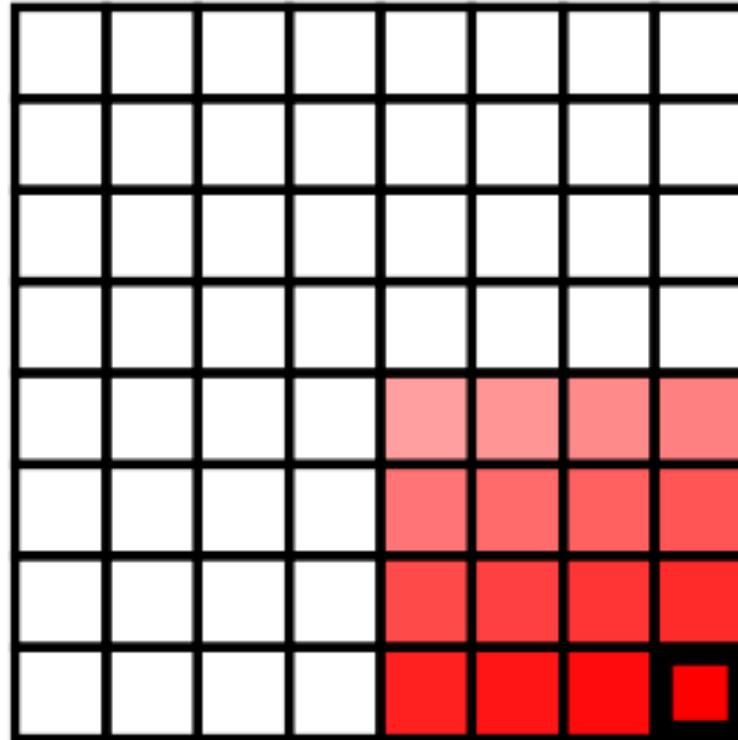


A mem:480 cache hit:360



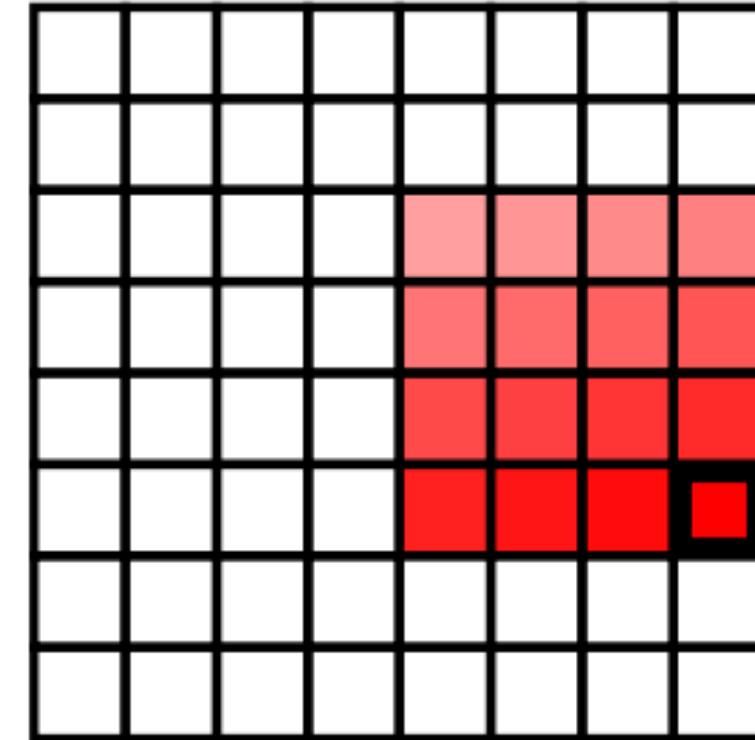
mem:1440

B mem:480 cache hit:416

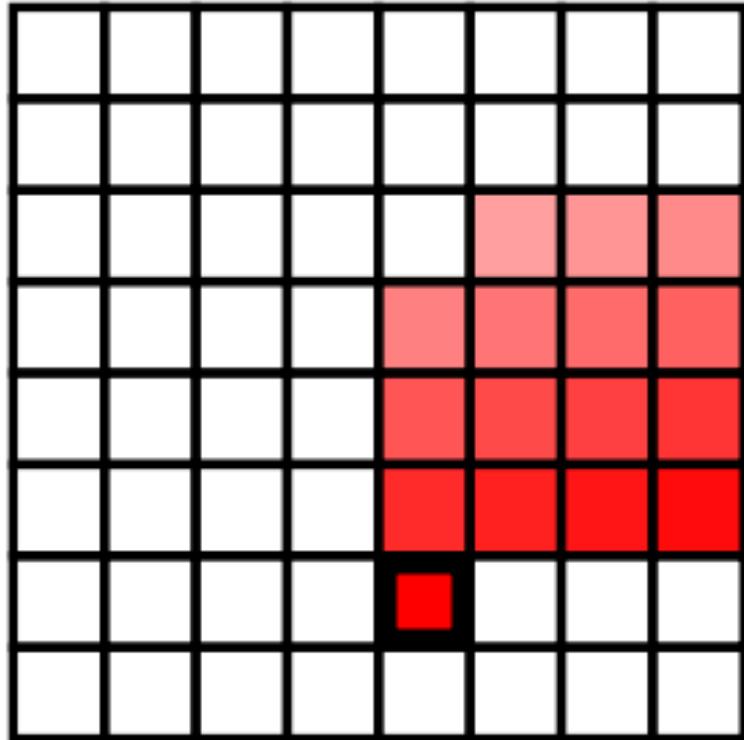


cache hits:1136 \approx 78%

C mem:480 cache hit:360

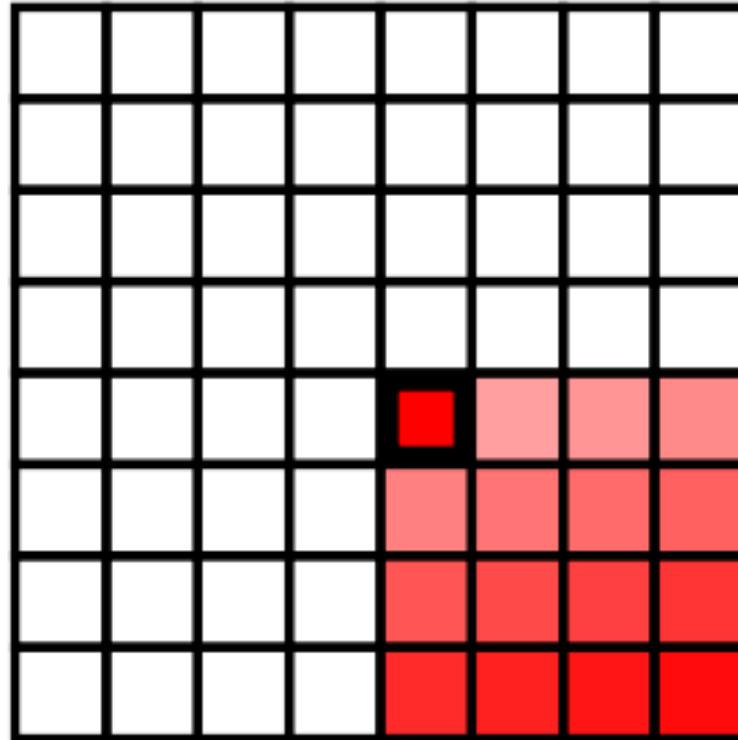


A mem:481 cache hit:360



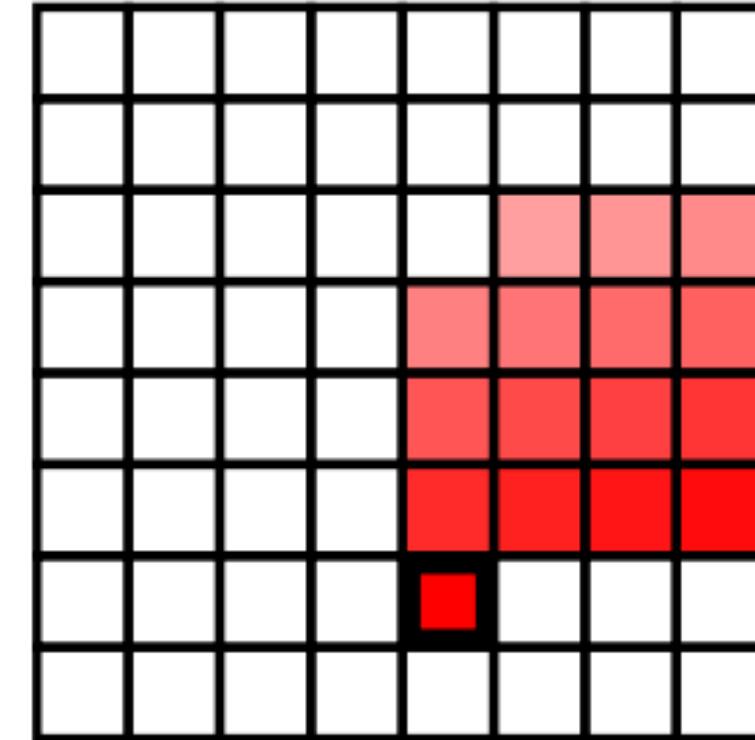
mem:1443

B mem:481 cache hit:417

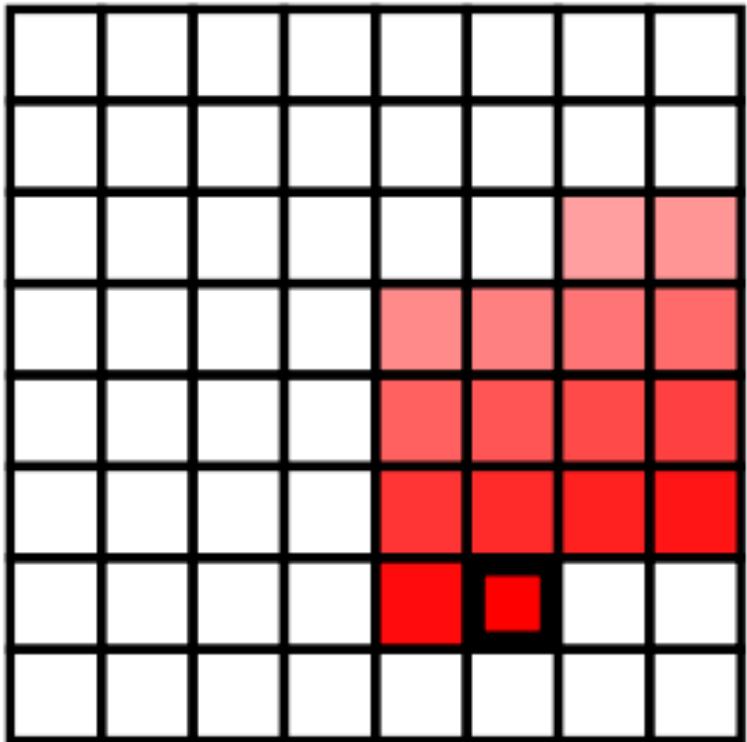


cache hits:1137 \approx 78%

C mem:481 cache hit:360

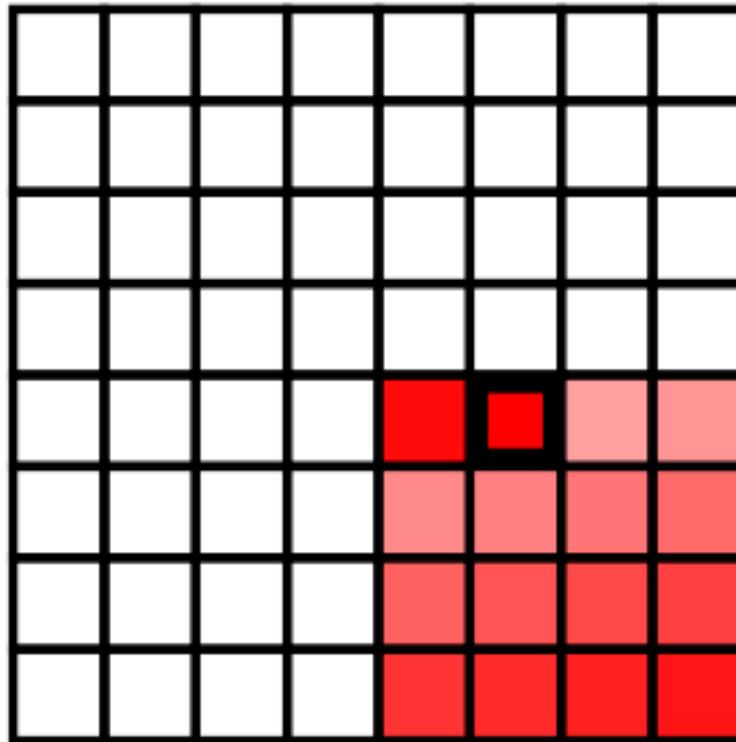


A mem:482 cache hit:360



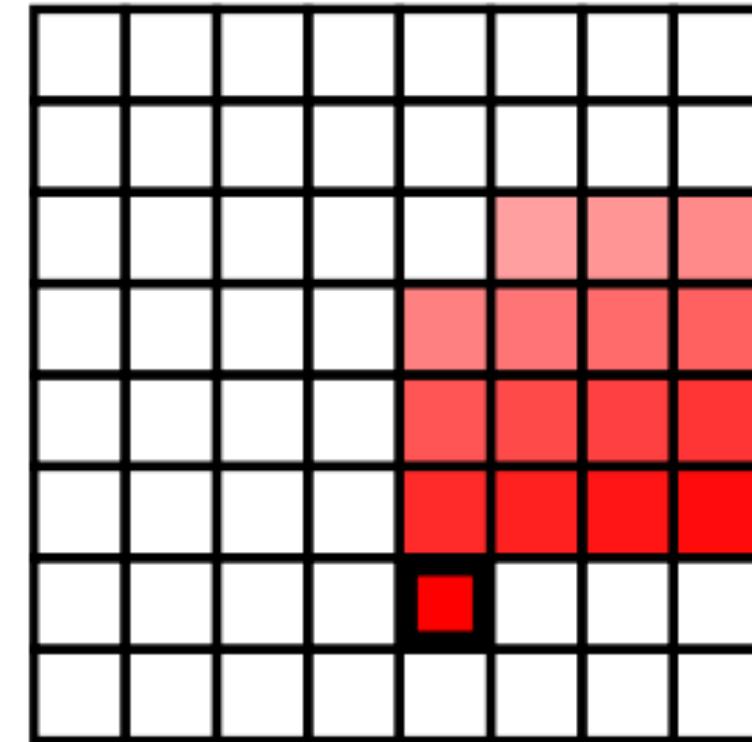
mem:1446

B mem:482 cache hit:418

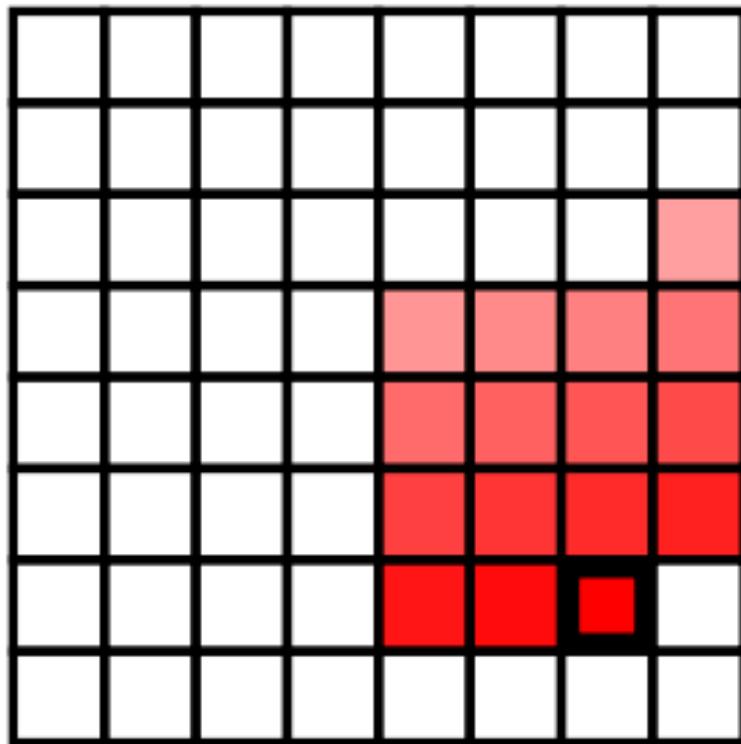


cache hits:1139 \approx 78%

C mem:482 cache hit:361

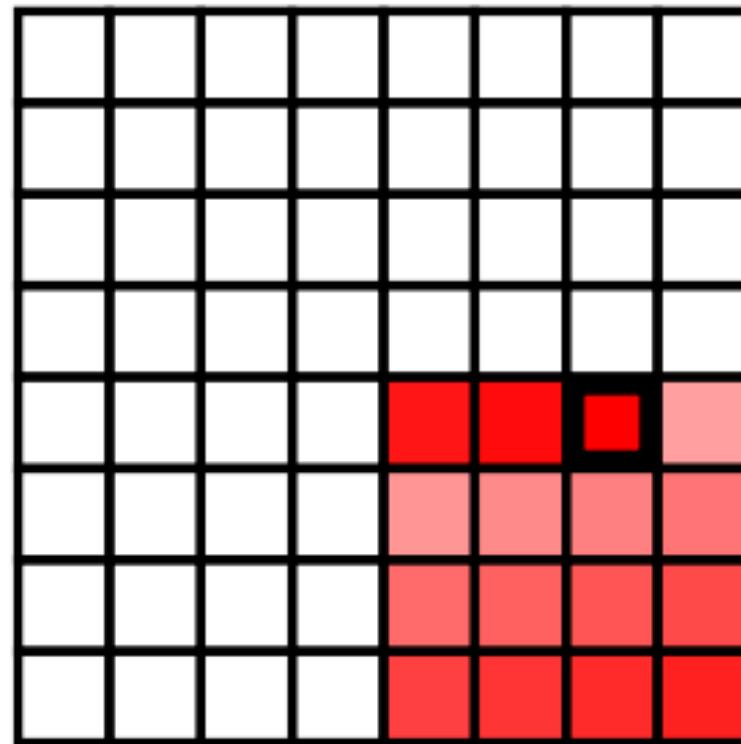


A mem:483 cache hit:360



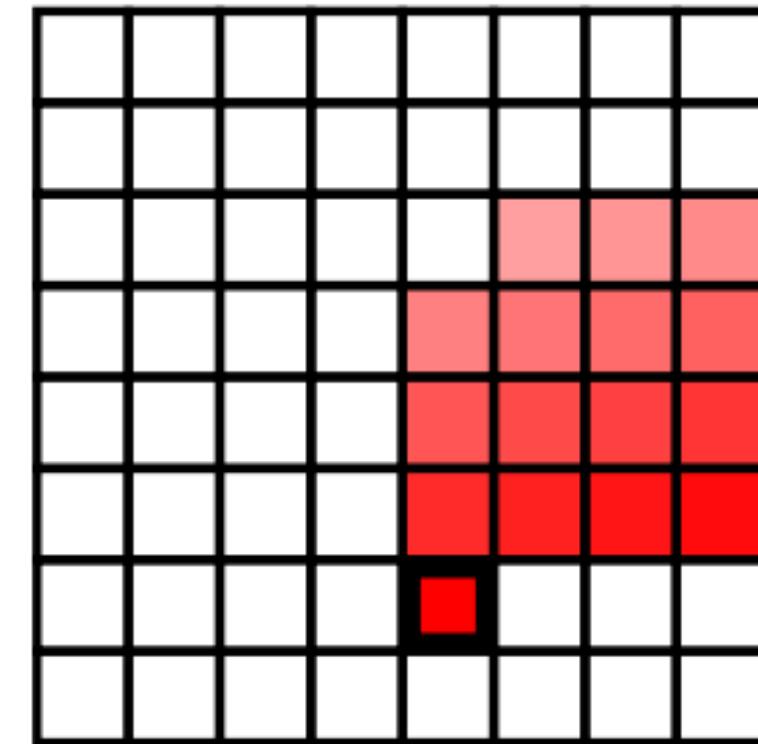
mem:1449

B mem:483 cache hit:419

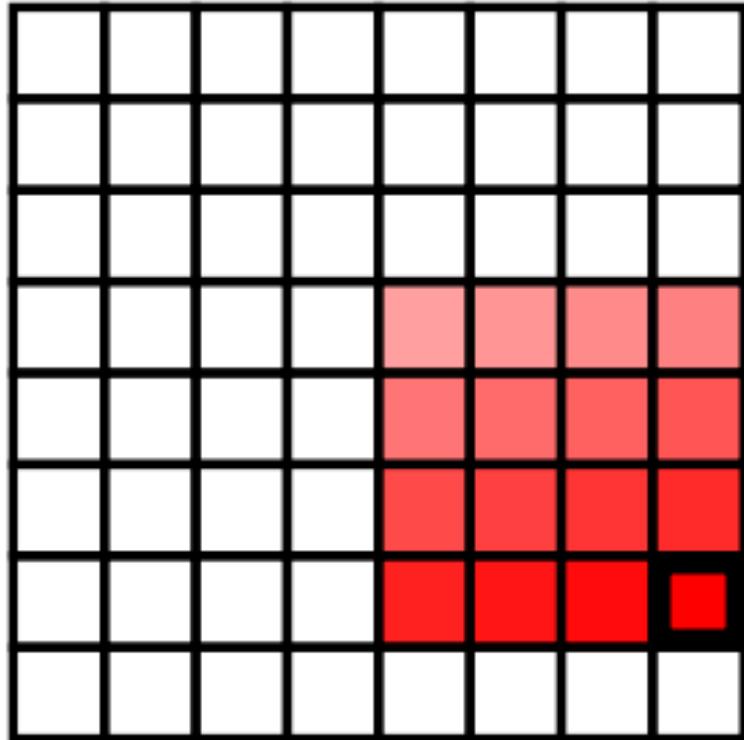


cache hits:1141 \approx 78%

C mem:483 cache hit:362

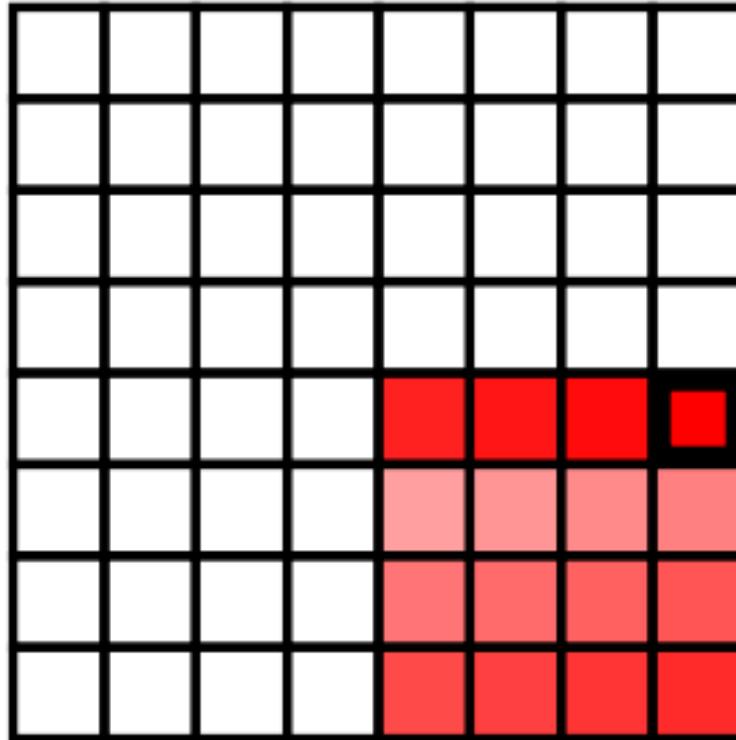


A mem:484 cache hit:360



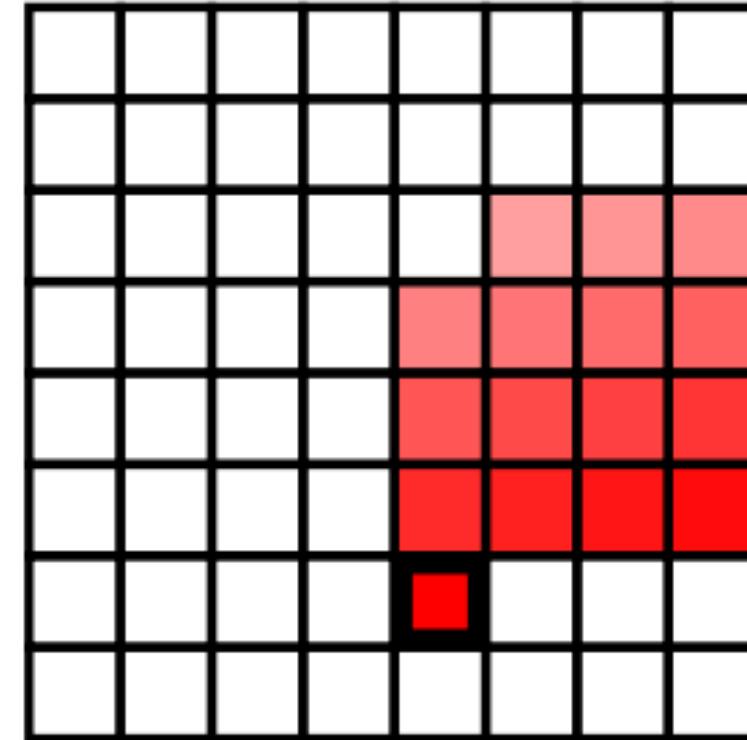
mem:1452

B mem:484 cache hit:420

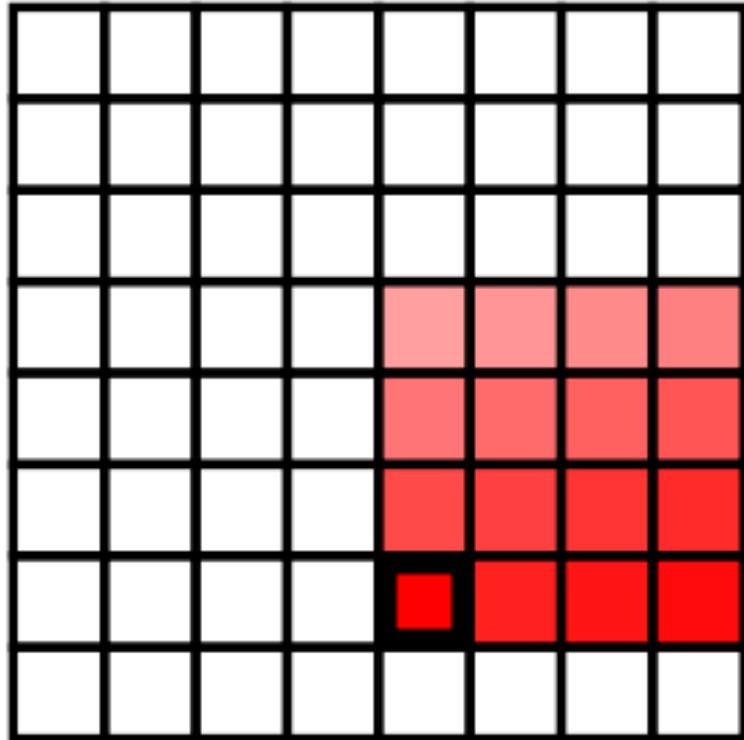


cache hits:1143 \approx 78%

C mem:484 cache hit:363

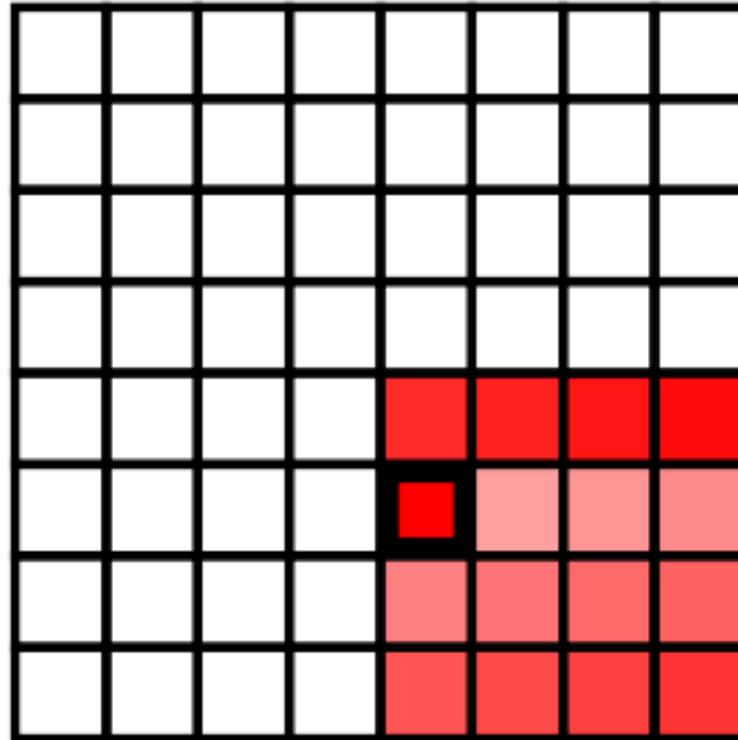


A mem:485 cache hit:361



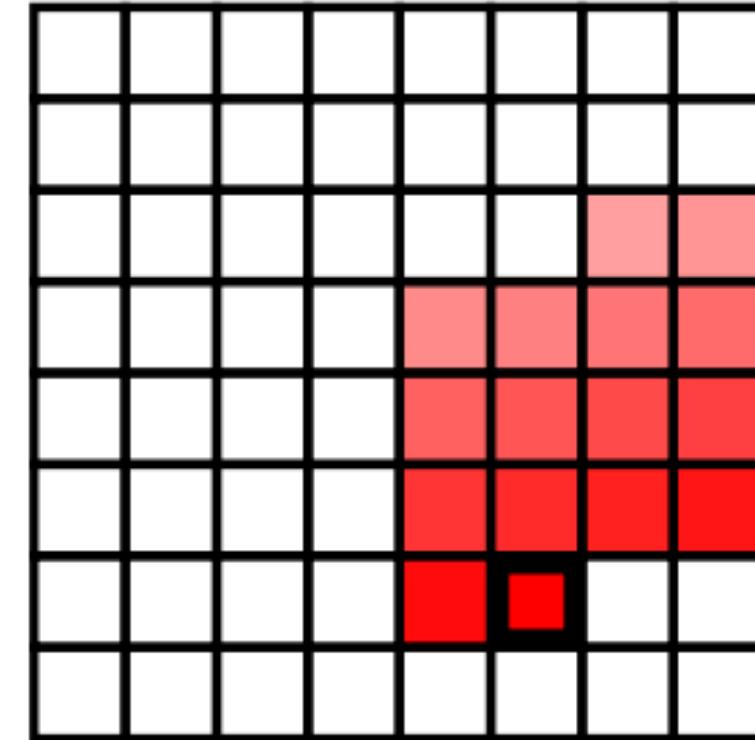
mem:1455

B mem:485 cache hit:421

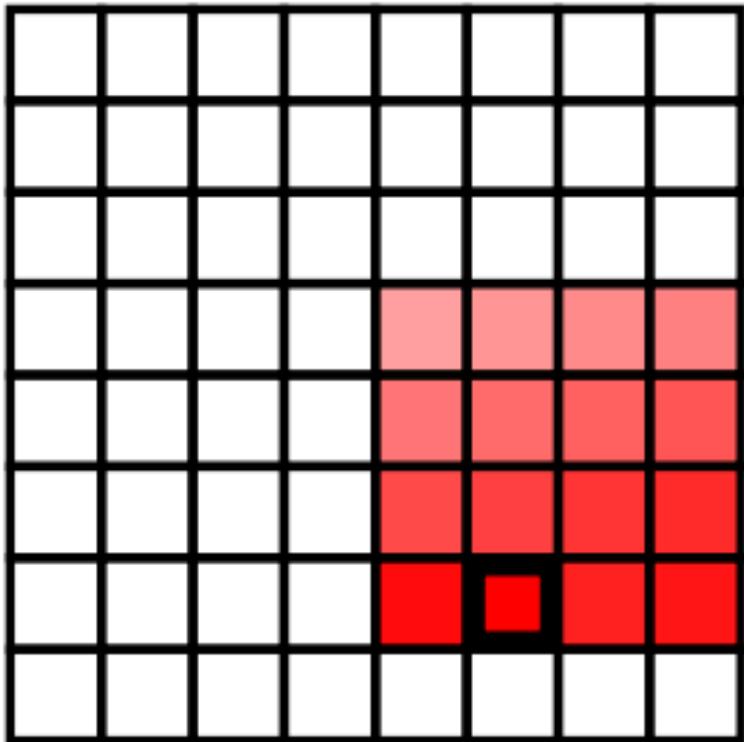


cache hits:1145 \approx 78%

C mem:485 cache hit:363

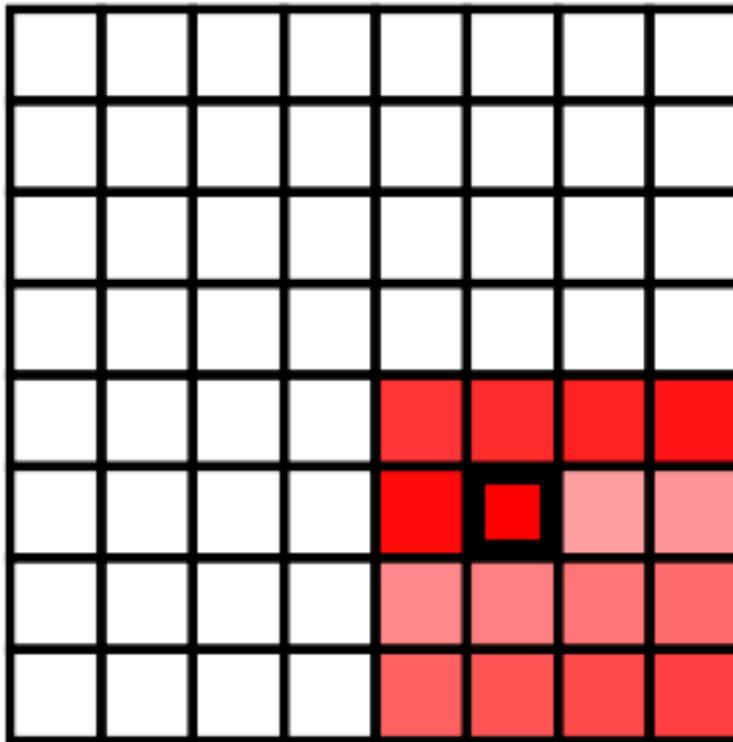


A mem:486 cache hit:362



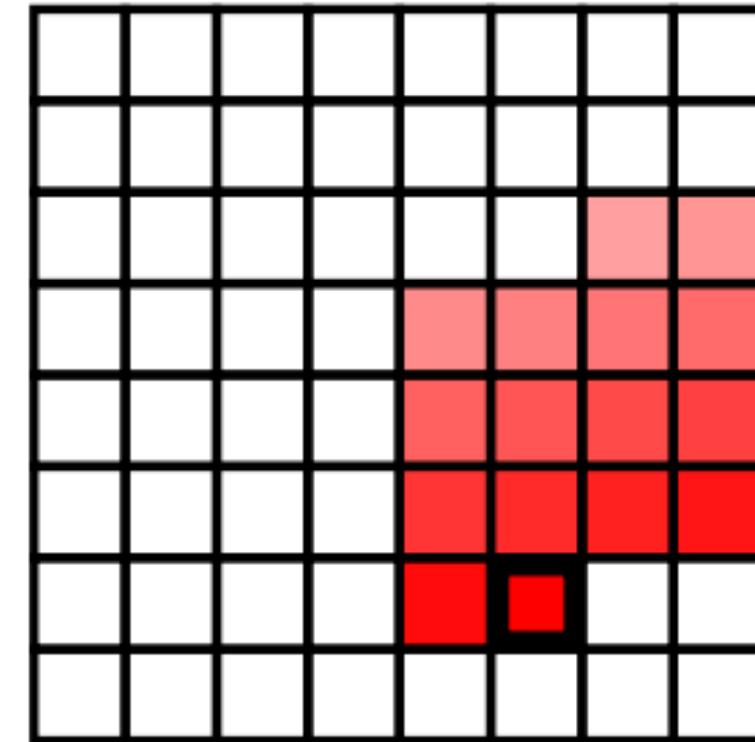
mem:1458

B mem:486 cache hit:422

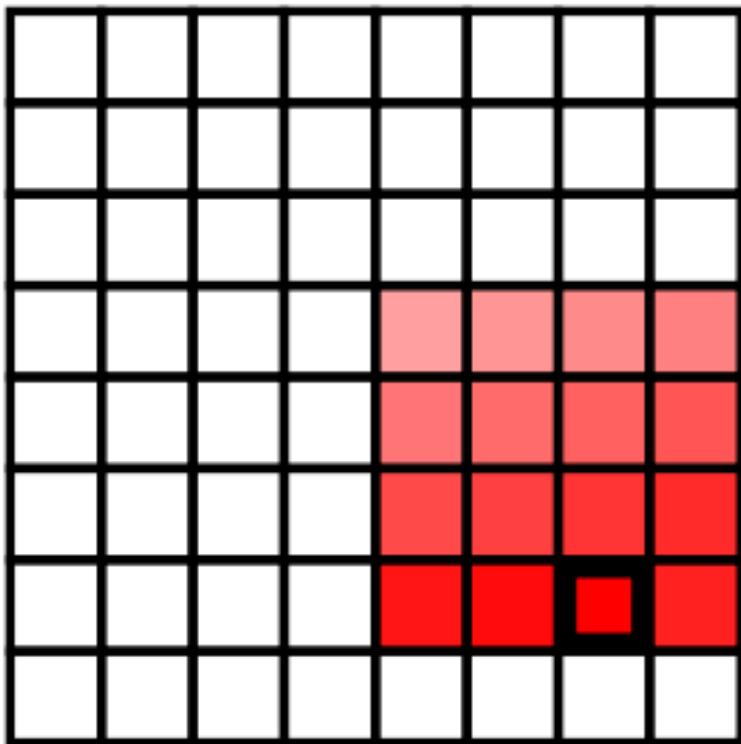


cache hits:1148 \approx 78%

C mem:486 cache hit:364

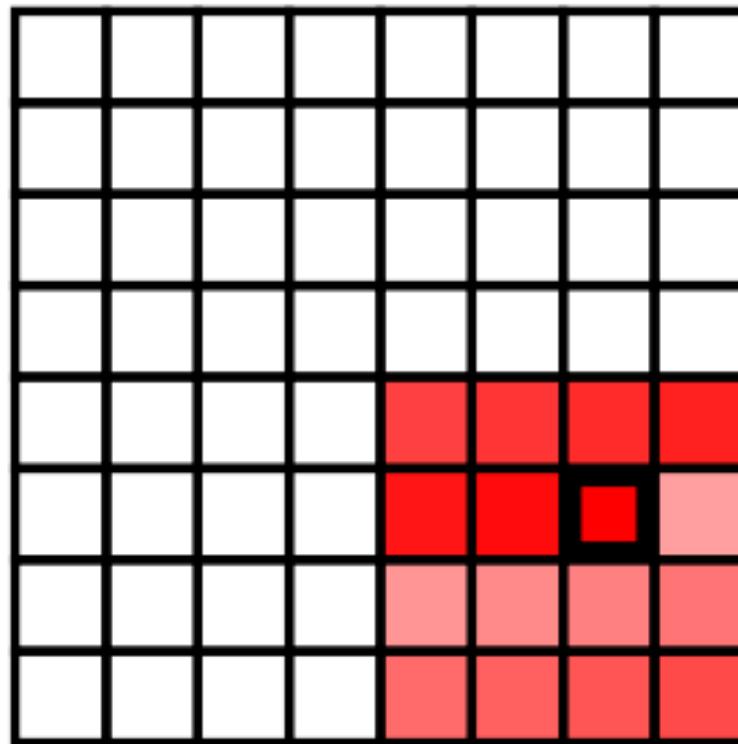


A mem:487 cache hit:363



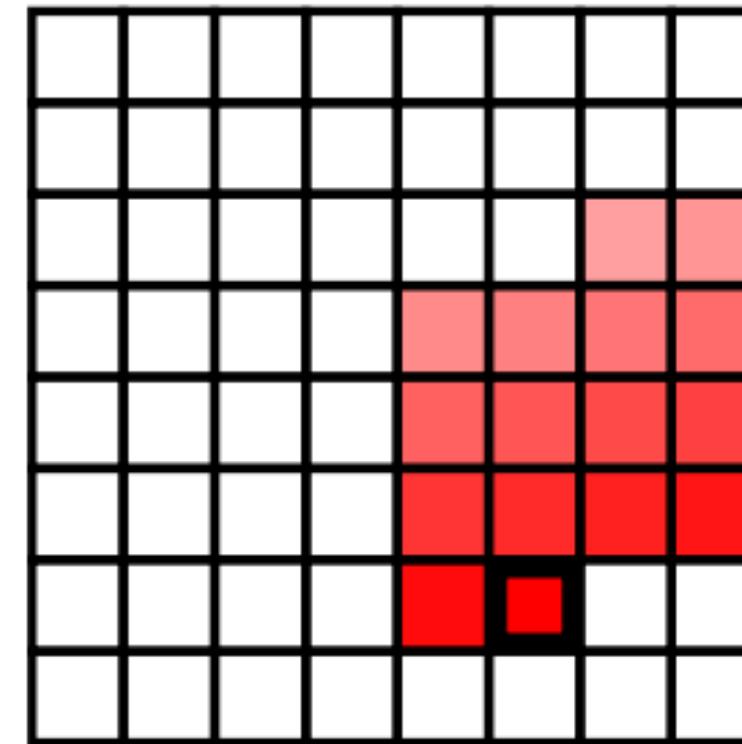
mem:1461

B mem:487 cache hit:423

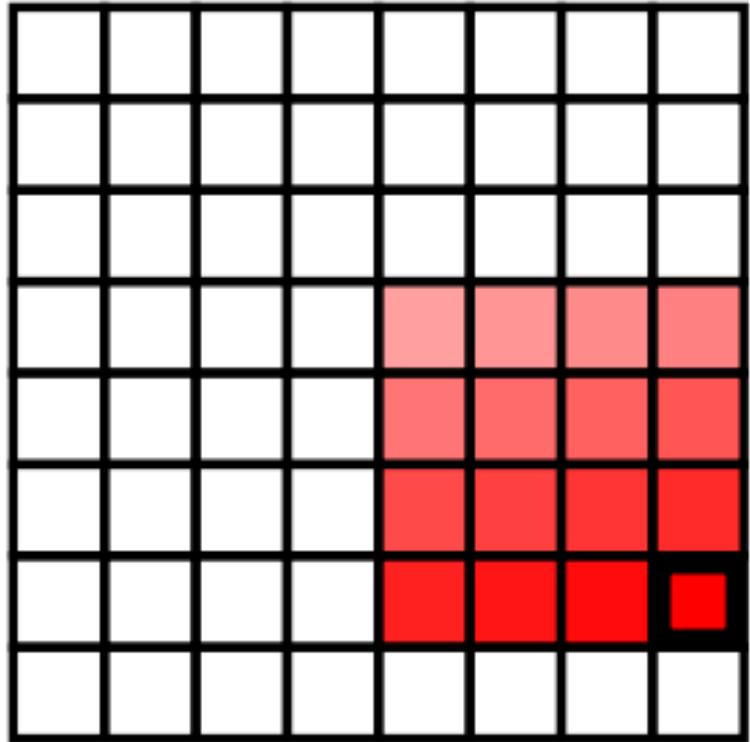


cache hits:1151 \approx 78%

C mem:487 cache hit:365

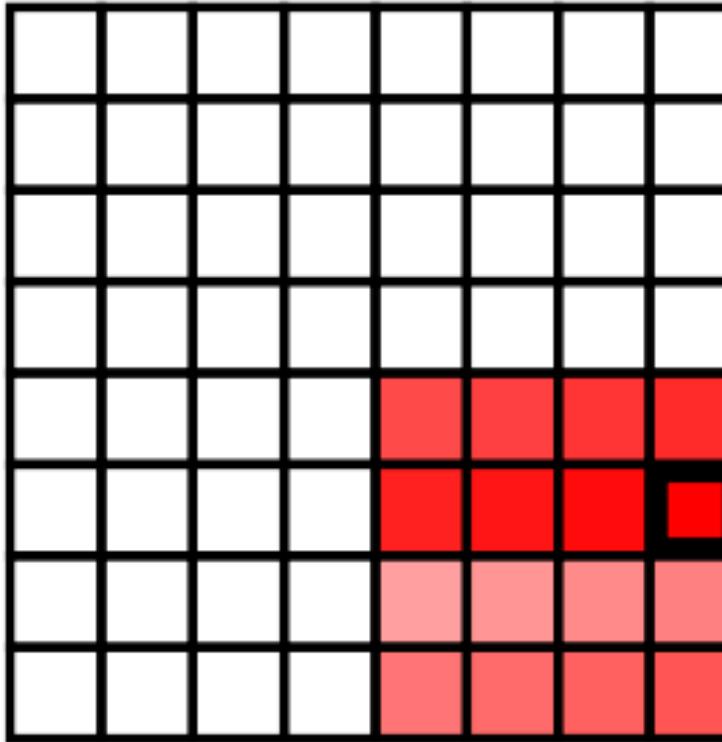


A mem:488 cache hit:364



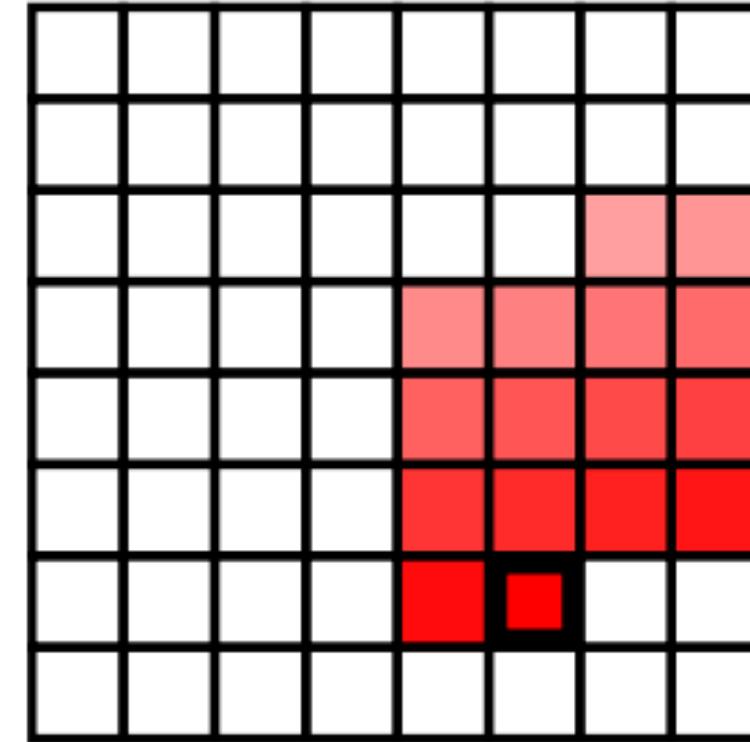
mem:1464

B mem:488 cache hit:424

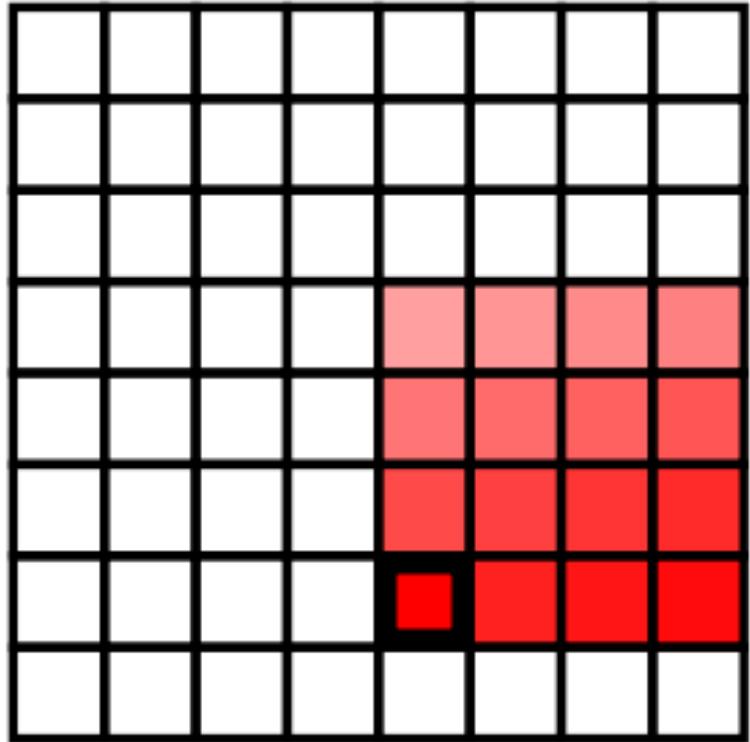


cache hits:1154 \approx 78%

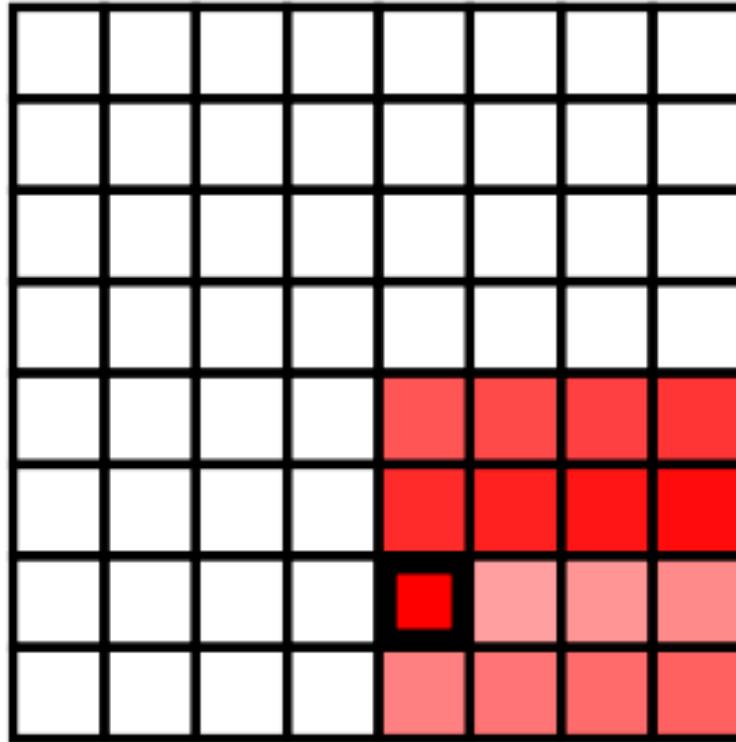
C mem:488 cache hit:366



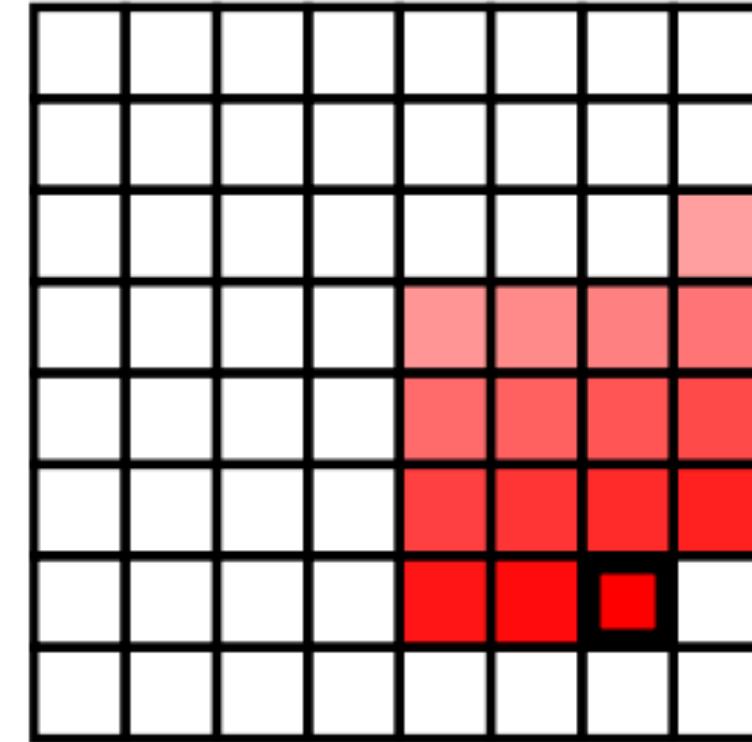
A mem:489 cache hit:365



B mem:489 cache hit:425



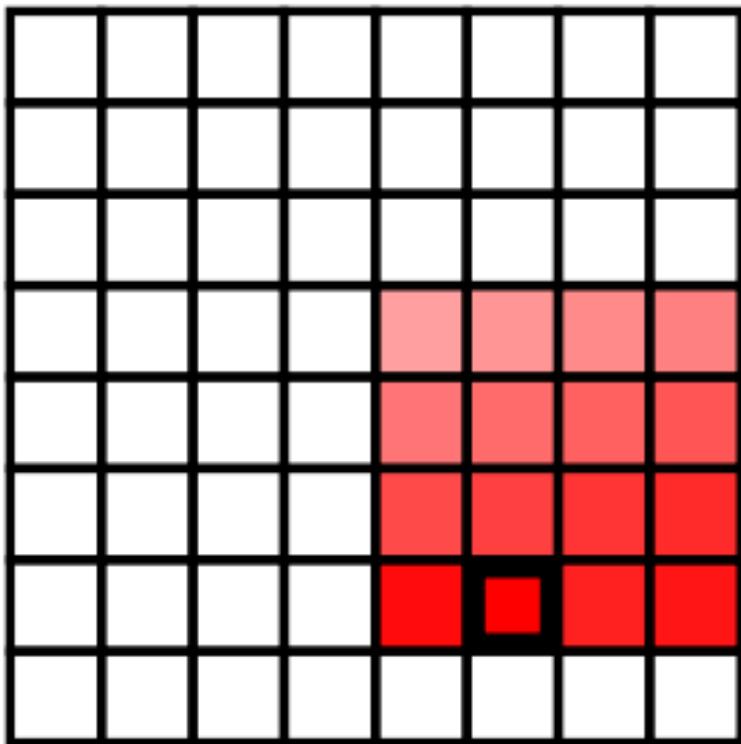
C mem:489 cache hit:366



mem:1467

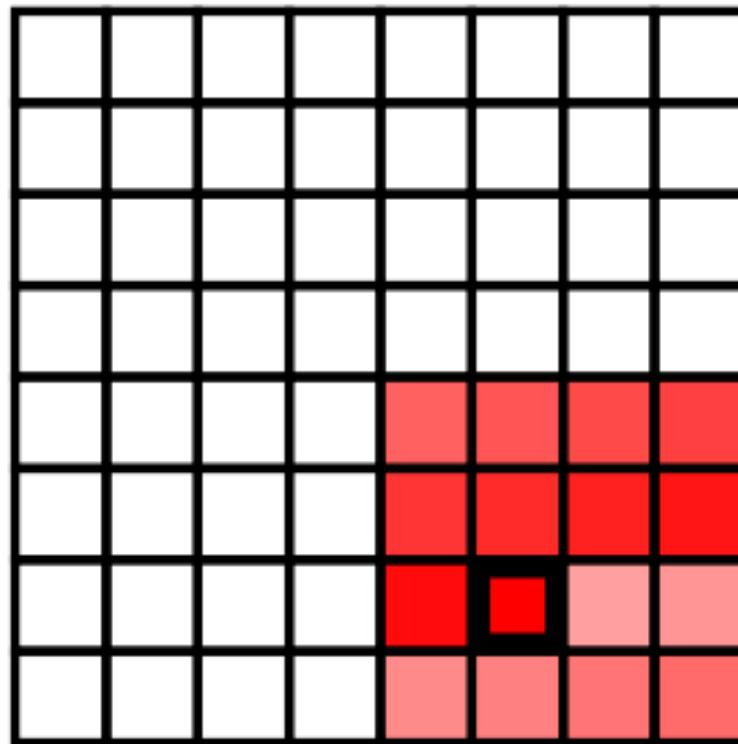
cache hits:1156 \approx 78%

A mem:490 cache hit:366



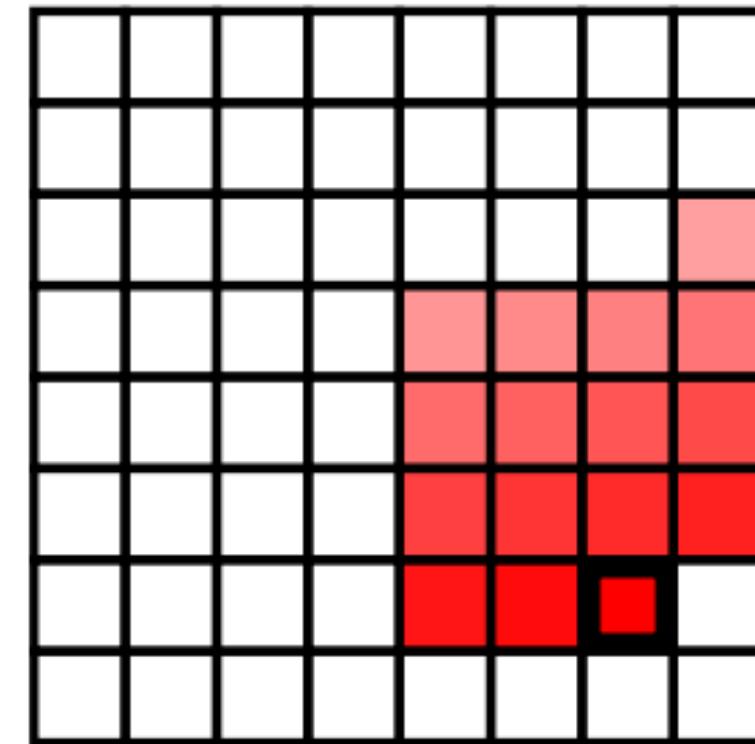
mem:1470

B mem:490 cache hit:426

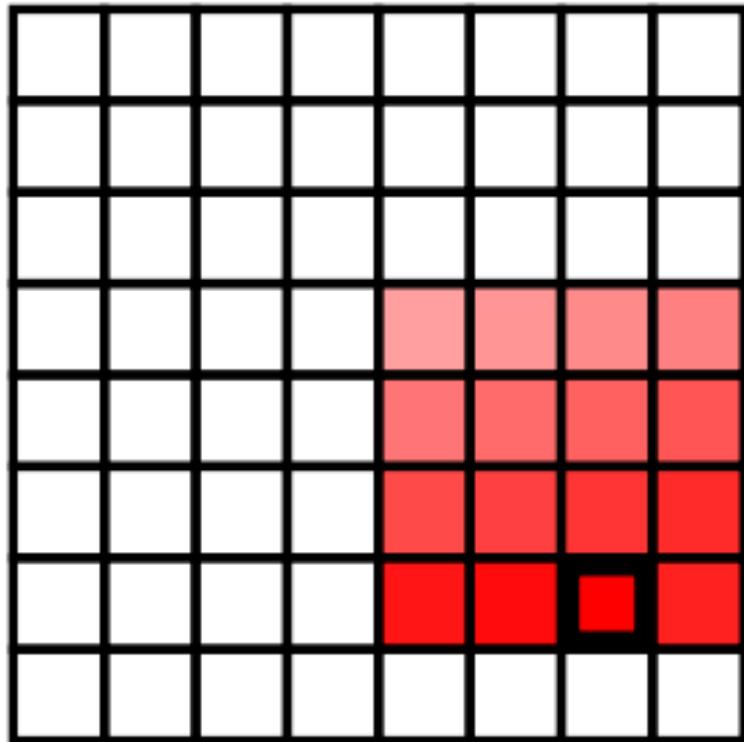


cache hits:1159 \approx 78%

C mem:490 cache hit:367

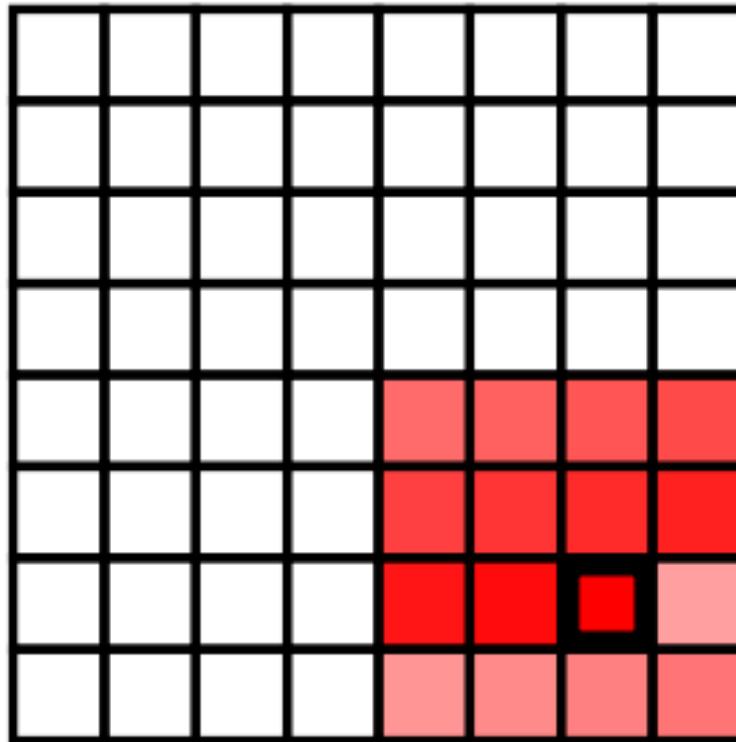


A mem:491 cache hit:367



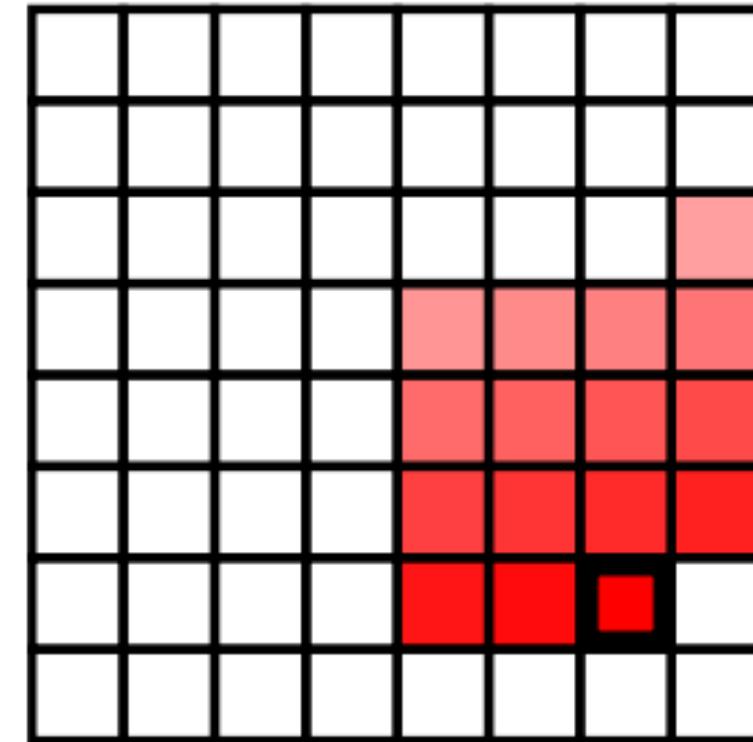
mem:1473

B mem:491 cache hit:427

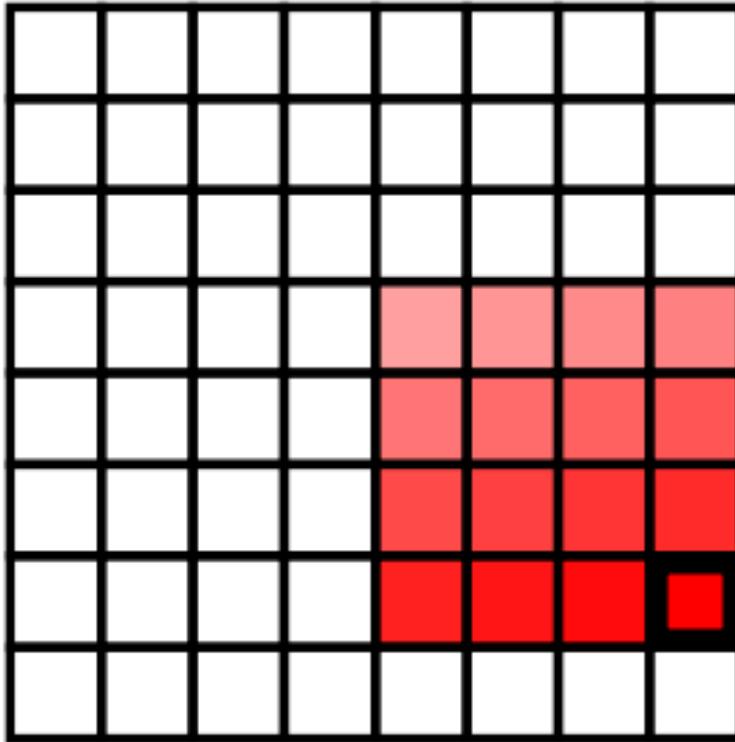


cache hits:1162 \approx 78%

C mem:491 cache hit:368

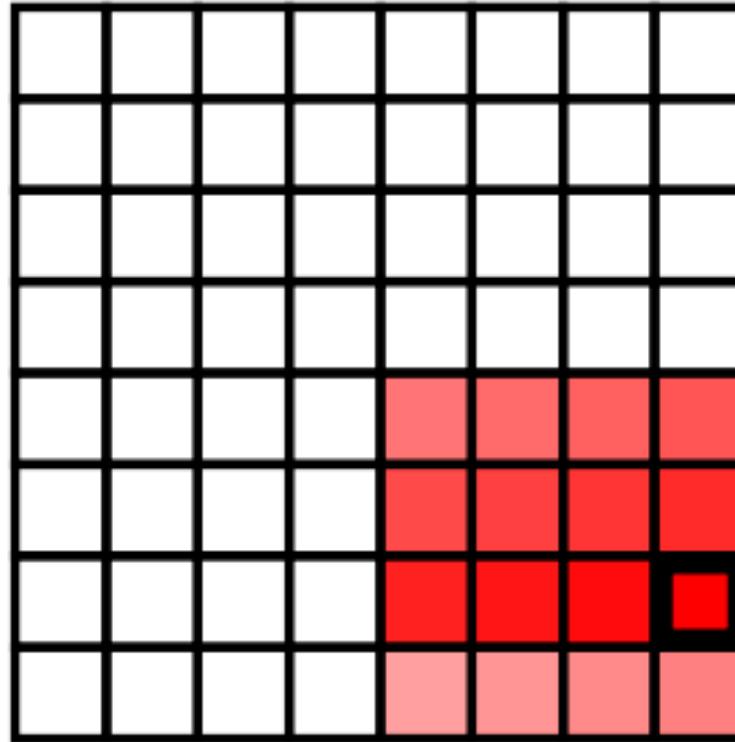


A mem:492 cache hit:368



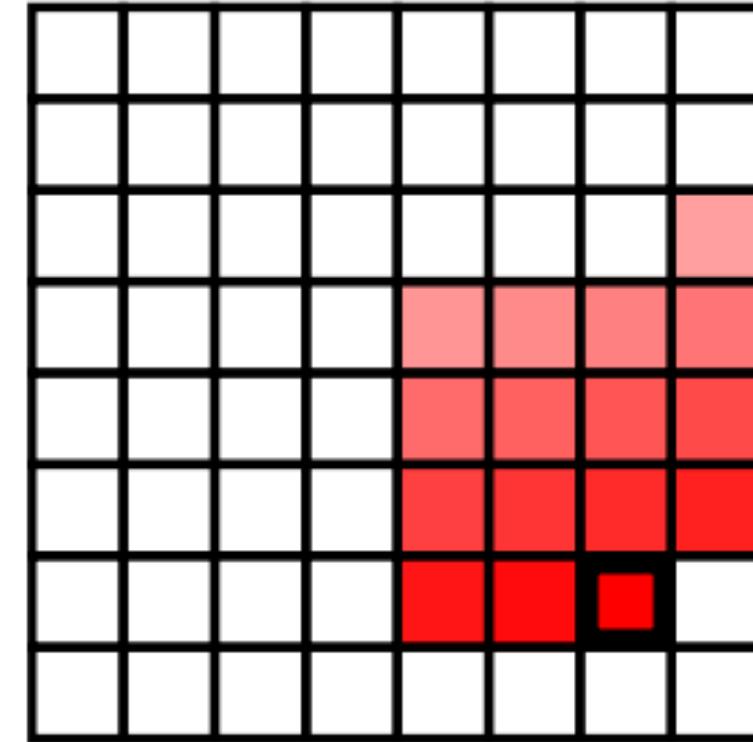
mem:1476

B mem:492 cache hit:428

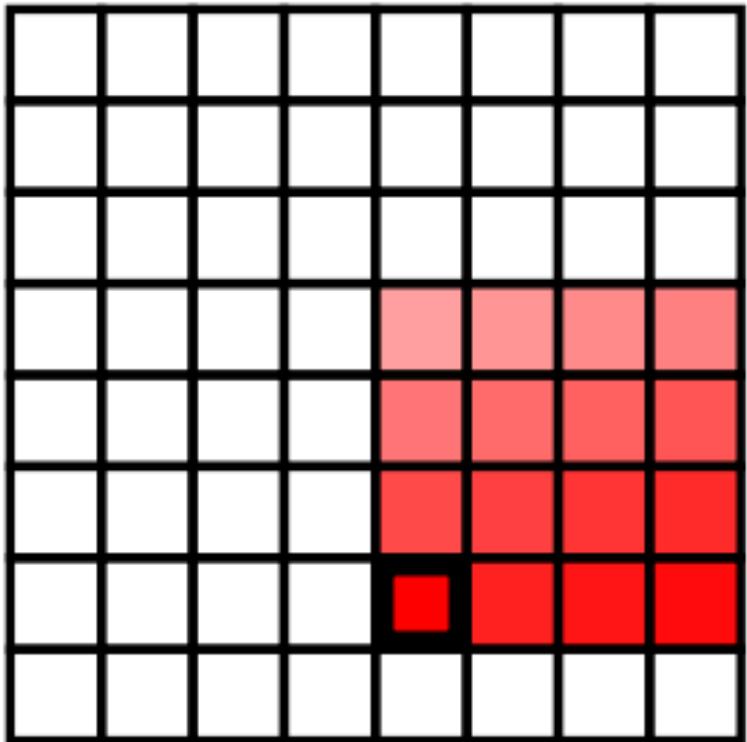


cache hits:1165 \approx 78%

C mem:492 cache hit:369

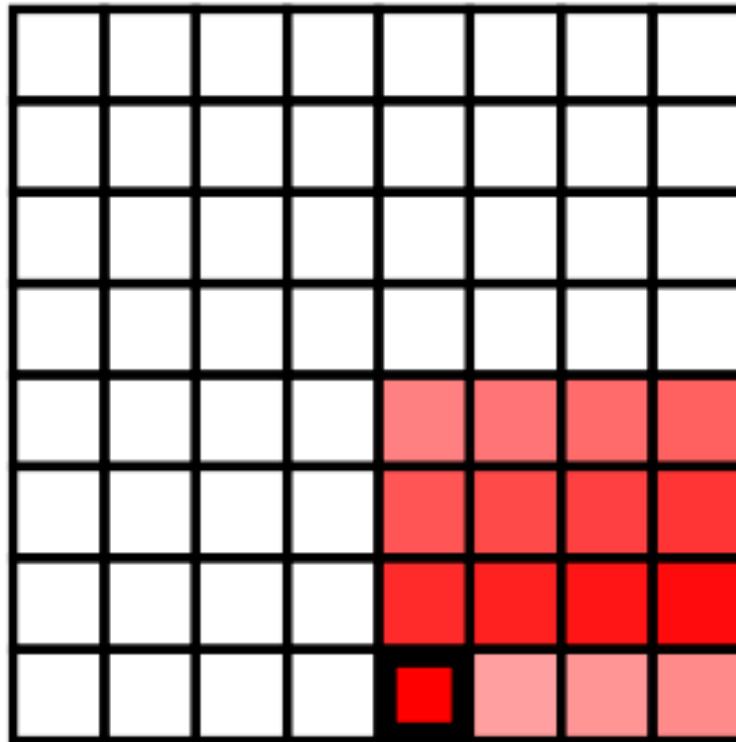


A mem:493 cache hit:369



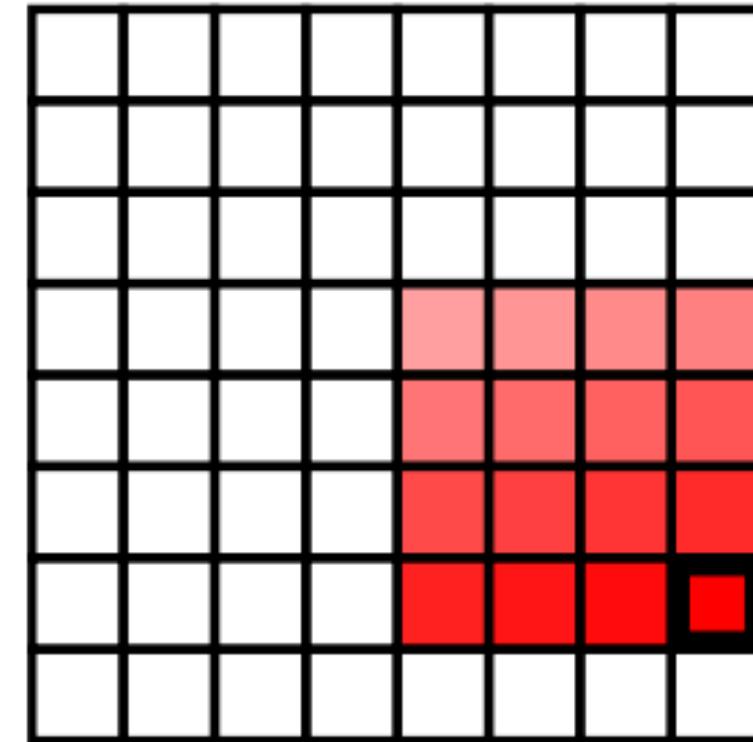
mem:1479

B mem:493 cache hit:429

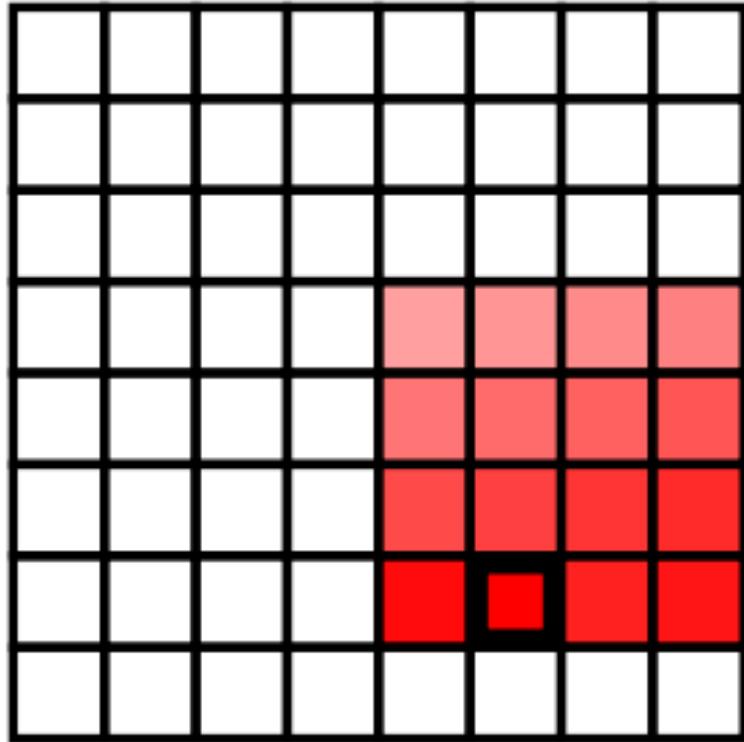


cache hits:1167 \approx 78%

C mem:493 cache hit:369

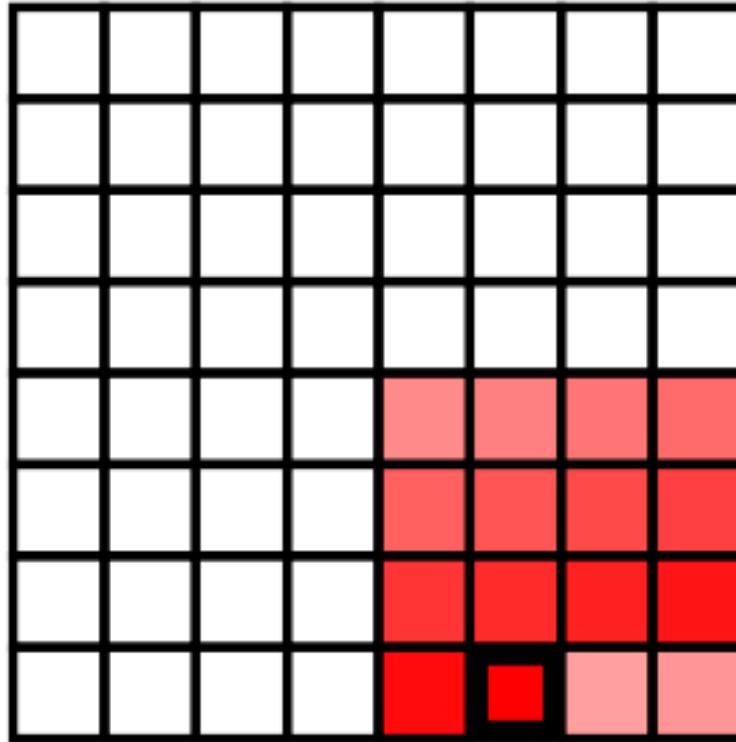


A mem:494 cache hit:370



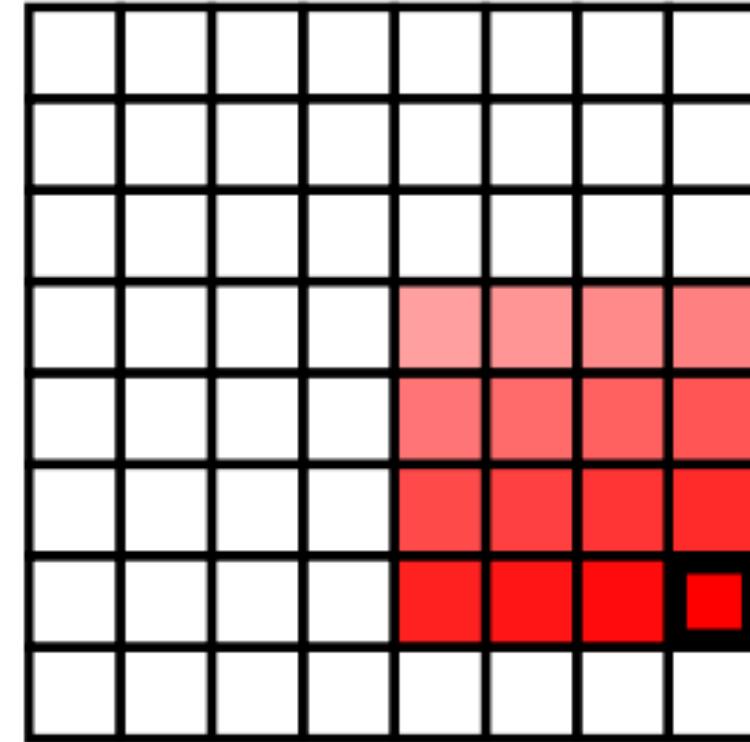
mem:1482

B mem:494 cache hit:430

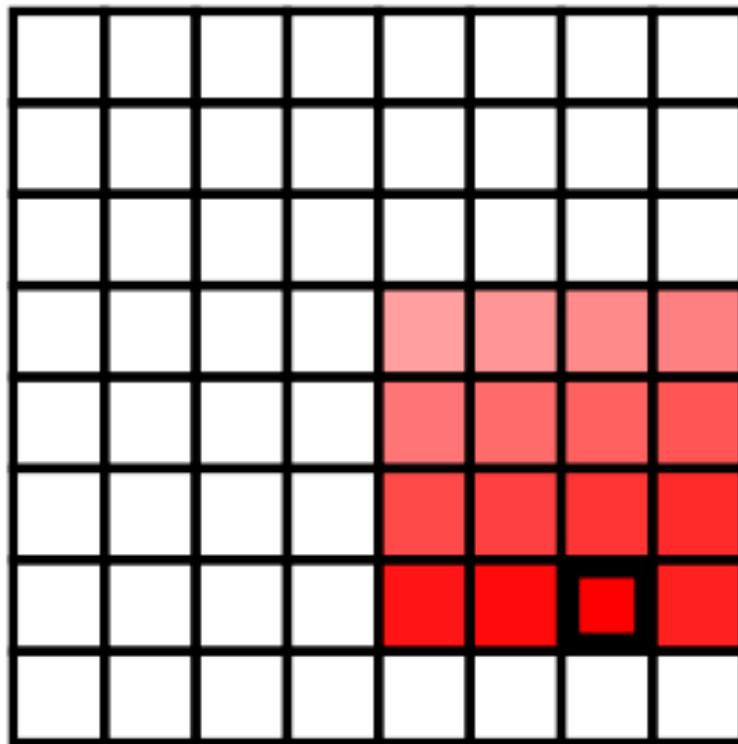


cache hits:1170 \approx 78%

C mem:494 cache hit:370

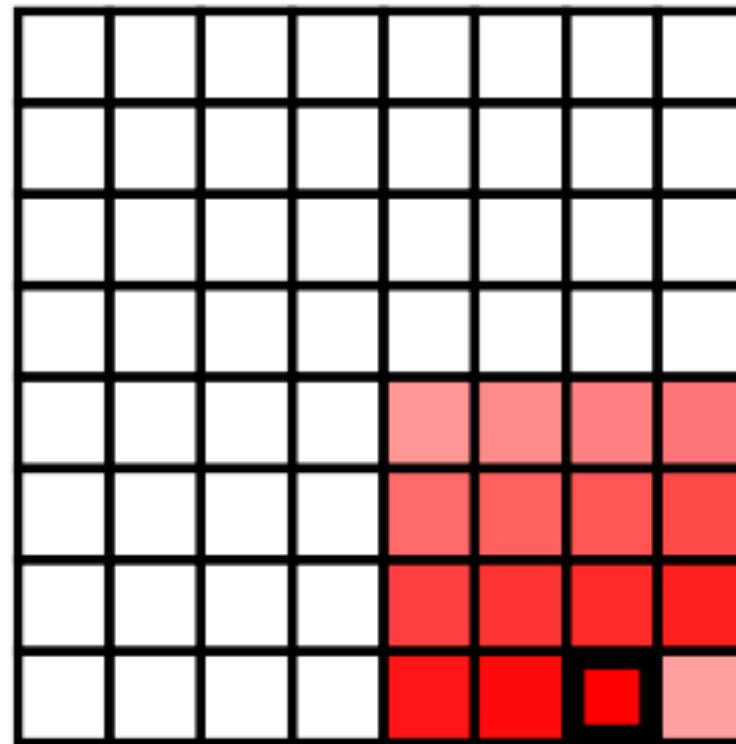


A mem:495 cache hit:371



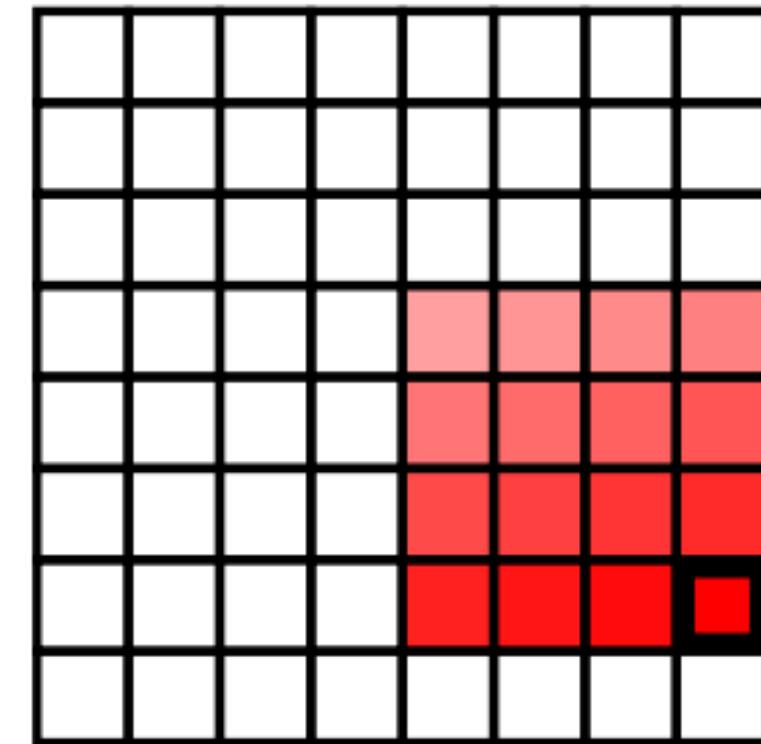
mem:1485

B mem:495 cache hit:431

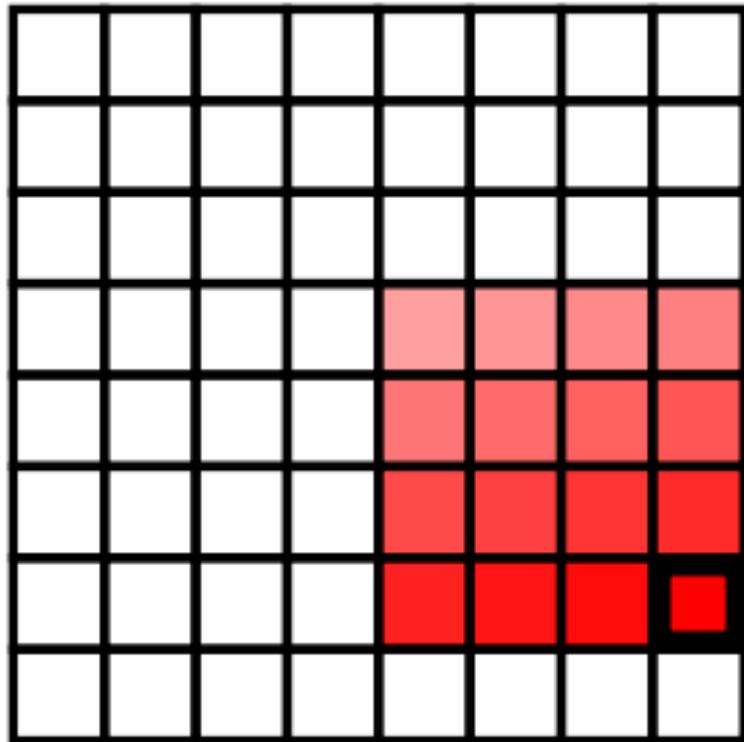


cache hits:1173 \approx 78%

C mem:495 cache hit:371

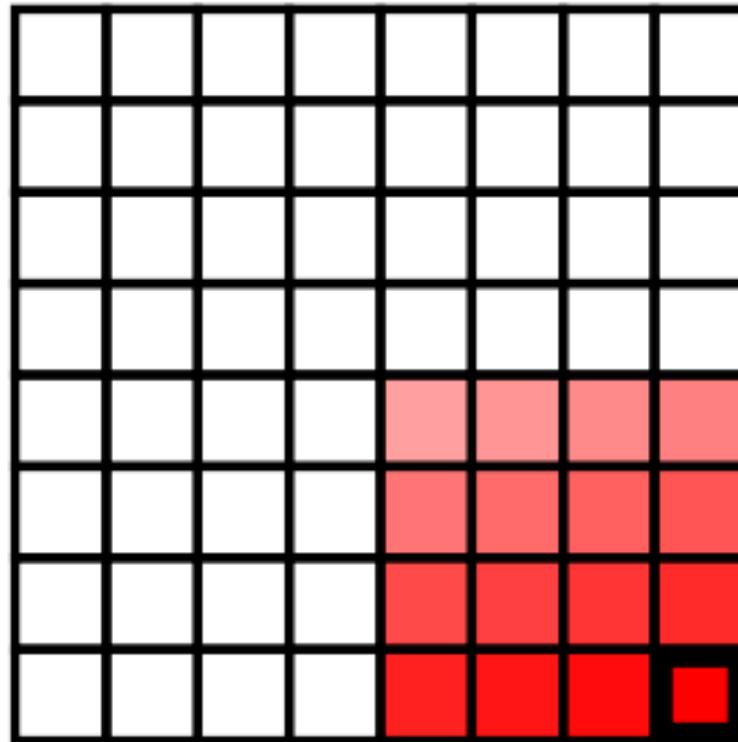


A mem:496 cache hit:372



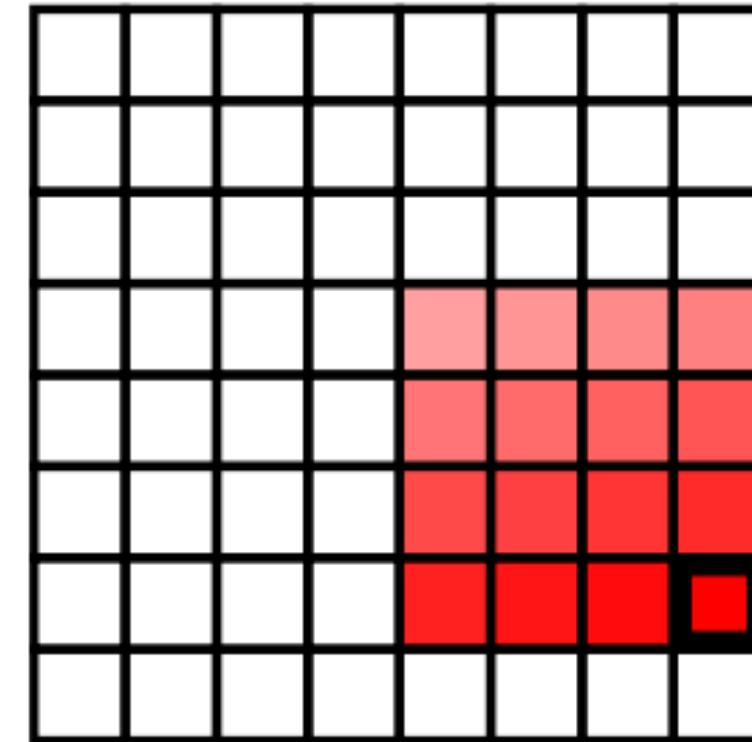
mem:1488

B mem:496 cache hit:432

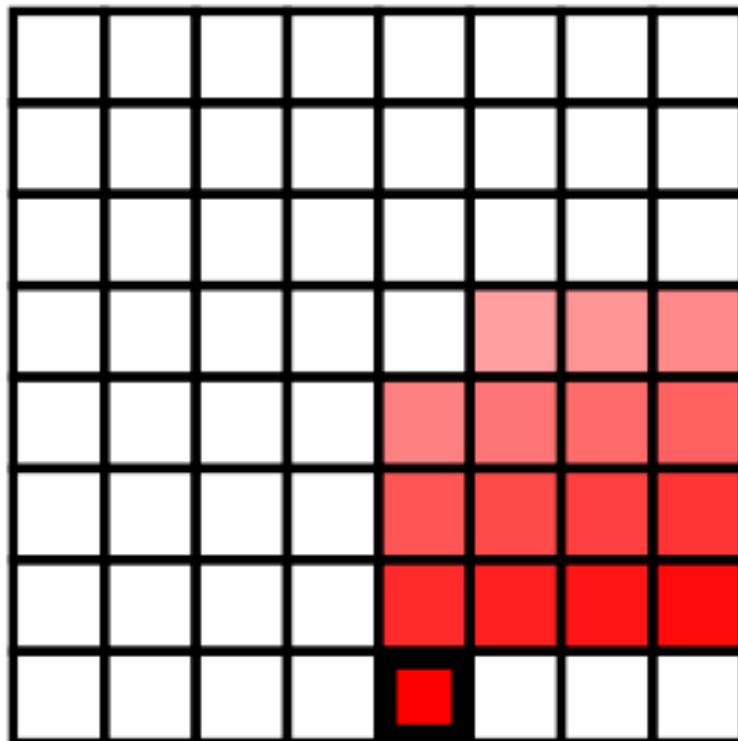


cache hits:1176 \approx 79%

C mem:496 cache hit:372

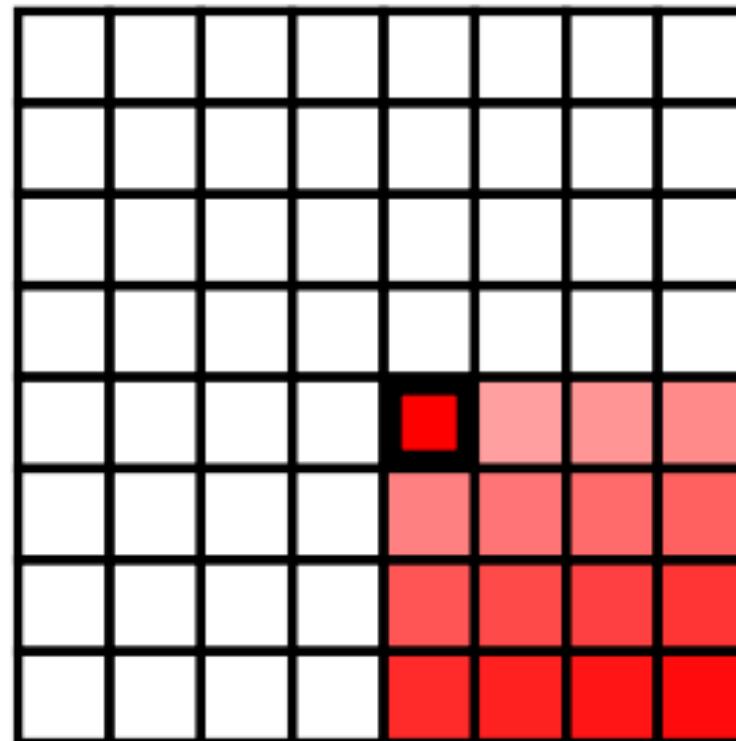


A mem:497 cache hit:372



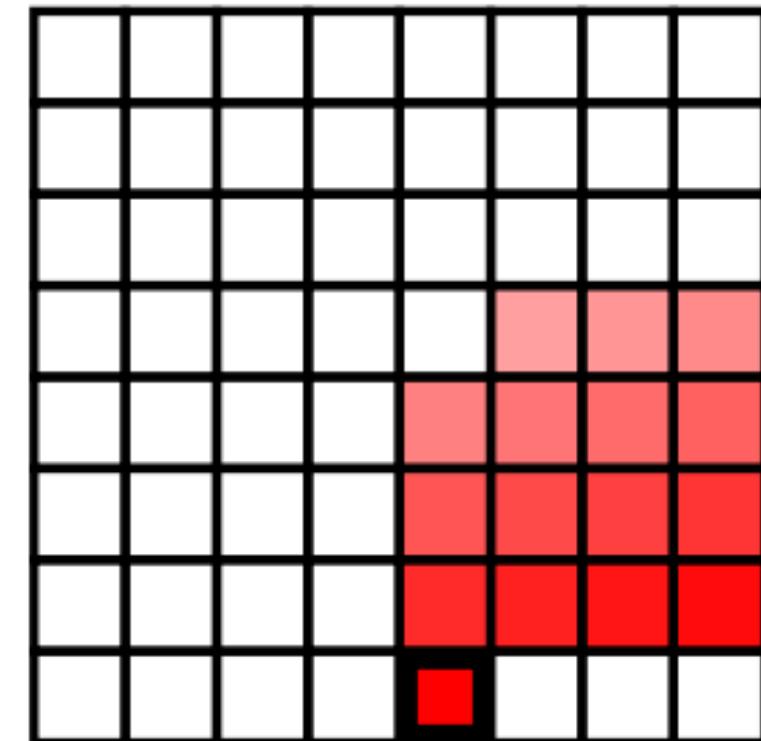
mem:1491

B mem:497 cache hit:433

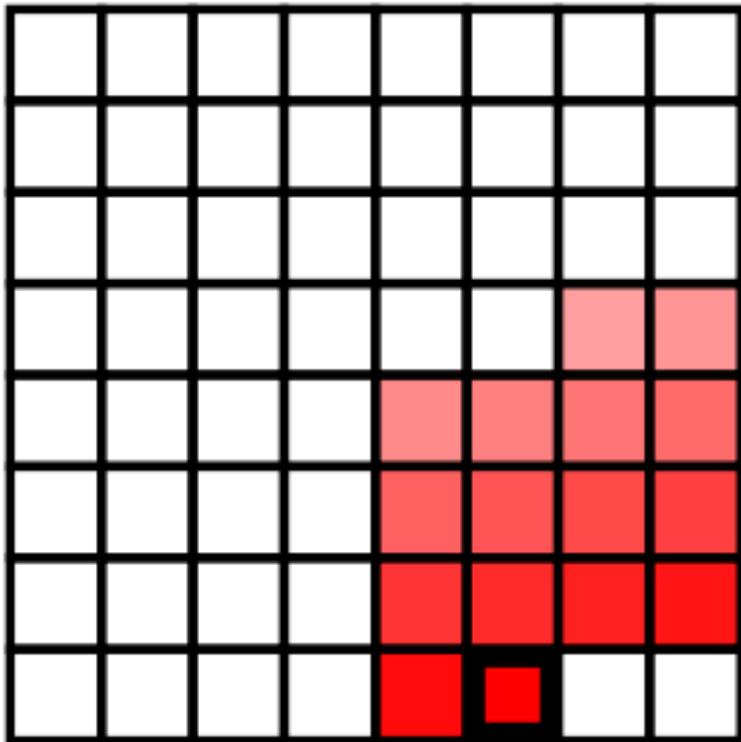


cache hits:1177 \approx 78%

C mem:497 cache hit:372

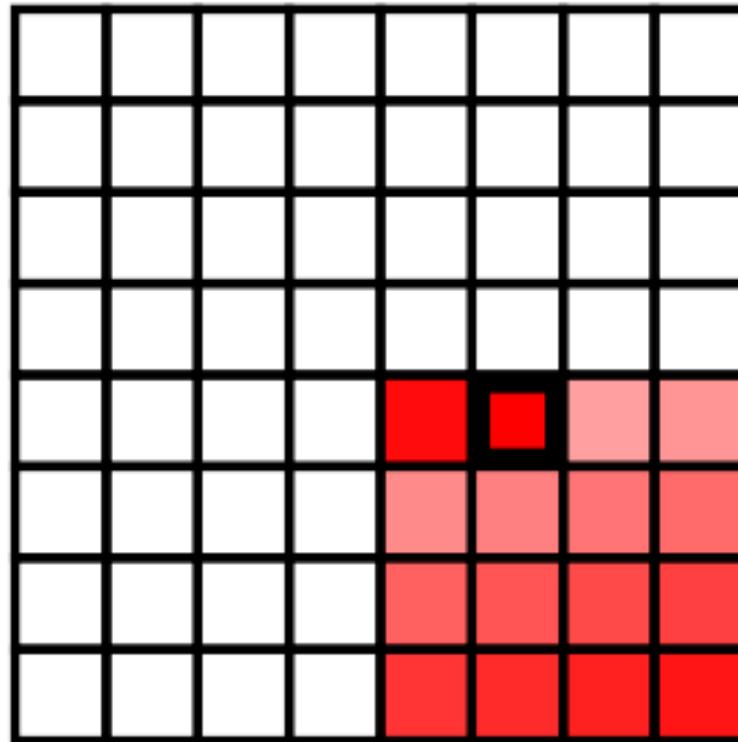


A mem:498 cache hit:372



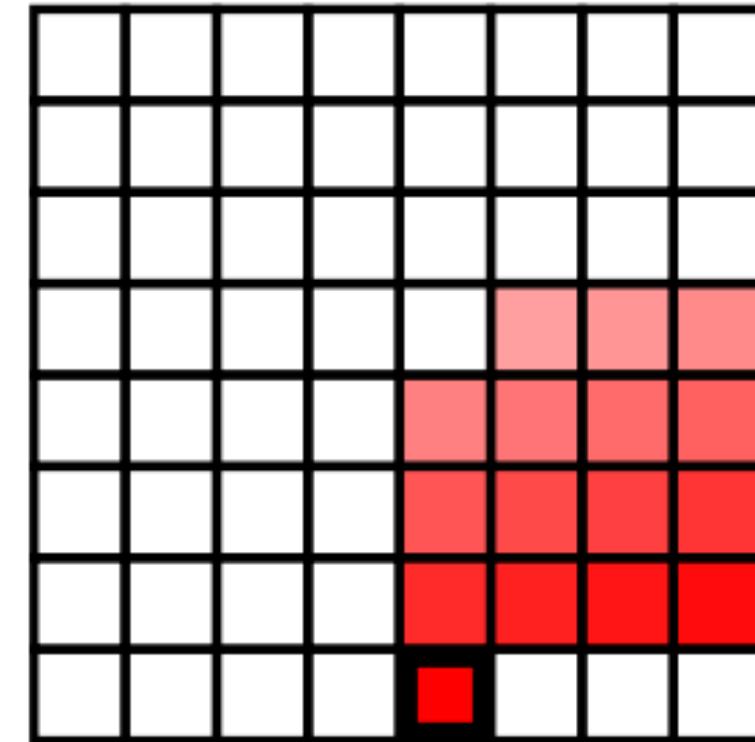
mem:1494

B mem:498 cache hit:434

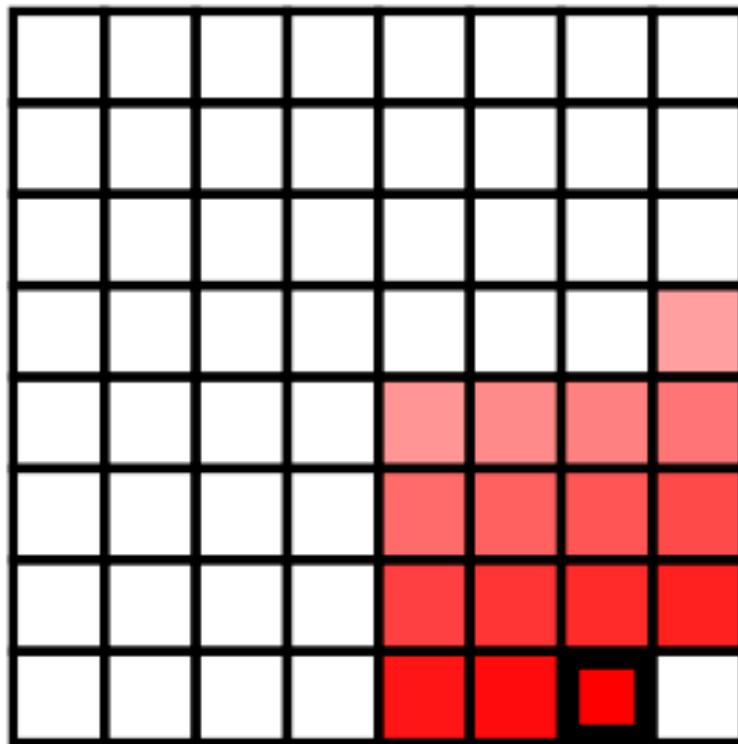


cache hits:1179 \approx 78%

C mem:498 cache hit:373

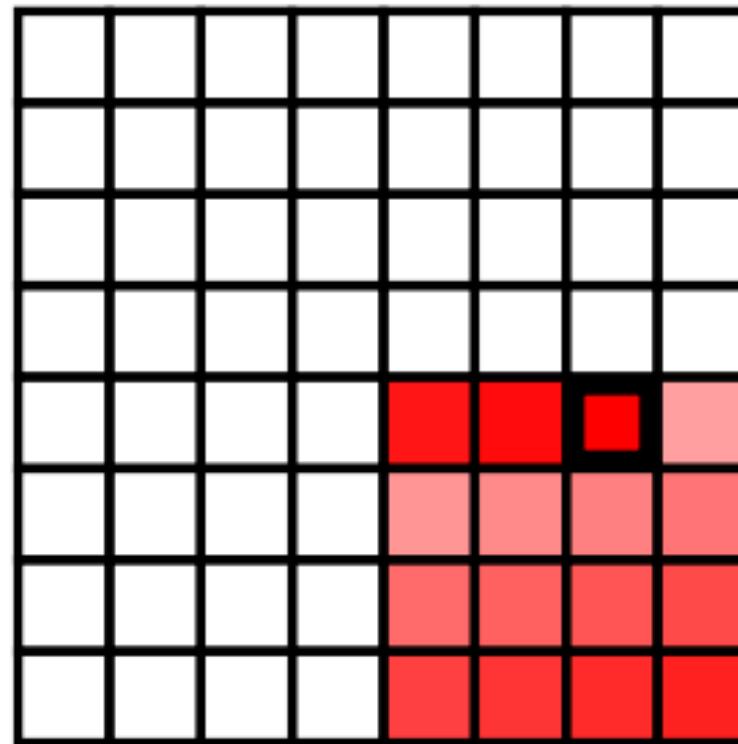


A mem:499 cache hit:372



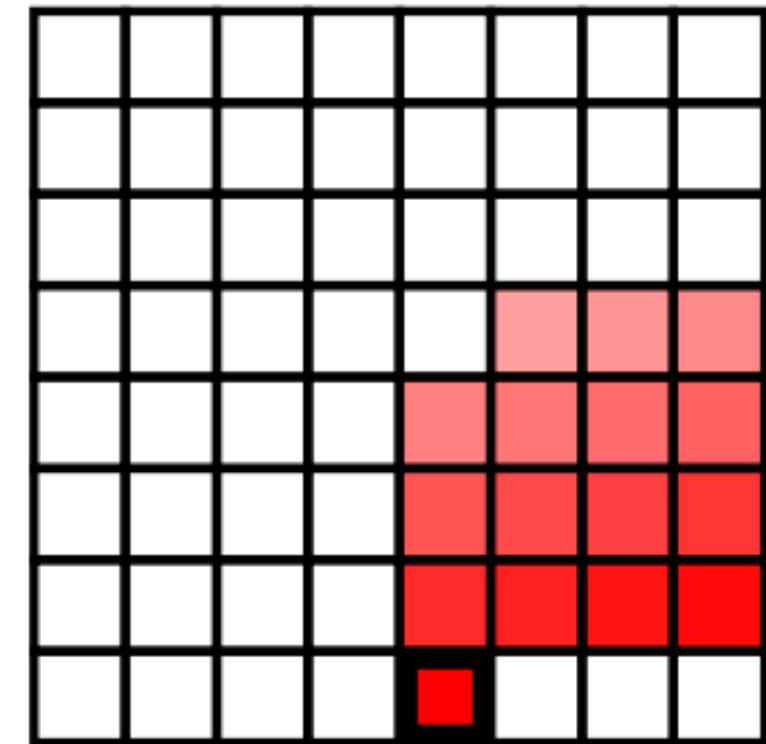
mem:1497

B mem:499 cache hit:435

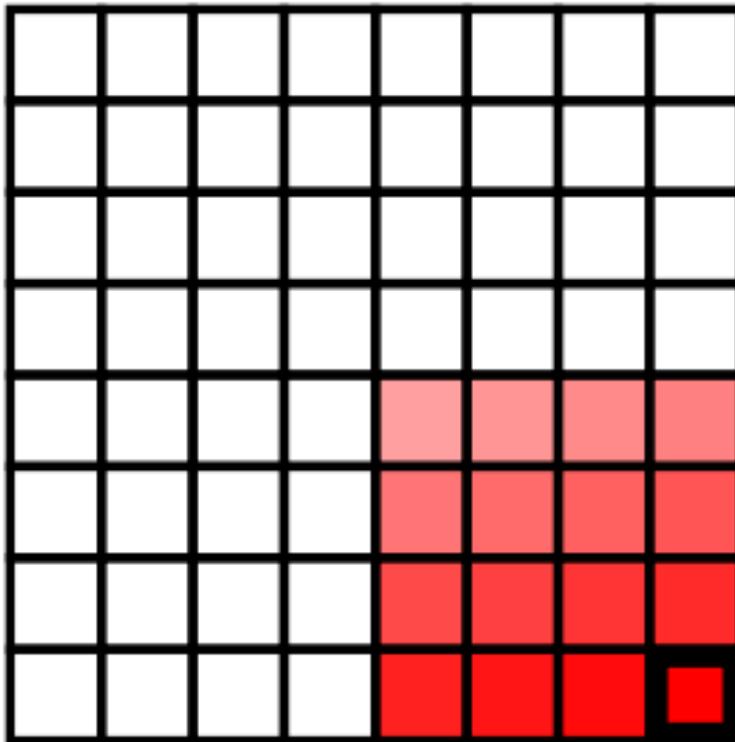


cache hits:1181 \approx 78%

C mem:499 cache hit:374

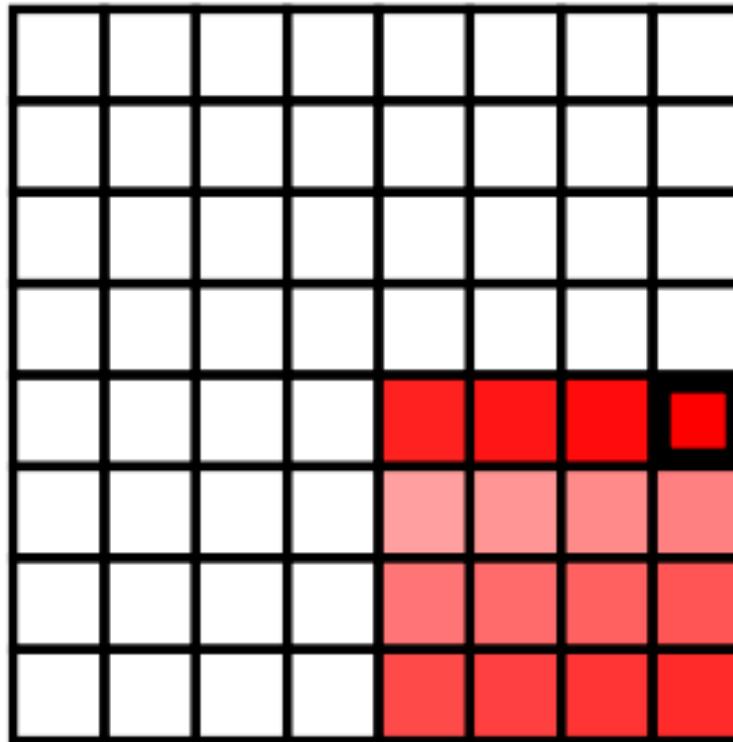


A mem:500 cache hit:372



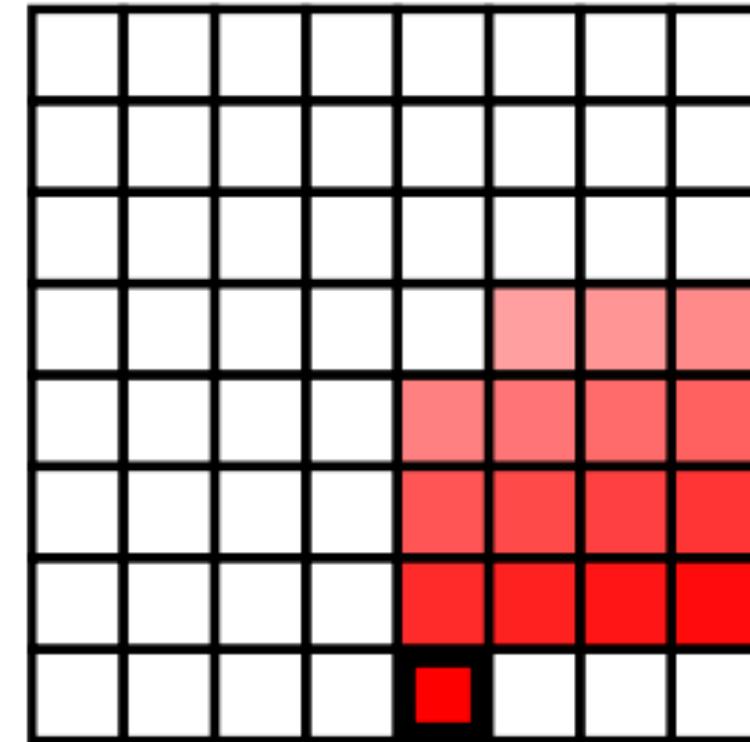
mem:1500

B mem:500 cache hit:436

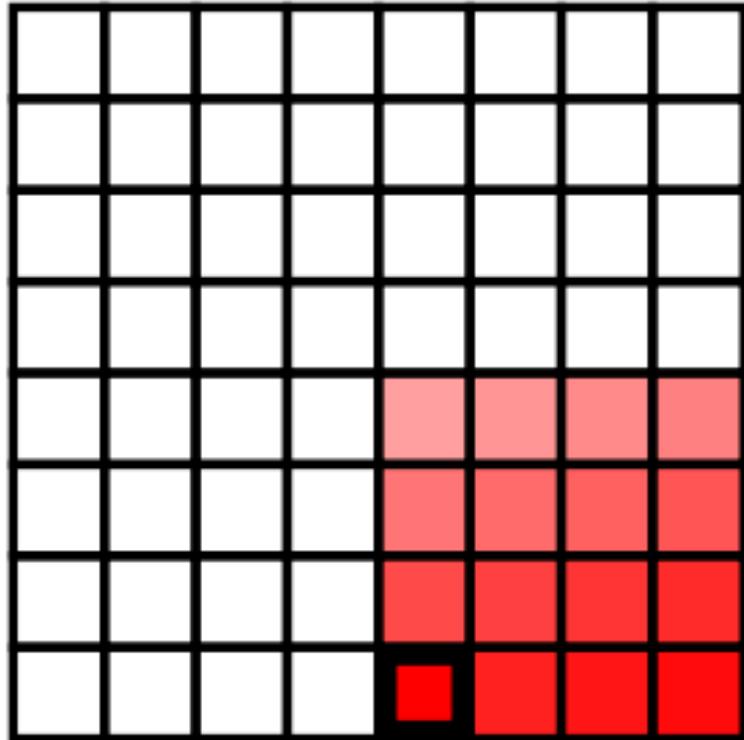


cache hits:1183 \approx 78%

C mem:500 cache hit:375

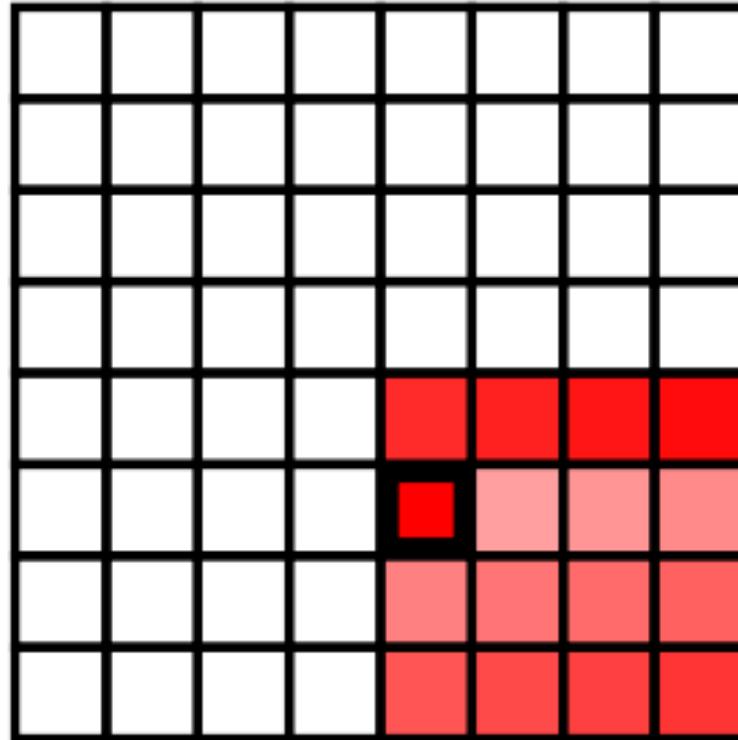


A mem:501 cache hit:373



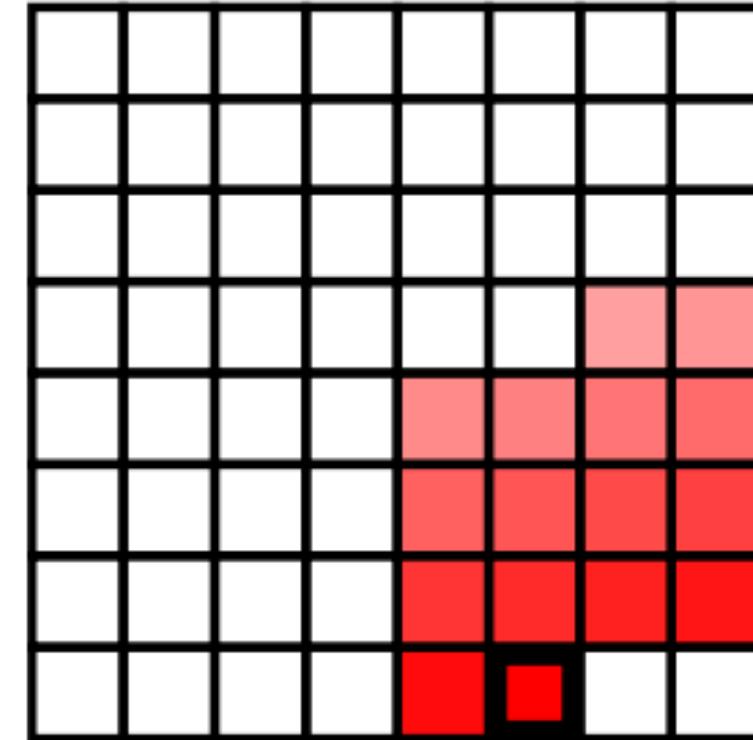
mem:1503

B mem:501 cache hit:437

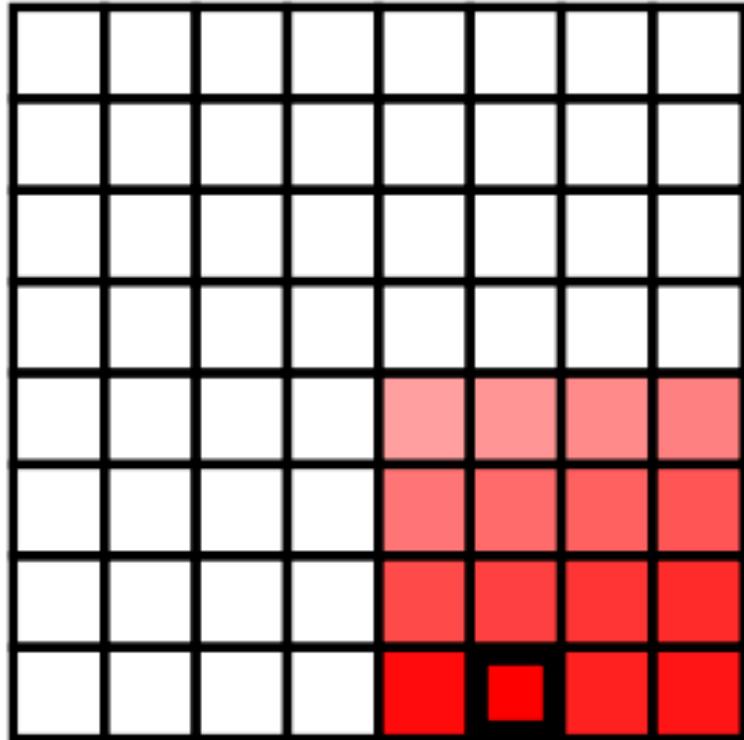


cache hits:1185 \approx 78%

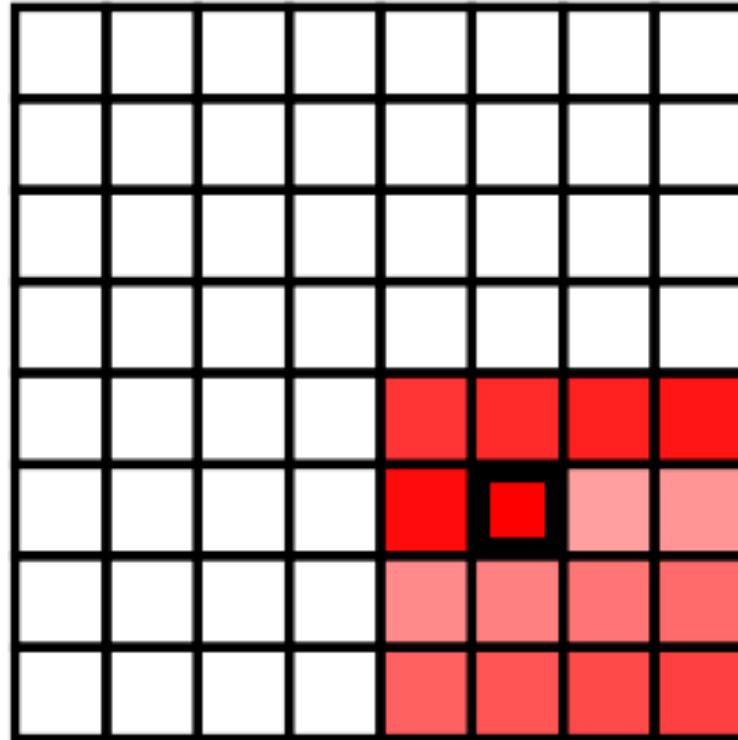
C mem:501 cache hit:375



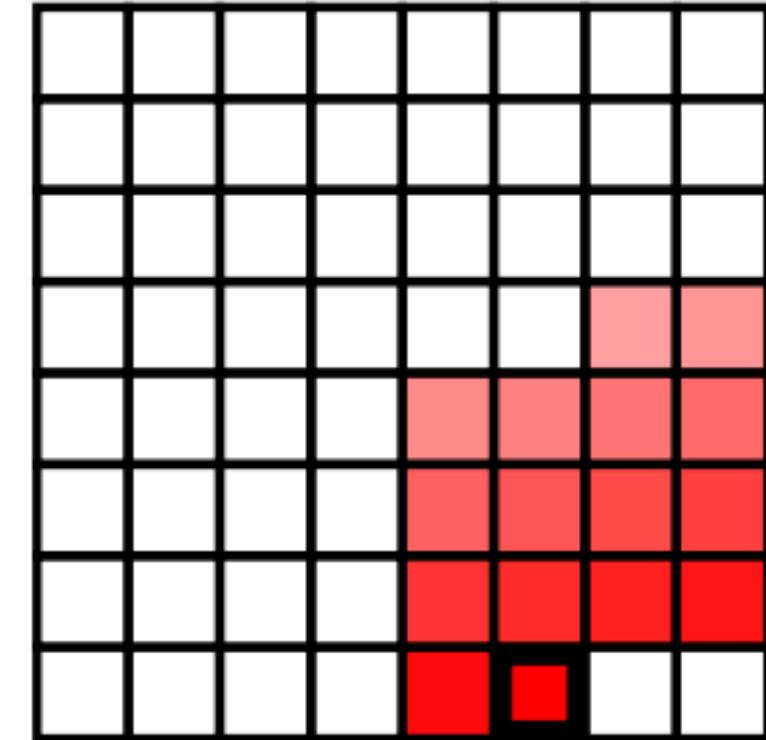
A mem:502 cache hit:374



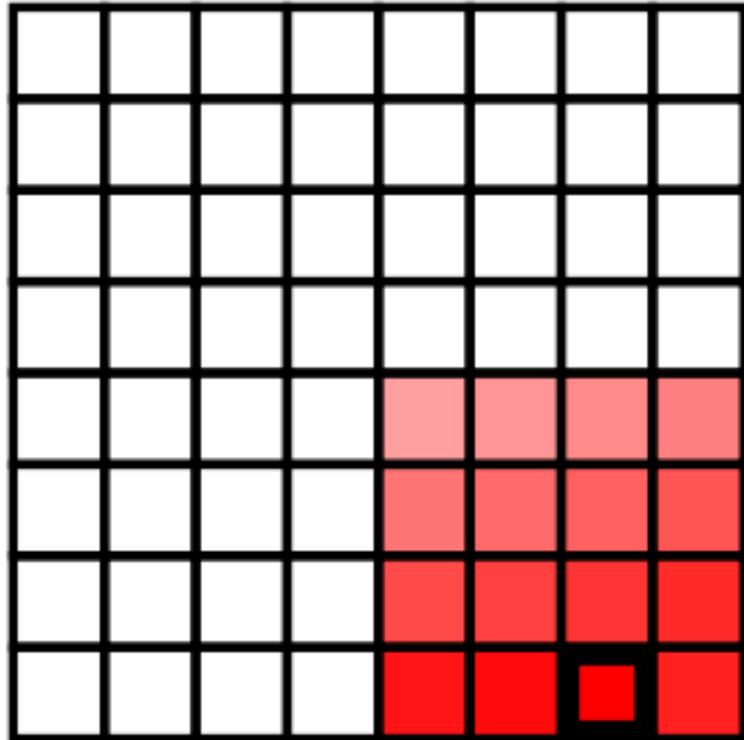
B mem:502 cache hit:438



C mem:502 cache hit:376

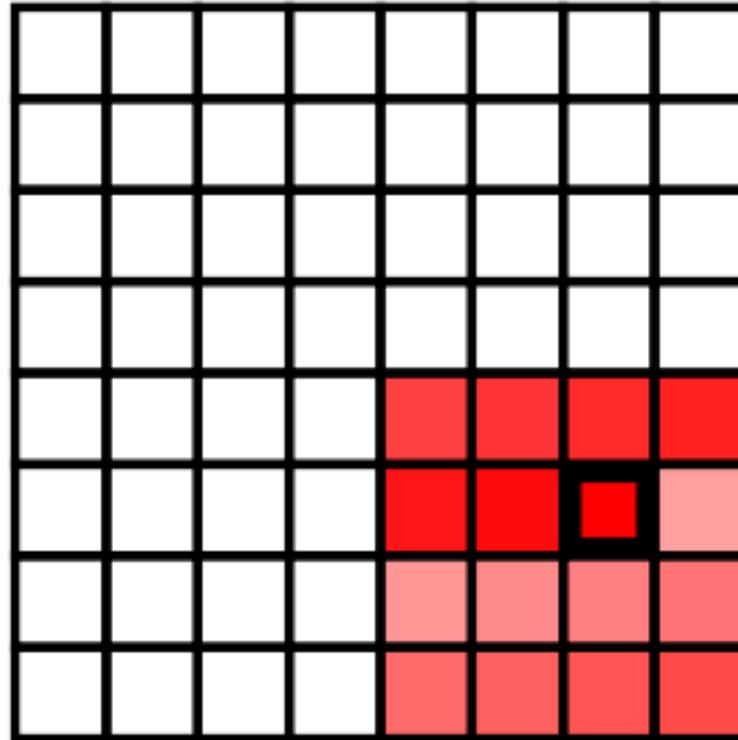


A mem:503 cache hit:375



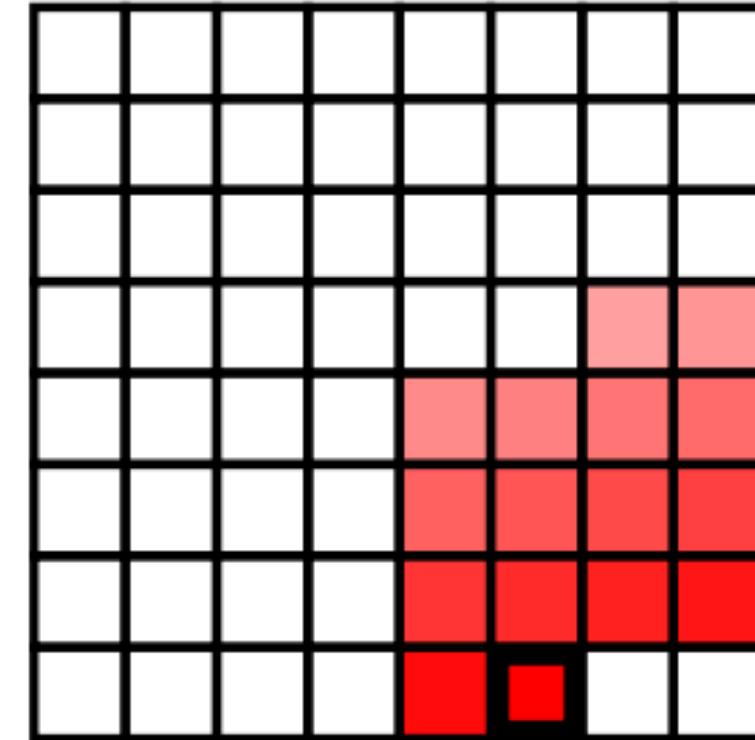
mem:1509

B mem:503 cache hit:439

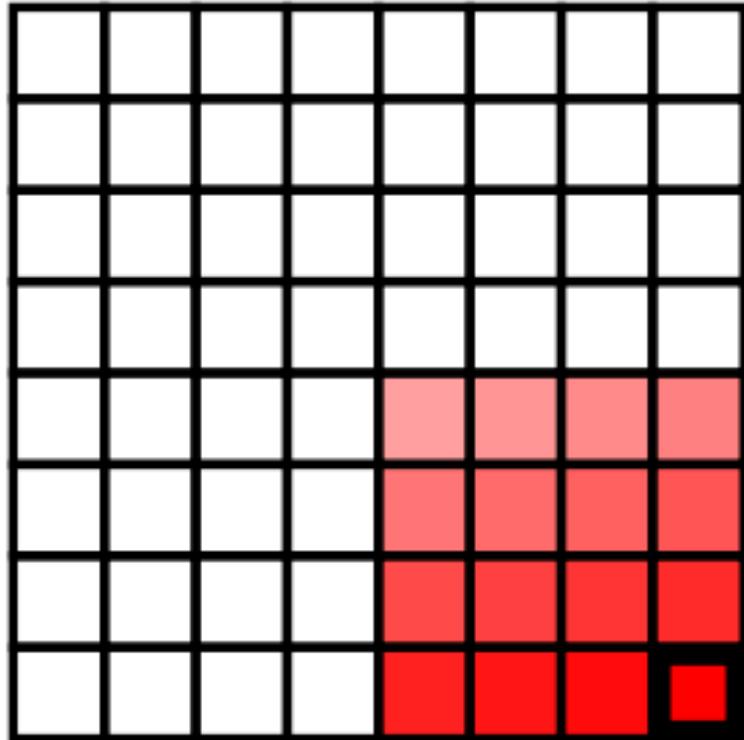


cache hits:1191 \approx 78%

C mem:503 cache hit:377

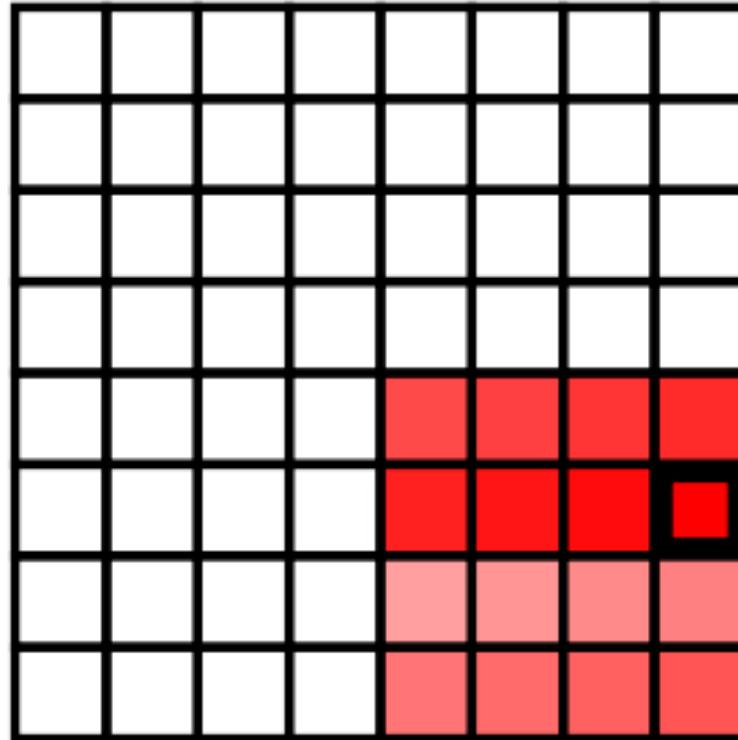


A mem:504 cache hit:376



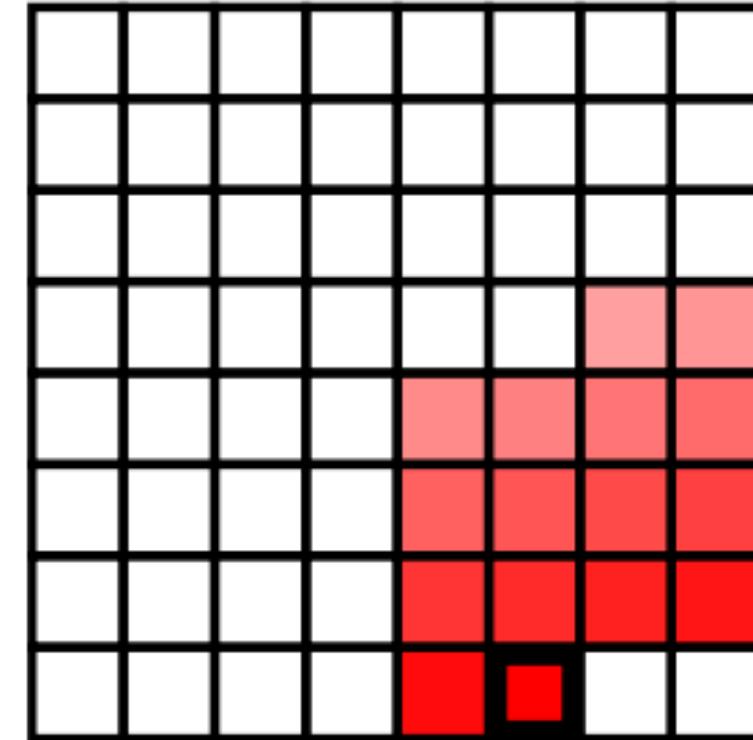
mem:1512

B mem:504 cache hit:440

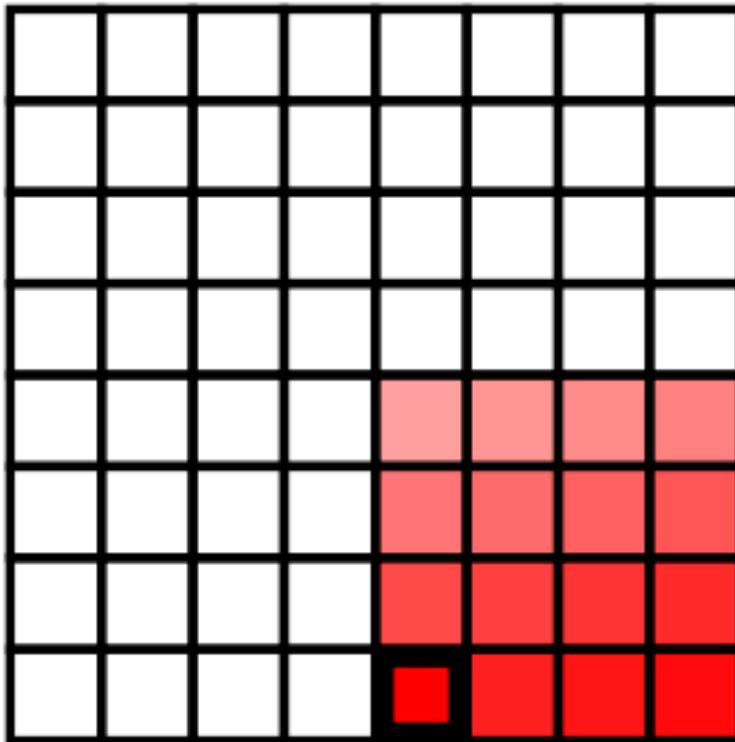


cache hits:1194 \approx 78%

C mem:504 cache hit:378

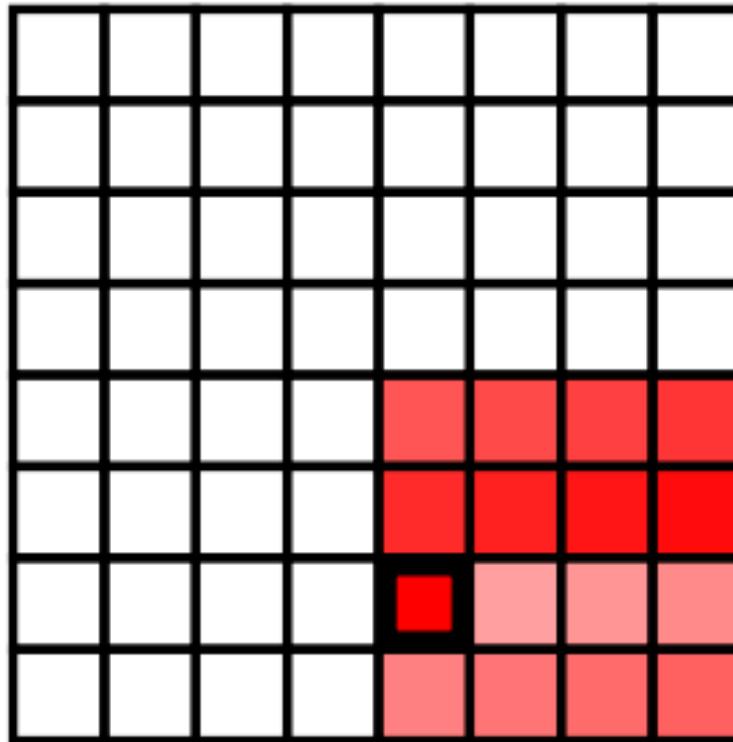


A mem:505 cache hit:377



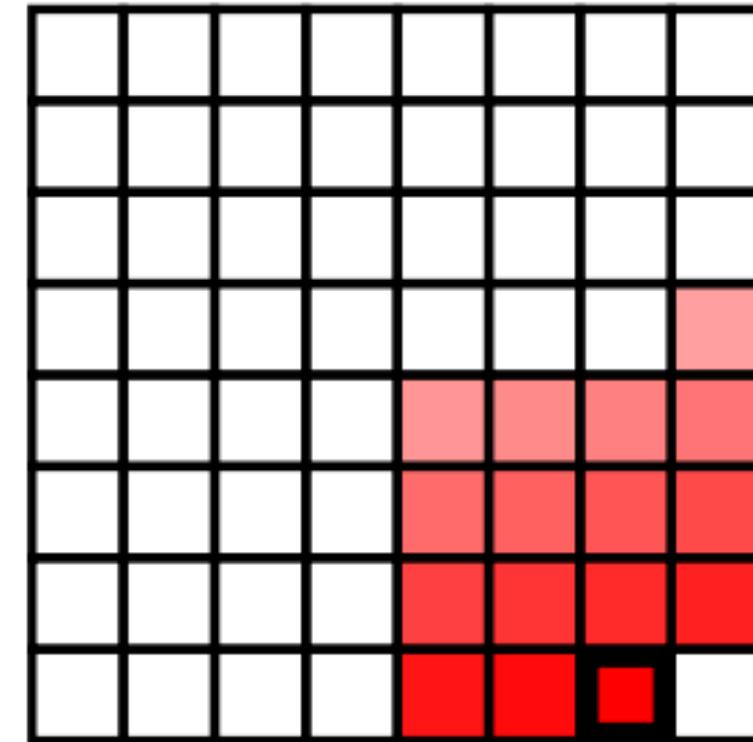
mem:1515

B mem:505 cache hit:441

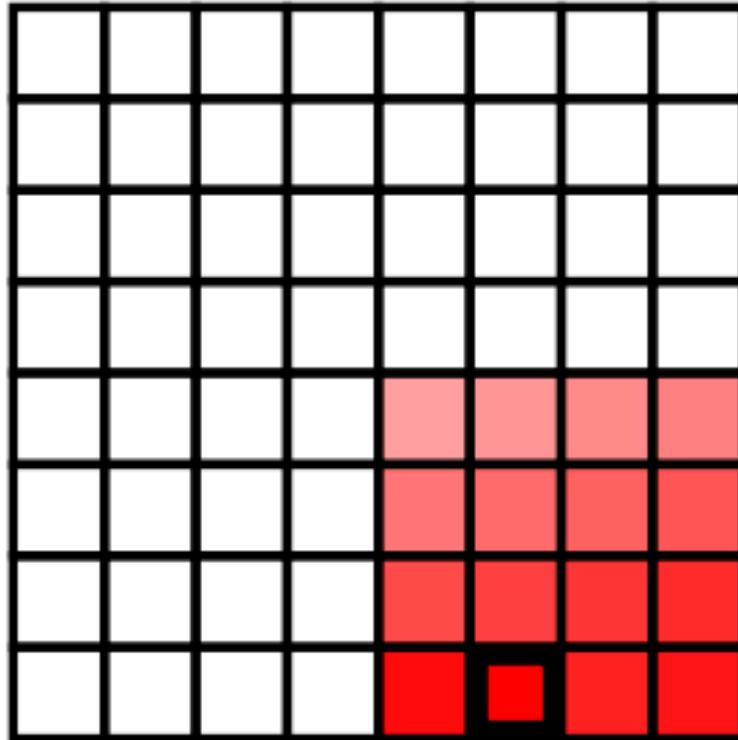


cache hits:1196 \approx 78%

C mem:505 cache hit:378

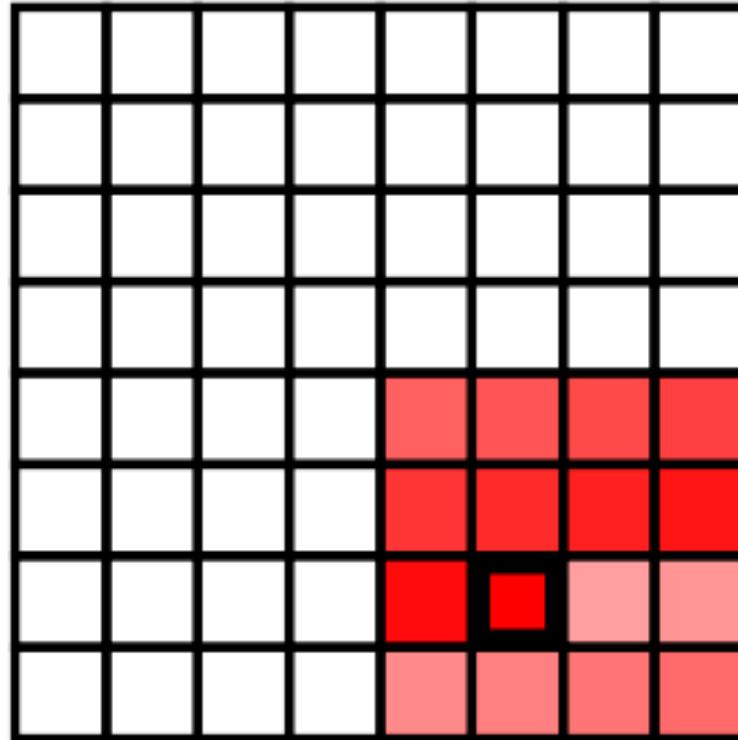


A mem:506 cache hit:378



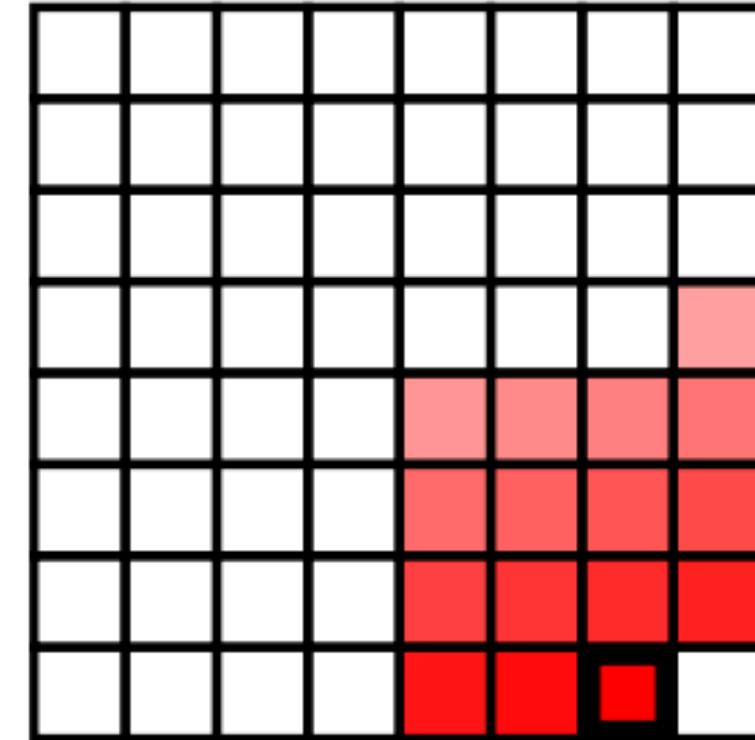
mem:1518

B mem:506 cache hit:442



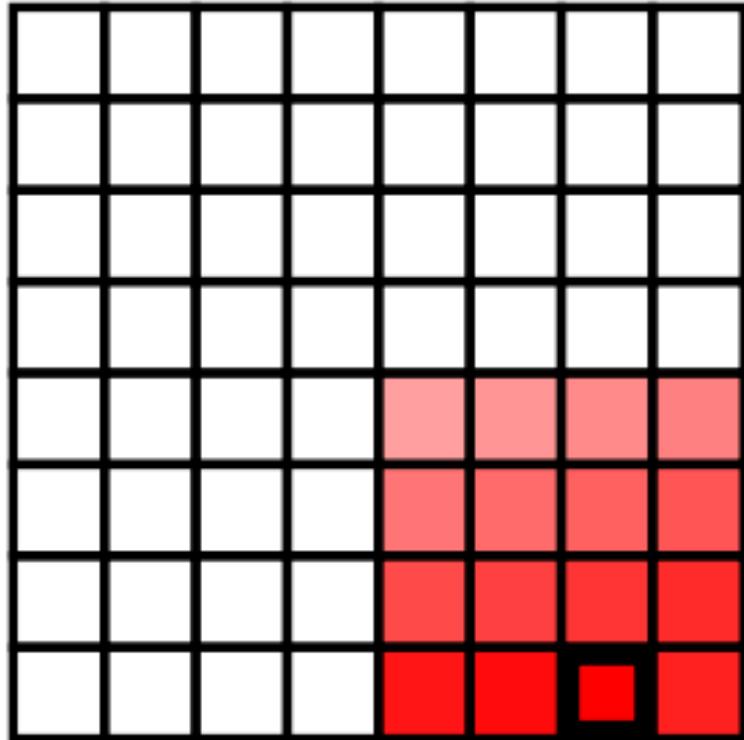
cache hits:1199 \approx 78%

C mem:506 cache hit:379



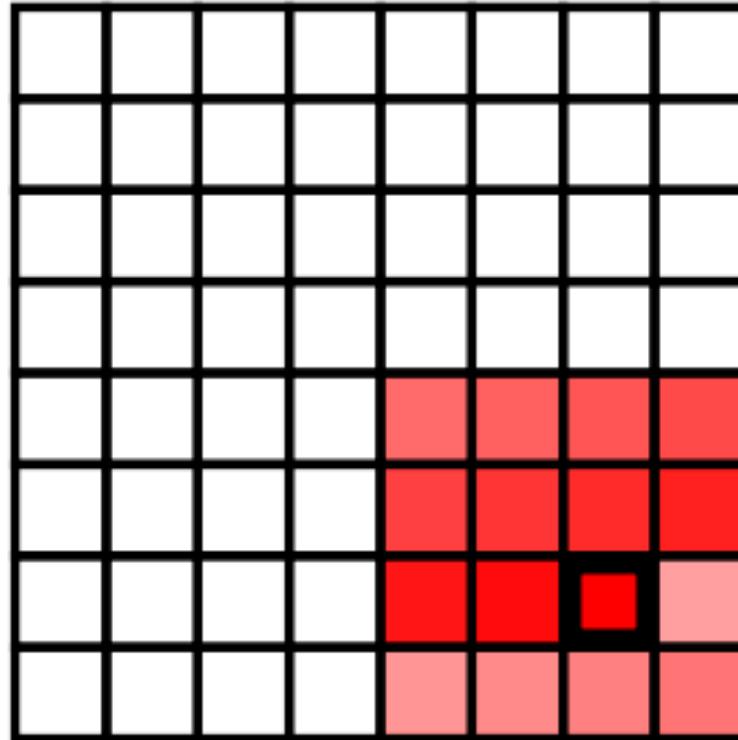
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A mem:507 cache hit:379



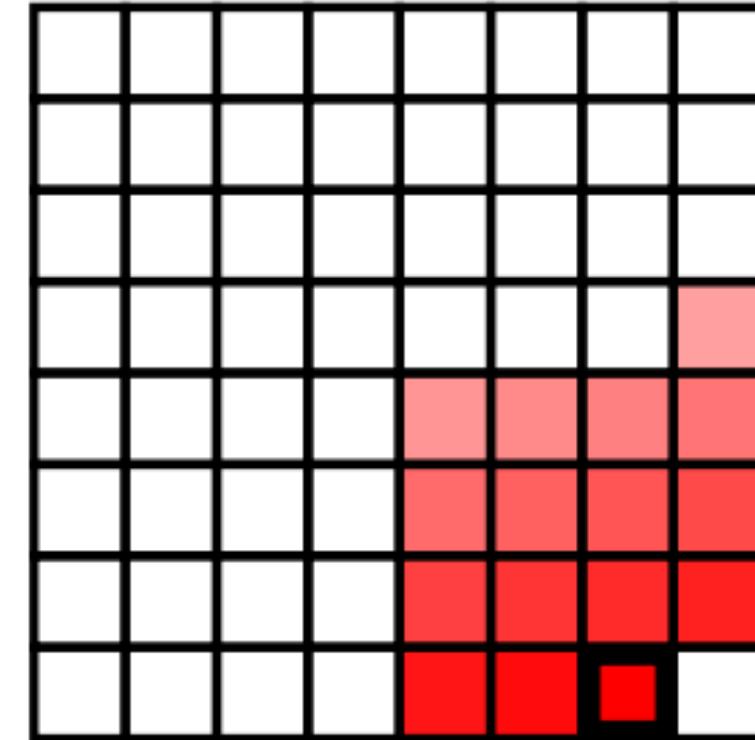
mem:1521

B mem:507 cache hit:443

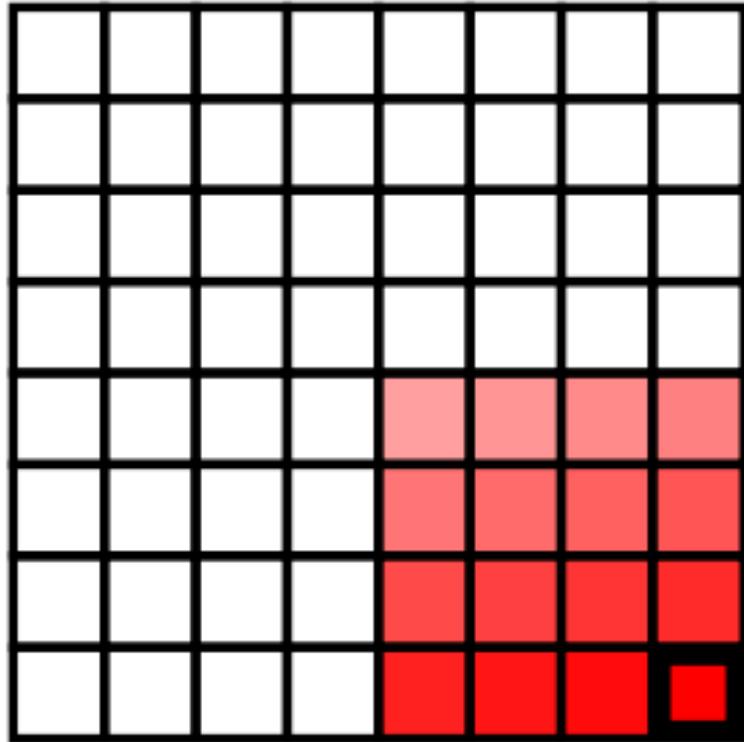


cache hits:1202 \approx 79%

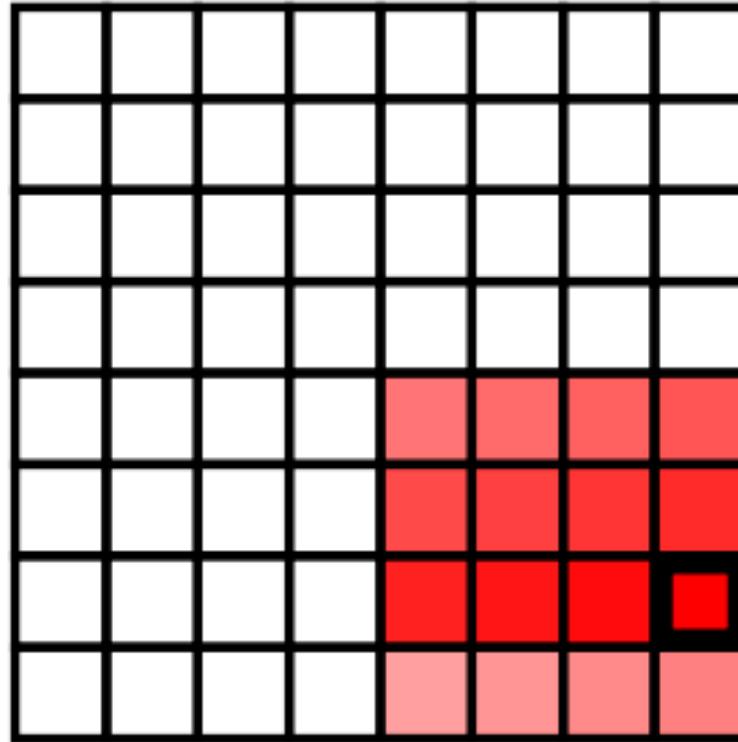
C mem:507 cache hit:380



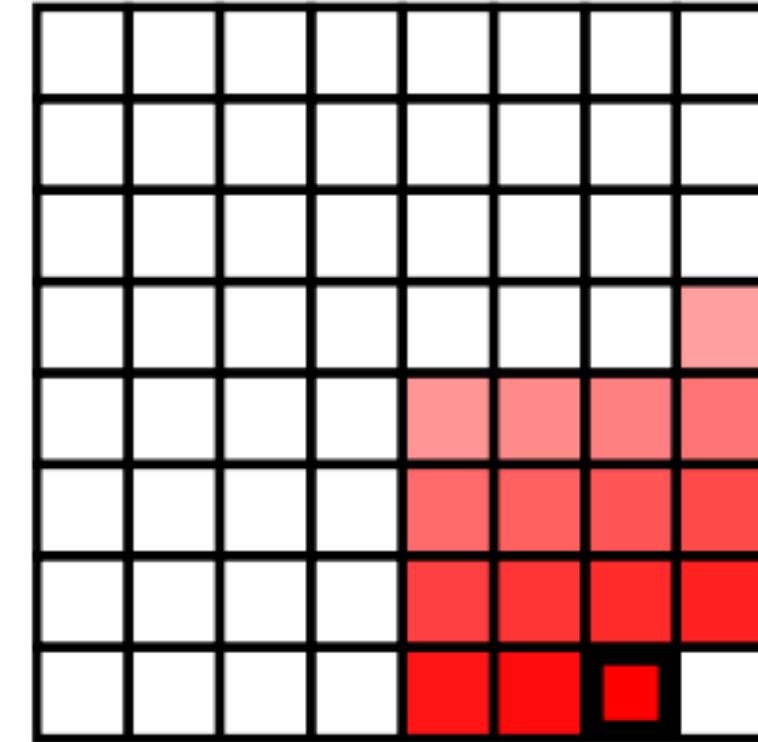
A mem:508 cache hit:380



B mem:508 cache hit:444



C mem:508 cache hit:381



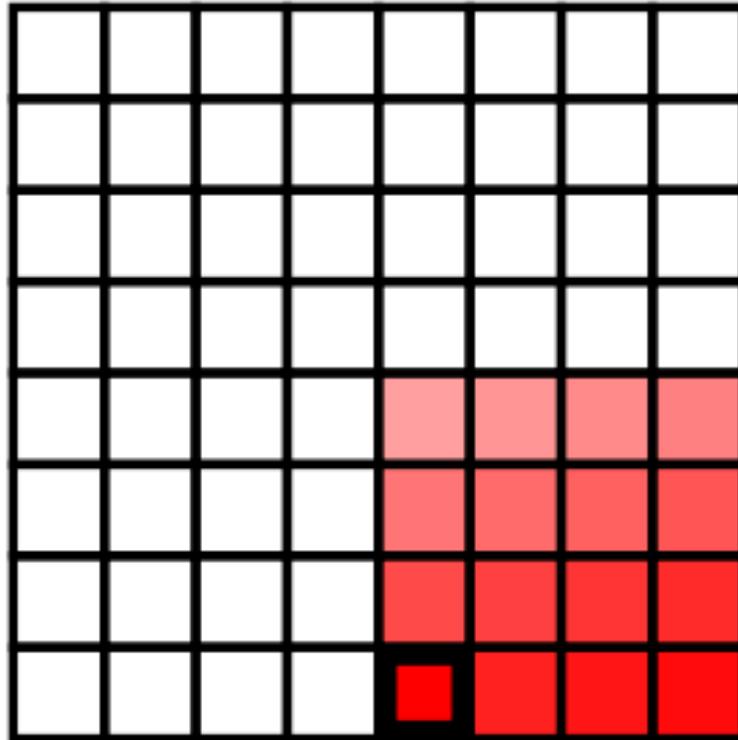
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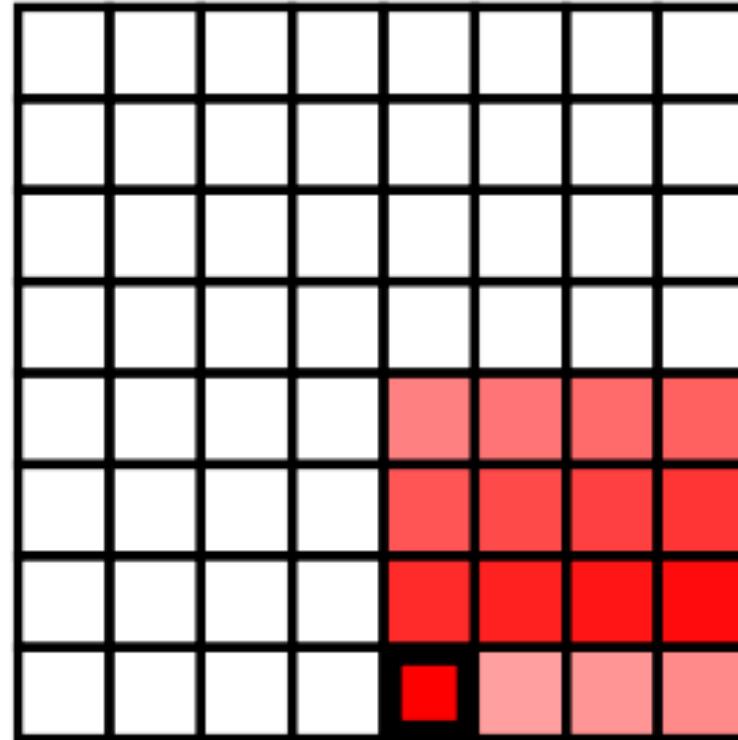
mem:1524

cache hits:1205 \approx 79%

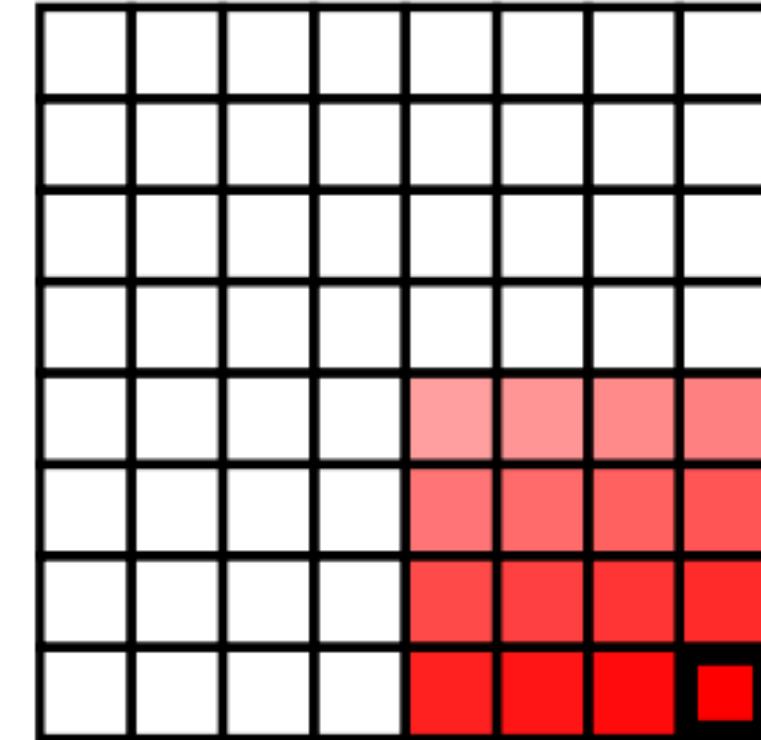
A mem:509 cache hit:381



B mem:509 cache hit:445



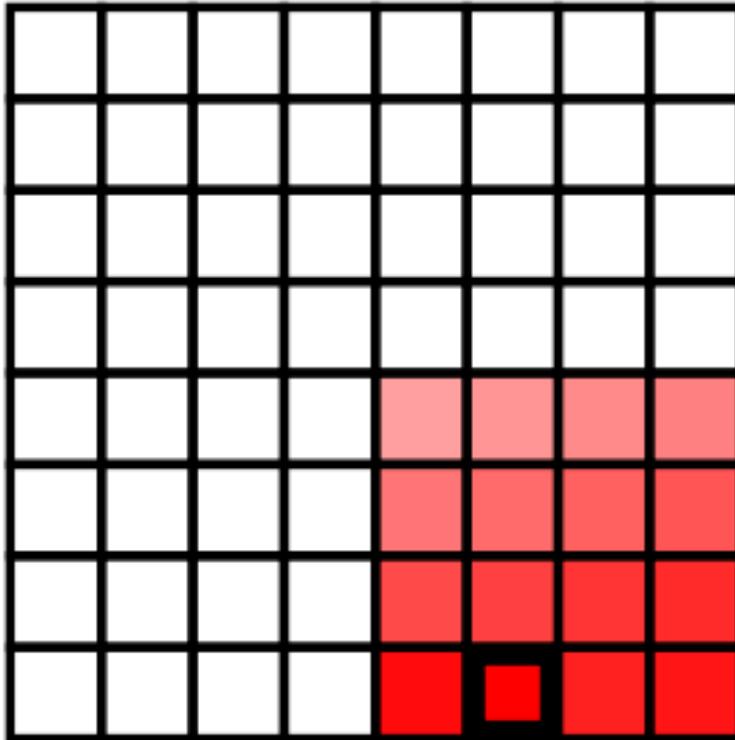
C mem:509 cache hit:381



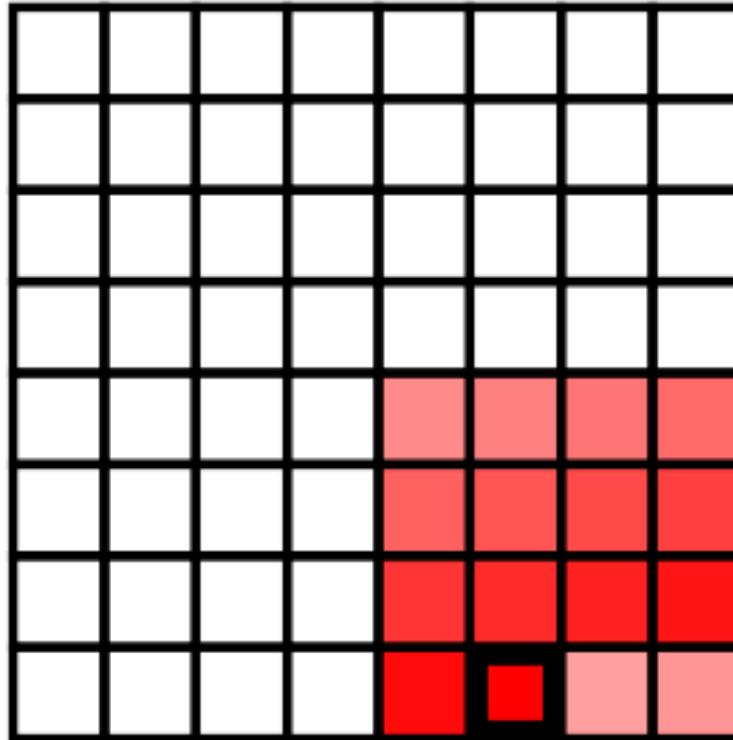
mem:1527

cache hits:1207 \approx 79%

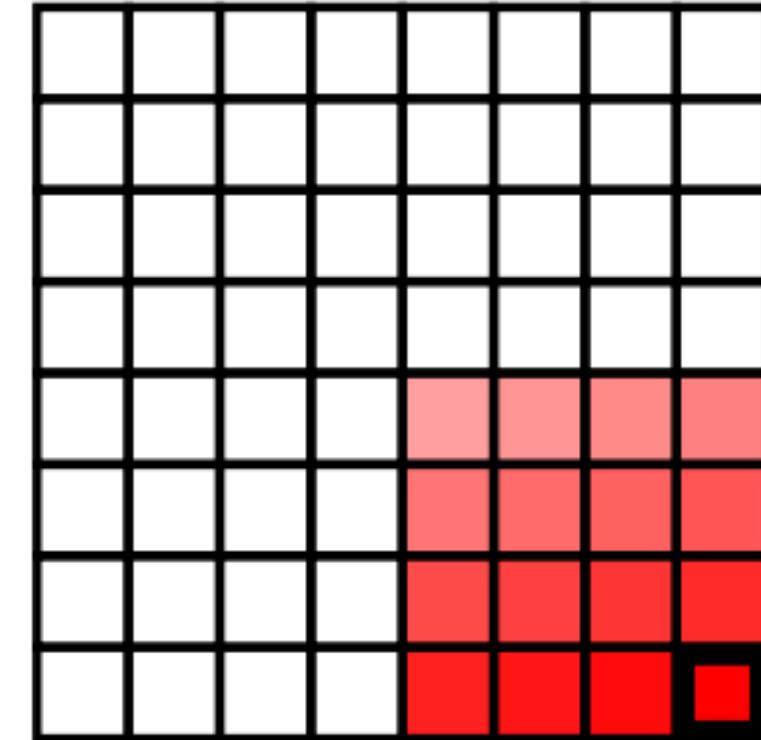
A mem:510 cache hit:382



B mem:510 cache hit:446



C mem:510 cache hit:382



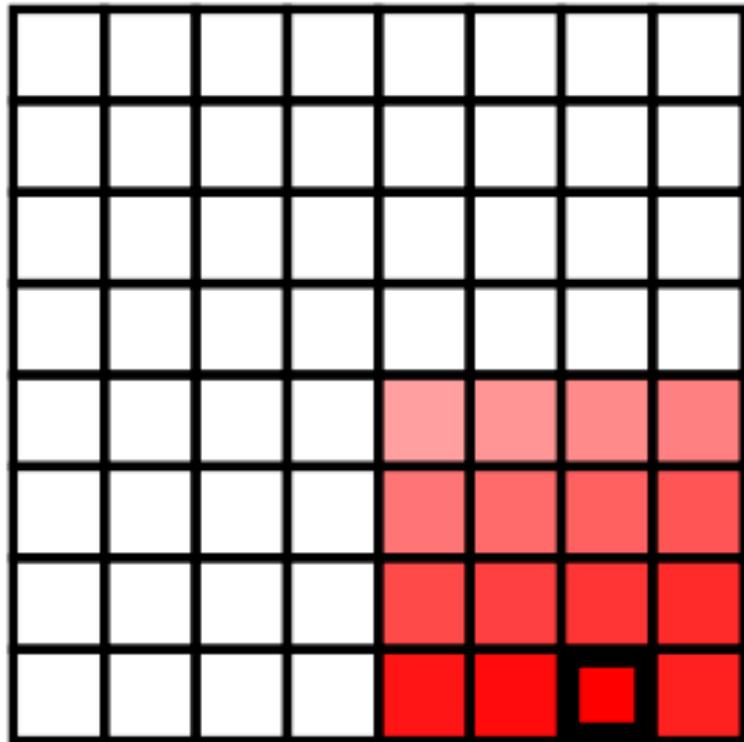
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mem:1530

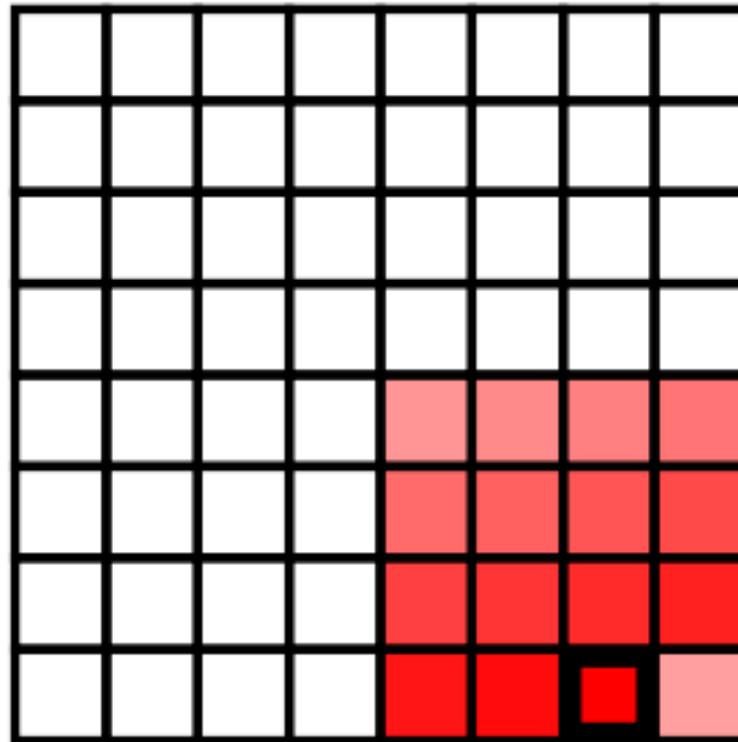
cache hits:1210 \approx 79%

A mem:511 cache hit:383



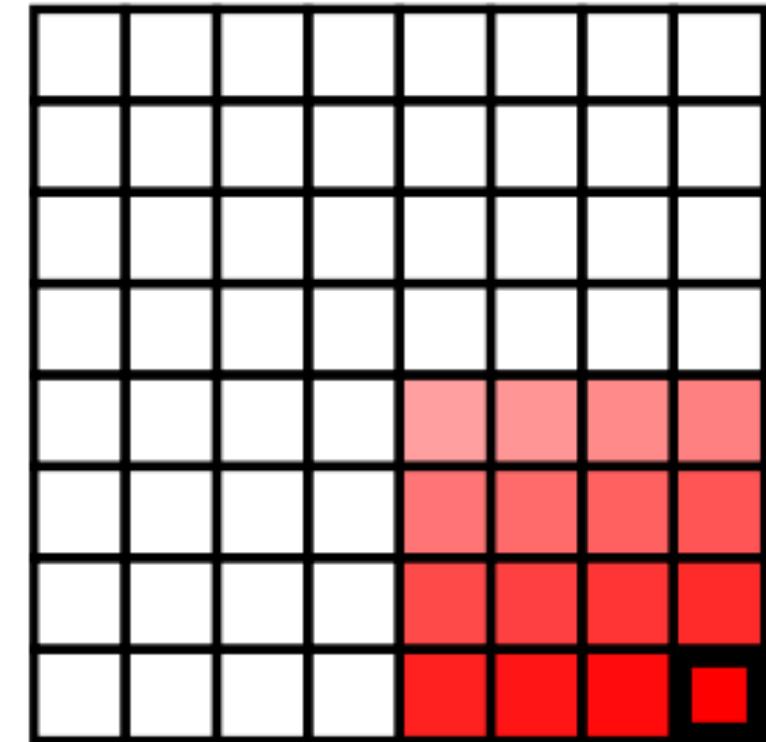
mem:1533

B mem:511 cache hit:447

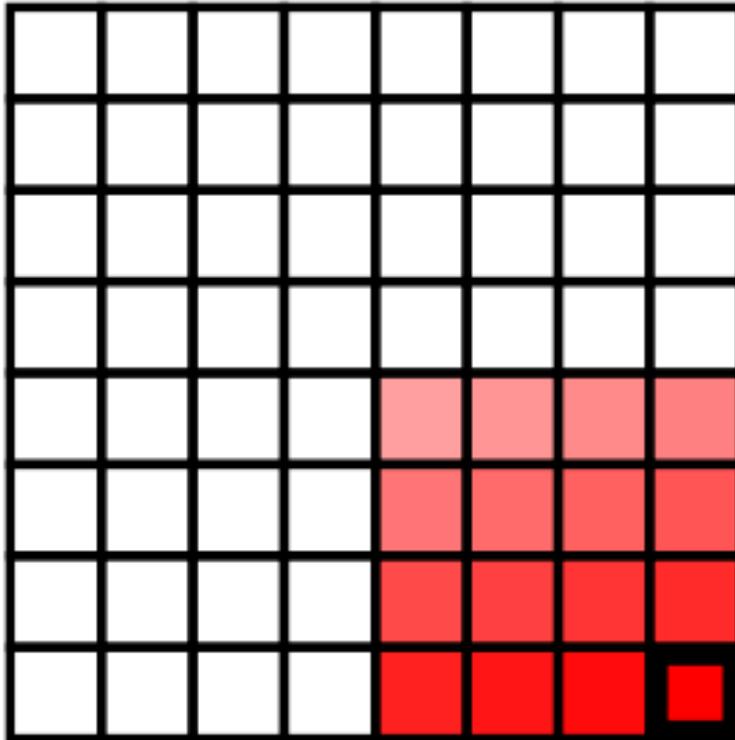


cache hits:1213 \approx 79%

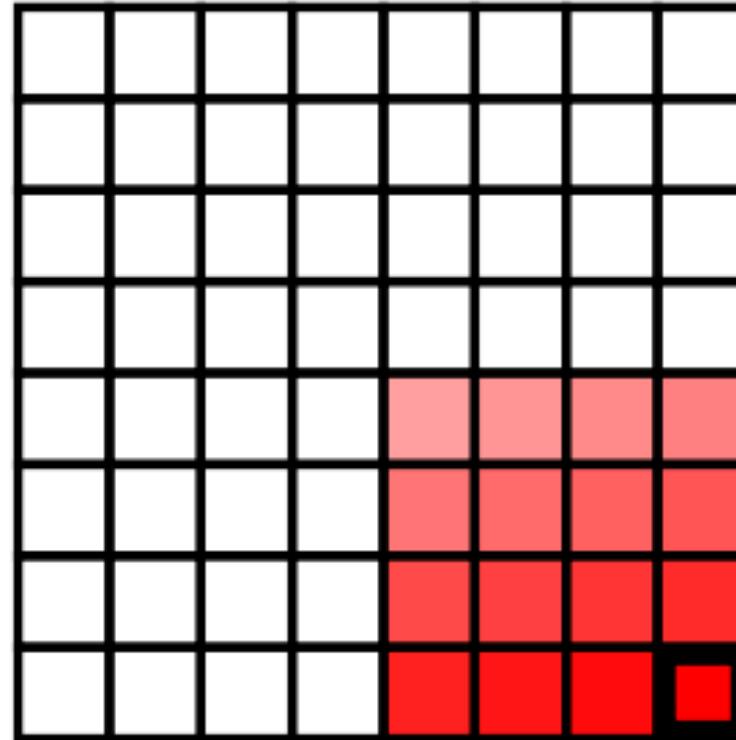
C mem:511 cache hit:383



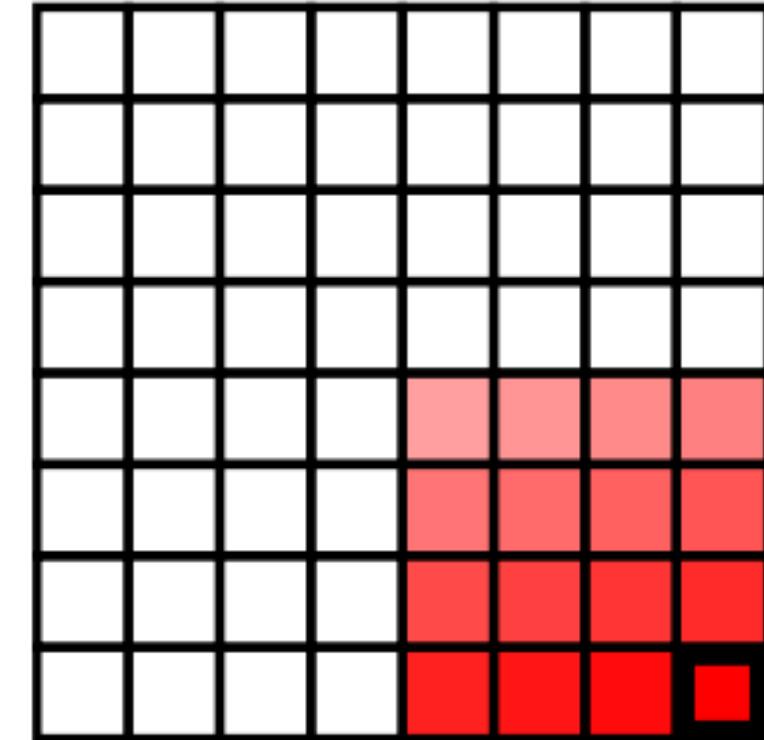
A mem:512 cache hit:384



B mem:512 cache hit:448



C mem:512 cache hit:384



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mem:1536

cache hits:1216 \approx 79%