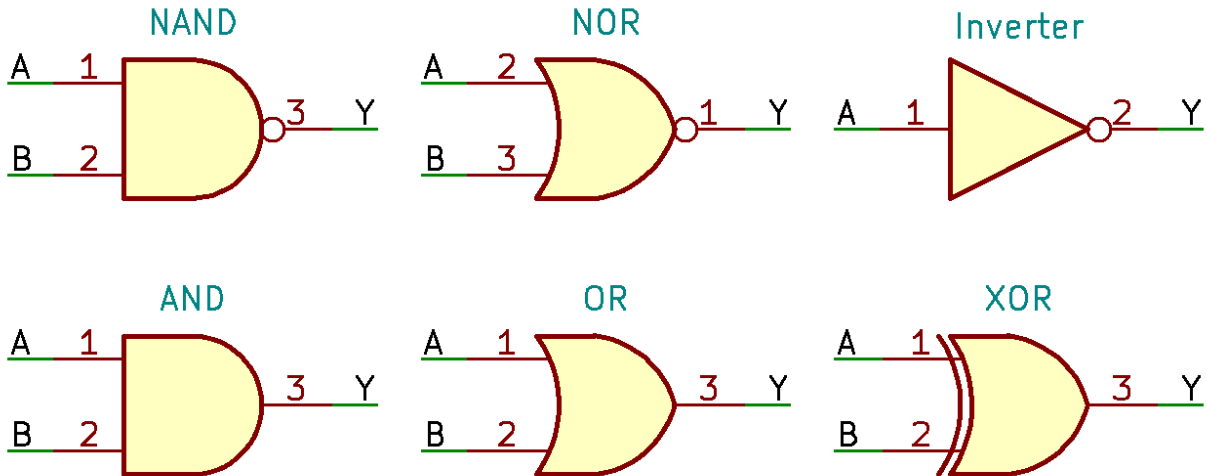


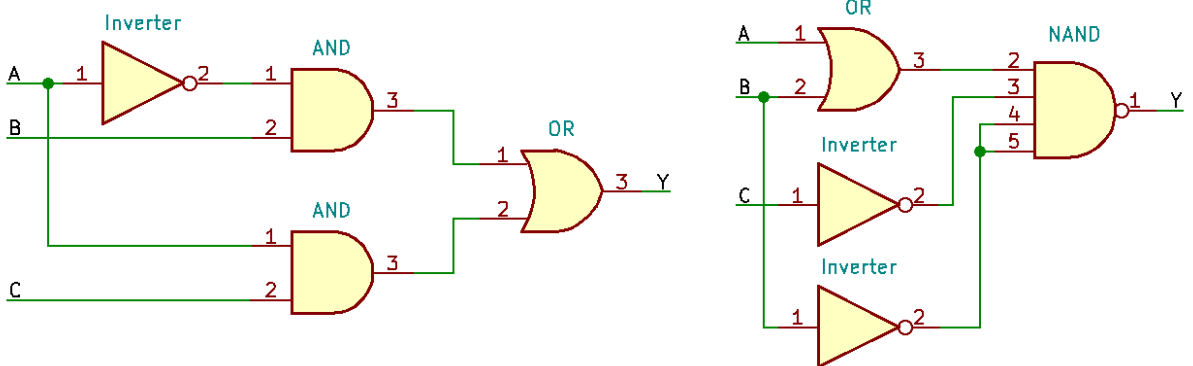
MAM – opakování digitální techniky

Kombinační logika



Pravdivostní tabulka hradel

		NAND	AND	NOR	OR	Inverter	XOR
A	B	Y	Y	Y	Y	Y	Y
0	0	1	0	1	0	1	0
0	1	1	0	0	1	1	1
1	0	1	0	0	1	0	1
1	1	0	1	0	1	0	0

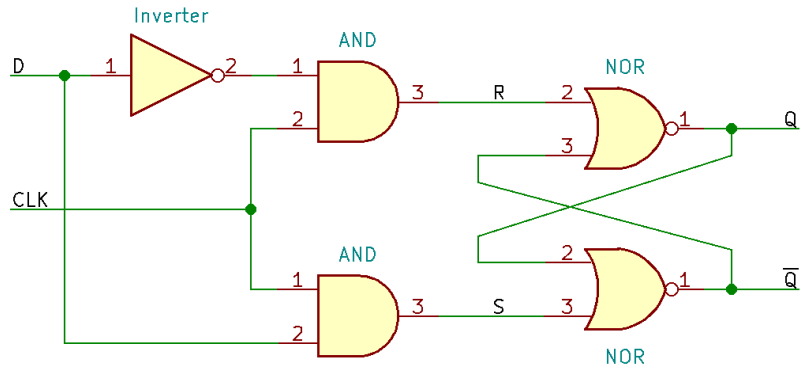
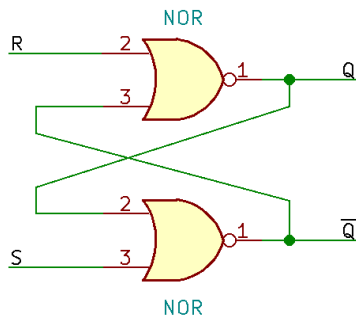


$$Y = \bar{A} \cdot B + A \cdot C$$

$$Y = \overline{(A + B) \cdot C \cdot \bar{B}}$$

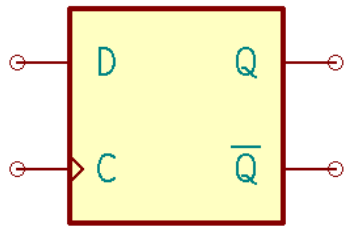
Sekvenční obvody

Paměťové členy



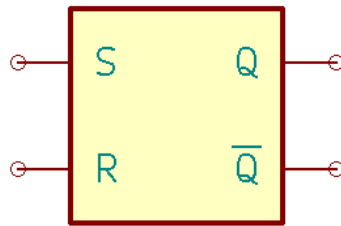
Bloky základních obvodů

Flip Flop D



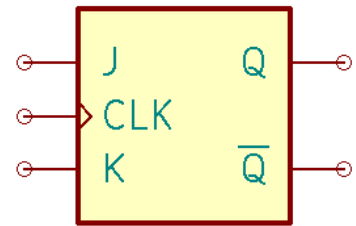
Flip Flop D			
D	C	Q_n	Q_{n+1}
0	\nearrow	x	0
1	\nearrow	x	1
X	\searrow	Q_n	Q_n
X	L/H	Q_n	Q_n

Flip Flop RS



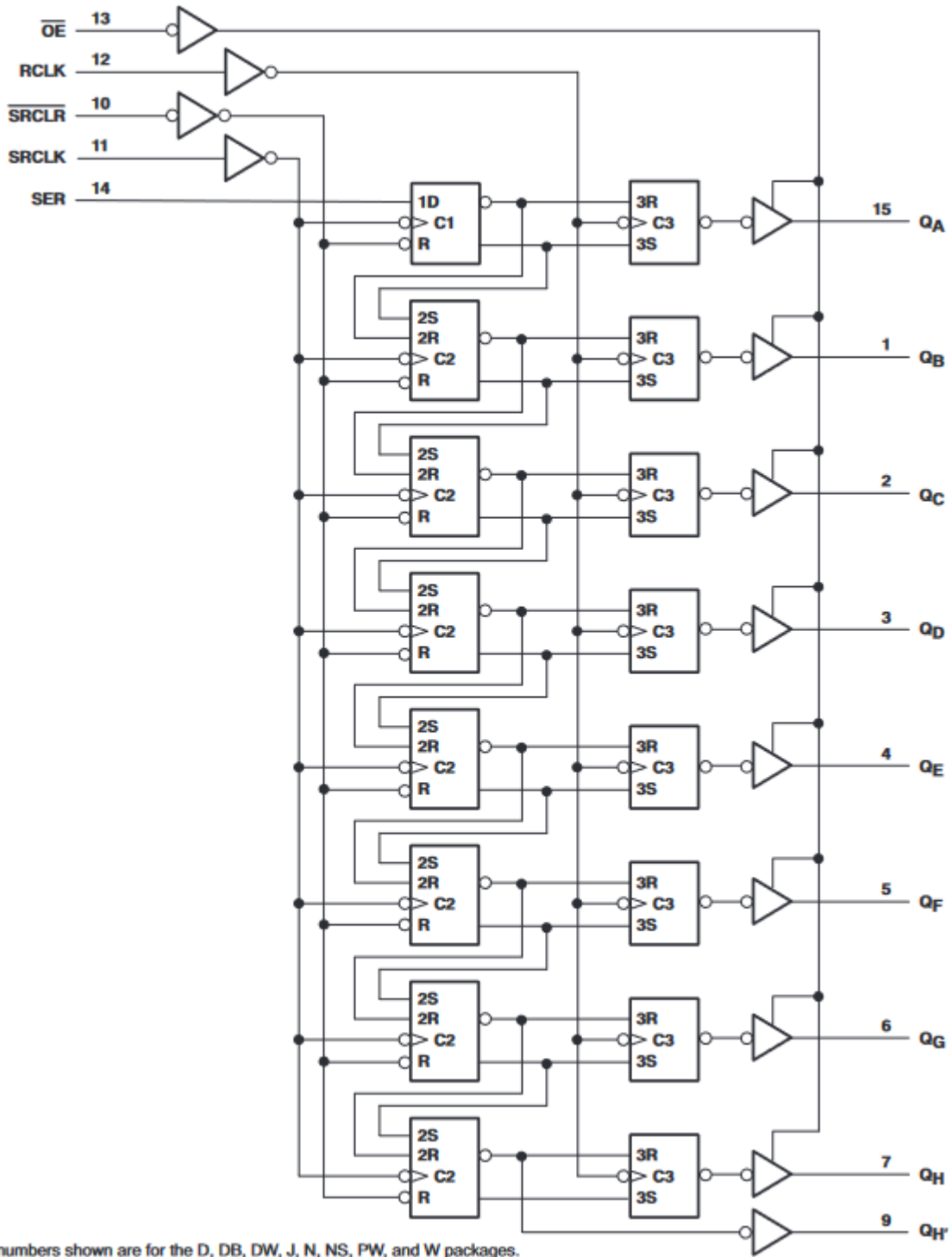
Flip Flop R-S			
S	R	Q_n	Q_{n+1}
1	0	X	1
0	1	X	0
0	0	Q_n	Q_n
1	1	Q_n	?

Flip Flop JK



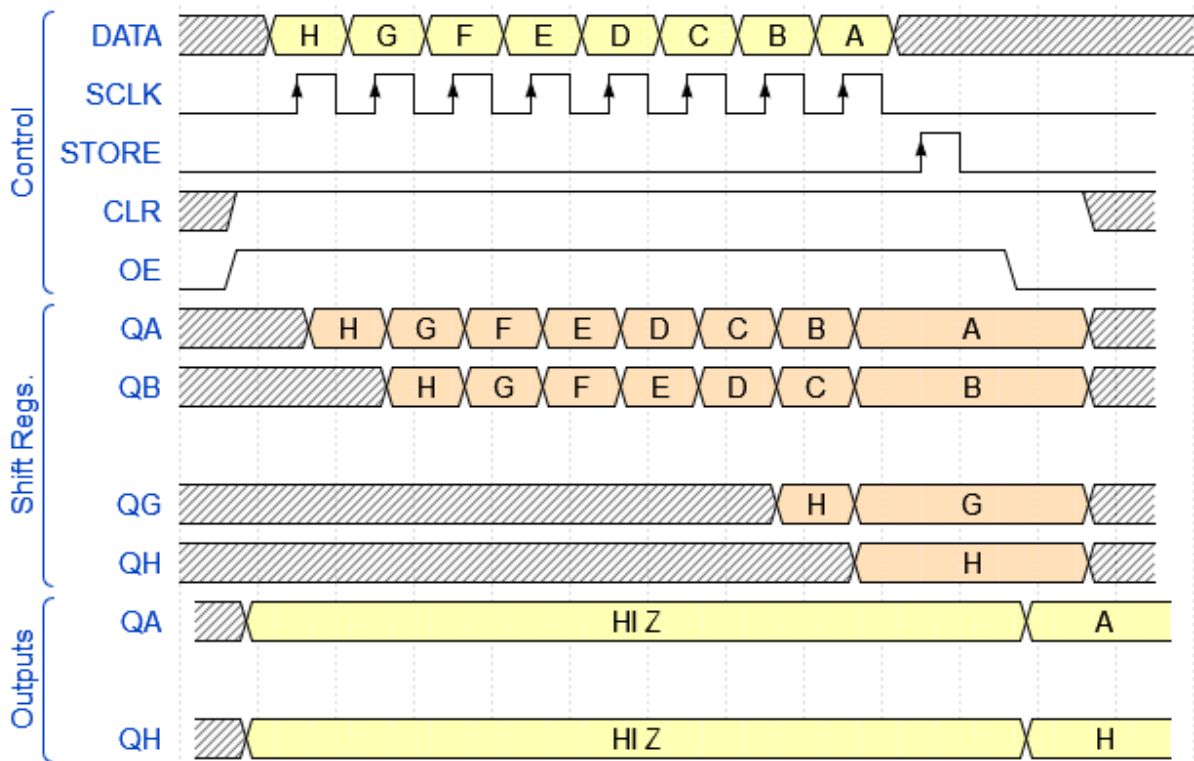
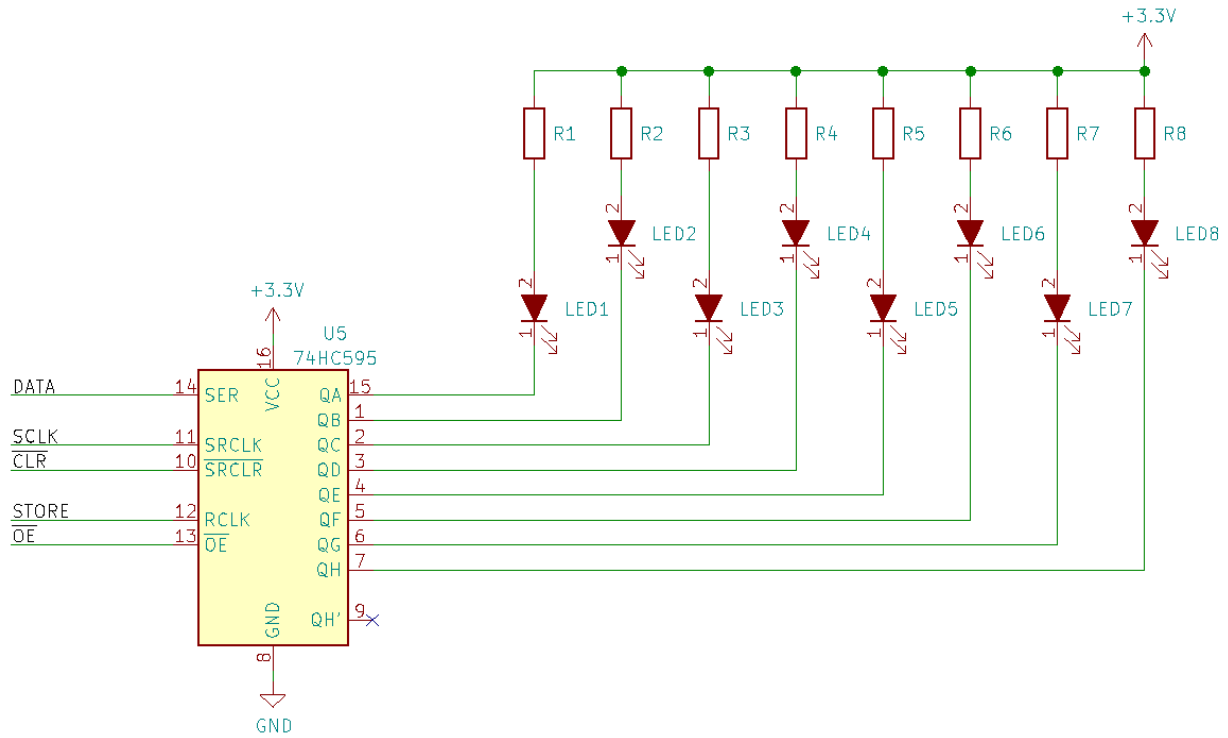
Flip Flop J-K				
J	K	CLK	Q_n	Q_{n+1}
0	0	\nearrow	Q_n	Q_n
1	0	\nearrow	x	1
0	1	\nearrow	x	0
1	1	\nearrow	Q_n	$\neg Q_n$
x	x	\searrow	Q_n	Q_n
x	x	L/H	Q_n	Q_n

Posuvný registr



Pin numbers shown are for the D, DB, DW, J, N, NS, PW, and W packages.

<https://www.ti.com/lit/ds/symlink/sn74hc595.pdf>



<https://wavedrom.com/editor.html>